

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                        | TURN OUT    |            |           |
|------------------------|-------------|------------|-----------|
|                        | Reg. Voters | Cards Cast | % Turnout |
| Jurisdiction Wide      |             |            |           |
| 100001.11-CP 100001.11 |             |            |           |
| Precinct TSx           | 1615        | 0          | 0.00%     |
| Precinct OS            | 1615        | 190        | 11.76%    |
| Precinct VBM           | 1615        | 781        | 48.36%    |
| MB Precinct            | 1615        | 0          | 0.00%     |
| Provisional            | 1615        | 17         | 1.05%     |
| Total                  | 1615        | 988        | 61.18%    |
| 100002.11-CP 100002.11 |             |            |           |
| Precinct TSx           | 1000        | 0          | 0.00%     |
| Precinct OS            | 1000        | 89         | 8.90%     |
| Precinct VBM           | 1000        | 476        | 47.60%    |
| MB Precinct            | 1000        | 0          | 0.00%     |
| Provisional            | 1000        | 20         | 2.00%     |
| Total                  | 1000        | 585        | 58.50%    |
| 100003.11-CP 100003.11 |             |            |           |
| Precinct TSx           | 1269        | 0          | 0.00%     |
| Precinct OS            | 1269        | 123        | 9.69%     |
| Precinct VBM           | 1269        | 773        | 60.91%    |
| MB Precinct            | 1269        | 0          | 0.00%     |
| Provisional            | 1269        | 24         | 1.89%     |
| Total                  | 1269        | 920        | 72.50%    |
| 100004.11-CP 100004.11 |             |            |           |
| Precinct TSx           | 1633        | 0          | 0.00%     |
| Precinct OS            | 1633        | 254        | 15.55%    |
| Precinct VBM           | 1633        | 738        | 45.19%    |
| MB Precinct            | 1633        | 0          | 0.00%     |
| Provisional            | 1633        | 4          | 0.24%     |
| Total                  | 1633        | 996        | 60.99%    |
| 100005.11-CP 100005.11 |             |            |           |
| Precinct TSx           | 1586        | 0          | 0.00%     |
| Precinct OS            | 1586        | 146        | 9.21%     |
| Precinct VBM           | 1586        | 895        | 56.43%    |
| MB Precinct            | 1586        | 0          | 0.00%     |
| Provisional            | 1586        | 23         | 1.45%     |
| Total                  | 1586        | 1064       | 67.09%    |
| 100006.11-CP 100006.11 |             |            |           |
| Precinct TSx           | 1928        | 0          | 0.00%     |
| Precinct OS            | 1928        | 252        | 13.07%    |
| Precinct VBM           | 1928        | 944        | 48.96%    |
| MB Precinct            | 1928        | 0          | 0.00%     |
| Provisional            | 1928        | 31         | 1.61%     |
| Total                  | 1928        | 1227       | 63.64%    |
| 100008.11-CP 100008.11 |             |            |           |
| Precinct TSx           | 1671        | 0          | 0.00%     |
| Precinct OS            | 1671        | 197        | 11.79%    |
| Precinct VBM           | 1671        | 740        | 44.28%    |
| MB Precinct            | 1671        | 0          | 0.00%     |
| Provisional            | 1671        | 17         | 1.02%     |
| Total                  | 1671        | 954        | 57.09%    |
| 100009.11-CP 100009.11 |             |            |           |
| Precinct TSx           | 1838        | 0          | 0.00%     |
| Precinct OS            | 1838        | 169        | 9.19%     |
| Precinct VBM           | 1838        | 751        | 40.86%    |
| MB Precinct            | 1838        | 0          | 0.00%     |
| Provisional            | 1838        | 26         | 1.41%     |
| Total                  | 1838        | 946        | 51.47%    |
| 100012.11-CP 100012.11 |             |            |           |
| Precinct TSx           | 2108        | 0          | 0.00%     |
| Precinct OS            | 2108        | 255        | 12.10%    |
| Precinct VBM           | 2108        | 968        | 45.92%    |

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|                        | TURN OUT    |            |           |
|------------------------|-------------|------------|-----------|
|                        | Reg. Voters | Cards Cast | % Turnout |
| MB Precinct            | 2108        | 0          | 0.00%     |
| Provisional            | 2108        | 29         | 1.38%     |
| Total                  | 2108        | 1252       | 59.39%    |
| 100013.11-CP 100013.11 |             |            |           |
| Precinct TSx           | 2087        | 0          | 0.00%     |
| Precinct OS            | 2087        | 250        | 11.98%    |
| Precinct VBM           | 2087        | 908        | 43.51%    |
| MB Precinct            | 2087        | 0          | 0.00%     |
| Provisional            | 2087        | 28         | 1.34%     |
| Total                  | 2087        | 1186       | 56.83%    |
| 100014.11-CP 100014.11 |             |            |           |
| Precinct TSx           | 1659        | 0          | 0.00%     |
| Precinct OS            | 1659        | 211        | 12.72%    |
| Precinct VBM           | 1659        | 706        | 42.56%    |
| MB Precinct            | 1659        | 0          | 0.00%     |
| Provisional            | 1659        | 30         | 1.81%     |
| Total                  | 1659        | 947        | 57.08%    |
| 100022.11-CP 100022.11 |             |            |           |
| Precinct TSx           | 1432        | 0          | 0.00%     |
| Precinct OS            | 1432        | 177        | 12.36%    |
| Precinct VBM           | 1432        | 678        | 47.35%    |
| MB Precinct            | 1432        | 0          | 0.00%     |
| Provisional            | 1432        | 13         | 0.91%     |
| Total                  | 1432        | 868        | 60.61%    |
| 100023.11-CP 100023.11 |             |            |           |
| Precinct TSx           | 2135        | 0          | 0.00%     |
| Precinct OS            | 2135        | 217        | 10.16%    |
| Precinct VBM           | 2135        | 1053       | 49.32%    |
| MB Precinct            | 2135        | 0          | 0.00%     |
| Provisional            | 2135        | 33         | 1.55%     |
| Total                  | 2135        | 1303       | 61.03%    |
| 200003.12-CP 200003.12 |             |            |           |
| Precinct TSx           | 1362        | 0          | 0.00%     |
| Precinct OS            | 1362        | 166        | 12.19%    |
| Precinct VBM           | 1362        | 558        | 40.97%    |
| MB Precinct            | 1362        | 0          | 0.00%     |
| Provisional            | 1362        | 31         | 2.28%     |
| Total                  | 1362        | 755        | 55.43%    |
| 200004.12-CP 200004.12 |             |            |           |
| Precinct TSx           | 1507        | 0          | 0.00%     |
| Precinct OS            | 1507        | 182        | 12.08%    |
| Precinct VBM           | 1507        | 662        | 43.93%    |
| MB Precinct            | 1507        | 0          | 0.00%     |
| Provisional            | 1507        | 14         | 0.93%     |
| Total                  | 1507        | 858        | 56.93%    |
| 200005.02-CP 200005.02 |             |            |           |
| Precinct TSx           | 867         | 0          | 0.00%     |
| Precinct OS            | 867         | 94         | 10.84%    |
| Precinct VBM           | 867         | 452        | 52.13%    |
| MB Precinct            | 867         | 0          | 0.00%     |
| Provisional            | 867         | 21         | 2.42%     |
| Total                  | 867         | 567        | 65.40%    |
| 200006.12-CP 200006.12 |             |            |           |
| Precinct TSx           | 1086        | 0          | 0.00%     |
| Precinct OS            | 1086        | 117        | 10.77%    |
| Precinct VBM           | 1086        | 532        | 48.99%    |
| MB Precinct            | 1086        | 0          | 0.00%     |
| Provisional            | 1086        | 29         | 2.67%     |
| Total                  | 1086        | 678        | 62.43%    |
| 200008.12-CP 200008.12 |             |            |           |
| Precinct TSx           | 999         | 0          | 0.00%     |

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|                        | TURN OUT    |            |           |
|------------------------|-------------|------------|-----------|
|                        | Reg. Voters | Cards Cast | % Turnout |
| Precinct OS            | 999         | 155        | 15.52%    |
| Precinct VBM           | 999         | 379        | 37.94%    |
| MB Precinct            | 999         | 0          | 0.00%     |
| Provisional            | 999         | 10         | 1.00%     |
| Total                  | 999         | 544        | 54.45%    |
| 200010.12-CP 200010.12 |             |            |           |
| Precinct TSx           | 1976        | 4          | 0.20%     |
| Precinct OS            | 1976        | 197        | 9.97%     |
| Precinct VBM           | 1976        | 859        | 43.47%    |
| MB Precinct            | 1976        | 0          | 0.00%     |
| Provisional            | 1976        | 34         | 1.72%     |
| Total                  | 1976        | 1094       | 55.36%    |
| 200012.12-CP 200012.12 |             |            |           |
| Precinct TSx           | 1064        | 0          | 0.00%     |
| Precinct OS            | 1064        | 145        | 13.63%    |
| Precinct VBM           | 1064        | 464        | 43.61%    |
| MB Precinct            | 1064        | 0          | 0.00%     |
| Provisional            | 1064        | 14         | 1.32%     |
| Total                  | 1064        | 623        | 58.55%    |
| 200013.12-CP 200013.12 |             |            |           |
| Precinct TSx           | 1574        | 0          | 0.00%     |
| Precinct OS            | 1574        | 211        | 13.41%    |
| Precinct VBM           | 1574        | 701        | 44.54%    |
| MB Precinct            | 1574        | 0          | 0.00%     |
| Provisional            | 1574        | 23         | 1.46%     |
| Total                  | 1574        | 935        | 59.40%    |
| 200014.12-CP 200014.12 |             |            |           |
| Precinct TSx           | 1438        | 0          | 0.00%     |
| Precinct OS            | 1438        | 201        | 13.98%    |
| Precinct VBM           | 1438        | 613        | 42.63%    |
| MB Precinct            | 1438        | 0          | 0.00%     |
| Provisional            | 1438        | 30         | 2.09%     |
| Total                  | 1438        | 844        | 58.69%    |
| 200015.02-CP 200015.02 |             |            |           |
| Precinct TSx           | 1832        | 0          | 0.00%     |
| Precinct OS            | 1832        | 218        | 11.90%    |
| Precinct VBM           | 1832        | 886        | 48.36%    |
| MB Precinct            | 1832        | 0          | 0.00%     |
| Provisional            | 1832        | 22         | 1.20%     |
| Total                  | 1832        | 1126       | 61.46%    |
| 200016.12-CP 200016.12 |             |            |           |
| Precinct TSx           | 301         | 0          | 0.00%     |
| Precinct OS            | 301         | 26         | 8.64%     |
| Precinct VBM           | 301         | 139        | 46.18%    |
| MB Precinct            | 301         | 0          | 0.00%     |
| Provisional            | 301         | 6          | 1.99%     |
| Total                  | 301         | 171        | 56.81%    |
| 200016.13-CP 200016.13 |             |            |           |
| Precinct TSx           | 565         | 0          | 0.00%     |
| Precinct OS            | 565         | 80         | 14.16%    |
| Precinct VBM           | 565         | 338        | 59.82%    |
| MB Precinct            | 565         | 0          | 0.00%     |
| Provisional            | 565         | 6          | 1.06%     |
| Total                  | 565         | 424        | 75.04%    |
| 200017.02-CP 200017.02 |             |            |           |
| Precinct TSx           | 1286        | 0          | 0.00%     |
| Precinct OS            | 1286        | 146        | 11.35%    |
| Precinct VBM           | 1286        | 594        | 46.19%    |
| MB Precinct            | 1286        | 0          | 0.00%     |
| Provisional            | 1286        | 12         | 0.93%     |
| Total                  | 1286        | 752        | 58.48%    |

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|                        | TURN OUT    |            |           |
|------------------------|-------------|------------|-----------|
|                        | Reg. Voters | Cards Cast | % Turnout |
| 200018.02-CP 200018.02 |             |            |           |
| Precinct TSx           | 1454        | 0          | 0.00%     |
| Precinct OS            | 1454        | 169        | 11.62%    |
| Precinct VBM           | 1454        | 704        | 48.42%    |
| MB Precinct            | 1454        | 0          | 0.00%     |
| Provisional            | 1454        | 13         | 0.89%     |
| Total                  | 1454        | 886        | 60.94%    |
| 200020.02-CP 200020.02 |             |            |           |
| Precinct TSx           | 1213        | 0          | 0.00%     |
| Precinct OS            | 1213        | 139        | 11.46%    |
| Precinct VBM           | 1213        | 526        | 43.36%    |
| MB Precinct            | 1213        | 0          | 0.00%     |
| Provisional            | 1213        | 19         | 1.57%     |
| Total                  | 1213        | 684        | 56.39%    |
| 200021.01-CP 200021.01 |             |            |           |
| Precinct TSx           | 436         | 0          | 0.00%     |
| Precinct OS            | 436         | 38         | 8.72%     |
| Precinct VBM           | 436         | 206        | 47.25%    |
| MB Precinct            | 436         | 0          | 0.00%     |
| Provisional            | 436         | 7          | 1.61%     |
| Total                  | 436         | 251        | 57.57%    |
| 200021.02-CP 200021.02 |             |            |           |
| Precinct TSx           | 1296        | 0          | 0.00%     |
| Precinct OS            | 1296        | 170        | 13.12%    |
| Precinct VBM           | 1296        | 752        | 58.02%    |
| MB Precinct            | 1296        | 0          | 0.00%     |
| Provisional            | 1296        | 20         | 1.54%     |
| Total                  | 1296        | 942        | 72.69%    |
| 200024.02-CP 200024.02 |             |            |           |
| Precinct TSx           | 555         | 0          | 0.00%     |
| Precinct OS            | 555         | 55         | 9.91%     |
| Precinct VBM           | 555         | 282        | 50.81%    |
| MB Precinct            | 555         | 0          | 0.00%     |
| Provisional            | 555         | 9          | 1.62%     |
| Total                  | 555         | 346        | 62.34%    |
| 300001.04-CP 300001.04 |             |            |           |
| Precinct TSx           | 259         | 0          | 0.00%     |
| Precinct OS            | 259         | 12         | 4.63%     |
| Precinct VBM           | 259         | 100        | 38.61%    |
| MB Precinct            | 259         | 0          | 0.00%     |
| Provisional            | 259         | 0          | 0.00%     |
| Total                  | 259         | 112        | 43.24%    |
| 300001.05-CP 300001.05 |             |            |           |
| Precinct TSx           | 731         | 0          | 0.00%     |
| Precinct OS            | 731         | 76         | 10.40%    |
| Precinct VBM           | 731         | 330        | 45.14%    |
| MB Precinct            | 731         | 0          | 0.00%     |
| Provisional            | 731         | 10         | 1.37%     |
| Total                  | 731         | 416        | 56.91%    |
| 300003.05-CP 300003.05 |             |            |           |
| Precinct TSx           | 1932        | 0          | 0.00%     |
| Precinct OS            | 1932        | 205        | 10.61%    |
| Precinct VBM           | 1932        | 924        | 47.83%    |
| MB Precinct            | 1932        | 0          | 0.00%     |
| Provisional            | 1932        | 27         | 1.40%     |
| Total                  | 1932        | 1156       | 59.83%    |
| 300004.05-CP 300004.05 |             |            |           |
| Precinct TSx           | 1273        | 0          | 0.00%     |
| Precinct OS            | 1273        | 175        | 13.75%    |
| Precinct VBM           | 1273        | 576        | 45.25%    |
| MB Precinct            | 1273        | 0          | 0.00%     |

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|                        | TURN OUT    |            |           |
|------------------------|-------------|------------|-----------|
|                        | Reg. Voters | Cards Cast | % Turnout |
| Provisional            | 1273        | 17         | 1.34%     |
| Total                  | 1273        | 768        | 60.33%    |
| 300005.05-CP 300005.05 |             |            |           |
| Precinct TSx           | 1374        | 0          | 0.00%     |
| Precinct OS            | 1374        | 163        | 11.86%    |
| Precinct VBM           | 1374        | 660        | 48.03%    |
| MB Precinct            | 1374        | 0          | 0.00%     |
| Provisional            | 1374        | 26         | 1.89%     |
| Total                  | 1374        | 849        | 61.79%    |
| 300008.05-CP 300008.05 |             |            |           |
| Precinct TSx           | 1366        | 0          | 0.00%     |
| Precinct OS            | 1366        | 139        | 10.18%    |
| Precinct VBM           | 1366        | 740        | 54.17%    |
| MB Precinct            | 1366        | 0          | 0.00%     |
| Provisional            | 1366        | 14         | 1.02%     |
| Total                  | 1366        | 893        | 65.37%    |
| 300009.04-CP 300009.04 |             |            |           |
| Precinct TSx           | 873         | 0          | 0.00%     |
| Precinct OS            | 873         | 121        | 13.86%    |
| Precinct VBM           | 873         | 395        | 45.25%    |
| MB Precinct            | 873         | 0          | 0.00%     |
| Provisional            | 873         | 13         | 1.49%     |
| Total                  | 873         | 529        | 60.60%    |
| 300010.04-CP 300010.04 |             |            |           |
| Precinct TSx           | 1340        | 0          | 0.00%     |
| Precinct OS            | 1340        | 171        | 12.76%    |
| Precinct VBM           | 1340        | 656        | 48.96%    |
| MB Precinct            | 1340        | 0          | 0.00%     |
| Provisional            | 1340        | 11         | 0.82%     |
| Total                  | 1340        | 838        | 62.54%    |
| 300011.04-CP 300011.04 |             |            |           |
| Precinct TSx           | 1491        | 0          | 0.00%     |
| Precinct OS            | 1491        | 142        | 9.52%     |
| Precinct VBM           | 1491        | 711        | 47.69%    |
| MB Precinct            | 1491        | 0          | 0.00%     |
| Provisional            | 1491        | 22         | 1.48%     |
| Total                  | 1491        | 875        | 58.69%    |
| 300013.05-CP 300013.05 |             |            |           |
| Precinct TSx           | 1490        | 3          | 0.20%     |
| Precinct OS            | 1490        | 128        | 8.59%     |
| Precinct VBM           | 1490        | 721        | 48.39%    |
| MB Precinct            | 1490        | 0          | 0.00%     |
| Provisional            | 1490        | 15         | 1.01%     |
| Total                  | 1490        | 867        | 58.19%    |
| 300015.04-CP 300015.04 |             |            |           |
| Precinct TSx           | 119         | 0          | 0.00%     |
| Precinct OS            | 119         | 10         | 8.40%     |
| Precinct VBM           | 119         | 63         | 52.94%    |
| MB Precinct            | 119         | 0          | 0.00%     |
| Provisional            | 119         | 4          | 3.36%     |
| Total                  | 119         | 77         | 64.71%    |
| 300015.05-CP 300015.05 |             |            |           |
| Precinct TSx           | 503         | 0          | 0.00%     |
| Precinct OS            | 503         | 66         | 13.12%    |
| Precinct VBM           | 503         | 266        | 52.88%    |
| MB Precinct            | 503         | 0          | 0.00%     |
| Provisional            | 503         | 2          | 0.40%     |
| Total                  | 503         | 334        | 66.40%    |
| 300017.05-CP 300017.05 |             |            |           |
| Precinct TSx           | 1240        | 0          | 0.00%     |
| Precinct OS            | 1240        | 117        | 9.44%     |

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|                        | TURN OUT    |            |           |
|------------------------|-------------|------------|-----------|
|                        | Reg. Voters | Cards Cast | % Turnout |
| Precinct VBM           | 1240        | 655        | 52.82%    |
| MB Precinct            | 1240        | 0          | 0.00%     |
| Provisional            | 1240        | 9          | 0.73%     |
| Total                  | 1240        | 781        | 62.98%    |
| 300018.04-CP 300018.04 |             |            |           |
| Precinct TSx           | 1306        | 0          | 0.00%     |
| Precinct OS            | 1306        | 148        | 11.33%    |
| Precinct VBM           | 1306        | 505        | 38.67%    |
| MB Precinct            | 1306        | 0          | 0.00%     |
| Provisional            | 1306        | 31         | 2.37%     |
| Total                  | 1306        | 684        | 52.37%    |
| 300020.04-CP 300020.04 |             |            |           |
| Precinct TSx           | 1401        | 0          | 0.00%     |
| Precinct OS            | 1401        | 168        | 11.99%    |
| Precinct VBM           | 1401        | 632        | 45.11%    |
| MB Precinct            | 1401        | 0          | 0.00%     |
| Provisional            | 1401        | 14         | 1.00%     |
| Total                  | 1401        | 814        | 58.10%    |
| 300021.03-CP 300021.03 |             |            |           |
| Precinct TSx           | 1234        | 0          | 0.00%     |
| Precinct OS            | 1234        | 153        | 12.40%    |
| Precinct VBM           | 1234        | 614        | 49.76%    |
| MB Precinct            | 1234        | 0          | 0.00%     |
| Provisional            | 1234        | 13         | 1.05%     |
| Total                  | 1234        | 780        | 63.21%    |
| 300022.05-CP 300022.05 |             |            |           |
| Precinct TSx           | 872         | 0          | 0.00%     |
| Precinct OS            | 872         | 101        | 11.58%    |
| Precinct VBM           | 872         | 356        | 40.83%    |
| MB Precinct            | 872         | 0          | 0.00%     |
| Provisional            | 872         | 12         | 1.38%     |
| Total                  | 872         | 469        | 53.78%    |
| 300023.03-CP 300023.03 |             |            |           |
| Precinct TSx           | 1819        | 0          | 0.00%     |
| Precinct OS            | 1819        | 228        | 12.53%    |
| Precinct VBM           | 1819        | 924        | 50.80%    |
| MB Precinct            | 1819        | 0          | 0.00%     |
| Provisional            | 1819        | 26         | 1.43%     |
| Total                  | 1819        | 1178       | 64.76%    |
| 300023.05-CP 300023.05 |             |            |           |
| Precinct TSx           | 386         | 0          | 0.00%     |
| Precinct OS            | 386         | 45         | 11.66%    |
| Precinct VBM           | 386         | 193        | 50.00%    |
| MB Precinct            | 386         | 0          | 0.00%     |
| Provisional            | 386         | 8          | 2.07%     |
| Total                  | 386         | 246        | 63.73%    |
| 400001.14-CP 400001.14 |             |            |           |
| Precinct TSx           | 1957        | 0          | 0.00%     |
| Precinct OS            | 1957        | 178        | 9.10%     |
| Precinct VBM           | 1957        | 992        | 50.69%    |
| MB Precinct            | 1957        | 0          | 0.00%     |
| Provisional            | 1957        | 23         | 1.18%     |
| Total                  | 1957        | 1193       | 60.96%    |
| 400002.08-CP 400002.08 |             |            |           |
| Precinct TSx           | 1508        | 0          | 0.00%     |
| Precinct OS            | 1508        | 200        | 13.26%    |
| Precinct VBM           | 1508        | 730        | 48.41%    |
| MB Precinct            | 1508        | 0          | 0.00%     |
| Provisional            | 1508        | 15         | 0.99%     |
| Total                  | 1508        | 945        | 62.67%    |
| 400003.08-CP 400003.08 |             |            |           |

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|                        | TURN OUT    |            |           |
|------------------------|-------------|------------|-----------|
|                        | Reg. Voters | Cards Cast | % Turnout |
| Precinct TSx           | 1800        | 0          | 0.00%     |
| Precinct OS            | 1800        | 245        | 13.61%    |
| Precinct VBM           | 1800        | 773        | 42.94%    |
| MB Precinct            | 1800        | 0          | 0.00%     |
| Provisional            | 1800        | 35         | 1.94%     |
| Total                  | 1800        | 1053       | 58.50%    |
| 400004.14-CP 400004.14 |             |            |           |
| Precinct TSx           | 1536        | 0          | 0.00%     |
| Precinct OS            | 1536        | 194        | 12.63%    |
| Precinct VBM           | 1536        | 662        | 43.10%    |
| MB Precinct            | 1536        | 0          | 0.00%     |
| Provisional            | 1536        | 19         | 1.24%     |
| Total                  | 1536        | 875        | 56.97%    |
| 400005.08-CP 400005.08 |             |            |           |
| Precinct TSx           | 655         | 0          | 0.00%     |
| Precinct OS            | 655         | 79         | 12.06%    |
| Precinct VBM           | 655         | 262        | 40.00%    |
| MB Precinct            | 655         | 0          | 0.00%     |
| Provisional            | 655         | 11         | 1.68%     |
| Total                  | 655         | 352        | 53.74%    |
| 400006.08-CP 400006.08 |             |            |           |
| Precinct TSx           | 1537        | 0          | 0.00%     |
| Precinct OS            | 1537        | 228        | 14.83%    |
| Precinct VBM           | 1537        | 722        | 46.97%    |
| MB Precinct            | 1537        | 0          | 0.00%     |
| Provisional            | 1537        | 19         | 1.24%     |
| Total                  | 1537        | 969        | 63.04%    |
| 400007.08-CP 400007.08 |             |            |           |
| Precinct TSx           | 1766        | 0          | 0.00%     |
| Precinct OS            | 1766        | 228        | 12.91%    |
| Precinct VBM           | 1766        | 765        | 43.32%    |
| MB Precinct            | 1766        | 0          | 0.00%     |
| Provisional            | 1766        | 22         | 1.25%     |
| Total                  | 1766        | 1015       | 57.47%    |
| 400010.08-CP 400010.08 |             |            |           |
| Precinct TSx           | 1875        | 0          | 0.00%     |
| Precinct OS            | 1875        | 244        | 13.01%    |
| Precinct VBM           | 1875        | 869        | 46.35%    |
| MB Precinct            | 1875        | 0          | 0.00%     |
| Provisional            | 1875        | 31         | 1.65%     |
| Total                  | 1875        | 1144       | 61.01%    |
| 400012.08-CP 400012.08 |             |            |           |
| Precinct TSx           | 877         | 0          | 0.00%     |
| Precinct OS            | 877         | 113        | 12.88%    |
| Precinct VBM           | 877         | 434        | 49.49%    |
| MB Precinct            | 877         | 0          | 0.00%     |
| Provisional            | 877         | 22         | 2.51%     |
| Total                  | 877         | 569        | 64.88%    |
| 400013.08-CP 400013.08 |             |            |           |
| Precinct TSx           | 758         | 0          | 0.00%     |
| Precinct OS            | 758         | 127        | 16.75%    |
| Precinct VBM           | 758         | 315        | 41.56%    |
| MB Precinct            | 758         | 0          | 0.00%     |
| Provisional            | 758         | 4          | 0.53%     |
| Total                  | 758         | 446        | 58.84%    |
| 400014.08-CP 400014.08 |             |            |           |
| Precinct TSx           | 1526        | 0          | 0.00%     |
| Precinct OS            | 1526        | 230        | 15.07%    |
| Precinct VBM           | 1526        | 736        | 48.23%    |
| MB Precinct            | 1526        | 0          | 0.00%     |
| Provisional            | 1526        | 36         | 2.36%     |

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|                        | TURN OUT    |            |           |
|------------------------|-------------|------------|-----------|
|                        | Reg. Voters | Cards Cast | % Turnout |
| Total                  | 1526        | 1002       | 65.66%    |
| 400017.08-CP 400017.08 |             |            |           |
| Precinct TSx           | 1495        | 0          | 0.00%     |
| Precinct OS            | 1495        | 175        | 11.71%    |
| Precinct VBM           | 1495        | 752        | 50.30%    |
| MB Precinct            | 1495        | 0          | 0.00%     |
| Provisional            | 1495        | 28         | 1.87%     |
| Total                  | 1495        | 955        | 63.88%    |
| 400018.08-CP 400018.08 |             |            |           |
| Precinct TSx           | 597         | 0          | 0.00%     |
| Precinct OS            | 597         | 86         | 14.41%    |
| Precinct VBM           | 597         | 276        | 46.23%    |
| MB Precinct            | 597         | 0          | 0.00%     |
| Provisional            | 597         | 7          | 1.17%     |
| Total                  | 597         | 369        | 61.81%    |
| 400020.08-CP 400020.08 |             |            |           |
| Precinct TSx           | 724         | 0          | 0.00%     |
| Precinct OS            | 724         | 114        | 15.75%    |
| Precinct VBM           | 724         | 343        | 47.38%    |
| MB Precinct            | 724         | 0          | 0.00%     |
| Provisional            | 724         | 2          | 0.28%     |
| Total                  | 724         | 459        | 63.40%    |
| 400021.07-CP 400021.07 |             |            |           |
| Precinct TSx           | 1332        | 0          | 0.00%     |
| Precinct OS            | 1332        | 127        | 9.53%     |
| Precinct VBM           | 1332        | 767        | 57.58%    |
| MB Precinct            | 1332        | 0          | 0.00%     |
| Provisional            | 1332        | 18         | 1.35%     |
| Total                  | 1332        | 912        | 68.47%    |
| 500001.10-CP 500001.10 |             |            |           |
| Precinct TSx           | 1717        | 0          | 0.00%     |
| Precinct OS            | 1717        | 210        | 12.23%    |
| Precinct VBM           | 1717        | 617        | 35.93%    |
| MB Precinct            | 1717        | 0          | 0.00%     |
| Provisional            | 1717        | 19         | 1.11%     |
| Total                  | 1717        | 846        | 49.27%    |
| 500002.10-CP 500002.10 |             |            |           |
| Precinct TSx           | 1381        | 0          | 0.00%     |
| Precinct OS            | 1381        | 137        | 9.92%     |
| Precinct VBM           | 1381        | 412        | 29.83%    |
| MB Precinct            | 1381        | 0          | 0.00%     |
| Provisional            | 1381        | 31         | 2.24%     |
| Total                  | 1381        | 580        | 42.00%    |
| 500003.10-CP 500003.10 |             |            |           |
| Precinct TSx           | 1065        | 0          | 0.00%     |
| Precinct OS            | 1065        | 131        | 12.30%    |
| Precinct VBM           | 1065        | 346        | 32.49%    |
| MB Precinct            | 1065        | 0          | 0.00%     |
| Provisional            | 1065        | 11         | 1.03%     |
| Total                  | 1065        | 488        | 45.82%    |
| 500004.10-CP 500004.10 |             |            |           |
| Precinct TSx           | 981         | 0          | 0.00%     |
| Precinct OS            | 981         | 122        | 12.44%    |
| Precinct VBM           | 981         | 264        | 26.91%    |
| MB Precinct            | 981         | 0          | 0.00%     |
| Provisional            | 981         | 21         | 2.14%     |
| Total                  | 981         | 407        | 41.49%    |
| 500005.10-CP 500005.10 |             |            |           |
| Precinct TSx           | 946         | 0          | 0.00%     |
| Precinct OS            | 946         | 107        | 11.31%    |
| Precinct VBM           | 946         | 250        | 26.43%    |



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|                        | TURN OUT    |            |           |
|------------------------|-------------|------------|-----------|
|                        | Reg. Voters | Cards Cast | % Turnout |
| MB Precinct            | 946         | 0          | 0.00%     |
| Provisional            | 946         | 16         | 1.69%     |
| Total                  | 946         | 373        | 39.43%    |
| 500006.10-CP 500006.10 |             |            |           |
| Precinct TSx           | 1661        | 0          | 0.00%     |
| Precinct OS            | 1661        | 190        | 11.44%    |
| Precinct VBM           | 1661        | 498        | 29.98%    |
| MB Precinct            | 1661        | 0          | 0.00%     |
| Provisional            | 1661        | 58         | 3.49%     |
| Total                  | 1661        | 746        | 44.91%    |
| 500007.10-CP 500007.10 |             |            |           |
| Precinct TSx           | 1788        | 0          | 0.00%     |
| Precinct OS            | 1788        | 208        | 11.63%    |
| Precinct VBM           | 1788        | 688        | 38.48%    |
| MB Precinct            | 1788        | 0          | 0.00%     |
| Provisional            | 1788        | 20         | 1.12%     |
| Total                  | 1788        | 916        | 51.23%    |
| 500009.10-CP 500009.10 |             |            |           |
| Precinct TSx           | 1073        | 0          | 0.00%     |
| Precinct OS            | 1073        | 121        | 11.28%    |
| Precinct VBM           | 1073        | 304        | 28.33%    |
| MB Precinct            | 1073        | 0          | 0.00%     |
| Provisional            | 1073        | 38         | 3.54%     |
| Total                  | 1073        | 463        | 43.15%    |
| 500011.10-CP 500011.10 |             |            |           |
| Precinct TSx           | 1635        | 0          | 0.00%     |
| Precinct OS            | 1635        | 178        | 10.89%    |
| Precinct VBM           | 1635        | 380        | 23.24%    |
| MB Precinct            | 1635        | 0          | 0.00%     |
| Provisional            | 1635        | 40         | 2.45%     |
| Total                  | 1635        | 598        | 36.57%    |
| 500012.10-CP 500012.10 |             |            |           |
| Precinct TSx           | 1574        | 1          | 0.06%     |
| Precinct OS            | 1574        | 195        | 12.39%    |
| Precinct VBM           | 1574        | 601        | 38.18%    |
| MB Precinct            | 1574        | 0          | 0.00%     |
| Provisional            | 1574        | 37         | 2.35%     |
| Total                  | 1574        | 834        | 52.99%    |
| 500015.10-CP 500015.10 |             |            |           |
| Precinct TSx           | 978         | 0          | 0.00%     |
| Precinct OS            | 978         | 105        | 10.74%    |
| Precinct VBM           | 978         | 407        | 41.62%    |
| MB Precinct            | 978         | 0          | 0.00%     |
| Provisional            | 978         | 13         | 1.33%     |
| Total                  | 978         | 525        | 53.68%    |
| 500020.10-CP 500020.10 |             |            |           |
| Precinct TSx           | 1530        | 0          | 0.00%     |
| Precinct OS            | 1530        | 161        | 10.52%    |
| Precinct VBM           | 1530        | 659        | 43.07%    |
| MB Precinct            | 1530        | 0          | 0.00%     |
| Provisional            | 1530        | 27         | 1.76%     |
| Total                  | 1530        | 847        | 55.36%    |
| 500021.10-CP 500021.10 |             |            |           |
| Precinct TSx           | 1404        | 0          | 0.00%     |
| Precinct OS            | 1404        | 169        | 12.04%    |
| Precinct VBM           | 1404        | 632        | 45.01%    |
| MB Precinct            | 1404        | 0          | 0.00%     |
| Provisional            | 1404        | 28         | 1.99%     |
| Total                  | 1404        | 829        | 59.05%    |
| 95U201.01-MB 95U201.01 |             |            |           |
| Precinct TSx           | 8           | 0          | 0.00%     |

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|                        | TURN OUT    |            |           |
|------------------------|-------------|------------|-----------|
|                        | Reg. Voters | Cards Cast | % Turnout |
| Precinct OS            | 8           | 0          | 0.00%     |
| Precinct VBM           | 8           | 0          | 0.00%     |
| MB Precinct            | 8           | 7          | 87.50%    |
| Provisional            | 8           | 0          | 0.00%     |
| Total                  | 8           | 7          | 87.50%    |
| 95U201.02-MB 95U201.02 |             |            |           |
| Precinct TSx           | 541         | 0          | 0.00%     |
| Precinct OS            | 541         | 0          | 0.00%     |
| Precinct VBM           | 541         | 0          | 0.00%     |
| MB Precinct            | 541         | 358        | 66.17%    |
| Provisional            | 541         | 1          | 0.18%     |
| Total                  | 541         | 359        | 66.36%    |
| 95U202.02-MB 95U202.02 |             |            |           |
| Precinct TSx           | 859         | 0          | 0.00%     |
| Precinct OS            | 859         | 0          | 0.00%     |
| Precinct VBM           | 859         | 0          | 0.00%     |
| MB Precinct            | 859         | 574        | 66.82%    |
| Provisional            | 859         | 4          | 0.47%     |
| Total                  | 859         | 578        | 67.29%    |
| 95U203.02-MB 95U203.02 |             |            |           |
| Precinct TSx           | 0           | 0          | -         |
| Precinct OS            | 0           | 0          | -         |
| Precinct VBM           | 0           | 0          | -         |
| MB Precinct            | 0           | 0          | -         |
| Provisional            | 0           | 0          | -         |
| Total                  | 0           | 0          | -         |
| 95U204.01-MB 95U204.01 |             |            |           |
| Precinct TSx           | 895         | 0          | 0.00%     |
| Precinct OS            | 895         | 0          | 0.00%     |
| Precinct VBM           | 895         | 0          | 0.00%     |
| MB Precinct            | 895         | 651        | 72.74%    |
| Provisional            | 895         | 2          | 0.22%     |
| Total                  | 895         | 653        | 72.96%    |
| 95U301.05-MB 95U301.05 |             |            |           |
| Precinct TSx           | 244         | 0          | 0.00%     |
| Precinct OS            | 244         | 0          | 0.00%     |
| Precinct VBM           | 244         | 0          | 0.00%     |
| MB Precinct            | 244         | 151        | 61.89%    |
| Provisional            | 244         | 2          | 0.82%     |
| Total                  | 244         | 153        | 62.70%    |
| 95U302.04-MB 95U302.04 |             |            |           |
| Precinct TSx           | 0           | 0          | -         |
| Precinct OS            | 0           | 0          | -         |
| Precinct VBM           | 0           | 0          | -         |
| MB Precinct            | 0           | 1          | -         |
| Provisional            | 0           | 0          | -         |
| Total                  | 0           | 1          | -         |
| 95U302.05-MB 95U302.05 |             |            |           |
| Precinct TSx           | 128         | 0          | 0.00%     |
| Precinct OS            | 128         | 0          | 0.00%     |
| Precinct VBM           | 128         | 0          | 0.00%     |
| MB Precinct            | 128         | 86         | 67.19%    |
| Provisional            | 128         | 0          | 0.00%     |
| Total                  | 128         | 86         | 67.19%    |
| 95U303.03-MB 95U303.03 |             |            |           |
| Precinct TSx           | 144         | 0          | 0.00%     |
| Precinct OS            | 144         | 0          | 0.00%     |
| Precinct VBM           | 144         | 0          | 0.00%     |
| MB Precinct            | 144         | 84         | 58.33%    |
| Provisional            | 144         | 0          | 0.00%     |
| Total                  | 144         | 84         | 58.33%    |

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|                        | Reg. Voters | Cards Cast | % Turnout |
| 95U303.05-MB 95U303.05 |             |            |           |
| Precinct TSx           | 159         | 0          | 0.00%     |
| Precinct OS            | 159         | 0          | 0.00%     |
| Precinct VBM           | 159         | 0          | 0.00%     |
| MB Precinct            | 159         | 111        | 69.81%    |
| Provisional            | 159         | 1          | 0.63%     |
| Total                  | 159         | 112        | 70.44%    |
| 95U304.05-MB 95U304.05 |             |            |           |
| Precinct TSx           | 0           | 0          | -         |
| Precinct OS            | 0           | 0          | -         |
| Precinct VBM           | 0           | 0          | -         |
| MB Precinct            | 0           | 0          | -         |
| Provisional            | 0           | 0          | -         |
| Total                  | 0           | 0          | -         |
| 95U305.05-MB 95U305.05 |             |            |           |
| Precinct TSx           | 7           | 0          | 0.00%     |
| Precinct OS            | 7           | 0          | 0.00%     |
| Precinct VBM           | 7           | 0          | 0.00%     |
| MB Precinct            | 7           | 7          | 100.00%   |
| Provisional            | 7           | 0          | 0.00%     |
| Total                  | 7           | 7          | 100.00%   |
| 95U401.08-MB 95U401.08 |             |            |           |
| Precinct TSx           | 39          | 0          | 0.00%     |
| Precinct OS            | 39          | 0          | 0.00%     |
| Precinct VBM           | 39          | 0          | 0.00%     |
| MB Precinct            | 39          | 16         | 41.03%    |
| Provisional            | 39          | 0          | 0.00%     |
| Total                  | 39          | 16         | 41.03%    |
| 95U402.07-MB 95U402.07 |             |            |           |
| Precinct TSx           | 413         | 0          | 0.00%     |
| Precinct OS            | 413         | 0          | 0.00%     |
| Precinct VBM           | 413         | 0          | 0.00%     |
| MB Precinct            | 413         | 256        | 61.99%    |
| Provisional            | 413         | 1          | 0.24%     |
| Total                  | 413         | 257        | 62.23%    |
| 95U402.08-MB 95U402.08 |             |            |           |
| Precinct TSx           | 873         | 0          | 0.00%     |
| Precinct OS            | 873         | 0          | 0.00%     |
| Precinct VBM           | 873         | 0          | 0.00%     |
| MB Precinct            | 873         | 566        | 64.83%    |
| Provisional            | 873         | 3          | 0.34%     |
| Total                  | 873         | 569        | 65.18%    |
| 95U403.08-MB 95U403.08 |             |            |           |
| Precinct TSx           | 1591        | 0          | 0.00%     |
| Precinct OS            | 1591        | 0          | 0.00%     |
| Precinct VBM           | 1591        | 0          | 0.00%     |
| MB Precinct            | 1591        | 1042       | 65.49%    |
| Provisional            | 1591        | 13         | 0.82%     |
| Total                  | 1591        | 1055       | 66.31%    |
| 95U404.06-MB 95U404.06 |             |            |           |
| Precinct TSx           | 0           | 0          | -         |
| Precinct OS            | 0           | 0          | -         |
| Precinct VBM           | 0           | 0          | -         |
| MB Precinct            | 0           | 0          | -         |
| Provisional            | 0           | 0          | -         |
| Total                  | 0           | 0          | -         |
| 95U404.08-MB 95U404.08 |             |            |           |
| Precinct TSx           | 0           | 0          | -         |
| Precinct OS            | 0           | 0          | -         |
| Precinct VBM           | 0           | 0          | -         |
| MB Precinct            | 0           | 0          | -         |

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|                               | TURN OUT    |            |           |
|-------------------------------|-------------|------------|-----------|
|                               | Reg. Voters | Cards Cast | % Turnout |
| Provisional                   | 0           | 0          | -         |
| Total                         | 0           | 0          | -         |
| 95U501.09-MB 95U501.09        |             |            |           |
| Precinct TSx                  | 0           | 0          | -         |
| Precinct OS                   | 0           | 0          | -         |
| Precinct VBM                  | 0           | 0          | -         |
| MB Precinct                   | 0           | 2          | -         |
| Provisional                   | 0           | 0          | -         |
| Total                         | 0           | 2          | -         |
| 95U501.10-MB 95U501.10        |             |            |           |
| Precinct TSx                  | 286         | 0          | 0.00%     |
| Precinct OS                   | 286         | 0          | 0.00%     |
| Precinct VBM                  | 286         | 0          | 0.00%     |
| MB Precinct                   | 286         | 172        | 60.14%    |
| Provisional                   | 286         | 2          | 0.70%     |
| Total                         | 286         | 174        | 60.84%    |
| 95U598.10-MB 95U598.10        |             |            |           |
| Precinct TSx                  | 568         | 0          | 0.00%     |
| Precinct OS                   | 568         | 0          | 0.00%     |
| Precinct VBM                  | 568         | 0          | 0.00%     |
| MB Precinct                   | 568         | 239        | 42.08%    |
| Provisional                   | 568         | 3          | 0.53%     |
| Total                         | 568         | 242        | 42.61%    |
| 95U599.10-MB 95U599.10        |             |            |           |
| Precinct TSx                  | 75          | 0          | 0.00%     |
| Precinct OS                   | 75          | 0          | 0.00%     |
| Precinct VBM                  | 75          | 0          | 0.00%     |
| MB Precinct                   | 75          | 38         | 50.67%    |
| Provisional                   | 75          | 0          | 0.00%     |
| Total                         | 75          | 38         | 50.67%    |
| 96U101.11-MB 96U101.11        |             |            |           |
| Precinct TSx                  | 626         | 0          | 0.00%     |
| Precinct OS                   | 626         | 0          | 0.00%     |
| Precinct VBM                  | 626         | 0          | 0.00%     |
| MB Precinct                   | 626         | 423        | 67.57%    |
| Provisional                   | 626         | 6          | 0.96%     |
| Total                         | 626         | 429        | 68.53%    |
| 96U401.14-MB 96U401.14        |             |            |           |
| Precinct TSx                  | 566         | 0          | 0.00%     |
| Precinct OS                   | 566         | 0          | 0.00%     |
| Precinct VBM                  | 566         | 0          | 0.00%     |
| MB Precinct                   | 566         | 359        | 63.43%    |
| Provisional                   | 566         | 6          | 1.06%     |
| Total                         | 566         | 365        | 64.49%    |
| Total                         |             |            |           |
| Precinct TSx                  | 109479      | 8          | 0.01%     |
| Precinct OS                   | 109479      | 12009      | 10.97%    |
| Precinct VBM                  | 109479      | 45535      | 41.59%    |
| MB Precinct                   | 109479      | 5143       | 4.70%     |
| Provisional                   | 109479      | 1584       | 1.45%     |
| Total                         | 109479      | 64279      | 58.71%    |
| STATE BOARD OF EQUALIZATION   |             |            |           |
| STATE BOARD OF EQUALIZATION 1 |             |            |           |
| Precinct TSx                  | 109479      | 8          | 0.01%     |
| Precinct OS                   | 109479      | 12009      | 10.97%    |
| Precinct VBM                  | 109479      | 45535      | 41.59%    |
| MB Precinct                   | 109479      | 5143       | 4.70%     |
| Provisional                   | 109479      | 1584       | 1.45%     |
| Total                         | 109479      | 64279      | 58.71%    |
| Total                         |             |            |           |

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|                                     | TURN OUT      |              |               |
|-------------------------------------|---------------|--------------|---------------|
|                                     | Reg. Voters   | Cards Cast   | % Turnout     |
| Precinct TSx                        | 109479        | 8            | 0.01%         |
| Precinct OS                         | 109479        | 12009        | 10.97%        |
| Precinct VBM                        | 109479        | 45535        | 41.59%        |
| MB Precinct                         | 109479        | 5143         | 4.70%         |
| Provisional                         | 109479        | 1584         | 1.45%         |
| <b>Total</b>                        | <b>109479</b> | <b>64279</b> | <b>58.71%</b> |
| <b>UNITED STATES SENATE</b>         |               |              |               |
| <b>UNITED STATES SENATE</b>         |               |              |               |
| Precinct TSx                        | 109479        | 8            | 0.01%         |
| Precinct OS                         | 109479        | 12009        | 10.97%        |
| Precinct VBM                        | 109479        | 45535        | 41.59%        |
| MB Precinct                         | 109479        | 5143         | 4.70%         |
| Provisional                         | 109479        | 1584         | 1.45%         |
| <b>Total</b>                        | <b>109479</b> | <b>64279</b> | <b>58.71%</b> |
| <b>Total</b>                        |               |              |               |
| Precinct TSx                        | 109479        | 8            | 0.01%         |
| Precinct OS                         | 109479        | 12009        | 10.97%        |
| Precinct VBM                        | 109479        | 45535        | 41.59%        |
| MB Precinct                         | 109479        | 5143         | 4.70%         |
| Provisional                         | 109479        | 1584         | 1.45%         |
| <b>Total</b>                        | <b>109479</b> | <b>64279</b> | <b>58.71%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |               |              |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |               |              |               |
| Precinct TSx                        | 109479        | 8            | 0.01%         |
| Precinct OS                         | 109479        | 12009        | 10.97%        |
| Precinct VBM                        | 109479        | 45535        | 41.59%        |
| MB Precinct                         | 109479        | 5143         | 4.70%         |
| Provisional                         | 109479        | 1584         | 1.45%         |
| <b>Total</b>                        | <b>109479</b> | <b>64279</b> | <b>58.71%</b> |
| <b>Total</b>                        |               |              |               |
| Precinct TSx                        | 109479        | 8            | 0.01%         |
| Precinct OS                         | 109479        | 12009        | 10.97%        |
| Precinct VBM                        | 109479        | 45535        | 41.59%        |
| MB Precinct                         | 109479        | 5143         | 4.70%         |
| Provisional                         | 109479        | 1584         | 1.45%         |
| <b>Total</b>                        | <b>109479</b> | <b>64279</b> | <b>58.71%</b> |
| <b>STATE SENATE</b>                 |               |              |               |
| <b>1ST STATE SENATE DISTRICT</b>    |               |              |               |
| Precinct TSx                        | 109479        | 8            | 0.01%         |
| Precinct OS                         | 109479        | 12009        | 10.97%        |
| Precinct VBM                        | 109479        | 45535        | 41.59%        |
| MB Precinct                         | 109479        | 5143         | 4.70%         |
| Provisional                         | 109479        | 1584         | 1.45%         |
| <b>Total</b>                        | <b>109479</b> | <b>64279</b> | <b>58.71%</b> |
| <b>Total</b>                        |               |              |               |
| Precinct TSx                        | 109479        | 8            | 0.01%         |
| Precinct OS                         | 109479        | 12009        | 10.97%        |
| Precinct VBM                        | 109479        | 45535        | 41.59%        |
| MB Precinct                         | 109479        | 5143         | 4.70%         |
| Provisional                         | 109479        | 1584         | 1.45%         |
| <b>Total</b>                        | <b>109479</b> | <b>64279</b> | <b>58.71%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |               |              |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |               |              |               |
| Precinct TSx                        | 70961         | 4            | 0.01%         |
| Precinct OS                         | 70961         | 7627         | 10.75%        |
| Precinct VBM                        | 70961         | 28225        | 39.78%        |
| MB Precinct                         | 70961         | 4361         | 6.15%         |

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|                                   | TURN OUT    |            |           |
|-----------------------------------|-------------|------------|-----------|
|                                   | Reg. Voters | Cards Cast | % Turnout |
| Provisional                       | 70961       | 1038       | 1.46%     |
| Total                             | 70961       | 41255      | 58.14%    |
| <b>6TH STATE ASSEMBLY DIST</b>    |             |            |           |
| Precinct TSx                      | 38518       | 4          | 0.01%     |
| Precinct OS                       | 38518       | 4382       | 11.38%    |
| Precinct VBM                      | 38518       | 17310      | 44.94%    |
| MB Precinct                       | 38518       | 782        | 2.03%     |
| Provisional                       | 38518       | 546        | 1.42%     |
| Total                             | 38518       | 23024      | 59.77%    |
| <b>Total</b>                      |             |            |           |
| Precinct TSx                      | 109479      | 8          | 0.01%     |
| Precinct OS                       | 109479      | 12009      | 10.97%    |
| Precinct VBM                      | 109479      | 45535      | 41.59%    |
| MB Precinct                       | 109479      | 5143       | 4.70%     |
| Provisional                       | 109479      | 1584       | 1.45%     |
| Total                             | 109479      | 64279      | 58.71%    |
| <b>SUPERVISORIAL DISTRICTS</b>    |             |            |           |
| <b>1ST SUPERVISORIAL DISTRICT</b> |             |            |           |
| Precinct TSx                      | 22587       | 0          | 0.00%     |
| Precinct OS                       | 22587       | 2530       | 11.20%    |
| Precinct VBM                      | 22587       | 10411      | 46.09%    |
| MB Precinct                       | 22587       | 423        | 1.87%     |
| Provisional                       | 22587       | 301        | 1.33%     |
| Total                             | 22587       | 13665      | 60.50%    |
| <b>2ND SUPERVISORIAL DISTRICT</b> |             |            |           |
| Precinct TSx                      | 23114       | 4          | 0.02%     |
| Precinct OS                       | 23114       | 2509       | 10.85%    |
| Precinct VBM                      | 23114       | 9647       | 41.74%    |
| MB Precinct                       | 23114       | 1590       | 6.88%     |
| Provisional                       | 23114       | 327        | 1.41%     |
| Total                             | 23114       | 14077      | 60.90%    |
| <b>3RD SUPERVISORIAL DISTRICT</b> |             |            |           |
| Precinct TSx                      | 21691       | 3          | 0.01%     |
| Precinct OS                       | 21691       | 2368       | 10.92%    |
| Precinct VBM                      | 21691       | 10021      | 46.20%    |
| MB Precinct                       | 21691       | 440        | 2.03%     |
| Provisional                       | 21691       | 277        | 1.28%     |
| Total                             | 21691       | 13109      | 60.44%    |
| <b>4TH SUPERVISORIAL DISTRICT</b> |             |            |           |
| Precinct TSx                      | 23425       | 0          | 0.00%     |
| Precinct OS                       | 23425       | 2568       | 10.96%    |
| Precinct VBM                      | 23425       | 9398       | 40.12%    |
| MB Precinct                       | 23425       | 2239       | 9.56%     |
| Provisional                       | 23425       | 315        | 1.34%     |
| Total                             | 23425       | 14520      | 61.99%    |
| <b>5TH SUPERVISORIAL DISTRICT</b> |             |            |           |
| Precinct TSx                      | 18662       | 1          | 0.01%     |
| Precinct OS                       | 18662       | 2034       | 10.90%    |
| Precinct VBM                      | 18662       | 6058       | 32.46%    |
| MB Precinct                       | 18662       | 451        | 2.42%     |
| Provisional                       | 18662       | 364        | 1.95%     |
| Total                             | 18662       | 8908       | 47.73%    |
| <b>Total</b>                      |             |            |           |
| Precinct TSx                      | 109479      | 8          | 0.01%     |
| Precinct OS                       | 109479      | 12009      | 10.97%    |
| Precinct VBM                      | 109479      | 45535      | 41.59%    |
| MB Precinct                       | 109479      | 5143       | 4.70%     |
| Provisional                       | 109479      | 1584       | 1.45%     |
| Total                             | 109479      | 64279      | 58.71%    |

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|                                 | TURN OUT    |            |           |
|---------------------------------|-------------|------------|-----------|
|                                 | Reg. Voters | Cards Cast | % Turnout |
| <b>CITIES</b>                   |             |            |           |
| <b>CITY OF PLACERVILLE</b>      |             |            |           |
| Precinct TSx                    | 5569        | 0          | 0.00%     |
| Precinct OS                     | 5569        | 631        | 11.33%    |
| Precinct VBM                    | 5569        | 2590       | 46.51%    |
| MB Precinct                     | 5569        | 0          | 0.00%     |
| Provisional                     | 5569        | 80         | 1.44%     |
| Total                           | 5569        | 3301       | 59.27%    |
| <b>CITY OF SOUTH LAKE TAHOE</b> |             |            |           |
| Precinct TSx                    | 9465        | 0          | 0.00%     |
| Precinct OS                     | 9465        | 1063       | 11.23%    |
| Precinct VBM                    | 9465        | 2796       | 29.54%    |
| MB Precinct                     | 9465        | 0          | 0.00%     |
| Provisional                     | 9465        | 224        | 2.37%     |
| Total                           | 9465        | 4083       | 43.14%    |
| <b>UNINCORPORATED AREA</b>      |             |            |           |
| Precinct TSx                    | 94445       | 8          | 0.01%     |
| Precinct OS                     | 94445       | 10315      | 10.92%    |
| Precinct VBM                    | 94445       | 40149      | 42.51%    |
| MB Precinct                     | 94445       | 5143       | 5.45%     |
| Provisional                     | 94445       | 1280       | 1.36%     |
| Total                           | 94445       | 56895      | 60.24%    |
| <b>Total</b>                    |             |            |           |
| Precinct TSx                    | 109479      | 8          | 0.01%     |
| Precinct OS                     | 109479      | 12009      | 10.97%    |
| Precinct VBM                    | 109479      | 45535      | 41.59%    |
| MB Precinct                     | 109479      | 5143       | 4.70%     |
| Provisional                     | 109479      | 1584       | 1.45%     |
| Total                           | 109479      | 64279      | 58.71%    |

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|                        | President Democratic Party |          |               |             |                 |                    |
|------------------------|----------------------------|----------|---------------|-------------|-----------------|--------------------|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | HILLARY CLINTON | ROQUE DE LA FUENTE |
| Jurisdiction Wide      |                            |          |               |             |                 |                    |
| 100001.11-CP 100001.11 |                            |          |               |             |                 |                    |
| Precinct TSx           | 420                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 420                        | 1        | 92            | 89          | 45 50.56%       | 0 0.00%            |
| Precinct VBM           | 420                        | 1        | 258           | 250         | 147 58.80%      | 0 0.00%            |
| MB Precinct            | 420                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 420                        | 1        | 9             | 9           | 3 33.33%        | 0 0.00%            |
| Total                  | 420                        | 5        | 359           | 348         | 195 56.03%      | 0 0.00%            |
| 100002.11-CP 100002.11 |                            |          |               |             |                 |                    |
| Precinct TSx           | 269                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 269                        | 1        | 50            | 47          | 21 44.68%       | 0 0.00%            |
| Precinct VBM           | 269                        | 1        | 165           | 159         | 97 61.01%       | 0 0.00%            |
| MB Precinct            | 269                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 269                        | 1        | 6             | 6           | 1 16.67%        | 0 0.00%            |
| Total                  | 269                        | 5        | 221           | 212         | 119 56.13%      | 0 0.00%            |
| 100003.11-CP 100003.11 |                            |          |               |             |                 |                    |
| Precinct TSx           | 390                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 390                        | 1        | 37            | 37          | 20 54.05%       | 0 0.00%            |
| Precinct VBM           | 390                        | 1        | 315           | 306         | 201 65.69%      | 1 0.33%            |
| MB Precinct            | 390                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 390                        | 1        | 11            | 10          | 5 50.00%        | 0 0.00%            |
| Total                  | 390                        | 5        | 363           | 353         | 226 64.02%      | 1 0.28%            |
| 100004.11-CP 100004.11 |                            |          |               |             |                 |                    |
| Precinct TSx           | 473                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 473                        | 1        | 131           | 131         | 63 48.09%       | 0 0.00%            |
| Precinct VBM           | 473                        | 1        | 288           | 282         | 177 62.77%      | 2 0.71%            |
| MB Precinct            | 473                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 473                        | 1        | 3             | 3           | 1 33.33%        | 0 0.00%            |
| Total                  | 473                        | 5        | 422           | 416         | 241 57.93%      | 2 0.48%            |
| 100005.11-CP 100005.11 |                            |          |               |             |                 |                    |
| Precinct TSx           | 335                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 335                        | 1        | 54            | 54          | 34 62.96%       | 1 1.85%            |
| Precinct VBM           | 335                        | 1        | 243           | 235         | 155 65.96%      | 0 0.00%            |
| MB Precinct            | 335                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 335                        | 1        | 9             | 7           | 2 28.57%        | 0 0.00%            |
| Total                  | 335                        | 5        | 306           | 296         | 191 64.53%      | 1 0.34%            |
| 100006.11-CP 100006.11 |                            |          |               |             |                 |                    |
| Precinct TSx           | 524                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 524                        | 1        | 103           | 103         | 46 44.66%       | 0 0.00%            |
| Precinct VBM           | 524                        | 1        | 359           | 351         | 206 58.69%      | 1 0.28%            |
| MB Precinct            | 524                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 524                        | 1        | 14            | 14          | 4 28.57%        | 0 0.00%            |
| Total                  | 524                        | 5        | 476           | 468         | 256 54.70%      | 1 0.21%            |
| 100008.11-CP 100008.11 |                            |          |               |             |                 |                    |
| Precinct TSx           | 455                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 455                        | 1        | 99            | 99          | 52 52.53%       | 0 0.00%            |
| Precinct VBM           | 455                        | 1        | 249           | 243         | 164 67.49%      | 0 0.00%            |
| MB Precinct            | 455                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 455                        | 1        | 10            | 10          | 4 40.00%        | 0 0.00%            |
| Total                  | 455                        | 5        | 358           | 352         | 220 62.50%      | 0 0.00%            |
| 100009.11-CP 100009.11 |                            |          |               |             |                 |                    |
| Precinct TSx           | 497                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 497                        | 1        | 82            | 81          | 46 56.79%       | 0 0.00%            |
| Precinct VBM           | 497                        | 1        | 282           | 276         | 164 59.42%      | 0 0.00%            |
| MB Precinct            | 497                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 497                        | 1        | 13            | 13          | 4 30.77%        | 0 0.00%            |
| Total                  | 497                        | 5        | 377           | 370         | 214 57.84%      | 0 0.00%            |
| 100012.11-CP 100012.11 |                            |          |               |             |                 |                    |
| Precinct TSx           | 580                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 580                        | 1        | 112           | 112         | 60 53.57%       | 0 0.00%            |
| Precinct VBM           | 580                        | 1        | 347           | 338         | 194 57.40%      | 0 0.00%            |



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|                        | President Democratic Party |          |               |             |                 |                    |
|------------------------|----------------------------|----------|---------------|-------------|-----------------|--------------------|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | HILLARY CLINTON | ROQUE DE LA FUENTE |
| MB Precinct            | 580                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 580                        | 1        | 14            | 14          | 5               | 35.71%             |
| Total                  | 580                        | 5        | 473           | 464         | 259             | 55.82%             |
| 100013.11-CP 100013.11 |                            |          |               |             |                 |                    |
| Precinct TSx           | 531                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 531                        | 1        | 111           | 111         | 62              | 55.86%             |
| Precinct VBM           | 531                        | 1        | 308           | 303         | 202             | 66.67%             |
| MB Precinct            | 531                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 531                        | 1        | 14            | 13          | 7               | 53.85%             |
| Total                  | 531                        | 5        | 433           | 427         | 271             | 63.47%             |
| 100014.11-CP 100014.11 |                            |          |               |             |                 |                    |
| Precinct TSx           | 408                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 408                        | 1        | 90            | 89          | 43              | 48.31%             |
| Precinct VBM           | 408                        | 1        | 235           | 224         | 142             | 63.39%             |
| MB Precinct            | 408                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 408                        | 1        | 12            | 12          | 7               | 58.33%             |
| Total                  | 408                        | 5        | 337           | 325         | 192             | 59.08%             |
| 100022.11-CP 100022.11 |                            |          |               |             |                 |                    |
| Precinct TSx           | 373                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 373                        | 1        | 90            | 89          | 45              | 50.56%             |
| Precinct VBM           | 373                        | 1        | 225           | 221         | 135             | 61.09%             |
| MB Precinct            | 373                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 373                        | 1        | 6             | 6           | 3               | 50.00%             |
| Total                  | 373                        | 5        | 321           | 316         | 183             | 57.91%             |
| 100023.11-CP 100023.11 |                            |          |               |             |                 |                    |
| Precinct TSx           | 545                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 545                        | 1        | 85            | 85          | 46              | 54.12%             |
| Precinct VBM           | 545                        | 1        | 336           | 331         | 204             | 61.63%             |
| MB Precinct            | 545                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 545                        | 1        | 15            | 15          | 7               | 46.67%             |
| Total                  | 545                        | 5        | 436           | 431         | 257             | 59.63%             |
| 200003.12-CP 200003.12 |                            |          |               |             |                 |                    |
| Precinct TSx           | 368                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 368                        | 1        | 81            | 79          | 24              | 30.38%             |
| Precinct VBM           | 368                        | 1        | 213           | 207         | 92              | 44.44%             |
| MB Precinct            | 368                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 368                        | 1        | 15            | 15          | 3               | 20.00%             |
| Total                  | 368                        | 5        | 309           | 301         | 119             | 39.53%             |
| 200004.12-CP 200004.12 |                            |          |               |             |                 |                    |
| Precinct TSx           | 433                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 433                        | 1        | 79            | 79          | 36              | 45.57%             |
| Precinct VBM           | 433                        | 1        | 281           | 276         | 137             | 49.64%             |
| MB Precinct            | 433                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 433                        | 1        | 6             | 6           | 3               | 50.00%             |
| Total                  | 433                        | 5        | 366           | 361         | 176             | 48.75%             |
| 200005.02-CP 200005.02 |                            |          |               |             |                 |                    |
| Precinct TSx           | 274                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 274                        | 1        | 47            | 47          | 24              | 51.06%             |
| Precinct VBM           | 274                        | 1        | 167           | 164         | 71              | 43.29%             |
| MB Precinct            | 274                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 274                        | 1        | 15            | 15          | 5               | 33.33%             |
| Total                  | 274                        | 5        | 229           | 226         | 100             | 44.25%             |
| 200006.12-CP 200006.12 |                            |          |               |             |                 |                    |
| Precinct TSx           | 248                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 248                        | 1        | 37            | 36          | 17              | 47.22%             |
| Precinct VBM           | 248                        | 1        | 167           | 166         | 85              | 51.20%             |
| MB Precinct            | 248                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 248                        | 1        | 16            | 16          | 7               | 43.75%             |
| Total                  | 248                        | 5        | 220           | 218         | 109             | 50.00%             |
| 200008.12-CP 200008.12 |                            |          |               |             |                 |                    |
| Precinct TSx           | 286                        | 1        | 0             | 0           | 0               | -                  |

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|                               | President Democratic Party |          |               |             |                   |                    |
|-------------------------------|----------------------------|----------|---------------|-------------|-------------------|--------------------|
|                               | Reg. Voters                | Vote For | Times Counted | Total Votes | HILLARY CLINTON   | ROQUE DE LA FUENTE |
| Precinct OS                   | 286                        | 1        | 67            | 64          | 31 48.44%         | 0 0.00%            |
| Precinct VBM                  | 286                        | 1        | 156           | 155         | 79 50.97%         | 0 0.00%            |
| MB Precinct                   | 286                        | 1        | 0             | 0           | 0 -               | 0 -                |
| Provisional                   | 286                        | 1        | 5             | 5           | 1 20.00%          | 0 0.00%            |
| <b>Total</b>                  | <b>286</b>                 | <b>5</b> | <b>228</b>    | <b>224</b>  | <b>111 49.55%</b> | <b>0 0.00%</b>     |
| <b>200010.12-CP 200010.12</b> |                            |          |               |             |                   |                    |
| Precinct TSx                  | 627                        | 1        | 2             | 2           | 2 100.00%         | 0 0.00%            |
| Precinct OS                   | 627                        | 1        | 109           | 109         | 62 56.88%         | 0 0.00%            |
| Precinct VBM                  | 627                        | 1        | 347           | 341         | 212 62.17%        | 1 0.29%            |
| MB Precinct                   | 627                        | 1        | 0             | 0           | 0 -               | 0 -                |
| Provisional                   | 627                        | 1        | 18            | 18          | 7 38.89%          | 0 0.00%            |
| <b>Total</b>                  | <b>627</b>                 | <b>5</b> | <b>476</b>    | <b>470</b>  | <b>283 60.21%</b> | <b>1 0.21%</b>     |
| <b>200012.12-CP 200012.12</b> |                            |          |               |             |                   |                    |
| Precinct TSx                  | 294                        | 1        | 0             | 0           | 0 -               | 0 -                |
| Precinct OS                   | 294                        | 1        | 69            | 69          | 26 37.68%         | 0 0.00%            |
| Precinct VBM                  | 294                        | 1        | 164           | 160         | 85 53.13%         | 0 0.00%            |
| MB Precinct                   | 294                        | 1        | 0             | 0           | 0 -               | 0 -                |
| Provisional                   | 294                        | 1        | 8             | 8           | 2 25.00%          | 0 0.00%            |
| <b>Total</b>                  | <b>294</b>                 | <b>5</b> | <b>241</b>    | <b>237</b>  | <b>113 47.68%</b> | <b>0 0.00%</b>     |
| <b>200013.12-CP 200013.12</b> |                            |          |               |             |                   |                    |
| Precinct TSx                  | 388                        | 1        | 0             | 0           | 0 -               | 0 -                |
| Precinct OS                   | 388                        | 1        | 89            | 89          | 44 49.44%         | 0 0.00%            |
| Precinct VBM                  | 388                        | 1        | 218           | 212         | 133 62.74%        | 0 0.00%            |
| MB Precinct                   | 388                        | 1        | 0             | 0           | 0 -               | 0 -                |
| Provisional                   | 388                        | 1        | 10            | 10          | 3 30.00%          | 0 0.00%            |
| <b>Total</b>                  | <b>388</b>                 | <b>5</b> | <b>317</b>    | <b>311</b>  | <b>180 57.88%</b> | <b>0 0.00%</b>     |
| <b>200014.12-CP 200014.12</b> |                            |          |               |             |                   |                    |
| Precinct TSx                  | 408                        | 1        | 0             | 0           | 0 -               | 0 -                |
| Precinct OS                   | 408                        | 1        | 81            | 81          | 34 41.98%         | 0 0.00%            |
| Precinct VBM                  | 408                        | 1        | 233           | 227         | 115 50.66%        | 0 0.00%            |
| MB Precinct                   | 408                        | 1        | 0             | 0           | 0 -               | 0 -                |
| Provisional                   | 408                        | 1        | 16            | 16          | 2 12.50%          | 0 0.00%            |
| <b>Total</b>                  | <b>408</b>                 | <b>5</b> | <b>330</b>    | <b>324</b>  | <b>151 46.60%</b> | <b>0 0.00%</b>     |
| <b>200015.02-CP 200015.02</b> |                            |          |               |             |                   |                    |
| Precinct TSx                  | 561                        | 1        | 0             | 0           | 0 -               | 0 -                |
| Precinct OS                   | 561                        | 1        | 108           | 107         | 37 34.58%         | 0 0.00%            |
| Precinct VBM                  | 561                        | 1        | 356           | 344         | 142 41.28%        | 0 0.00%            |
| MB Precinct                   | 561                        | 1        | 0             | 0           | 0 -               | 0 -                |
| Provisional                   | 561                        | 1        | 9             | 9           | 3 33.33%          | 0 0.00%            |
| <b>Total</b>                  | <b>561</b>                 | <b>5</b> | <b>473</b>    | <b>460</b>  | <b>182 39.57%</b> | <b>0 0.00%</b>     |
| <b>200016.12-CP 200016.12</b> |                            |          |               |             |                   |                    |
| Precinct TSx                  | 67                         | 1        | 0             | 0           | 0 -               | 0 -                |
| Precinct OS                   | 67                         | 1        | 6             | 6           | 4 66.67%          | 0 0.00%            |
| Precinct VBM                  | 67                         | 1        | 37            | 37          | 21 56.76%         | 0 0.00%            |
| MB Precinct                   | 67                         | 1        | 0             | 0           | 0 -               | 0 -                |
| Provisional                   | 67                         | 1        | 1             | 1           | 1 100.00%         | 0 0.00%            |
| <b>Total</b>                  | <b>67</b>                  | <b>5</b> | <b>44</b>     | <b>44</b>   | <b>26 59.09%</b>  | <b>0 0.00%</b>     |
| <b>200016.13-CP 200016.13</b> |                            |          |               |             |                   |                    |
| Precinct TSx                  | 125                        | 1        | 0             | 0           | 0 -               | 0 -                |
| Precinct OS                   | 125                        | 1        | 20            | 20          | 11 55.00%         | 0 0.00%            |
| Precinct VBM                  | 125                        | 1        | 100           | 97          | 48 49.48%         | 0 0.00%            |
| MB Precinct                   | 125                        | 1        | 0             | 0           | 0 -               | 0 -                |
| Provisional                   | 125                        | 1        | 0             | 0           | 0 -               | 0 -                |
| <b>Total</b>                  | <b>125</b>                 | <b>5</b> | <b>120</b>    | <b>117</b>  | <b>59 50.43%</b>  | <b>0 0.00%</b>     |
| <b>200017.02-CP 200017.02</b> |                            |          |               |             |                   |                    |
| Precinct TSx                  | 395                        | 1        | 0             | 0           | 0 -               | 0 -                |
| Precinct OS                   | 395                        | 1        | 60            | 59          | 28 47.46%         | 0 0.00%            |
| Precinct VBM                  | 395                        | 1        | 255           | 249         | 112 44.98%        | 0 0.00%            |
| MB Precinct                   | 395                        | 1        | 0             | 0           | 0 -               | 0 -                |
| Provisional                   | 395                        | 1        | 5             | 5           | 1 20.00%          | 0 0.00%            |
| <b>Total</b>                  | <b>395</b>                 | <b>5</b> | <b>320</b>    | <b>313</b>  | <b>141 45.05%</b> | <b>0 0.00%</b>     |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                        | President Democratic Party |          |               |             |                 |        |                    |       |
|------------------------|----------------------------|----------|---------------|-------------|-----------------|--------|--------------------|-------|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | HILLARY CLINTON |        | ROQUE DE LA FUENTE |       |
| 200018.02-CP 200018.02 |                            |          |               |             |                 |        |                    |       |
| Precinct TSx           | 385                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Precinct OS            | 385                        | 1        | 56            | 53          | 24              | 45.28% | 0                  | 0.00% |
| Precinct VBM           | 385                        | 1        | 255           | 251         | 113             | 45.02% | 0                  | 0.00% |
| MB Precinct            | 385                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Provisional            | 385                        | 1        | 4             | 4           | 1               | 25.00% | 0                  | 0.00% |
| Total                  | 385                        | 5        | 315           | 308         | 138             | 44.81% | 0                  | 0.00% |
| 200020.02-CP 200020.02 |                            |          |               |             |                 |        |                    |       |
| Precinct TSx           | 325                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Precinct OS            | 325                        | 1        | 49            | 49          | 18              | 36.73% | 0                  | 0.00% |
| Precinct VBM           | 325                        | 1        | 186           | 183         | 99              | 54.10% | 1                  | 0.55% |
| MB Precinct            | 325                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Provisional            | 325                        | 1        | 6             | 6           | 0               | 0.00%  | 0                  | 0.00% |
| Total                  | 325                        | 5        | 241           | 238         | 117             | 49.16% | 1                  | 0.42% |
| 200021.01-CP 200021.01 |                            |          |               |             |                 |        |                    |       |
| Precinct TSx           | 90                         | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Precinct OS            | 90                         | 1        | 15            | 14          | 3               | 21.43% | 0                  | 0.00% |
| Precinct VBM           | 90                         | 1        | 61            | 59          | 28              | 47.46% | 0                  | 0.00% |
| MB Precinct            | 90                         | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Provisional            | 90                         | 1        | 3             | 3           | 2               | 66.67% | 0                  | 0.00% |
| Total                  | 90                         | 5        | 79            | 76          | 33              | 43.42% | 0                  | 0.00% |
| 200021.02-CP 200021.02 |                            |          |               |             |                 |        |                    |       |
| Precinct TSx           | 362                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Precinct OS            | 362                        | 1        | 59            | 59          | 30              | 50.85% | 0                  | 0.00% |
| Precinct VBM           | 362                        | 1        | 263           | 252         | 144             | 57.14% | 0                  | 0.00% |
| MB Precinct            | 362                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Provisional            | 362                        | 1        | 5             | 5           | 2               | 40.00% | 0                  | 0.00% |
| Total                  | 362                        | 5        | 327           | 316         | 176             | 55.70% | 0                  | 0.00% |
| 200024.02-CP 200024.02 |                            |          |               |             |                 |        |                    |       |
| Precinct TSx           | 130                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Precinct OS            | 130                        | 1        | 13            | 13          | 5               | 38.46% | 0                  | 0.00% |
| Precinct VBM           | 130                        | 1        | 83            | 79          | 34              | 43.04% | 0                  | 0.00% |
| MB Precinct            | 130                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Provisional            | 130                        | 1        | 2             | 2           | 1               | 50.00% | 0                  | 0.00% |
| Total                  | 130                        | 5        | 98            | 94          | 40              | 42.55% | 0                  | 0.00% |
| 300001.04-CP 300001.04 |                            |          |               |             |                 |        |                    |       |
| Precinct TSx           | 95                         | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Precinct OS            | 95                         | 1        | 6             | 6           | 3               | 50.00% | 0                  | 0.00% |
| Precinct VBM           | 95                         | 1        | 56            | 56          | 23              | 41.07% | 0                  | 0.00% |
| MB Precinct            | 95                         | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Provisional            | 95                         | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Total                  | 95                         | 5        | 62            | 62          | 26              | 41.94% | 0                  | 0.00% |
| 300001.05-CP 300001.05 |                            |          |               |             |                 |        |                    |       |
| Precinct TSx           | 259                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Precinct OS            | 259                        | 1        | 36            | 36          | 16              | 44.44% | 0                  | 0.00% |
| Precinct VBM           | 259                        | 1        | 154           | 153         | 69              | 45.10% | 2                  | 1.31% |
| MB Precinct            | 259                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Provisional            | 259                        | 1        | 7             | 7           | 2               | 28.57% | 0                  | 0.00% |
| Total                  | 259                        | 5        | 197           | 196         | 87              | 44.39% | 2                  | 1.02% |
| 300003.05-CP 300003.05 |                            |          |               |             |                 |        |                    |       |
| Precinct TSx           | 717                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Precinct OS            | 717                        | 1        | 104           | 102         | 35              | 34.31% | 0                  | 0.00% |
| Precinct VBM           | 717                        | 1        | 446           | 438         | 213             | 48.63% | 1                  | 0.23% |
| MB Precinct            | 717                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Provisional            | 717                        | 1        | 18            | 18          | 5               | 27.78% | 0                  | 0.00% |
| Total                  | 717                        | 5        | 568           | 558         | 253             | 45.34% | 1                  | 0.18% |
| 300004.05-CP 300004.05 |                            |          |               |             |                 |        |                    |       |
| Precinct TSx           | 452                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |
| Precinct OS            | 452                        | 1        | 103           | 102         | 39              | 38.24% | 0                  | 0.00% |
| Precinct VBM           | 452                        | 1        | 270           | 261         | 133             | 50.96% | 1                  | 0.38% |
| MB Precinct            | 452                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | President Democratic Party |          |               |             |                 |                    |
|------------------------|----------------------------|----------|---------------|-------------|-----------------|--------------------|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | HILLARY CLINTON | ROQUE DE LA FUENTE |
| Provisional            | 452                        | 1        | 10            | 10          | 2 20.00%        | 0 0.00%            |
| Total                  | 452                        | 5        | 383           | 373         | 174 46.65%      | 1 0.27%            |
| 300005.05-CP 300005.05 |                            |          |               |             |                 |                    |
| Precinct TSx           | 476                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 476                        | 1        | 82            | 82          | 24 29.27%       | 0 0.00%            |
| Precinct VBM           | 476                        | 1        | 295           | 281         | 122 43.42%      | 0 0.00%            |
| MB Precinct            | 476                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 476                        | 1        | 16            | 16          | 3 18.75%        | 0 0.00%            |
| Total                  | 476                        | 5        | 393           | 379         | 149 39.31%      | 0 0.00%            |
| 300008.05-CP 300008.05 |                            |          |               |             |                 |                    |
| Precinct TSx           | 364                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 364                        | 1        | 52            | 52          | 20 38.46%       | 0 0.00%            |
| Precinct VBM           | 364                        | 1        | 272           | 263         | 120 45.63%      | 0 0.00%            |
| MB Precinct            | 364                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 364                        | 1        | 4             | 4           | 0 0.00%         | 0 0.00%            |
| Total                  | 364                        | 5        | 328           | 319         | 140 43.89%      | 0 0.00%            |
| 300009.04-CP 300009.04 |                            |          |               |             |                 |                    |
| Precinct TSx           | 244                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 244                        | 1        | 49            | 49          | 22 44.90%       | 0 0.00%            |
| Precinct VBM           | 244                        | 1        | 146           | 145         | 75 51.72%       | 1 0.69%            |
| MB Precinct            | 244                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 244                        | 1        | 8             | 8           | 1 12.50%        | 0 0.00%            |
| Total                  | 244                        | 5        | 203           | 202         | 98 48.51%       | 1 0.50%            |
| 300010.04-CP 300010.04 |                            |          |               |             |                 |                    |
| Precinct TSx           | 419                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 419                        | 1        | 54            | 54          | 19 35.19%       | 2 3.70%            |
| Precinct VBM           | 419                        | 1        | 260           | 253         | 158 62.45%      | 1 0.40%            |
| MB Precinct            | 419                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 419                        | 1        | 5             | 5           | 1 20.00%        | 0 0.00%            |
| Total                  | 419                        | 5        | 319           | 312         | 178 57.05%      | 3 0.96%            |
| 300011.04-CP 300011.04 |                            |          |               |             |                 |                    |
| Precinct TSx           | 441                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 441                        | 1        | 50            | 48          | 23 47.92%       | 0 0.00%            |
| Precinct VBM           | 441                        | 1        | 268           | 263         | 145 55.13%      | 0 0.00%            |
| MB Precinct            | 441                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 441                        | 1        | 11            | 10          | 2 20.00%        | 0 0.00%            |
| Total                  | 441                        | 5        | 329           | 321         | 170 52.96%      | 0 0.00%            |
| 300013.05-CP 300013.05 |                            |          |               |             |                 |                    |
| Precinct TSx           | 393                        | 1        | 1             | 1           | 0 0.00%         | 0 0.00%            |
| Precinct OS            | 393                        | 1        | 54            | 52          | 24 46.15%       | 0 0.00%            |
| Precinct VBM           | 393                        | 1        | 257           | 250         | 126 50.40%      | 0 0.00%            |
| MB Precinct            | 393                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 393                        | 1        | 5             | 5           | 1 20.00%        | 0 0.00%            |
| Total                  | 393                        | 5        | 317           | 308         | 151 49.03%      | 0 0.00%            |
| 300015.04-CP 300015.04 |                            |          |               |             |                 |                    |
| Precinct TSx           | 28                         | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 28                         | 1        | 3             | 2           | 2 100.00%       | 0 0.00%            |
| Precinct VBM           | 28                         | 1        | 19            | 19          | 8 42.11%        | 0 0.00%            |
| MB Precinct            | 28                         | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 28                         | 1        | 2             | 2           | 0 0.00%         | 0 0.00%            |
| Total                  | 28                         | 5        | 24            | 23          | 10 43.48%       | 0 0.00%            |
| 300015.05-CP 300015.05 |                            |          |               |             |                 |                    |
| Precinct TSx           | 179                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 179                        | 1        | 33            | 32          | 14 43.75%       | 0 0.00%            |
| Precinct VBM           | 179                        | 1        | 126           | 122         | 65 53.28%       | 0 0.00%            |
| MB Precinct            | 179                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 179                        | 1        | 1             | 1           | 1 100.00%       | 0 0.00%            |
| Total                  | 179                        | 5        | 160           | 155         | 80 51.61%       | 0 0.00%            |
| 300017.05-CP 300017.05 |                            |          |               |             |                 |                    |
| Precinct TSx           | 359                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 359                        | 1        | 46            | 44          | 25 56.82%       | 0 0.00%            |



**COUNTY OF EL DORADO**  
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|                        | President Democratic Party |          |               |             |                 |                    |
|------------------------|----------------------------|----------|---------------|-------------|-----------------|--------------------|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | HILLARY CLINTON | ROQUE DE LA FUENTE |
| Precinct TSx           | 459                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 459                        | 1        | 80            | 79          | 27              | 34.18%             |
| Precinct VBM           | 459                        | 1        | 281           | 277         | 129             | 46.57%             |
| MB Precinct            | 459                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 459                        | 1        | 14            | 14          | 3               | 21.43%             |
| Total                  | 459                        | 5        | 375           | 370         | 159             | 42.97%             |
| 400004.14-CP 400004.14 |                            |          |               |             |                 |                    |
| Precinct TSx           | 446                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 446                        | 1        | 79            | 77          | 27              | 35.06%             |
| Precinct VBM           | 446                        | 1        | 256           | 250         | 122             | 48.80%             |
| MB Precinct            | 446                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 446                        | 1        | 6             | 6           | 2               | 33.33%             |
| Total                  | 446                        | 5        | 341           | 333         | 151             | 45.35%             |
| 400005.08-CP 400005.08 |                            |          |               |             |                 |                    |
| Precinct TSx           | 167                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 167                        | 1        | 33            | 32          | 15              | 46.88%             |
| Precinct VBM           | 167                        | 1        | 100           | 99          | 38              | 38.38%             |
| MB Precinct            | 167                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 167                        | 1        | 4             | 4           | 1               | 25.00%             |
| Total                  | 167                        | 5        | 137           | 135         | 54              | 40.00%             |
| 400006.08-CP 400006.08 |                            |          |               |             |                 |                    |
| Precinct TSx           | 461                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 461                        | 1        | 104           | 102         | 37              | 36.27%             |
| Precinct VBM           | 461                        | 1        | 310           | 307         | 181             | 58.96%             |
| MB Precinct            | 461                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 461                        | 1        | 10            | 10          | 4               | 40.00%             |
| Total                  | 461                        | 5        | 424           | 419         | 222             | 52.98%             |
| 400007.08-CP 400007.08 |                            |          |               |             |                 |                    |
| Precinct TSx           | 405                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 405                        | 1        | 73            | 72          | 30              | 41.67%             |
| Precinct VBM           | 405                        | 1        | 248           | 241         | 103             | 42.74%             |
| MB Precinct            | 405                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 405                        | 1        | 7             | 7           | 1               | 14.29%             |
| Total                  | 405                        | 5        | 328           | 320         | 134             | 41.88%             |
| 400010.08-CP 400010.08 |                            |          |               |             |                 |                    |
| Precinct TSx           | 604                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 604                        | 1        | 107           | 106         | 42              | 39.62%             |
| Precinct VBM           | 604                        | 1        | 379           | 369         | 173             | 46.88%             |
| MB Precinct            | 604                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 604                        | 1        | 13            | 13          | 2               | 15.38%             |
| Total                  | 604                        | 5        | 499           | 488         | 217             | 44.47%             |
| 400012.08-CP 400012.08 |                            |          |               |             |                 |                    |
| Precinct TSx           | 267                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 267                        | 1        | 57            | 57          | 22              | 38.60%             |
| Precinct VBM           | 267                        | 1        | 177           | 173         | 83              | 47.98%             |
| MB Precinct            | 267                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 267                        | 1        | 12            | 11          | 4               | 36.36%             |
| Total                  | 267                        | 5        | 246           | 241         | 109             | 45.23%             |
| 400013.08-CP 400013.08 |                            |          |               |             |                 |                    |
| Precinct TSx           | 227                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 227                        | 1        | 41            | 38          | 13              | 34.21%             |
| Precinct VBM           | 227                        | 1        | 125           | 123         | 51              | 41.46%             |
| MB Precinct            | 227                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 227                        | 1        | 2             | 2           | 1               | 50.00%             |
| Total                  | 227                        | 5        | 168           | 163         | 65              | 39.88%             |
| 400014.08-CP 400014.08 |                            |          |               |             |                 |                    |
| Precinct TSx           | 382                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 382                        | 1        | 91            | 90          | 37              | 41.11%             |
| Precinct VBM           | 382                        | 1        | 255           | 253         | 152             | 60.08%             |
| MB Precinct            | 382                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 382                        | 1        | 10            | 10          | 3               | 30.00%             |

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|------------------------|----------------------------|----------|---------------|-------------|-----------------|--------|--------------------|-------|--|--|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | HILLARY CLINTON |        | ROQUE DE LA FUENTE |       |  |  |
| Total                  | 382                        | 5        | 356           | 353         | 192             | 54.39% | 0                  | 0.00% |  |  |
| 400017.08-CP 400017.08 |                            |          |               |             |                 |        |                    |       |  |  |
| Precinct TSx           | 373                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Precinct OS            | 373                        | 1        | 67            | 65          | 25              | 38.46% | 0                  | 0.00% |  |  |
| Precinct VBM           | 373                        | 1        | 236           | 227         | 119             | 52.42% | 1                  | 0.44% |  |  |
| MB Precinct            | 373                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Provisional            | 373                        | 1        | 12            | 11          | 1               | 9.09%  | 0                  | 0.00% |  |  |
| Total                  | 373                        | 5        | 315           | 303         | 145             | 47.85% | 1                  | 0.33% |  |  |
| 400018.08-CP 400018.08 |                            |          |               |             |                 |        |                    |       |  |  |
| Precinct TSx           | 134                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Precinct OS            | 134                        | 1        | 32            | 32          | 13              | 40.63% | 0                  | 0.00% |  |  |
| Precinct VBM           | 134                        | 1        | 79            | 76          | 40              | 52.63% | 0                  | 0.00% |  |  |
| MB Precinct            | 134                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Provisional            | 134                        | 1        | 6             | 6           | 1               | 16.67% | 0                  | 0.00% |  |  |
| Total                  | 134                        | 5        | 117           | 114         | 54              | 47.37% | 0                  | 0.00% |  |  |
| 400020.08-CP 400020.08 |                            |          |               |             |                 |        |                    |       |  |  |
| Precinct TSx           | 213                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Precinct OS            | 213                        | 1        | 37            | 36          | 15              | 41.67% | 0                  | 0.00% |  |  |
| Precinct VBM           | 213                        | 1        | 135           | 133         | 52              | 39.10% | 2                  | 1.50% |  |  |
| MB Precinct            | 213                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Provisional            | 213                        | 1        | 2             | 2           | 0               | 0.00%  | 0                  | 0.00% |  |  |
| Total                  | 213                        | 5        | 174           | 171         | 67              | 39.18% | 2                  | 1.17% |  |  |
| 400021.07-CP 400021.07 |                            |          |               |             |                 |        |                    |       |  |  |
| Precinct TSx           | 335                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Precinct OS            | 335                        | 1        | 55            | 54          | 31              | 57.41% | 0                  | 0.00% |  |  |
| Precinct VBM           | 335                        | 1        | 227           | 221         | 120             | 54.30% | 0                  | 0.00% |  |  |
| MB Precinct            | 335                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Provisional            | 335                        | 1        | 11            | 9           | 2               | 22.22% | 0                  | 0.00% |  |  |
| Total                  | 335                        | 5        | 293           | 284         | 153             | 53.87% | 0                  | 0.00% |  |  |
| 500001.10-CP 500001.10 |                            |          |               |             |                 |        |                    |       |  |  |
| Precinct TSx           | 671                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Precinct OS            | 671                        | 1        | 145           | 141         | 56              | 39.72% | 0                  | 0.00% |  |  |
| Precinct VBM           | 671                        | 1        | 348           | 342         | 172             | 50.29% | 0                  | 0.00% |  |  |
| MB Precinct            | 671                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Provisional            | 671                        | 1        | 18            | 18          | 3               | 16.67% | 0                  | 0.00% |  |  |
| Total                  | 671                        | 5        | 511           | 501         | 231             | 46.11% | 0                  | 0.00% |  |  |
| 500002.10-CP 500002.10 |                            |          |               |             |                 |        |                    |       |  |  |
| Precinct TSx           | 569                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Precinct OS            | 569                        | 1        | 98            | 98          | 28              | 28.57% | 0                  | 0.00% |  |  |
| Precinct VBM           | 569                        | 1        | 262           | 261         | 112             | 42.91% | 0                  | 0.00% |  |  |
| MB Precinct            | 569                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Provisional            | 569                        | 1        | 24            | 24          | 4               | 16.67% | 0                  | 0.00% |  |  |
| Total                  | 569                        | 5        | 384           | 383         | 144             | 37.60% | 0                  | 0.00% |  |  |
| 500003.10-CP 500003.10 |                            |          |               |             |                 |        |                    |       |  |  |
| Precinct TSx           | 427                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Precinct OS            | 427                        | 1        | 101           | 101         | 34              | 33.66% | 0                  | 0.00% |  |  |
| Precinct VBM           | 427                        | 1        | 209           | 206         | 79              | 38.35% | 0                  | 0.00% |  |  |
| MB Precinct            | 427                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Provisional            | 427                        | 1        | 8             | 8           | 0               | 0.00%  | 0                  | 0.00% |  |  |
| Total                  | 427                        | 5        | 318           | 315         | 113             | 35.87% | 0                  | 0.00% |  |  |
| 500004.10-CP 500004.10 |                            |          |               |             |                 |        |                    |       |  |  |
| Precinct TSx           | 380                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Precinct OS            | 380                        | 1        | 87            | 86          | 20              | 23.26% | 0                  | 0.00% |  |  |
| Precinct VBM           | 380                        | 1        | 152           | 149         | 58              | 38.93% | 0                  | 0.00% |  |  |
| MB Precinct            | 380                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Provisional            | 380                        | 1        | 15            | 14          | 1               | 7.14%  | 0                  | 0.00% |  |  |
| Total                  | 380                        | 5        | 254           | 249         | 79              | 31.73% | 0                  | 0.00% |  |  |
| 500005.10-CP 500005.10 |                            |          |               |             |                 |        |                    |       |  |  |
| Precinct TSx           | 405                        | 1        | 0             | 0           | 0               | -      | 0                  | -     |  |  |
| Precinct OS            | 405                        | 1        | 78            | 78          | 24              | 30.77% | 0                  | 0.00% |  |  |
| Precinct VBM           | 405                        | 1        | 164           | 158         | 68              | 43.04% | 0                  | 0.00% |  |  |

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|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | HILLARY CLINTON | ROQUE DE LA FUENTE |
| MB Precinct            | 405                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 405                        | 1        | 12            | 12          | 0               | 0.00%              |
| Total                  | 405                        | 5        | 254           | 248         | 92              | 37.10%             |
| 500006.10-CP 500006.10 |                            |          |               |             |                 |                    |
| Precinct TSx           | 720                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 720                        | 1        | 142           | 142         | 39              | 27.46%             |
| Precinct VBM           | 720                        | 1        | 333           | 330         | 138             | 41.82%             |
| MB Precinct            | 720                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 720                        | 1        | 48            | 48          | 6               | 12.50%             |
| Total                  | 720                        | 5        | 523           | 520         | 183             | 35.19%             |
| 500007.10-CP 500007.10 |                            |          |               |             |                 |                    |
| Precinct TSx           | 626                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 626                        | 1        | 129           | 128         | 40              | 31.25%             |
| Precinct VBM           | 626                        | 1        | 354           | 347         | 161             | 46.40%             |
| MB Precinct            | 626                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 626                        | 1        | 16            | 16          | 5               | 31.25%             |
| Total                  | 626                        | 5        | 499           | 491         | 206             | 41.96%             |
| 500009.10-CP 500009.10 |                            |          |               |             |                 |                    |
| Precinct TSx           | 444                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 444                        | 1        | 101           | 101         | 33              | 32.67%             |
| Precinct VBM           | 444                        | 1        | 190           | 187         | 94              | 50.27%             |
| MB Precinct            | 444                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 444                        | 1        | 27            | 27          | 9               | 33.33%             |
| Total                  | 444                        | 5        | 318           | 315         | 136             | 43.17%             |
| 500011.10-CP 500011.10 |                            |          |               |             |                 |                    |
| Precinct TSx           | 680                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 680                        | 1        | 132           | 132         | 29              | 21.97%             |
| Precinct VBM           | 680                        | 1        | 244           | 241         | 103             | 42.74%             |
| MB Precinct            | 680                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 680                        | 1        | 31            | 30          | 3               | 10.00%             |
| Total                  | 680                        | 5        | 407           | 403         | 135             | 33.50%             |
| 500012.10-CP 500012.10 |                            |          |               |             |                 |                    |
| Precinct TSx           | 628                        | 1        | 1             | 1           | 0               | 0.00%              |
| Precinct OS            | 628                        | 1        | 146           | 141         | 51              | 36.17%             |
| Precinct VBM           | 628                        | 1        | 340           | 337         | 145             | 43.03%             |
| MB Precinct            | 628                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 628                        | 1        | 32            | 31          | 8               | 25.81%             |
| Total                  | 628                        | 5        | 519           | 510         | 204             | 40.00%             |
| 500015.10-CP 500015.10 |                            |          |               |             |                 |                    |
| Precinct TSx           | 421                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 421                        | 1        | 74            | 74          | 35              | 47.30%             |
| Precinct VBM           | 421                        | 1        | 248           | 245         | 127             | 51.84%             |
| MB Precinct            | 421                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 421                        | 1        | 9             | 9           | 2               | 22.22%             |
| Total                  | 421                        | 5        | 331           | 328         | 164             | 50.00%             |
| 500020.10-CP 500020.10 |                            |          |               |             |                 |                    |
| Precinct TSx           | 482                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 482                        | 1        | 75            | 73          | 34              | 46.58%             |
| Precinct VBM           | 482                        | 1        | 278           | 273         | 132             | 48.35%             |
| MB Precinct            | 482                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 482                        | 1        | 12            | 11          | 1               | 9.09%              |
| Total                  | 482                        | 5        | 365           | 357         | 167             | 46.78%             |
| 500021.10-CP 500021.10 |                            |          |               |             |                 |                    |
| Precinct TSx           | 381                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 381                        | 1        | 75            | 75          | 36              | 48.00%             |
| Precinct VBM           | 381                        | 1        | 237           | 231         | 101             | 43.72%             |
| MB Precinct            | 381                        | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 381                        | 1        | 13            | 12          | 5               | 41.67%             |
| Total                  | 381                        | 5        | 325           | 318         | 142             | 44.65%             |
| 95U201.01-MB 95U201.01 |                            |          |               |             |                 |                    |
| Precinct TSx           | 2                          | 1        | 0             | 0           | 0               | -                  |



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|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | HILLARY CLINTON | ROQUE DE LA FUENTE |
| Precinct OS            | 2                          | 1        | 0             | 0           | 0               | -                  |
| Precinct VBM           | 2                          | 1        | 0             | 0           | 0               | -                  |
| MB Precinct            | 2                          | 1        | 3             | 3           | 0               | 0.00%              |
| Provisional            | 2                          | 1        | 0             | 0           | 0               | -                  |
| Total                  | 2                          | 5        | 3             | 3           | 0               | 0.00%              |
| 95U201.02-MB 95U201.02 |                            |          |               |             |                 |                    |
| Precinct TSx           | 139                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 139                        | 1        | 0             | 0           | 0               | -                  |
| Precinct VBM           | 139                        | 1        | 0             | 0           | 0               | -                  |
| MB Precinct            | 139                        | 1        | 118           | 113         | 61              | 53.98%             |
| Provisional            | 139                        | 1        | 0             | 0           | 0               | -                  |
| Total                  | 139                        | 5        | 118           | 113         | 61              | 53.98%             |
| 95U202.02-MB 95U202.02 |                            |          |               |             |                 |                    |
| Precinct TSx           | 266                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 266                        | 1        | 0             | 0           | 0               | -                  |
| Precinct VBM           | 266                        | 1        | 0             | 0           | 0               | -                  |
| MB Precinct            | 266                        | 1        | 228           | 224         | 93              | 41.52%             |
| Provisional            | 266                        | 1        | 2             | 2           | 1               | 50.00%             |
| Total                  | 266                        | 5        | 230           | 226         | 94              | 41.59%             |
| 95U203.02-MB 95U203.02 |                            |          |               |             |                 |                    |
| Precinct TSx           | 0                          | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 0                          | 1        | 0             | 0           | 0               | -                  |
| Precinct VBM           | 0                          | 1        | 0             | 0           | 0               | -                  |
| MB Precinct            | 0                          | 1        | 0             | 0           | 0               | -                  |
| Provisional            | 0                          | 1        | 0             | 0           | 0               | -                  |
| Total                  | 0                          | 5        | 0             | 0           | 0               | -                  |
| 95U204.01-MB 95U204.01 |                            |          |               |             |                 |                    |
| Precinct TSx           | 242                        | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 242                        | 1        | 0             | 0           | 0               | -                  |
| Precinct VBM           | 242                        | 1        | 0             | 0           | 0               | -                  |
| MB Precinct            | 242                        | 1        | 212           | 205         | 103             | 50.24%             |
| Provisional            | 242                        | 1        | 0             | 0           | 0               | -                  |
| Total                  | 242                        | 5        | 212           | 205         | 103             | 50.24%             |
| 95U301.05-MB 95U301.05 |                            |          |               |             |                 |                    |
| Precinct TSx           | 65                         | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 65                         | 1        | 0             | 0           | 0               | -                  |
| Precinct VBM           | 65                         | 1        | 0             | 0           | 0               | -                  |
| MB Precinct            | 65                         | 1        | 55            | 54          | 17              | 31.48%             |
| Provisional            | 65                         | 1        | 1             | 1           | 0               | 0.00%              |
| Total                  | 65                         | 5        | 56            | 55          | 17              | 30.91%             |
| 95U302.04-MB 95U302.04 |                            |          |               |             |                 |                    |
| Precinct TSx           | 0                          | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 0                          | 1        | 0             | 0           | 0               | -                  |
| Precinct VBM           | 0                          | 1        | 0             | 0           | 0               | -                  |
| MB Precinct            | 0                          | 1        | 1             | 1           | 1               | 100.00%            |
| Provisional            | 0                          | 1        | 0             | 0           | 0               | -                  |
| Total                  | 0                          | 5        | 1             | 1           | 1               | 100.00%            |
| 95U302.05-MB 95U302.05 |                            |          |               |             |                 |                    |
| Precinct TSx           | 37                         | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 37                         | 1        | 0             | 0           | 0               | -                  |
| Precinct VBM           | 37                         | 1        | 0             | 0           | 0               | -                  |
| MB Precinct            | 37                         | 1        | 33            | 31          | 11              | 35.48%             |
| Provisional            | 37                         | 1        | 0             | 0           | 0               | -                  |
| Total                  | 37                         | 5        | 33            | 31          | 11              | 35.48%             |
| 95U303.03-MB 95U303.03 |                            |          |               |             |                 |                    |
| Precinct TSx           | 43                         | 1        | 0             | 0           | 0               | -                  |
| Precinct OS            | 43                         | 1        | 0             | 0           | 0               | -                  |
| Precinct VBM           | 43                         | 1        | 0             | 0           | 0               | -                  |
| MB Precinct            | 43                         | 1        | 38            | 38          | 12              | 31.58%             |
| Provisional            | 43                         | 1        | 0             | 0           | 0               | -                  |
| Total                  | 43                         | 5        | 38            | 38          | 12              | 31.58%             |

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|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | HILLARY CLINTON | ROQUE DE LA FUENTE |
| 95U303.05-MB 95U303.05 |                            |          |               |             |                 |                    |
| Precinct TSx           | 49                         | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 49                         | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct VBM           | 49                         | 1        | 0             | 0           | 0 -             | 0 -                |
| MB Precinct            | 49                         | 1        | 35            | 35          | 20 57.14%       | 0 0.00%            |
| Provisional            | 49                         | 1        | 0             | 0           | 0 -             | 0 -                |
| Total                  | 49                         | 5        | 35            | 35          | 20 57.14%       | 0 0.00%            |
| 95U304.05-MB 95U304.05 |                            |          |               |             |                 |                    |
| Precinct TSx           | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct VBM           | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| MB Precinct            | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| Total                  | 0                          | 5        | 0             | 0           | 0 -             | 0 -                |
| 95U305.05-MB 95U305.05 |                            |          |               |             |                 |                    |
| Precinct TSx           | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct VBM           | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| MB Precinct            | 0                          | 1        | 1             | 1           | 0 0.00%         | 0 0.00%            |
| Provisional            | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| Total                  | 0                          | 5        | 1             | 1           | 0 0.00%         | 0 0.00%            |
| 95U401.08-MB 95U401.08 |                            |          |               |             |                 |                    |
| Precinct TSx           | 13                         | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 13                         | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct VBM           | 13                         | 1        | 0             | 0           | 0 -             | 0 -                |
| MB Precinct            | 13                         | 1        | 8             | 8           | 3 37.50%        | 0 0.00%            |
| Provisional            | 13                         | 1        | 0             | 0           | 0 -             | 0 -                |
| Total                  | 13                         | 5        | 8             | 8           | 3 37.50%        | 0 0.00%            |
| 95U402.07-MB 95U402.07 |                            |          |               |             |                 |                    |
| Precinct TSx           | 119                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 119                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct VBM           | 119                        | 1        | 0             | 0           | 0 -             | 0 -                |
| MB Precinct            | 119                        | 1        | 100           | 100         | 40 40.00%       | 0 0.00%            |
| Provisional            | 119                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Total                  | 119                        | 5        | 100           | 100         | 40 40.00%       | 0 0.00%            |
| 95U402.08-MB 95U402.08 |                            |          |               |             |                 |                    |
| Precinct TSx           | 225                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 225                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct VBM           | 225                        | 1        | 0             | 0           | 0 -             | 0 -                |
| MB Precinct            | 225                        | 1        | 194           | 188         | 84 44.68%       | 1 0.53%            |
| Provisional            | 225                        | 1        | 3             | 3           | 0 0.00%         | 0 0.00%            |
| Total                  | 225                        | 5        | 197           | 191         | 84 43.98%       | 1 0.52%            |
| 95U403.08-MB 95U403.08 |                            |          |               |             |                 |                    |
| Precinct TSx           | 527                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 527                        | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct VBM           | 527                        | 1        | 0             | 0           | 0 -             | 0 -                |
| MB Precinct            | 527                        | 1        | 474           | 469         | 202 43.07%      | 1 0.21%            |
| Provisional            | 527                        | 1        | 5             | 5           | 1 20.00%        | 0 0.00%            |
| Total                  | 527                        | 5        | 479           | 474         | 203 42.83%      | 1 0.21%            |
| 95U404.06-MB 95U404.06 |                            |          |               |             |                 |                    |
| Precinct TSx           | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct VBM           | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| MB Precinct            | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| Provisional            | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| Total                  | 0                          | 5        | 0             | 0           | 0 -             | 0 -                |
| 95U404.08-MB 95U404.08 |                            |          |               |             |                 |                    |
| Precinct TSx           | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct OS            | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| Precinct VBM           | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |
| MB Precinct            | 0                          | 1        | 0             | 0           | 0 -             | 0 -                |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                     | President Democratic Party |            |               |              |                 |               |                    |
|-------------------------------------|----------------------------|------------|---------------|--------------|-----------------|---------------|--------------------|
|                                     | Reg. Voters                | Vote For   | Times Counted | Total Votes  | HILLARY CLINTON |               | ROQUE DE LA FUENTE |
| Precinct TSx                        | 32659                      | 102        | 4             | 4            | 2               | 50.00%        | 0 0.00%            |
| Precinct OS                         | 32659                      | 102        | 5574          | 5496         | 2289            | 41.65%        | 6 0.11%            |
| Precinct VBM                        | 32659                      | 102        | 18011         | 17598        | 9099            | 51.70%        | 21 0.12%           |
| MB Precinct                         | 32659                      | 102        | 2018          | 1978         | 889             | 44.94%        | 4 0.20%            |
| Provisional                         | 32659                      | 102        | 835           | 820          | 213             | 25.98%        | 0 0.00%            |
| <b>Total</b>                        | <b>32659</b>               | <b>510</b> | <b>26442</b>  | <b>25896</b> | <b>12492</b>    | <b>48.24%</b> | <b>31 0.12%</b>    |
| <b>UNITED STATES SENATE</b>         |                            |            |               |              |                 |               |                    |
| <b>UNITED STATES SENATE</b>         |                            |            |               |              |                 |               |                    |
| Precinct TSx                        | 32659                      | 102        | 4             | 4            | 2               | 50.00%        | 0 0.00%            |
| Precinct OS                         | 32659                      | 102        | 5574          | 5496         | 2289            | 41.65%        | 6 0.11%            |
| Precinct VBM                        | 32659                      | 102        | 18011         | 17598        | 9099            | 51.70%        | 21 0.12%           |
| MB Precinct                         | 32659                      | 102        | 2018          | 1978         | 889             | 44.94%        | 4 0.20%            |
| Provisional                         | 32659                      | 102        | 835           | 820          | 213             | 25.98%        | 0 0.00%            |
| <b>Total</b>                        | <b>32659</b>               | <b>510</b> | <b>26442</b>  | <b>25896</b> | <b>12492</b>    | <b>48.24%</b> | <b>31 0.12%</b>    |
| <b>Total</b>                        |                            |            |               |              |                 |               |                    |
| Precinct TSx                        | 32659                      | 102        | 4             | 4            | 2               | 50.00%        | 0 0.00%            |
| Precinct OS                         | 32659                      | 102        | 5574          | 5496         | 2289            | 41.65%        | 6 0.11%            |
| Precinct VBM                        | 32659                      | 102        | 18011         | 17598        | 9099            | 51.70%        | 21 0.12%           |
| MB Precinct                         | 32659                      | 102        | 2018          | 1978         | 889             | 44.94%        | 4 0.20%            |
| Provisional                         | 32659                      | 102        | 835           | 820          | 213             | 25.98%        | 0 0.00%            |
| <b>Total</b>                        | <b>32659</b>               | <b>510</b> | <b>26442</b>  | <b>25896</b> | <b>12492</b>    | <b>48.24%</b> | <b>31 0.12%</b>    |
| <b>UNITED STATES REPRESENTATIVE</b> |                            |            |               |              |                 |               |                    |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                            |            |               |              |                 |               |                    |
| Precinct TSx                        | 32659                      | 102        | 4             | 4            | 2               | 50.00%        | 0 0.00%            |
| Precinct OS                         | 32659                      | 102        | 5574          | 5496         | 2289            | 41.65%        | 6 0.11%            |
| Precinct VBM                        | 32659                      | 102        | 18011         | 17598        | 9099            | 51.70%        | 21 0.12%           |
| MB Precinct                         | 32659                      | 102        | 2018          | 1978         | 889             | 44.94%        | 4 0.20%            |
| Provisional                         | 32659                      | 102        | 835           | 820          | 213             | 25.98%        | 0 0.00%            |
| <b>Total</b>                        | <b>32659</b>               | <b>510</b> | <b>26442</b>  | <b>25896</b> | <b>12492</b>    | <b>48.24%</b> | <b>31 0.12%</b>    |
| <b>Total</b>                        |                            |            |               |              |                 |               |                    |
| Precinct TSx                        | 32659                      | 102        | 4             | 4            | 2               | 50.00%        | 0 0.00%            |
| Precinct OS                         | 32659                      | 102        | 5574          | 5496         | 2289            | 41.65%        | 6 0.11%            |
| Precinct VBM                        | 32659                      | 102        | 18011         | 17598        | 9099            | 51.70%        | 21 0.12%           |
| MB Precinct                         | 32659                      | 102        | 2018          | 1978         | 889             | 44.94%        | 4 0.20%            |
| Provisional                         | 32659                      | 102        | 835           | 820          | 213             | 25.98%        | 0 0.00%            |
| <b>Total</b>                        | <b>32659</b>               | <b>510</b> | <b>26442</b>  | <b>25896</b> | <b>12492</b>    | <b>48.24%</b> | <b>31 0.12%</b>    |
| <b>STATE SENATE</b>                 |                            |            |               |              |                 |               |                    |
| <b>1ST STATE SENATE DISTRICT</b>    |                            |            |               |              |                 |               |                    |
| Precinct TSx                        | 32659                      | 102        | 4             | 4            | 2               | 50.00%        | 0 0.00%            |
| Precinct OS                         | 32659                      | 102        | 5574          | 5496         | 2289            | 41.65%        | 6 0.11%            |
| Precinct VBM                        | 32659                      | 102        | 18011         | 17598        | 9099            | 51.70%        | 21 0.12%           |
| MB Precinct                         | 32659                      | 102        | 2018          | 1978         | 889             | 44.94%        | 4 0.20%            |
| Provisional                         | 32659                      | 102        | 835           | 820          | 213             | 25.98%        | 0 0.00%            |
| <b>Total</b>                        | <b>32659</b>               | <b>510</b> | <b>26442</b>  | <b>25896</b> | <b>12492</b>    | <b>48.24%</b> | <b>31 0.12%</b>    |
| <b>Total</b>                        |                            |            |               |              |                 |               |                    |
| Precinct TSx                        | 32659                      | 102        | 4             | 4            | 2               | 50.00%        | 0 0.00%            |
| Precinct OS                         | 32659                      | 102        | 5574          | 5496         | 2289            | 41.65%        | 6 0.11%            |
| Precinct VBM                        | 32659                      | 102        | 18011         | 17598        | 9099            | 51.70%        | 21 0.12%           |
| MB Precinct                         | 32659                      | 102        | 2018          | 1978         | 889             | 44.94%        | 4 0.20%            |
| Provisional                         | 32659                      | 102        | 835           | 820          | 213             | 25.98%        | 0 0.00%            |
| <b>Total</b>                        | <b>32659</b>               | <b>510</b> | <b>26442</b>  | <b>25896</b> | <b>12492</b>    | <b>48.24%</b> | <b>31 0.12%</b>    |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                            |            |               |              |                 |               |                    |
| <b>5TH STATE ASSEMBLY DIST</b>      |                            |            |               |              |                 |               |                    |
| Precinct TSx                        | 22300                      | 75         | 2             | 2            | 0               | 0.00%         | 0 0.00%            |
| Precinct OS                         | 22300                      | 75         | 3647          | 3590         | 1365            | 38.02%        | 4 0.11%            |
| Precinct VBM                        | 22300                      | 75         | 11856         | 11585        | 5572            | 48.10%        | 13 0.11%           |
| MB Precinct                         | 22300                      | 75         | 1753          | 1720         | 756             | 43.95%        | 3 0.17%            |

**COUNTY OF EL DORADO**  
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|                                   | President Democratic Party |          |               |             |                 |         |                    |       |
|-----------------------------------|----------------------------|----------|---------------|-------------|-----------------|---------|--------------------|-------|
|                                   | Reg. Voters                | Vote For | Times Counted | Total Votes | HILLARY CLINTON |         | ROQUE DE LA FUENTE |       |
| Provisional                       | 22300                      | 75       | 577           | 566         | 126             | 22.26%  | 0                  | 0.00% |
| Total                             | 22300                      | 375      | 17835         | 17463       | 7819            | 44.77%  | 20                 | 0.11% |
| <b>6TH STATE ASSEMBLY DIST</b>    |                            |          |               |             |                 |         |                    |       |
| Precinct TSx                      | 10359                      | 27       | 2             | 2           | 2               | 100.00% | 0                  | 0.00% |
| Precinct OS                       | 10359                      | 27       | 1927          | 1906        | 924             | 48.48%  | 2                  | 0.10% |
| Precinct VBM                      | 10359                      | 27       | 6155          | 6013        | 3527            | 58.66%  | 8                  | 0.13% |
| MB Precinct                       | 10359                      | 27       | 265           | 258         | 133             | 51.55%  | 1                  | 0.39% |
| Provisional                       | 10359                      | 27       | 258           | 254         | 87              | 34.25%  | 0                  | 0.00% |
| Total                             | 10359                      | 135      | 8607          | 8433        | 4673            | 55.41%  | 11                 | 0.13% |
| <b>Total</b>                      |                            |          |               |             |                 |         |                    |       |
| Precinct TSx                      | 32659                      | 102      | 4             | 4           | 2               | 50.00%  | 0                  | 0.00% |
| Precinct OS                       | 32659                      | 102      | 5574          | 5496        | 2289            | 41.65%  | 6                  | 0.11% |
| Precinct VBM                      | 32659                      | 102      | 18011         | 17598       | 9099            | 51.70%  | 21                 | 0.12% |
| MB Precinct                       | 32659                      | 102      | 2018          | 1978        | 889             | 44.94%  | 4                  | 0.20% |
| Provisional                       | 32659                      | 102      | 835           | 820         | 213             | 25.98%  | 0                  | 0.00% |
| Total                             | 32659                      | 510      | 26442         | 25896       | 12492           | 48.24%  | 31                 | 0.12% |
| <b>SUPERVISORIAL DISTRICTS</b>    |                            |          |               |             |                 |         |                    |       |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                            |          |               |             |                 |         |                    |       |
| Precinct TSx                      | 5956                       | 14       | 0             | 0           | 0               | -       | 0                  | -     |
| Precinct OS                       | 5956                       | 14       | 1136          | 1127        | 583             | 51.73%  | 2                  | 0.18% |
| Precinct VBM                      | 5956                       | 14       | 3610          | 3519        | 2188            | 62.18%  | 7                  | 0.20% |
| MB Precinct                       | 5956                       | 14       | 130           | 128         | 73              | 57.03%  | 1                  | 0.78% |
| Provisional                       | 5956                       | 14       | 140           | 136         | 53              | 38.97%  | 0                  | 0.00% |
| Total                             | 5956                       | 70       | 5016          | 4910        | 2897            | 59.00%  | 10                 | 0.20% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                            |          |               |             |                 |         |                    |       |
| Precinct TSx                      | 6415                       | 23       | 2             | 2           | 2               | 100.00% | 0                  | 0.00% |
| Precinct OS                       | 6415                       | 23       | 1045          | 1033        | 458             | 44.34%  | 0                  | 0.00% |
| Precinct VBM                      | 6415                       | 23       | 3542          | 3459        | 1750            | 50.59%  | 2                  | 0.06% |
| MB Precinct                       | 6415                       | 23       | 561           | 545         | 257             | 47.16%  | 0                  | 0.00% |
| Provisional                       | 6415                       | 23       | 146           | 146         | 45              | 30.82%  | 0                  | 0.00% |
| Total                             | 6415                       | 115      | 5296          | 5185        | 2512            | 48.45%  | 2                  | 0.04% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                            |          |               |             |                 |         |                    |       |
| Precinct TSx                      | 6715                       | 26       | 1             | 1           | 0               | 0.00%   | 0                  | 0.00% |
| Precinct OS                       | 6715                       | 26       | 1016          | 995         | 399             | 40.10%  | 3                  | 0.30% |
| Precinct VBM                      | 6715                       | 26       | 4087          | 3974        | 1994            | 50.18%  | 7                  | 0.18% |
| MB Precinct                       | 6715                       | 26       | 163           | 160         | 61              | 38.13%  | 0                  | 0.00% |
| Provisional                       | 6715                       | 26       | 140           | 138         | 35              | 25.36%  | 0                  | 0.00% |
| Total                             | 6715                       | 130      | 5407          | 5268        | 2489            | 47.25%  | 10                 | 0.19% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                            |          |               |             |                 |         |                    |       |
| Precinct TSx                      | 6418                       | 22       | 0             | 0           | 0               | -       | 0                  | -     |
| Precinct OS                       | 6418                       | 22       | 994           | 971         | 390             | 40.16%  | 0                  | 0.00% |
| Precinct VBM                      | 6418                       | 22       | 3413          | 3339        | 1677            | 50.22%  | 4                  | 0.12% |
| MB Precinct                       | 6418                       | 22       | 911           | 895         | 389             | 43.46%  | 2                  | 0.22% |
| Provisional                       | 6418                       | 22       | 141           | 137         | 32              | 23.36%  | 0                  | 0.00% |
| Total                             | 6418                       | 110      | 5459          | 5342        | 2488            | 46.57%  | 6                  | 0.11% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                            |          |               |             |                 |         |                    |       |
| Precinct TSx                      | 7155                       | 17       | 1             | 1           | 0               | 0.00%   | 0                  | 0.00% |
| Precinct OS                       | 7155                       | 17       | 1383          | 1370        | 459             | 33.50%  | 1                  | 0.07% |
| Precinct VBM                      | 7155                       | 17       | 3359          | 3307        | 1490            | 45.06%  | 1                  | 0.03% |
| MB Precinct                       | 7155                       | 17       | 253           | 250         | 109             | 43.60%  | 1                  | 0.40% |
| Provisional                       | 7155                       | 17       | 268           | 263         | 48              | 18.25%  | 0                  | 0.00% |
| Total                             | 7155                       | 85       | 5264          | 5191        | 2106            | 40.57%  | 3                  | 0.06% |
| <b>Total</b>                      |                            |          |               |             |                 |         |                    |       |
| Precinct TSx                      | 32659                      | 102      | 4             | 4           | 2               | 50.00%  | 0                  | 0.00% |
| Precinct OS                       | 32659                      | 102      | 5574          | 5496        | 2289            | 41.65%  | 6                  | 0.11% |
| Precinct VBM                      | 32659                      | 102      | 18011         | 17598       | 9099            | 51.70%  | 21                 | 0.12% |
| MB Precinct                       | 32659                      | 102      | 2018          | 1978        | 889             | 44.94%  | 4                  | 0.20% |
| Provisional                       | 32659                      | 102      | 835           | 820         | 213             | 25.98%  | 0                  | 0.00% |
| Total                             | 32659                      | 510      | 26442         | 25896       | 12492           | 48.24%  | 31                 | 0.12% |

**COUNTY OF EL DORADO**  
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|                                 | President Democratic Party |          |               |             |                 |        |                    |       |
|---------------------------------|----------------------------|----------|---------------|-------------|-----------------|--------|--------------------|-------|
|                                 | Reg. Voters                | Vote For | Times Counted | Total Votes | HILLARY CLINTON |        | ROQUE DE LA FUENTE |       |
| <b>CITIES</b>                   |                            |          |               |             |                 |        |                    |       |
| <b>CITY OF PLACERVILLE</b>      |                            |          |               |             |                 |        |                    |       |
| Precinct TSx                    | 1999                       | 5        | 0             | 0           | 0               | -      | 0                  | -     |
| Precinct OS                     | 1999                       | 5        | 331           | 328         | 117             | 35.67% | 0                  | 0.00% |
| Precinct VBM                    | 1999                       | 5        | 1221          | 1189        | 560             | 47.10% | 4                  | 0.34% |
| MB Precinct                     | 1999                       | 5        | 0             | 0           | 0               | -      | 0                  | -     |
| Provisional                     | 1999                       | 5        | 51            | 51          | 12              | 23.53% | 0                  | 0.00% |
| Total                           | 1999                       | 25       | 1603          | 1568        | 689             | 43.94% | 4                  | 0.26% |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                            |          |               |             |                 |        |                    |       |
| Precinct TSx                    | 3824                       | 7        | 0             | 0           | 0               | -      | 0                  | -     |
| Precinct OS                     | 3824                       | 7        | 767           | 765         | 213             | 27.84% | 1                  | 0.13% |
| Precinct VBM                    | 3824                       | 7        | 1699          | 1673        | 734             | 43.87% | 1                  | 0.06% |
| MB Precinct                     | 3824                       | 7        | 0             | 0           | 0               | -      | 0                  | -     |
| Provisional                     | 3824                       | 7        | 173           | 171         | 28              | 16.37% | 0                  | 0.00% |
| Total                           | 3824                       | 35       | 2639          | 2609        | 975             | 37.37% | 2                  | 0.08% |
| <b>UNINCORPORATED AREA</b>      |                            |          |               |             |                 |        |                    |       |
| Precinct TSx                    | 26836                      | 90       | 4             | 4           | 2               | 50.00% | 0                  | 0.00% |
| Precinct OS                     | 26836                      | 90       | 4476          | 4403        | 1959            | 44.49% | 5                  | 0.11% |
| Precinct VBM                    | 26836                      | 90       | 15091         | 14736       | 7805            | 52.97% | 16                 | 0.11% |
| MB Precinct                     | 26836                      | 90       | 2018          | 1978        | 889             | 44.94% | 4                  | 0.20% |
| Provisional                     | 26836                      | 90       | 611           | 598         | 173             | 28.93% | 0                  | 0.00% |
| Total                           | 26836                      | 450      | 22200         | 21719       | 10828           | 49.85% | 25                 | 0.12% |
| <b>Total</b>                    |                            |          |               |             |                 |        |                    |       |
| Precinct TSx                    | 32659                      | 102      | 4             | 4           | 2               | 50.00% | 0                  | 0.00% |
| Precinct OS                     | 32659                      | 102      | 5574          | 5496        | 2289            | 41.65% | 6                  | 0.11% |
| Precinct VBM                    | 32659                      | 102      | 18011         | 17598       | 9099            | 51.70% | 21                 | 0.12% |
| MB Precinct                     | 32659                      | 102      | 2018          | 1978        | 889             | 44.94% | 4                  | 0.20% |
| Provisional                     | 32659                      | 102      | 835           | 820         | 213             | 25.98% | 0                  | 0.00% |
| Total                           | 32659                      | 510      | 26442         | 25896       | 12492           | 48.24% | 31                 | 0.12% |

**COUNTY OF EL DORADO**  
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|------------------------|----------------------------|-------|------------|--------|----------------|--------|-------------------|-------|
|                        | HENRY HEWES                |       | KEITH JUDD |        | BERNIE SANDERS |        | MICHAEL STEINBERG |       |
| Jurisdiction Wide      |                            |       |            |        |                |        |                   |       |
| 100001.11-CP 100001.11 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 43             | 48.31% | 1                 | 1.12% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00%  | 100            | 40.00% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 1          | 11.11% | 5              | 55.56% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.29%  | 148            | 42.53% | 1                 | 0.29% |
| 100002.11-CP 100002.11 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 25             | 53.19% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00%  | 57             | 35.85% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 5              | 83.33% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 87             | 41.04% | 0                 | 0.00% |
| 100003.11-CP 100003.11 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 16             | 43.24% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00%  | 99             | 32.35% | 2                 | 0.65% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 5              | 50.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 120            | 33.99% | 2                 | 0.57% |
| 100004.11-CP 100004.11 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 68             | 51.91% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00%  | 99             | 35.11% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 1              | 33.33% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 168            | 40.38% | 0                 | 0.00% |
| 100005.11-CP 100005.11 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 19             | 35.19% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00%  | 76             | 32.34% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 5              | 71.43% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 100            | 33.78% | 0                 | 0.00% |
| 100006.11-CP 100006.11 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 57             | 55.34% | 0                 | 0.00% |
| Precinct VBM           | 1                          | 0.28% | 0          | 0.00%  | 136            | 38.75% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 10             | 71.43% | 0                 | 0.00% |
| Total                  | 1                          | 0.21% | 0          | 0.00%  | 203            | 43.38% | 0                 | 0.00% |
| 100008.11-CP 100008.11 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 44             | 44.44% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.41%  | 73             | 30.04% | 1                 | 0.41% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 6              | 60.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.28%  | 123            | 34.94% | 1                 | 0.28% |
| 100009.11-CP 100009.11 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 34             | 41.98% | 1                 | 1.23% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00%  | 107            | 38.77% | 3                 | 1.09% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 9              | 69.23% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 150            | 40.54% | 4                 | 1.08% |
| 100012.11-CP 100012.11 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 1                          | 0.89% | 0          | 0.00%  | 50             | 44.64% | 0                 | 0.00% |
| Precinct VBM           | 1                          | 0.30% | 0          | 0.00%  | 133            | 39.35% | 2                 | 0.59% |

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|                        | HENRY HEWES                |       | KEITH JUDD |       | BERNIE SANDERS |        | MICHAEL STEINBERG |       |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 9              | 64.29% | 0                 | 0.00% |
| Total                  | 2                          | 0.43% | 0          | 0.00% | 192            | 41.38% | 2                 | 0.43% |
| 100013.11-CP 100013.11 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 48             | 43.24% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 95             | 31.35% | 1                 | 0.33% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 6              | 46.15% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 149            | 34.89% | 1                 | 0.23% |
| 100014.11-CP 100014.11 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 45             | 50.56% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 2          | 0.89% | 74             | 33.04% | 1                 | 0.45% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 5              | 41.67% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 2          | 0.62% | 124            | 38.15% | 1                 | 0.31% |
| 100022.11-CP 100022.11 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 1                          | 1.12% | 0          | 0.00% | 42             | 47.19% | 0                 | 0.00% |
| Precinct VBM           | 1                          | 0.45% | 0          | 0.00% | 81             | 36.65% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 3              | 50.00% | 0                 | 0.00% |
| Total                  | 2                          | 0.63% | 0          | 0.00% | 126            | 39.87% | 0                 | 0.00% |
| 100023.11-CP 100023.11 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 35             | 41.18% | 1                 | 1.18% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.30% | 115            | 34.74% | 3                 | 0.91% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 8              | 53.33% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.23% | 158            | 36.66% | 4                 | 0.93% |
| 200003.12-CP 200003.12 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 1          | 1.27% | 51             | 64.56% | 0                 | 0.00% |
| Precinct VBM           | 1                          | 0.48% | 0          | 0.00% | 105            | 50.72% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 12             | 80.00% | 0                 | 0.00% |
| Total                  | 1                          | 0.33% | 1          | 0.33% | 168            | 55.81% | 0                 | 0.00% |
| 200004.12-CP 200004.12 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 43             | 54.43% | 0                 | 0.00% |
| Precinct VBM           | 1                          | 0.36% | 0          | 0.00% | 128            | 46.38% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 3              | 50.00% | 0                 | 0.00% |
| Total                  | 1                          | 0.28% | 0          | 0.00% | 174            | 48.20% | 0                 | 0.00% |
| 200005.02-CP 200005.02 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 22             | 46.81% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 85             | 51.83% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 9              | 60.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 116            | 51.33% | 0                 | 0.00% |
| 200006.12-CP 200006.12 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 16             | 44.44% | 1                 | 2.78% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.60% | 72             | 43.37% | 4                 | 2.41% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 9              | 56.25% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.46% | 97             | 44.50% | 5                 | 2.29% |
| 200008.12-CP 200008.12 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |



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|                        | HENRY HEWES                |       | KEITH JUDD |        | BERNIE SANDERS |        | MICHAEL STEINBERG |       |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 32             | 50.00% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00%  | 73             | 47.10% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 4              | 80.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 109            | 48.66% | 0                 | 0.00% |
| 200010.12-CP 200010.12 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | 0.00% | 0          | 0.00%  | 0              | 0.00%  | 0                 | 0.00% |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 44             | 40.37% | 1                 | 0.92% |
| Precinct VBM           | 1                          | 0.29% | 0          | 0.00%  | 123            | 36.07% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 11             | 61.11% | 0                 | 0.00% |
| Total                  | 1                          | 0.21% | 0          | 0.00%  | 178            | 37.87% | 1                 | 0.21% |
| 200012.12-CP 200012.12 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 41             | 59.42% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00%  | 72             | 45.00% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 6              | 75.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 119            | 50.21% | 0                 | 0.00% |
| 200013.12-CP 200013.12 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 43             | 48.31% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.47%  | 76             | 35.85% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 6              | 60.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.32%  | 125            | 40.19% | 0                 | 0.00% |
| 200014.12-CP 200014.12 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 47             | 58.02% | 0                 | 0.00% |
| Precinct VBM           | 1                          | 0.44% | 1          | 0.44%  | 103            | 45.37% | 2                 | 0.88% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 14             | 87.50% | 0                 | 0.00% |
| Total                  | 1                          | 0.31% | 1          | 0.31%  | 164            | 50.62% | 2                 | 0.62% |
| 200015.02-CP 200015.02 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 1          | 0.93%  | 65             | 60.75% | 1                 | 0.93% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00%  | 191            | 55.52% | 2                 | 0.58% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 6              | 66.67% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.22%  | 262            | 56.96% | 3                 | 0.65% |
| 200016.12-CP 200016.12 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 1          | 16.67% | 1              | 16.67% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 2.70%  | 14             | 37.84% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 0              | 0.00%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 2          | 4.55%  | 15             | 34.09% | 0                 | 0.00% |
| 200016.13-CP 200016.13 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 9              | 45.00% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00%  | 46             | 47.42% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 55             | 47.01% | 0                 | 0.00% |
| 200017.02-CP 200017.02 |                            |       |            |        |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00%  | 31             | 52.54% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00%  | 129            | 51.81% | 2                 | 0.80% |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 4              | 80.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 164            | 52.40% | 2                 | 0.64% |

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|                        | HENRY HEWES                |       | KEITH JUDD |       | BERNIE SANDERS |         | MICHAEL STEINBERG |       |
| 200018.02-CP 200018.02 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 25             | 47.17%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 2          | 0.80% | 131            | 52.19%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 3              | 75.00%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 2          | 0.65% | 159            | 51.62%  | 0                 | 0.00% |
| 200020.02-CP 200020.02 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 30             | 61.22%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 77             | 42.08%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 6              | 100.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 113            | 47.48%  | 0                 | 0.00% |
| 200021.01-CP 200021.01 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 10             | 71.43%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 1.69% | 28             | 47.46%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 1              | 33.33%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 1.32% | 39             | 51.32%  | 0                 | 0.00% |
| 200021.02-CP 200021.02 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 27             | 45.76%  | 2                 | 3.39% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 102            | 40.48%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 3              | 60.00%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 132            | 41.77%  | 2                 | 0.63% |
| 200024.02-CP 200024.02 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 7              | 53.85%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 1.27% | 43             | 54.43%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 1              | 50.00%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 1.06% | 51             | 54.26%  | 0                 | 0.00% |
| 300001.04-CP 300001.04 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 3              | 50.00%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 30             | 53.57%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 33             | 53.23%  | 0                 | 0.00% |
| 300001.05-CP 300001.05 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 20             | 55.56%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 77             | 50.33%  | 1                 | 0.65% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 5              | 71.43%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 102            | 52.04%  | 1                 | 0.51% |
| 300003.05-CP 300003.05 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 64             | 62.75%  | 0                 | 0.00% |
| Precinct VBM           | 1                          | 0.23% | 0          | 0.00% | 213            | 48.63%  | 1                 | 0.23% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 13             | 72.22%  | 0                 | 0.00% |
| Total                  | 1                          | 0.18% | 0          | 0.00% | 290            | 51.97%  | 1                 | 0.18% |
| 300004.05-CP 300004.05 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 60             | 58.82%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 120            | 45.98%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |

**COUNTY OF EL DORADO**  
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|                        | President Democratic Party |       |            |       |                |         |                   |       |
|------------------------|----------------------------|-------|------------|-------|----------------|---------|-------------------|-------|
|                        | HENRY HEWES                |       | KEITH JUDD |       | BERNIE SANDERS |         | MICHAEL STEINBERG |       |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 7              | 70.00%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 187            | 50.13%  | 0                 | 0.00% |
| 300005.05-CP 300005.05 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 1          | 1.22% | 54             | 65.85%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 3          | 1.07% | 145            | 51.60%  | 1                 | 0.36% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 13             | 81.25%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 4          | 1.06% | 212            | 55.94%  | 1                 | 0.26% |
| 300008.05-CP 300008.05 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 29             | 55.77%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.38% | 131            | 49.81%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 4              | 100.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.31% | 164            | 51.41%  | 0                 | 0.00% |
| 300009.04-CP 300009.04 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 27             | 55.10%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.69% | 62             | 42.76%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 5              | 62.50%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.50% | 94             | 46.53%  | 0                 | 0.00% |
| 300010.04-CP 300010.04 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 1          | 1.85% | 29             | 53.70%  | 0                 | 0.00% |
| Precinct VBM           | 1                          | 0.40% | 0          | 0.00% | 84             | 33.20%  | 2                 | 0.79% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 4              | 80.00%  | 0                 | 0.00% |
| Total                  | 1                          | 0.32% | 1          | 0.32% | 117            | 37.50%  | 2                 | 0.64% |
| 300011.04-CP 300011.04 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 22             | 45.83%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.38% | 106            | 40.30%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 8              | 80.00%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.31% | 136            | 42.37%  | 0                 | 0.00% |
| 300013.05-CP 300013.05 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | 0.00% | 0          | 0.00% | 1              | 100.00% | 0                 | 0.00% |
| Precinct OS            | 1                          | 1.92% | 0          | 0.00% | 26             | 50.00%  | 0                 | 0.00% |
| Precinct VBM           | 2                          | 0.80% | 1          | 0.40% | 115            | 46.00%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 4              | 80.00%  | 0                 | 0.00% |
| Total                  | 3                          | 0.97% | 1          | 0.32% | 146            | 47.40%  | 0                 | 0.00% |
| 300015.04-CP 300015.04 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 0              | 0.00%   | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 11             | 57.89%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 2              | 100.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 13             | 56.52%  | 0                 | 0.00% |
| 300015.05-CP 300015.05 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 18             | 56.25%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 55             | 45.08%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 0              | 0.00%   | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 73             | 47.10%  | 0                 | 0.00% |
| 300017.05-CP 300017.05 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 18             | 40.91%  | 0                 | 0.00% |

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|                        | HENRY HEWES                |       | KEITH JUDD |       | BERNIE SANDERS |         | MICHAEL STEINBERG |       |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 119            | 47.60%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 4              | 100.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 141            | 47.32%  | 0                 | 0.00% |
| 300018.04-CP 300018.04 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 32             | 64.00%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.52% | 79             | 41.15%  | 2                 | 1.04% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 8              | 47.06%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.39% | 119            | 45.95%  | 2                 | 0.77% |
| 300020.04-CP 300020.04 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 2          | 2.74% | 42             | 57.53%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 2          | 0.89% | 99             | 44.00%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 1              | 100.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 4          | 1.34% | 142            | 47.49%  | 0                 | 0.00% |
| 300021.03-CP 300021.03 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 1                          | 1.45% | 0          | 0.00% | 41             | 59.42%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 112            | 46.67%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 3              | 75.00%  | 0                 | 0.00% |
| Total                  | 1                          | 0.32% | 0          | 0.00% | 156            | 49.84%  | 0                 | 0.00% |
| 300022.05-CP 300022.05 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 18             | 56.25%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 77             | 47.24%  | 1                 | 0.61% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 5              | 71.43%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 100            | 49.50%  | 1                 | 0.50% |
| 300023.03-CP 300023.03 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 44             | 48.89%  | 0                 | 0.00% |
| Precinct VBM           | 2                          | 0.60% | 0          | 0.00% | 154            | 46.53%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 8              | 66.67%  | 0                 | 0.00% |
| Total                  | 2                          | 0.46% | 0          | 0.00% | 206            | 47.58%  | 0                 | 0.00% |
| 300023.05-CP 300023.05 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 11             | 55.00%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 30             | 43.48%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 4              | 66.67%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 45             | 47.37%  | 0                 | 0.00% |
| 400001.14-CP 400001.14 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 44             | 62.86%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 145            | 39.62%  | 3                 | 0.82% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 9              | 75.00%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 198            | 44.20%  | 3                 | 0.67% |
| 400002.08-CP 400002.08 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 28             | 45.90%  | 1                 | 1.64% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.45% | 107            | 47.77%  | 4                 | 1.79% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 4              | 57.14%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.34% | 139            | 47.60%  | 5                 | 1.71% |
| 400003.08-CP 400003.08 |                            |       |            |       |                |         |                   |       |

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|                        | HENRY HEWES                |       | KEITH JUDD |       | BERNIE SANDERS |        | MICHAEL STEINBERG |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 48             | 60.76% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 137            | 49.46% | 1                 | 0.36% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 11             | 78.57% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 196            | 52.97% | 1                 | 0.27% |
| 400004.14-CP 400004.14 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 45             | 58.44% | 1                 | 1.30% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 122            | 48.80% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 4              | 66.67% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 171            | 51.35% | 1                 | 0.30% |
| 400005.08-CP 400005.08 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 16             | 50.00% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 58             | 58.59% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 2              | 50.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 76             | 56.30% | 0                 | 0.00% |
| 400006.08-CP 400006.08 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 1                          | 0.98% | 0          | 0.00% | 63             | 61.76% | 0                 | 0.00% |
| Precinct VBM           | 2                          | 0.65% | 1          | 0.33% | 115            | 37.46% | 3                 | 0.98% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 6              | 60.00% | 0                 | 0.00% |
| Total                  | 3                          | 0.72% | 1          | 0.24% | 184            | 43.91% | 3                 | 0.72% |
| 400007.08-CP 400007.08 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 40             | 55.56% | 1                 | 1.39% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 128            | 53.11% | 2                 | 0.83% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 5              | 71.43% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 173            | 54.06% | 3                 | 0.94% |
| 400010.08-CP 400010.08 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 63             | 59.43% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 2          | 0.54% | 179            | 48.51% | 2                 | 0.54% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 11             | 84.62% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 2          | 0.41% | 253            | 51.84% | 2                 | 0.41% |
| 400012.08-CP 400012.08 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 35             | 61.40% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.58% | 87             | 50.29% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 7              | 63.64% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.41% | 129            | 53.53% | 0                 | 0.00% |
| 400013.08-CP 400013.08 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 24             | 63.16% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 69             | 56.10% | 1                 | 0.81% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 1              | 50.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 94             | 57.67% | 1                 | 0.61% |
| 400014.08-CP 400014.08 |                            |       |            |       |                |        |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 51             | 56.67% | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 94             | 37.15% | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 7              | 70.00% | 0                 | 0.00% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                        | President Democratic Party |       |            |       |                |         |                   |       |
|------------------------|----------------------------|-------|------------|-------|----------------|---------|-------------------|-------|
|                        | HENRY HEWES                |       | KEITH JUDD |       | BERNIE SANDERS |         | MICHAEL STEINBERG |       |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 152            | 43.06%  | 0                 | 0.00% |
| 400017.08-CP 400017.08 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 1          | 1.54% | 37             | 56.92%  | 1                 | 1.54% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 99             | 43.61%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 10             | 90.91%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.33% | 146            | 48.18%  | 1                 | 0.33% |
| 400018.08-CP 400018.08 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 17             | 53.13%  | 1                 | 3.13% |
| Precinct VBM           | 1                          | 1.32% | 1          | 1.32% | 31             | 40.79%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 5              | 83.33%  | 0                 | 0.00% |
| Total                  | 1                          | 0.88% | 1          | 0.88% | 53             | 46.49%  | 1                 | 0.88% |
| 400020.08-CP 400020.08 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 20             | 55.56%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.75% | 70             | 52.63%  | 2                 | 1.50% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 2              | 100.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.58% | 92             | 53.80%  | 2                 | 1.17% |
| 400021.07-CP 400021.07 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 21             | 38.89%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.45% | 89             | 40.27%  | 1                 | 0.45% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 7              | 77.78%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.35% | 117            | 41.20%  | 1                 | 0.35% |
| 500001.10-CP 500001.10 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 82             | 58.16%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.29% | 164            | 47.95%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 14             | 77.78%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.20% | 260            | 51.90%  | 0                 | 0.00% |
| 500002.10-CP 500002.10 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 69             | 70.41%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 146            | 55.94%  | 1                 | 0.38% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 20             | 83.33%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 235            | 61.36%  | 1                 | 0.26% |
| 500003.10-CP 500003.10 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 67             | 66.34%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 124            | 60.19%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 7              | 87.50%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 198            | 62.86%  | 0                 | 0.00% |
| 500004.10-CP 500004.10 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 66             | 76.74%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 86             | 57.72%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 13             | 92.86%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 165            | 66.27%  | 0                 | 0.00% |
| 500005.10-CP 500005.10 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 52             | 66.67%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.63% | 88             | 55.70%  | 0                 | 0.00% |

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|                        | HENRY HEWES                |       | KEITH JUDD |       | BERNIE SANDERS |         | MICHAEL STEINBERG |       |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 12             | 100.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.40% | 152            | 61.29%  | 0                 | 0.00% |
| 500006.10-CP 500006.10 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 103            | 72.54%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 187            | 56.67%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 42             | 87.50%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 332            | 63.85%  | 0                 | 0.00% |
| 500007.10-CP 500007.10 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 1                          | 0.78% | 0          | 0.00% | 85             | 66.41%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 179            | 51.59%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 11             | 68.75%  | 0                 | 0.00% |
| Total                  | 1                          | 0.20% | 0          | 0.00% | 275            | 56.01%  | 0                 | 0.00% |
| 500009.10-CP 500009.10 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 68             | 67.33%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1          | 0.53% | 88             | 47.06%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 18             | 66.67%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 1          | 0.32% | 174            | 55.24%  | 0                 | 0.00% |
| 500011.10-CP 500011.10 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 1                          | 0.76% | 0          | 0.00% | 101            | 76.52%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 135            | 56.02%  | 1                 | 0.41% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 27             | 90.00%  | 0                 | 0.00% |
| Total                  | 1                          | 0.25% | 0          | 0.00% | 263            | 65.26%  | 1                 | 0.25% |
| 500012.10-CP 500012.10 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | 0.00% | 0          | 0.00% | 1              | 100.00% | 0                 | 0.00% |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 89             | 63.12%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 184            | 54.60%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 23             | 74.19%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 297            | 58.24%  | 0                 | 0.00% |
| 500015.10-CP 500015.10 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 39             | 52.70%  | 0                 | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0          | 0.00% | 114            | 46.53%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 7              | 77.78%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 160            | 48.78%  | 0                 | 0.00% |
| 500020.10-CP 500020.10 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 36             | 49.32%  | 0                 | 0.00% |
| Precinct VBM           | 2                          | 0.73% | 3          | 1.10% | 125            | 45.79%  | 1                 | 0.37% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 10             | 90.91%  | 0                 | 0.00% |
| Total                  | 2                          | 0.56% | 3          | 0.84% | 171            | 47.90%  | 1                 | 0.28% |
| 500021.10-CP 500021.10 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | 0.00% | 0          | 0.00% | 37             | 49.33%  | 0                 | 0.00% |
| Precinct VBM           | 1                          | 0.43% | 0          | 0.00% | 127            | 54.98%  | 0                 | 0.00% |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 7              | 58.33%  | 0                 | 0.00% |
| Total                  | 1                          | 0.31% | 0          | 0.00% | 171            | 53.77%  | 0                 | 0.00% |
| 95U201.01-MB 95U201.01 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |

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|                        | HENRY HEWES                |       | KEITH JUDD |        | BERNIE SANDERS |         | MICHAEL STEINBERG |       |
| Precinct OS            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | 0.00% | 1          | 33.33% | 2              | 66.67%  | 0                 | 0.00% |
| Provisional            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Total                  | 0                          | 0.00% | 1          | 33.33% | 2              | 66.67%  | 0                 | 0.00% |
| 95U201.02-MB 95U201.02 |                            |       |            |        |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | 0.00% | 0          | 0.00%  | 49             | 43.36%  | 0                 | 0.00% |
| Provisional            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 49             | 43.36%  | 0                 | 0.00% |
| 95U202.02-MB 95U202.02 |                            |       |            |        |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | 0.00% | 0          | 0.00%  | 121            | 54.02%  | 1                 | 0.45% |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 1              | 50.00%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 122            | 53.98%  | 1                 | 0.44% |
| 95U203.02-MB 95U203.02 |                            |       |            |        |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Provisional            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Total                  | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| 95U204.01-MB 95U204.01 |                            |       |            |        |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| MB Precinct            | 1                          | 0.49% | 0          | 0.00%  | 94             | 45.85%  | 0                 | 0.00% |
| Provisional            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Total                  | 1                          | 0.49% | 0          | 0.00%  | 94             | 45.85%  | 0                 | 0.00% |
| 95U301.05-MB 95U301.05 |                            |       |            |        |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | 0.00% | 0          | 0.00%  | 35             | 64.81%  | 0                 | 0.00% |
| Provisional            | 0                          | 0.00% | 0          | 0.00%  | 1              | 100.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 36             | 65.45%  | 0                 | 0.00% |
| 95U302.04-MB 95U302.04 |                            |       |            |        |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | 0.00% | 0          | 0.00%  | 0              | 0.00%   | 0                 | 0.00% |
| Provisional            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 0              | 0.00%   | 0                 | 0.00% |
| 95U302.05-MB 95U302.05 |                            |       |            |        |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | 0.00% | 0          | 0.00%  | 20             | 64.52%  | 0                 | 0.00% |
| Provisional            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 20             | 64.52%  | 0                 | 0.00% |
| 95U303.03-MB 95U303.03 |                            |       |            |        |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | 0.00% | 0          | 0.00%  | 20             | 52.63%  | 1                 | 2.63% |
| Provisional            | 0                          | -     | 0          | -      | 0              | -       | 0                 | -     |
| Total                  | 0                          | 0.00% | 0          | 0.00%  | 20             | 52.63%  | 1                 | 2.63% |



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|                        | HENRY HEWES                |       | KEITH JUDD |       | BERNIE SANDERS |         | MICHAEL STEINBERG |       |
| 95U303.05-MB 95U303.05 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | 0.00% | 0          | 0.00% | 13             | 37.14%  | 0                 | 0.00% |
| Provisional            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 13             | 37.14%  | 0                 | 0.00% |
| 95U304.05-MB 95U304.05 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Total                  | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| 95U305.05-MB 95U305.05 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | 0.00% | 0          | 0.00% | 1              | 100.00% | 0                 | 0.00% |
| Provisional            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 1              | 100.00% | 0                 | 0.00% |
| 95U401.08-MB 95U401.08 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | 0.00% | 0          | 0.00% | 3              | 37.50%  | 0                 | 0.00% |
| Provisional            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 3              | 37.50%  | 0                 | 0.00% |
| 95U402.07-MB 95U402.07 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | 0.00% | 0          | 0.00% | 55             | 55.00%  | 2                 | 2.00% |
| Provisional            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 55             | 55.00%  | 2                 | 2.00% |
| 95U402.08-MB 95U402.08 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | 0.00% | 0          | 0.00% | 91             | 48.40%  | 3                 | 1.60% |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 3              | 100.00% | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 94             | 49.21%  | 3                 | 1.57% |
| 95U403.08-MB 95U403.08 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | 0.00% | 0          | 0.00% | 249            | 53.09%  | 1                 | 0.21% |
| Provisional            | 0                          | 0.00% | 0          | 0.00% | 4              | 80.00%  | 0                 | 0.00% |
| Total                  | 0                          | 0.00% | 0          | 0.00% | 253            | 53.38%  | 1                 | 0.21% |
| 95U404.06-MB 95U404.06 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Provisional            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Total                  | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| 95U404.08-MB 95U404.08 |                            |       |            |       |                |         |                   |       |
| Precinct TSx           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM           | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct            | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                               | President Democratic Party |       |            |       |                |         |                   |       |
|-------------------------------|----------------------------|-------|------------|-------|----------------|---------|-------------------|-------|
|                               | HENRY HEWES                |       | KEITH JUDD |       | BERNIE SANDERS |         | MICHAEL STEINBERG |       |
| Provisional                   | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Total                         | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| 95U501.09-MB 95U501.09        |                            |       |            |       |                |         |                   |       |
| Precinct TSx                  | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS                   | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM                  | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct                   | 0                          | 0.00% | 0          | 0.00% | 2              | 100.00% | 0                 | 0.00% |
| Provisional                   | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Total                         | 0                          | 0.00% | 0          | 0.00% | 2              | 100.00% | 0                 | 0.00% |
| 95U501.10-MB 95U501.10        |                            |       |            |       |                |         |                   |       |
| Precinct TSx                  | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS                   | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM                  | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct                   | 0                          | 0.00% | 0          | 0.00% | 43             | 57.33%  | 0                 | 0.00% |
| Provisional                   | 0                          | 0.00% | 0          | 0.00% | 2              | 100.00% | 0                 | 0.00% |
| Total                         | 0                          | 0.00% | 0          | 0.00% | 45             | 58.44%  | 0                 | 0.00% |
| 95U598.10-MB 95U598.10        |                            |       |            |       |                |         |                   |       |
| Precinct TSx                  | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS                   | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM                  | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct                   | 0                          | 0.00% | 0          | 0.00% | 87             | 57.62%  | 0                 | 0.00% |
| Provisional                   | 0                          | 0.00% | 0          | 0.00% | 0              | 0.00%   | 0                 | 0.00% |
| Total                         | 0                          | 0.00% | 0          | 0.00% | 87             | 57.24%  | 0                 | 0.00% |
| 95U599.10-MB 95U599.10        |                            |       |            |       |                |         |                   |       |
| Precinct TSx                  | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS                   | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM                  | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct                   | 0                          | 0.00% | 0          | 0.00% | 7              | 31.82%  | 0                 | 0.00% |
| Provisional                   | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Total                         | 0                          | 0.00% | 0          | 0.00% | 7              | 31.82%  | 0                 | 0.00% |
| 96U101.11-MB 96U101.11        |                            |       |            |       |                |         |                   |       |
| Precinct TSx                  | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS                   | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM                  | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct                   | 0                          | 0.00% | 0          | 0.00% | 52             | 40.63%  | 0                 | 0.00% |
| Provisional                   | 0                          | 0.00% | 0          | 0.00% | 4              | 100.00% | 0                 | 0.00% |
| Total                         | 0                          | 0.00% | 0          | 0.00% | 56             | 42.42%  | 0                 | 0.00% |
| 96U401.14-MB 96U401.14        |                            |       |            |       |                |         |                   |       |
| Precinct TSx                  | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS                   | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct VBM                  | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| MB Precinct                   | 0                          | 0.00% | 0          | 0.00% | 66             | 50.77%  | 1                 | 0.77% |
| Provisional                   | 0                          | 0.00% | 0          | 0.00% | 5              | 100.00% | 0                 | 0.00% |
| Total                         | 0                          | 0.00% | 0          | 0.00% | 71             | 52.59%  | 1                 | 0.74% |
| Total                         |                            |       |            |       |                |         |                   |       |
| Precinct TSx                  | 0                          | 0.00% | 0          | 0.00% | 2              | 50.00%  | 0                 | 0.00% |
| Precinct OS                   | 7                          | 0.13% | 8          | 0.15% | 3074           | 55.93%  | 13                | 0.24% |
| Precinct VBM                  | 19                         | 0.11% | 36         | 0.20% | 7939           | 45.11%  | 53                | 0.30% |
| MB Precinct                   | 1                          | 0.05% | 1          | 0.05% | 1010           | 51.06%  | 9                 | 0.46% |
| Provisional                   | 0                          | 0.00% | 1          | 0.12% | 595            | 72.56%  | 0                 | 0.00% |
| Total                         | 27                         | 0.10% | 46         | 0.18% | 12620          | 48.73%  | 75                | 0.29% |
| STATE BOARD OF EQUALIZATION   |                            |       |            |       |                |         |                   |       |
| STATE BOARD OF EQUALIZATION 1 |                            |       |            |       |                |         |                   |       |
| Precinct TSx                  | 0                          | 0.00% | 0          | 0.00% | 2              | 50.00%  | 0                 | 0.00% |
| Precinct OS                   | 7                          | 0.13% | 8          | 0.15% | 3074           | 55.93%  | 13                | 0.24% |
| Precinct VBM                  | 19                         | 0.11% | 36         | 0.20% | 7939           | 45.11%  | 53                | 0.30% |
| MB Precinct                   | 1                          | 0.05% | 1          | 0.05% | 1010           | 51.06%  | 9                 | 0.46% |
| Provisional                   | 0                          | 0.00% | 1          | 0.12% | 595            | 72.56%  | 0                 | 0.00% |
| Total                         | 27                         | 0.10% | 46         | 0.18% | 12620          | 48.73%  | 75                | 0.29% |
| Total                         |                            |       |            |       |                |         |                   |       |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                     | President Democratic Party |              |            |              |                |               |                   |              |
|-------------------------------------|----------------------------|--------------|------------|--------------|----------------|---------------|-------------------|--------------|
|                                     | HENRY HEWES                |              | KEITH JUDD |              | BERNIE SANDERS |               | MICHAEL STEINBERG |              |
| Precinct TSx                        | 0                          | 0.00%        | 0          | 0.00%        | 2              | 50.00%        | 0                 | 0.00%        |
| Precinct OS                         | 7                          | 0.13%        | 8          | 0.15%        | 3074           | 55.93%        | 13                | 0.24%        |
| Precinct VBM                        | 19                         | 0.11%        | 36         | 0.20%        | 7939           | 45.11%        | 53                | 0.30%        |
| MB Precinct                         | 1                          | 0.05%        | 1          | 0.05%        | 1010           | 51.06%        | 9                 | 0.46%        |
| Provisional                         | 0                          | 0.00%        | 1          | 0.12%        | 595            | 72.56%        | 0                 | 0.00%        |
| <b>Total</b>                        | <b>27</b>                  | <b>0.10%</b> | <b>46</b>  | <b>0.18%</b> | <b>12620</b>   | <b>48.73%</b> | <b>75</b>         | <b>0.29%</b> |
| <b>UNITED STATES SENATE</b>         |                            |              |            |              |                |               |                   |              |
| <b>UNITED STATES SENATE</b>         |                            |              |            |              |                |               |                   |              |
| Precinct TSx                        | 0                          | 0.00%        | 0          | 0.00%        | 2              | 50.00%        | 0                 | 0.00%        |
| Precinct OS                         | 7                          | 0.13%        | 8          | 0.15%        | 3074           | 55.93%        | 13                | 0.24%        |
| Precinct VBM                        | 19                         | 0.11%        | 36         | 0.20%        | 7939           | 45.11%        | 53                | 0.30%        |
| MB Precinct                         | 1                          | 0.05%        | 1          | 0.05%        | 1010           | 51.06%        | 9                 | 0.46%        |
| Provisional                         | 0                          | 0.00%        | 1          | 0.12%        | 595            | 72.56%        | 0                 | 0.00%        |
| <b>Total</b>                        | <b>27</b>                  | <b>0.10%</b> | <b>46</b>  | <b>0.18%</b> | <b>12620</b>   | <b>48.73%</b> | <b>75</b>         | <b>0.29%</b> |
| <b>Total</b>                        |                            |              |            |              |                |               |                   |              |
| Precinct TSx                        | 0                          | 0.00%        | 0          | 0.00%        | 2              | 50.00%        | 0                 | 0.00%        |
| Precinct OS                         | 7                          | 0.13%        | 8          | 0.15%        | 3074           | 55.93%        | 13                | 0.24%        |
| Precinct VBM                        | 19                         | 0.11%        | 36         | 0.20%        | 7939           | 45.11%        | 53                | 0.30%        |
| MB Precinct                         | 1                          | 0.05%        | 1          | 0.05%        | 1010           | 51.06%        | 9                 | 0.46%        |
| Provisional                         | 0                          | 0.00%        | 1          | 0.12%        | 595            | 72.56%        | 0                 | 0.00%        |
| <b>Total</b>                        | <b>27</b>                  | <b>0.10%</b> | <b>46</b>  | <b>0.18%</b> | <b>12620</b>   | <b>48.73%</b> | <b>75</b>         | <b>0.29%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |                            |              |            |              |                |               |                   |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                            |              |            |              |                |               |                   |              |
| Precinct TSx                        | 0                          | 0.00%        | 0          | 0.00%        | 2              | 50.00%        | 0                 | 0.00%        |
| Precinct OS                         | 7                          | 0.13%        | 8          | 0.15%        | 3074           | 55.93%        | 13                | 0.24%        |
| Precinct VBM                        | 19                         | 0.11%        | 36         | 0.20%        | 7939           | 45.11%        | 53                | 0.30%        |
| MB Precinct                         | 1                          | 0.05%        | 1          | 0.05%        | 1010           | 51.06%        | 9                 | 0.46%        |
| Provisional                         | 0                          | 0.00%        | 1          | 0.12%        | 595            | 72.56%        | 0                 | 0.00%        |
| <b>Total</b>                        | <b>27</b>                  | <b>0.10%</b> | <b>46</b>  | <b>0.18%</b> | <b>12620</b>   | <b>48.73%</b> | <b>75</b>         | <b>0.29%</b> |
| <b>Total</b>                        |                            |              |            |              |                |               |                   |              |
| Precinct TSx                        | 0                          | 0.00%        | 0          | 0.00%        | 2              | 50.00%        | 0                 | 0.00%        |
| Precinct OS                         | 7                          | 0.13%        | 8          | 0.15%        | 3074           | 55.93%        | 13                | 0.24%        |
| Precinct VBM                        | 19                         | 0.11%        | 36         | 0.20%        | 7939           | 45.11%        | 53                | 0.30%        |
| MB Precinct                         | 1                          | 0.05%        | 1          | 0.05%        | 1010           | 51.06%        | 9                 | 0.46%        |
| Provisional                         | 0                          | 0.00%        | 1          | 0.12%        | 595            | 72.56%        | 0                 | 0.00%        |
| <b>Total</b>                        | <b>27</b>                  | <b>0.10%</b> | <b>46</b>  | <b>0.18%</b> | <b>12620</b>   | <b>48.73%</b> | <b>75</b>         | <b>0.29%</b> |
| <b>STATE SENATE</b>                 |                            |              |            |              |                |               |                   |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                            |              |            |              |                |               |                   |              |
| Precinct TSx                        | 0                          | 0.00%        | 0          | 0.00%        | 2              | 50.00%        | 0                 | 0.00%        |
| Precinct OS                         | 7                          | 0.13%        | 8          | 0.15%        | 3074           | 55.93%        | 13                | 0.24%        |
| Precinct VBM                        | 19                         | 0.11%        | 36         | 0.20%        | 7939           | 45.11%        | 53                | 0.30%        |
| MB Precinct                         | 1                          | 0.05%        | 1          | 0.05%        | 1010           | 51.06%        | 9                 | 0.46%        |
| Provisional                         | 0                          | 0.00%        | 1          | 0.12%        | 595            | 72.56%        | 0                 | 0.00%        |
| <b>Total</b>                        | <b>27</b>                  | <b>0.10%</b> | <b>46</b>  | <b>0.18%</b> | <b>12620</b>   | <b>48.73%</b> | <b>75</b>         | <b>0.29%</b> |
| <b>Total</b>                        |                            |              |            |              |                |               |                   |              |
| Precinct TSx                        | 0                          | 0.00%        | 0          | 0.00%        | 2              | 50.00%        | 0                 | 0.00%        |
| Precinct OS                         | 7                          | 0.13%        | 8          | 0.15%        | 3074           | 55.93%        | 13                | 0.24%        |
| Precinct VBM                        | 19                         | 0.11%        | 36         | 0.20%        | 7939           | 45.11%        | 53                | 0.30%        |
| MB Precinct                         | 1                          | 0.05%        | 1          | 0.05%        | 1010           | 51.06%        | 9                 | 0.46%        |
| Provisional                         | 0                          | 0.00%        | 1          | 0.12%        | 595            | 72.56%        | 0                 | 0.00%        |
| <b>Total</b>                        | <b>27</b>                  | <b>0.10%</b> | <b>46</b>  | <b>0.18%</b> | <b>12620</b>   | <b>48.73%</b> | <b>75</b>         | <b>0.29%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                            |              |            |              |                |               |                   |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                            |              |            |              |                |               |                   |              |
| Precinct TSx                        | 0                          | 0.00%        | 0          | 0.00%        | 2              | 100.00%       | 0                 | 0.00%        |
| Precinct OS                         | 5                          | 0.14%        | 6          | 0.17%        | 2132           | 59.39%        | 7                 | 0.19%        |
| Precinct VBM                        | 12                         | 0.10%        | 28         | 0.24%        | 5615           | 48.47%        | 31                | 0.27%        |
| MB Precinct                         | 1                          | 0.06%        | 1          | 0.06%        | 892            | 51.86%        | 8                 | 0.47%        |

**COUNTY OF EL DORADO**  
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|                                   | President Democratic Party |       |            |       |                |         |                   |       |
|-----------------------------------|----------------------------|-------|------------|-------|----------------|---------|-------------------|-------|
|                                   | HENRY HEWES                |       | KEITH JUDD |       | BERNIE SANDERS |         | MICHAEL STEINBERG |       |
| Provisional                       | 0                          | 0.00% | 0          | 0.00% | 431            | 76.15%  | 0                 | 0.00% |
| Total                             | 18                         | 0.10% | 35         | 0.20% | 9072           | 51.95%  | 46                | 0.26% |
| <b>6TH STATE ASSEMBLY DIST</b>    |                            |       |            |       |                |         |                   |       |
| Precinct TSx                      | 0                          | 0.00% | 0          | 0.00% | 0              | 0.00%   | 0                 | 0.00% |
| Precinct OS                       | 2                          | 0.10% | 2          | 0.10% | 942            | 49.42%  | 6                 | 0.31% |
| Precinct VBM                      | 7                          | 0.12% | 8          | 0.13% | 2324           | 38.65%  | 22                | 0.37% |
| MB Precinct                       | 0                          | 0.00% | 0          | 0.00% | 118            | 45.74%  | 1                 | 0.39% |
| Provisional                       | 0                          | 0.00% | 1          | 0.39% | 164            | 64.57%  | 0                 | 0.00% |
| Total                             | 9                          | 0.11% | 11         | 0.13% | 3548           | 42.07%  | 29                | 0.34% |
| <b>Total</b>                      |                            |       |            |       |                |         |                   |       |
| Precinct TSx                      | 0                          | 0.00% | 0          | 0.00% | 2              | 50.00%  | 0                 | 0.00% |
| Precinct OS                       | 7                          | 0.13% | 8          | 0.15% | 3074           | 55.93%  | 13                | 0.24% |
| Precinct VBM                      | 19                         | 0.11% | 36         | 0.20% | 7939           | 45.11%  | 53                | 0.30% |
| MB Precinct                       | 1                          | 0.05% | 1          | 0.05% | 1010           | 51.06%  | 9                 | 0.46% |
| Provisional                       | 0                          | 0.00% | 1          | 0.12% | 595            | 72.56%  | 0                 | 0.00% |
| Total                             | 27                         | 0.10% | 46         | 0.18% | 12620          | 48.73%  | 75                | 0.29% |
| <b>SUPERVISORIAL DISTRICTS</b>    |                            |       |            |       |                |         |                   |       |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                            |       |            |       |                |         |                   |       |
| Precinct TSx                      | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS                       | 2                          | 0.18% | 0          | 0.00% | 526            | 46.67%  | 3                 | 0.27% |
| Precinct VBM                      | 3                          | 0.09% | 4          | 0.11% | 1245           | 35.38%  | 13                | 0.37% |
| MB Precinct                       | 0                          | 0.00% | 0          | 0.00% | 52             | 40.63%  | 0                 | 0.00% |
| Provisional                       | 0                          | 0.00% | 1          | 0.74% | 81             | 59.56%  | 0                 | 0.00% |
| Total                             | 5                          | 0.10% | 5          | 0.10% | 1904           | 38.78%  | 16                | 0.33% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                            |       |            |       |                |         |                   |       |
| Precinct TSx                      | 0                          | 0.00% | 0          | 0.00% | 0              | 0.00%   | 0                 | 0.00% |
| Precinct OS                       | 0                          | 0.00% | 3          | 0.29% | 544            | 52.66%  | 5                 | 0.48% |
| Precinct VBM                      | 4                          | 0.12% | 8          | 0.23% | 1598           | 46.20%  | 10                | 0.29% |
| MB Precinct                       | 1                          | 0.18% | 1          | 0.18% | 266            | 48.81%  | 1                 | 0.18% |
| Provisional                       | 0                          | 0.00% | 0          | 0.00% | 99             | 67.81%  | 0                 | 0.00% |
| Total                             | 5                          | 0.10% | 12         | 0.23% | 2507           | 48.35%  | 16                | 0.31% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                            |       |            |       |                |         |                   |       |
| Precinct TSx                      | 0                          | 0.00% | 0          | 0.00% | 1              | 100.00% | 0                 | 0.00% |
| Precinct OS                       | 2                          | 0.20% | 4          | 0.40% | 558            | 56.08%  | 0                 | 0.00% |
| Precinct VBM                      | 6                          | 0.15% | 10         | 0.25% | 1819           | 45.77%  | 8                 | 0.20% |
| MB Precinct                       | 0                          | 0.00% | 0          | 0.00% | 89             | 55.63%  | 1                 | 0.63% |
| Provisional                       | 0                          | 0.00% | 0          | 0.00% | 99             | 71.74%  | 0                 | 0.00% |
| Total                             | 8                          | 0.15% | 14         | 0.27% | 2566           | 48.71%  | 9                 | 0.17% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                            |       |            |       |                |         |                   |       |
| Precinct TSx                      | 0                          | -     | 0          | -     | 0              | -       | 0                 | -     |
| Precinct OS                       | 1                          | 0.10% | 1          | 0.10% | 552            | 56.85%  | 5                 | 0.51% |
| Precinct VBM                      | 3                          | 0.09% | 8          | 0.24% | 1530           | 45.82%  | 19                | 0.57% |
| MB Precinct                       | 0                          | 0.00% | 0          | 0.00% | 464            | 51.84%  | 7                 | 0.78% |
| Provisional                       | 0                          | 0.00% | 0          | 0.00% | 103            | 75.18%  | 0                 | 0.00% |
| Total                             | 4                          | 0.07% | 9          | 0.17% | 2649           | 49.59%  | 31                | 0.58% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                            |       |            |       |                |         |                   |       |
| Precinct TSx                      | 0                          | 0.00% | 0          | 0.00% | 1              | 100.00% | 0                 | 0.00% |
| Precinct OS                       | 2                          | 0.15% | 0          | 0.00% | 894            | 65.26%  | 0                 | 0.00% |
| Precinct VBM                      | 3                          | 0.09% | 6          | 0.18% | 1747           | 52.83%  | 3                 | 0.09% |
| MB Precinct                       | 0                          | 0.00% | 0          | 0.00% | 139            | 55.60%  | 0                 | 0.00% |
| Provisional                       | 0                          | 0.00% | 0          | 0.00% | 213            | 80.99%  | 0                 | 0.00% |
| Total                             | 5                          | 0.10% | 6          | 0.12% | 2994           | 57.68%  | 3                 | 0.06% |
| <b>Total</b>                      |                            |       |            |       |                |         |                   |       |
| Precinct TSx                      | 0                          | 0.00% | 0          | 0.00% | 2              | 50.00%  | 0                 | 0.00% |
| Precinct OS                       | 7                          | 0.13% | 8          | 0.15% | 3074           | 55.93%  | 13                | 0.24% |
| Precinct VBM                      | 19                         | 0.11% | 36         | 0.20% | 7939           | 45.11%  | 53                | 0.30% |
| MB Precinct                       | 1                          | 0.05% | 1          | 0.05% | 1010           | 51.06%  | 9                 | 0.46% |
| Provisional                       | 0                          | 0.00% | 1          | 0.12% | 595            | 72.56%  | 0                 | 0.00% |
| Total                             | 27                         | 0.10% | 46         | 0.18% | 12620          | 48.73%  | 75                | 0.29% |

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|                                 | President Democratic Party |       |            |       |                |        |                   |       |
|---------------------------------|----------------------------|-------|------------|-------|----------------|--------|-------------------|-------|
|                                 | HENRY HEWES                |       | KEITH JUDD |       | BERNIE SANDERS |        | MICHAEL STEINBERG |       |
| <b>CITIES</b>                   |                            |       |            |       |                |        |                   |       |
| <b>CITY OF PLACERVILLE</b>      |                            |       |            |       |                |        |                   |       |
| Precinct TSx                    | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS                     | 0                          | 0.00% | 1          | 0.30% | 201            | 61.28% | 0                 | 0.00% |
| Precinct VBM                    | 1                          | 0.08% | 3          | 0.25% | 585            | 49.20% | 3                 | 0.25% |
| MB Precinct                     | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional                     | 0                          | 0.00% | 0          | 0.00% | 38             | 74.51% | 0                 | 0.00% |
| Total                           | 1                          | 0.06% | 4          | 0.26% | 824            | 52.55% | 3                 | 0.19% |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                            |       |            |       |                |        |                   |       |
| Precinct TSx                    | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Precinct OS                     | 2                          | 0.26% | 0          | 0.00% | 544            | 71.11% | 0                 | 0.00% |
| Precinct VBM                    | 0                          | 0.00% | 2          | 0.12% | 909            | 54.33% | 2                 | 0.12% |
| MB Precinct                     | 0                          | -     | 0          | -     | 0              | -      | 0                 | -     |
| Provisional                     | 0                          | 0.00% | 0          | 0.00% | 143            | 83.63% | 0                 | 0.00% |
| Total                           | 2                          | 0.08% | 2          | 0.08% | 1596           | 61.17% | 2                 | 0.08% |
| <b>UNINCORPORATED AREA</b>      |                            |       |            |       |                |        |                   |       |
| Precinct TSx                    | 0                          | 0.00% | 0          | 0.00% | 2              | 50.00% | 0                 | 0.00% |
| Precinct OS                     | 5                          | 0.11% | 7          | 0.16% | 2329           | 52.90% | 13                | 0.30% |
| Precinct VBM                    | 18                         | 0.12% | 31         | 0.21% | 6445           | 43.74% | 48                | 0.33% |
| MB Precinct                     | 1                          | 0.05% | 1          | 0.05% | 1010           | 51.06% | 9                 | 0.46% |
| Provisional                     | 0                          | 0.00% | 1          | 0.17% | 414            | 69.23% | 0                 | 0.00% |
| Total                           | 24                         | 0.11% | 40         | 0.18% | 10200          | 46.96% | 70                | 0.32% |
| <b>Total</b>                    |                            |       |            |       |                |        |                   |       |
| Precinct TSx                    | 0                          | 0.00% | 0          | 0.00% | 2              | 50.00% | 0                 | 0.00% |
| Precinct OS                     | 7                          | 0.13% | 8          | 0.15% | 3074           | 55.93% | 13                | 0.24% |
| Precinct VBM                    | 19                         | 0.11% | 36         | 0.20% | 7939           | 45.11% | 53                | 0.30% |
| MB Precinct                     | 1                          | 0.05% | 1          | 0.05% | 1010           | 51.06% | 9                 | 0.46% |
| Provisional                     | 0                          | 0.00% | 1          | 0.12% | 595            | 72.56% | 0                 | 0.00% |
| Total                           | 27                         | 0.10% | 46         | 0.18% | 12620          | 48.73% | 75                | 0.29% |

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|------------------------|-------------------------------|
|                        | WILLIE WILSON                 |
| Jurisdiction Wide      |                               |
| 100001.11-CP 100001.11 |                               |
| Precinct TSx           | 0 -                           |
| Precinct OS            | 0 0.00%                       |
| Precinct VBM           | 0 0.00%                       |
| MB Precinct            | 0 -                           |
| Provisional            | 0 0.00%                       |
| Total                  | 0 0.00%                       |
| 100002.11-CP 100002.11 |                               |
| Precinct TSx           | 0 -                           |
| Precinct OS            | 0 0.00%                       |
| Precinct VBM           | 1 0.63%                       |
| MB Precinct            | 0 -                           |
| Provisional            | 0 0.00%                       |
| Total                  | 1 0.47%                       |
| 100003.11-CP 100003.11 |                               |
| Precinct TSx           | 0 -                           |
| Precinct OS            | 0 0.00%                       |
| Precinct VBM           | 0 0.00%                       |
| MB Precinct            | 0 -                           |
| Provisional            | 0 0.00%                       |
| Total                  | 0 0.00%                       |
| 100004.11-CP 100004.11 |                               |
| Precinct TSx           | 0 -                           |
| Precinct OS            | 0 0.00%                       |
| Precinct VBM           | 0 0.00%                       |
| MB Precinct            | 0 -                           |
| Provisional            | 0 0.00%                       |
| Total                  | 0 0.00%                       |
| 100005.11-CP 100005.11 |                               |
| Precinct TSx           | 0 -                           |
| Precinct OS            | 0 0.00%                       |
| Precinct VBM           | 1 0.43%                       |
| MB Precinct            | 0 -                           |
| Provisional            | 0 0.00%                       |
| Total                  | 1 0.34%                       |
| 100006.11-CP 100006.11 |                               |
| Precinct TSx           | 0 -                           |
| Precinct OS            | 0 0.00%                       |
| Precinct VBM           | 1 0.28%                       |
| MB Precinct            | 0 -                           |
| Provisional            | 0 0.00%                       |
| Total                  | 1 0.21%                       |
| 100008.11-CP 100008.11 |                               |
| Precinct TSx           | 0 -                           |
| Precinct OS            | 0 0.00%                       |
| Precinct VBM           | 0 0.00%                       |
| MB Precinct            | 0 -                           |
| Provisional            | 0 0.00%                       |
| Total                  | 0 0.00%                       |
| 100009.11-CP 100009.11 |                               |
| Precinct TSx           | 0 -                           |
| Precinct OS            | 0 0.00%                       |
| Precinct VBM           | 0 0.00%                       |
| MB Precinct            | 0 -                           |
| Provisional            | 0 0.00%                       |
| Total                  | 0 0.00%                       |
| 100012.11-CP 100012.11 |                               |
| Precinct TSx           | 0 -                           |
| Precinct OS            | 0 0.00%                       |
| Precinct VBM           | 2 0.59%                       |

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|------------------------|-------------------------------|-------|
|                        | WILLIE WILSON                 |       |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 2                             | 0.43% |
| 100013.11-CP 100013.11 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 100014.11-CP 100014.11 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 1                             | 0.45% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 0.31% |
| 100022.11-CP 100022.11 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 100023.11-CP 100023.11 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 200003.12-CP 200003.12 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 4                             | 1.93% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 4                             | 1.33% |
| 200004.12-CP 200004.12 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 200005.02-CP 200005.02 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 3                             | 1.83% |
| MB Precinct            | 0                             | -     |
| Provisional            | 1                             | 6.67% |
| Total                  | 4                             | 1.77% |
| 200006.12-CP 200006.12 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 1                             | 2.78% |
| Precinct VBM           | 1                             | 0.60% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 2                             | 0.92% |
| 200008.12-CP 200008.12 |                               |       |
| Precinct TSx           | 0                             | -     |

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|------------------------|-------------------------------|-------|
|                        | WILLIE WILSON                 |       |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 1                             | 0.65% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 0.45% |
| 200010.12-CP 200010.12 |                               |       |
| Precinct TSx           | 0                             | 0.00% |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 200012.12-CP 200012.12 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 200013.12-CP 200013.12 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 200014.12-CP 200014.12 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 200015.02-CP 200015.02 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 1                             | 0.93% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 0.22% |
| 200016.12-CP 200016.12 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 200016.13-CP 200016.13 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | -     |
| Total                  | 0                             | 0.00% |
| 200017.02-CP 200017.02 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |



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|                        | WILLIE WILSON                 |       |
| 200018.02-CP 200018.02 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 4                             | 1.59% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 4                             | 1.30% |
| 200020.02-CP 200020.02 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 2                             | 1.09% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 2                             | 0.84% |
| 200021.01-CP 200021.01 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 1                             | 7.14% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 1.32% |
| 200021.02-CP 200021.02 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 200024.02-CP 200024.02 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 1                             | 7.69% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 1.06% |
| 300001.04-CP 300001.04 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 3                             | 5.36% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | -     |
| Total                  | 3                             | 4.84% |
| 300001.05-CP 300001.05 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 300003.05-CP 300003.05 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 1                             | 0.98% |
| Precinct VBM           | 4                             | 0.91% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 5                             | 0.90% |
| 300004.05-CP 300004.05 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 2                             | 1.96% |
| Precinct VBM           | 1                             | 0.38% |
| MB Precinct            | 0                             | -     |

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|------------------------|-------------------------------|-------|
|                        | WILLIE WILSON                 |       |
| Provisional            | 0                             | 0.00% |
| Total                  | 3                             | 0.80% |
| 300005.05-CP 300005.05 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 2                             | 0.71% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 2                             | 0.53% |
| 300008.05-CP 300008.05 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 2                             | 3.85% |
| Precinct VBM           | 4                             | 1.52% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 6                             | 1.88% |
| 300009.04-CP 300009.04 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 1                             | 0.69% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 0.50% |
| 300010.04-CP 300010.04 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 2                             | 0.79% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 2                             | 0.64% |
| 300011.04-CP 300011.04 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 1                             | 0.38% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 0.31% |
| 300013.05-CP 300013.05 |                               |       |
| Precinct TSx           | 0                             | 0.00% |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 3                             | 1.20% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 3                             | 0.97% |
| 300015.04-CP 300015.04 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 300015.05-CP 300015.05 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 300017.05-CP 300017.05 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |

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|                        | President Democratic<br>Party |       |
|------------------------|-------------------------------|-------|
|                        | WILLIE WILSON                 |       |
| Precinct VBM           | 1                             | 0.40% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 0.34% |
| 300018.04-CP 300018.04 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 1                             | 2.00% |
| Precinct VBM           | 1                             | 0.52% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 2                             | 0.77% |
| 300020.04-CP 300020.04 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 1                             | 1.37% |
| Precinct VBM           | 2                             | 0.89% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 3                             | 1.00% |
| 300021.03-CP 300021.03 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 1                             | 0.42% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 0.32% |
| 300022.05-CP 300022.05 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 2                             | 1.23% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 2                             | 0.99% |
| 300023.03-CP 300023.03 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 2                             | 0.60% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 2                             | 0.46% |
| 300023.05-CP 300023.05 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 400001.14-CP 400001.14 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 3                             | 0.82% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 3                             | 0.67% |
| 400002.08-CP 400002.08 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 1                             | 0.45% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 0.34% |
| 400003.08-CP 400003.08 |                               |       |

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|------------------------|-------------------------------|--------|
|                        | WILLIE WILSON                 |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 1                             | 1.27%  |
| Precinct VBM           | 3                             | 1.08%  |
| MB Precinct            | 0                             | -      |
| Provisional            | 0                             | 0.00%  |
| Total                  | 4                             | 1.08%  |
| 400004.14-CP 400004.14 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 1                             | 1.30%  |
| Precinct VBM           | 1                             | 0.40%  |
| MB Precinct            | 0                             | -      |
| Provisional            | 0                             | 0.00%  |
| Total                  | 2                             | 0.60%  |
| 400005.08-CP 400005.08 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 1                             | 3.13%  |
| Precinct VBM           | 0                             | 0.00%  |
| MB Precinct            | 0                             | -      |
| Provisional            | 1                             | 25.00% |
| Total                  | 2                             | 1.48%  |
| 400006.08-CP 400006.08 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | 0.00%  |
| Precinct VBM           | 2                             | 0.65%  |
| MB Precinct            | 0                             | -      |
| Provisional            | 0                             | 0.00%  |
| Total                  | 2                             | 0.48%  |
| 400007.08-CP 400007.08 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | 0.00%  |
| Precinct VBM           | 2                             | 0.83%  |
| MB Precinct            | 0                             | -      |
| Provisional            | 1                             | 14.29% |
| Total                  | 3                             | 0.94%  |
| 400010.08-CP 400010.08 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | 0.00%  |
| Precinct VBM           | 7                             | 1.90%  |
| MB Precinct            | 0                             | -      |
| Provisional            | 0                             | 0.00%  |
| Total                  | 7                             | 1.43%  |
| 400012.08-CP 400012.08 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | 0.00%  |
| Precinct VBM           | 0                             | 0.00%  |
| MB Precinct            | 0                             | -      |
| Provisional            | 0                             | 0.00%  |
| Total                  | 0                             | 0.00%  |
| 400013.08-CP 400013.08 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | 0.00%  |
| Precinct VBM           | 1                             | 0.81%  |
| MB Precinct            | 0                             | -      |
| Provisional            | 0                             | 0.00%  |
| Total                  | 1                             | 0.61%  |
| 400014.08-CP 400014.08 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | 0.00%  |
| Precinct VBM           | 0                             | 0.00%  |
| MB Precinct            | 0                             | -      |
| Provisional            | 0                             | 0.00%  |

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|------------------------|-------------------------------|-------|
|                        | WILLIE WILSON                 |       |
| Total                  | 0                             | 0.00% |
| 400017.08-CP 400017.08 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 1                             | 1.54% |
| Precinct VBM           | 3                             | 1.32% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 4                             | 1.32% |
| 400018.08-CP 400018.08 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 1                             | 3.13% |
| Precinct VBM           | 1                             | 1.32% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 2                             | 1.75% |
| 400020.08-CP 400020.08 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 1                             | 0.75% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 0.58% |
| 400021.07-CP 400021.07 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 1                             | 1.85% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 0.35% |
| 500001.10-CP 500001.10 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 1                             | 0.29% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 0.20% |
| 500002.10-CP 500002.10 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 1                             | 1.02% |
| Precinct VBM           | 1                             | 0.38% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 2                             | 0.52% |
| 500003.10-CP 500003.10 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 1                             | 0.49% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 0.32% |
| 500004.10-CP 500004.10 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 500005.10-CP 500005.10 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 1                             | 1.28% |
| Precinct VBM           | 0                             | 0.00% |

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|------------------------|-------------------------------|-------|
|                        | WILLIE WILSON                 |       |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 0.40% |
| 500006.10-CP 500006.10 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 2                             | 0.61% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 2                             | 0.38% |
| 500007.10-CP 500007.10 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 1                             | 0.78% |
| Precinct VBM           | 3                             | 0.86% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 4                             | 0.81% |
| 500009.10-CP 500009.10 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 2                             | 1.07% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 2                             | 0.63% |
| 500011.10-CP 500011.10 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 500012.10-CP 500012.10 |                               |       |
| Precinct TSx           | 0                             | 0.00% |
| Precinct OS            | 1                             | 0.71% |
| Precinct VBM           | 3                             | 0.89% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 4                             | 0.78% |
| 500015.10-CP 500015.10 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 500020.10-CP 500020.10 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | 0.00% |
| Precinct VBM           | 3                             | 1.10% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 3                             | 0.84% |
| 500021.10-CP 500021.10 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 1                             | 1.33% |
| Precinct VBM           | 0                             | 0.00% |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | 0.00% |
| Total                  | 1                             | 0.31% |
| 95U201.01-MB 95U201.01 |                               |       |
| Precinct TSx           | 0                             | -     |

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|------------------------|-------------------------------|-------|
|                        | WILLIE WILSON                 |       |
| Precinct OS            | 0                             | -     |
| Precinct VBM           | 0                             | -     |
| MB Precinct            | 0                             | 0.00% |
| Provisional            | 0                             | -     |
| Total                  | 0                             | 0.00% |
| 95U201.02-MB 95U201.02 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | -     |
| Precinct VBM           | 0                             | -     |
| MB Precinct            | 1                             | 0.88% |
| Provisional            | 0                             | -     |
| Total                  | 1                             | 0.88% |
| 95U202.02-MB 95U202.02 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | -     |
| Precinct VBM           | 0                             | -     |
| MB Precinct            | 3                             | 1.34% |
| Provisional            | 0                             | 0.00% |
| Total                  | 3                             | 1.33% |
| 95U203.02-MB 95U203.02 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | -     |
| Precinct VBM           | 0                             | -     |
| MB Precinct            | 0                             | -     |
| Provisional            | 0                             | -     |
| Total                  | 0                             | -     |
| 95U204.01-MB 95U204.01 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | -     |
| Precinct VBM           | 0                             | -     |
| MB Precinct            | 0                             | 0.00% |
| Provisional            | 0                             | -     |
| Total                  | 0                             | 0.00% |
| 95U301.05-MB 95U301.05 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | -     |
| Precinct VBM           | 0                             | -     |
| MB Precinct            | 0                             | 0.00% |
| Provisional            | 0                             | 0.00% |
| Total                  | 0                             | 0.00% |
| 95U302.04-MB 95U302.04 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | -     |
| Precinct VBM           | 0                             | -     |
| MB Precinct            | 0                             | 0.00% |
| Provisional            | 0                             | -     |
| Total                  | 0                             | 0.00% |
| 95U302.05-MB 95U302.05 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | -     |
| Precinct VBM           | 0                             | -     |
| MB Precinct            | 0                             | 0.00% |
| Provisional            | 0                             | -     |
| Total                  | 0                             | 0.00% |
| 95U303.03-MB 95U303.03 |                               |       |
| Precinct TSx           | 0                             | -     |
| Precinct OS            | 0                             | -     |
| Precinct VBM           | 0                             | -     |
| MB Precinct            | 3                             | 7.89% |
| Provisional            | 0                             | -     |
| Total                  | 3                             | 7.89% |

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|                        | WILLIE WILSON                 |        |
| 95U303.05-MB 95U303.05 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | -      |
| Precinct VBM           | 0                             | -      |
| MB Precinct            | 1                             | 2.86%  |
| Provisional            | 0                             | -      |
| Total                  | 1                             | 2.86%  |
| 95U304.05-MB 95U304.05 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | -      |
| Precinct VBM           | 0                             | -      |
| MB Precinct            | 0                             | -      |
| Provisional            | 0                             | -      |
| Total                  | 0                             | -      |
| 95U305.05-MB 95U305.05 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | -      |
| Precinct VBM           | 0                             | -      |
| MB Precinct            | 0                             | 0.00%  |
| Provisional            | 0                             | -      |
| Total                  | 0                             | 0.00%  |
| 95U401.08-MB 95U401.08 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | -      |
| Precinct VBM           | 0                             | -      |
| MB Precinct            | 1                             | 12.50% |
| Provisional            | 0                             | -      |
| Total                  | 1                             | 12.50% |
| 95U402.07-MB 95U402.07 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | -      |
| Precinct VBM           | 0                             | -      |
| MB Precinct            | 2                             | 2.00%  |
| Provisional            | 0                             | -      |
| Total                  | 2                             | 2.00%  |
| 95U402.08-MB 95U402.08 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | -      |
| Precinct VBM           | 0                             | -      |
| MB Precinct            | 2                             | 1.06%  |
| Provisional            | 0                             | 0.00%  |
| Total                  | 2                             | 1.05%  |
| 95U403.08-MB 95U403.08 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | -      |
| Precinct VBM           | 0                             | -      |
| MB Precinct            | 3                             | 0.64%  |
| Provisional            | 0                             | 0.00%  |
| Total                  | 3                             | 0.63%  |
| 95U404.06-MB 95U404.06 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | -      |
| Precinct VBM           | 0                             | -      |
| MB Precinct            | 0                             | -      |
| Provisional            | 0                             | -      |
| Total                  | 0                             | -      |
| 95U404.08-MB 95U404.08 |                               |        |
| Precinct TSx           | 0                             | -      |
| Precinct OS            | 0                             | -      |
| Precinct VBM           | 0                             | -      |
| MB Precinct            | 0                             | -      |



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|                               | WILLIE WILSON                 |       |
| Provisional                   | 0                             | -     |
| Total                         | 0                             | -     |
| 95U501.09-MB 95U501.09        |                               |       |
| Precinct TSx                  | 0                             | -     |
| Precinct OS                   | 0                             | -     |
| Precinct VBM                  | 0                             | -     |
| MB Precinct                   | 0                             | 0.00% |
| Provisional                   | 0                             | -     |
| Total                         | 0                             | 0.00% |
| 95U501.10-MB 95U501.10        |                               |       |
| Precinct TSx                  | 0                             | -     |
| Precinct OS                   | 0                             | -     |
| Precinct VBM                  | 0                             | -     |
| MB Precinct                   | 0                             | 0.00% |
| Provisional                   | 0                             | 0.00% |
| Total                         | 0                             | 0.00% |
| 95U598.10-MB 95U598.10        |                               |       |
| Precinct TSx                  | 0                             | -     |
| Precinct OS                   | 0                             | -     |
| Precinct VBM                  | 0                             | -     |
| MB Precinct                   | 0                             | 0.00% |
| Provisional                   | 0                             | 0.00% |
| Total                         | 0                             | 0.00% |
| 95U599.10-MB 95U599.10        |                               |       |
| Precinct TSx                  | 0                             | -     |
| Precinct OS                   | 0                             | -     |
| Precinct VBM                  | 0                             | -     |
| MB Precinct                   | 0                             | 0.00% |
| Provisional                   | 0                             | -     |
| Total                         | 0                             | 0.00% |
| 96U101.11-MB 96U101.11        |                               |       |
| Precinct TSx                  | 0                             | -     |
| Precinct OS                   | 0                             | -     |
| Precinct VBM                  | 0                             | -     |
| MB Precinct                   | 0                             | 0.00% |
| Provisional                   | 0                             | 0.00% |
| Total                         | 0                             | 0.00% |
| 96U401.14-MB 96U401.14        |                               |       |
| Precinct TSx                  | 0                             | -     |
| Precinct OS                   | 0                             | -     |
| Precinct VBM                  | 0                             | -     |
| MB Precinct                   | 1                             | 0.77% |
| Provisional                   | 0                             | 0.00% |
| Total                         | 1                             | 0.74% |
| Total                         |                               |       |
| Precinct TSx                  | 0                             | 0.00% |
| Precinct OS                   | 22                            | 0.40% |
| Precinct VBM                  | 92                            | 0.52% |
| MB Precinct                   | 17                            | 0.86% |
| Provisional                   | 3                             | 0.37% |
| Total                         | 134                           | 0.52% |
| STATE BOARD OF EQUALIZATION   |                               |       |
| STATE BOARD OF EQUALIZATION 1 |                               |       |
| Precinct TSx                  | 0                             | 0.00% |
| Precinct OS                   | 22                            | 0.40% |
| Precinct VBM                  | 92                            | 0.52% |
| MB Precinct                   | 17                            | 0.86% |
| Provisional                   | 3                             | 0.37% |
| Total                         | 134                           | 0.52% |
| Total                         |                               |       |

**COUNTY OF EL DORADO**  
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|                                     | President Democratic<br>Party |              |
|-------------------------------------|-------------------------------|--------------|
|                                     | WILLIE WILSON                 |              |
| Precinct TSx                        | 0                             | 0.00%        |
| Precinct OS                         | 22                            | 0.40%        |
| Precinct VBM                        | 92                            | 0.52%        |
| MB Precinct                         | 17                            | 0.86%        |
| Provisional                         | 3                             | 0.37%        |
| <b>Total</b>                        | <b>134</b>                    | <b>0.52%</b> |
| <b>UNITED STATES SENATE</b>         |                               |              |
| <b>UNITED STATES SENATE</b>         |                               |              |
| Precinct TSx                        | 0                             | 0.00%        |
| Precinct OS                         | 22                            | 0.40%        |
| Precinct VBM                        | 92                            | 0.52%        |
| MB Precinct                         | 17                            | 0.86%        |
| Provisional                         | 3                             | 0.37%        |
| <b>Total</b>                        | <b>134</b>                    | <b>0.52%</b> |
| <b>Total</b>                        |                               |              |
| Precinct TSx                        | 0                             | 0.00%        |
| Precinct OS                         | 22                            | 0.40%        |
| Precinct VBM                        | 92                            | 0.52%        |
| MB Precinct                         | 17                            | 0.86%        |
| Provisional                         | 3                             | 0.37%        |
| <b>Total</b>                        | <b>134</b>                    | <b>0.52%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |                               |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                               |              |
| Precinct TSx                        | 0                             | 0.00%        |
| Precinct OS                         | 22                            | 0.40%        |
| Precinct VBM                        | 92                            | 0.52%        |
| MB Precinct                         | 17                            | 0.86%        |
| Provisional                         | 3                             | 0.37%        |
| <b>Total</b>                        | <b>134</b>                    | <b>0.52%</b> |
| <b>Total</b>                        |                               |              |
| Precinct TSx                        | 0                             | 0.00%        |
| Precinct OS                         | 22                            | 0.40%        |
| Precinct VBM                        | 92                            | 0.52%        |
| MB Precinct                         | 17                            | 0.86%        |
| Provisional                         | 3                             | 0.37%        |
| <b>Total</b>                        | <b>134</b>                    | <b>0.52%</b> |
| <b>STATE SENATE</b>                 |                               |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                               |              |
| Precinct TSx                        | 0                             | 0.00%        |
| Precinct OS                         | 22                            | 0.40%        |
| Precinct VBM                        | 92                            | 0.52%        |
| MB Precinct                         | 17                            | 0.86%        |
| Provisional                         | 3                             | 0.37%        |
| <b>Total</b>                        | <b>134</b>                    | <b>0.52%</b> |
| <b>Total</b>                        |                               |              |
| Precinct TSx                        | 0                             | 0.00%        |
| Precinct OS                         | 22                            | 0.40%        |
| Precinct VBM                        | 92                            | 0.52%        |
| MB Precinct                         | 17                            | 0.86%        |
| Provisional                         | 3                             | 0.37%        |
| <b>Total</b>                        | <b>134</b>                    | <b>0.52%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                               |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                               |              |
| Precinct TSx                        | 0                             | 0.00%        |
| Precinct OS                         | 20                            | 0.56%        |
| Precinct VBM                        | 76                            | 0.66%        |
| MB Precinct                         | 16                            | 0.93%        |

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|                                   | President Democratic<br>Party |       |
|-----------------------------------|-------------------------------|-------|
|                                   | WILLIE WILSON                 |       |
| Provisional                       | 3                             | 0.53% |
| Total                             | 115                           | 0.66% |
| <b>6TH STATE ASSEMBLY DIST</b>    |                               |       |
| Precinct TSx                      | 0                             | 0.00% |
| Precinct OS                       | 2                             | 0.10% |
| Precinct VBM                      | 16                            | 0.27% |
| MB Precinct                       | 1                             | 0.39% |
| Provisional                       | 0                             | 0.00% |
| Total                             | 19                            | 0.23% |
| Total                             |                               |       |
| Precinct TSx                      | 0                             | 0.00% |
| Precinct OS                       | 22                            | 0.40% |
| Precinct VBM                      | 92                            | 0.52% |
| MB Precinct                       | 17                            | 0.86% |
| Provisional                       | 3                             | 0.37% |
| Total                             | 134                           | 0.52% |
| <b>SUPERVISORIAL DISTRICTS</b>    |                               |       |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                               |       |
| Precinct TSx                      | 0                             | -     |
| Precinct OS                       | 0                             | 0.00% |
| Precinct VBM                      | 6                             | 0.17% |
| MB Precinct                       | 0                             | 0.00% |
| Provisional                       | 0                             | 0.00% |
| Total                             | 6                             | 0.12% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                               |       |
| Precinct TSx                      | 0                             | 0.00% |
| Precinct OS                       | 4                             | 0.39% |
| Precinct VBM                      | 15                            | 0.43% |
| MB Precinct                       | 4                             | 0.73% |
| Provisional                       | 1                             | 0.68% |
| Total                             | 24                            | 0.46% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                               |       |
| Precinct TSx                      | 0                             | 0.00% |
| Precinct OS                       | 7                             | 0.70% |
| Precinct VBM                      | 30                            | 0.75% |
| MB Precinct                       | 4                             | 2.50% |
| Provisional                       | 0                             | 0.00% |
| Total                             | 41                            | 0.78% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                               |       |
| Precinct TSx                      | 0                             | -     |
| Precinct OS                       | 6                             | 0.62% |
| Precinct VBM                      | 25                            | 0.75% |
| MB Precinct                       | 9                             | 1.01% |
| Provisional                       | 2                             | 1.46% |
| Total                             | 42                            | 0.79% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                               |       |
| Precinct TSx                      | 0                             | 0.00% |
| Precinct OS                       | 5                             | 0.36% |
| Precinct VBM                      | 16                            | 0.48% |
| MB Precinct                       | 0                             | 0.00% |
| Provisional                       | 0                             | 0.00% |
| Total                             | 21                            | 0.40% |
| Total                             |                               |       |
| Precinct TSx                      | 0                             | 0.00% |
| Precinct OS                       | 22                            | 0.40% |
| Precinct VBM                      | 92                            | 0.52% |
| MB Precinct                       | 17                            | 0.86% |
| Provisional                       | 3                             | 0.37% |
| Total                             | 134                           | 0.52% |

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|                                 | President Democratic<br>Party |       |
|---------------------------------|-------------------------------|-------|
|                                 | WILLIE WILSON                 |       |
| <b>CITIES</b>                   |                               |       |
| <b>CITY OF PLACERVILLE</b>      |                               |       |
| Precinct TSx                    | 0                             | -     |
| Precinct OS                     | 3                             | 0.91% |
| Precinct VBM                    | 10                            | 0.84% |
| MB Precinct                     | 0                             | -     |
| Provisional                     | 0                             | 0.00% |
| Total                           | 13                            | 0.83% |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                               |       |
| Precinct TSx                    | 0                             | -     |
| Precinct OS                     | 3                             | 0.39% |
| Precinct VBM                    | 8                             | 0.48% |
| MB Precinct                     | 0                             | -     |
| Provisional                     | 0                             | 0.00% |
| Total                           | 11                            | 0.42% |
| <b>UNINCORPORATED AREA</b>      |                               |       |
| Precinct TSx                    | 0                             | 0.00% |
| Precinct OS                     | 16                            | 0.36% |
| Precinct VBM                    | 74                            | 0.50% |
| MB Precinct                     | 17                            | 0.86% |
| Provisional                     | 3                             | 0.50% |
| Total                           | 110                           | 0.51% |
| <b>Total</b>                    |                               |       |
| Precinct TSx                    | 0                             | 0.00% |
| Precinct OS                     | 22                            | 0.40% |
| Precinct VBM                    | 92                            | 0.52% |
| MB Precinct                     | 17                            | 0.86% |
| Provisional                     | 3                             | 0.37% |
| Total                           | 134                           | 0.52% |

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|                        | President Republican Party |          |               |             |          |        |             |       |
|------------------------|----------------------------|----------|---------------|-------------|----------|--------|-------------|-------|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | TED CRUZ |        | JIM GILMORE |       |
| Jurisdiction Wide      |                            |          |               |             |          |        |             |       |
| 100001.11-CP 100001.11 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 769                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 769                        | 1        | 86            | 83          | 2        | 2.41%  | 1           | 1.20% |
| Precinct VBM           | 769                        | 1        | 413           | 388         | 24       | 6.19%  | 0           | 0.00% |
| MB Precinct            | 769                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 769                        | 1        | 4             | 4           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 769                        | 5        | 503           | 475         | 26       | 5.47%  | 1           | 0.21% |
| 100002.11-CP 100002.11 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 507                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 507                        | 1        | 32            | 30          | 3        | 10.00% | 0           | 0.00% |
| Precinct VBM           | 507                        | 1        | 259           | 241         | 18       | 7.47%  | 2           | 0.83% |
| MB Precinct            | 507                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 507                        | 1        | 9             | 9           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 507                        | 5        | 300           | 280         | 21       | 7.50%  | 2           | 0.71% |
| 100003.11-CP 100003.11 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 596                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 596                        | 1        | 71            | 67          | 4        | 5.97%  | 0           | 0.00% |
| Precinct VBM           | 596                        | 1        | 368           | 352         | 21       | 5.97%  | 0           | 0.00% |
| MB Precinct            | 596                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 596                        | 1        | 8             | 8           | 1        | 12.50% | 0           | 0.00% |
| Total                  | 596                        | 5        | 447           | 427         | 26       | 6.09%  | 0           | 0.00% |
| 100004.11-CP 100004.11 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 745                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 745                        | 1        | 105           | 96          | 5        | 5.21%  | 1           | 1.04% |
| Precinct VBM           | 745                        | 1        | 359           | 341         | 30       | 8.80%  | 2           | 0.59% |
| MB Precinct            | 745                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 745                        | 1        | 1             | 1           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 745                        | 5        | 465           | 438         | 35       | 7.99%  | 3           | 0.68% |
| 100005.11-CP 100005.11 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 906                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 906                        | 1        | 80            | 79          | 4        | 5.06%  | 0           | 0.00% |
| Precinct VBM           | 906                        | 1        | 556           | 538         | 20       | 3.72%  | 4           | 0.74% |
| MB Precinct            | 906                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 906                        | 1        | 10            | 8           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 906                        | 5        | 646           | 625         | 24       | 3.84%  | 4           | 0.64% |
| 100006.11-CP 100006.11 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 937                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 937                        | 1        | 135           | 122         | 10       | 8.20%  | 1           | 0.82% |
| Precinct VBM           | 937                        | 1        | 454           | 427         | 27       | 6.32%  | 2           | 0.47% |
| MB Precinct            | 937                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 937                        | 1        | 12            | 12          | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 937                        | 5        | 601           | 561         | 37       | 6.60%  | 3           | 0.53% |
| 100008.11-CP 100008.11 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 742                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 742                        | 1        | 78            | 76          | 6        | 7.89%  | 0           | 0.00% |
| Precinct VBM           | 742                        | 1        | 373           | 353         | 19       | 5.38%  | 0           | 0.00% |
| MB Precinct            | 742                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 742                        | 1        | 5             | 5           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 742                        | 5        | 456           | 434         | 25       | 5.76%  | 0           | 0.00% |
| 100009.11-CP 100009.11 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 829                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 829                        | 1        | 69            | 67          | 8        | 11.94% | 0           | 0.00% |
| Precinct VBM           | 829                        | 1        | 364           | 338         | 24       | 7.10%  | 2           | 0.59% |
| MB Precinct            | 829                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 829                        | 1        | 11            | 10          | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 829                        | 5        | 444           | 415         | 32       | 7.71%  | 2           | 0.48% |
| 100012.11-CP 100012.11 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 1018                       | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 1018                       | 1        | 123           | 112         | 13       | 11.61% | 2           | 1.79% |
| Precinct VBM           | 1018                       | 1        | 490           | 466         | 20       | 4.29%  | 2           | 0.43% |

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|                        | President Republican Party |          |               |             |          |        |             |       |
|------------------------|----------------------------|----------|---------------|-------------|----------|--------|-------------|-------|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | TED CRUZ |        | JIM GILMORE |       |
| MB Precinct            | 1018                       | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 1018                       | 1        | 13            | 13          | 1        | 7.69%  | 0           | 0.00% |
| Total                  | 1018                       | 5        | 626           | 591         | 34       | 5.75%  | 4           | 0.68% |
| 100013.11-CP 100013.11 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 1003                       | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 1003                       | 1        | 114           | 109         | 11       | 10.09% | 2           | 1.83% |
| Precinct VBM           | 1003                       | 1        | 469           | 444         | 30       | 6.76%  | 3           | 0.68% |
| MB Precinct            | 1003                       | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 1003                       | 1        | 11            | 10          | 1        | 10.00% | 0           | 0.00% |
| Total                  | 1003                       | 5        | 594           | 563         | 42       | 7.46%  | 5           | 0.89% |
| 100014.11-CP 100014.11 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 842                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 842                        | 1        | 107           | 99          | 8        | 8.08%  | 0           | 0.00% |
| Precinct VBM           | 842                        | 1        | 380           | 352         | 28       | 7.95%  | 0           | 0.00% |
| MB Precinct            | 842                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 842                        | 1        | 15            | 13          | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 842                        | 5        | 502           | 464         | 36       | 7.76%  | 0           | 0.00% |
| 100022.11-CP 100022.11 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 732                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 732                        | 1        | 78            | 75          | 5        | 6.67%  | 0           | 0.00% |
| Precinct VBM           | 732                        | 1        | 363           | 349         | 20       | 5.73%  | 0           | 0.00% |
| MB Precinct            | 732                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 732                        | 1        | 5             | 4           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 732                        | 5        | 446           | 428         | 25       | 5.84%  | 0           | 0.00% |
| 100023.11-CP 100023.11 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 1088                       | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 1088                       | 1        | 118           | 115         | 16       | 13.91% | 0           | 0.00% |
| Precinct VBM           | 1088                       | 1        | 578           | 548         | 31       | 5.66%  | 3           | 0.55% |
| MB Precinct            | 1088                       | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 1088                       | 1        | 11            | 10          | 1        | 10.00% | 0           | 0.00% |
| Total                  | 1088                       | 5        | 707           | 673         | 48       | 7.13%  | 3           | 0.45% |
| 200003.12-CP 200003.12 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 594                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 594                        | 1        | 73            | 67          | 9        | 13.43% | 0           | 0.00% |
| Precinct VBM           | 594                        | 1        | 262           | 254         | 18       | 7.09%  | 0           | 0.00% |
| MB Precinct            | 594                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 594                        | 1        | 10            | 10          | 1        | 10.00% | 0           | 0.00% |
| Total                  | 594                        | 5        | 345           | 331         | 28       | 8.46%  | 0           | 0.00% |
| 200004.12-CP 200004.12 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 693                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 693                        | 1        | 90            | 88          | 11       | 12.50% | 1           | 1.14% |
| Precinct VBM           | 693                        | 1        | 301           | 285         | 26       | 9.12%  | 0           | 0.00% |
| MB Precinct            | 693                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 693                        | 1        | 6             | 5           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 693                        | 5        | 397           | 378         | 37       | 9.79%  | 1           | 0.26% |
| 200005.02-CP 200005.02 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 369                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 369                        | 1        | 41            | 40          | 8        | 20.00% | 0           | 0.00% |
| Precinct VBM           | 369                        | 1        | 212           | 206         | 7        | 3.40%  | 0           | 0.00% |
| MB Precinct            | 369                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 369                        | 1        | 6             | 6           | 1        | 16.67% | 0           | 0.00% |
| Total                  | 369                        | 5        | 259           | 252         | 16       | 6.35%  | 0           | 0.00% |
| 200006.12-CP 200006.12 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 579                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 579                        | 1        | 72            | 71          | 7        | 9.86%  | 1           | 1.41% |
| Precinct VBM           | 579                        | 1        | 300           | 294         | 21       | 7.14%  | 0           | 0.00% |
| MB Precinct            | 579                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 579                        | 1        | 11            | 11          | 1        | 9.09%  | 0           | 0.00% |
| Total                  | 579                        | 5        | 383           | 376         | 29       | 7.71%  | 1           | 0.27% |
| 200008.12-CP 200008.12 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 427                        | 1        | 0             | 0           | 0        | -      | 0           | -     |

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|                        | President Republican Party |          |               |             |          |        |             |        |
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|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | TED CRUZ |        | JIM GILMORE |        |
| Precinct OS            | 427                        | 1        | 75            | 73          | 7        | 9.59%  | 0           | 0.00%  |
| Precinct VBM           | 427                        | 1        | 169           | 160         | 13       | 8.13%  | 0           | 0.00%  |
| MB Precinct            | 427                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Provisional            | 427                        | 1        | 2             | 2           | 0        | 0.00%  | 0           | 0.00%  |
| Total                  | 427                        | 5        | 246           | 235         | 20       | 8.51%  | 0           | 0.00%  |
| 200010.12-CP 200010.12 |                            |          |               |             |          |        |             |        |
| Precinct TSx           | 864                        | 1        | 2             | 2           | 0        | 0.00%  | 1           | 50.00% |
| Precinct OS            | 864                        | 1        | 76            | 72          | 6        | 8.33%  | 0           | 0.00%  |
| Precinct VBM           | 864                        | 1        | 418           | 399         | 23       | 5.76%  | 2           | 0.50%  |
| MB Precinct            | 864                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Provisional            | 864                        | 1        | 10            | 9           | 0        | 0.00%  | 0           | 0.00%  |
| Total                  | 864                        | 5        | 506           | 482         | 29       | 6.02%  | 3           | 0.62%  |
| 200012.12-CP 200012.12 |                            |          |               |             |          |        |             |        |
| Precinct TSx           | 517                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Precinct OS            | 517                        | 1        | 60            | 55          | 7        | 12.73% | 0           | 0.00%  |
| Precinct VBM           | 517                        | 1        | 237           | 224         | 12       | 5.36%  | 0           | 0.00%  |
| MB Precinct            | 517                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Provisional            | 517                        | 1        | 4             | 4           | 1        | 25.00% | 0           | 0.00%  |
| Total                  | 517                        | 5        | 301           | 283         | 20       | 7.07%  | 0           | 0.00%  |
| 200013.12-CP 200013.12 |                            |          |               |             |          |        |             |        |
| Precinct TSx           | 788                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Precinct OS            | 788                        | 1        | 108           | 106         | 14       | 13.21% | 1           | 0.94%  |
| Precinct VBM           | 788                        | 1        | 391           | 372         | 22       | 5.91%  | 2           | 0.54%  |
| MB Precinct            | 788                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Provisional            | 788                        | 1        | 10            | 8           | 0        | 0.00%  | 0           | 0.00%  |
| Total                  | 788                        | 5        | 509           | 486         | 36       | 7.41%  | 3           | 0.62%  |
| 200014.12-CP 200014.12 |                            |          |               |             |          |        |             |        |
| Precinct TSx           | 659                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Precinct OS            | 659                        | 1        | 103           | 102         | 10       | 9.80%  | 0           | 0.00%  |
| Precinct VBM           | 659                        | 1        | 297           | 282         | 20       | 7.09%  | 2           | 0.71%  |
| MB Precinct            | 659                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Provisional            | 659                        | 1        | 8             | 8           | 1        | 12.50% | 0           | 0.00%  |
| Total                  | 659                        | 5        | 408           | 392         | 31       | 7.91%  | 2           | 0.51%  |
| 200015.02-CP 200015.02 |                            |          |               |             |          |        |             |        |
| Precinct TSx           | 733                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Precinct OS            | 733                        | 1        | 92            | 88          | 1        | 1.14%  | 0           | 0.00%  |
| Precinct VBM           | 733                        | 1        | 382           | 367         | 21       | 5.72%  | 2           | 0.54%  |
| MB Precinct            | 733                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Provisional            | 733                        | 1        | 8             | 8           | 0        | 0.00%  | 0           | 0.00%  |
| Total                  | 733                        | 5        | 482           | 463         | 22       | 4.75%  | 2           | 0.43%  |
| 200016.12-CP 200016.12 |                            |          |               |             |          |        |             |        |
| Precinct TSx           | 173                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Precinct OS            | 173                        | 1        | 17            | 16          | 0        | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 173                        | 1        | 83            | 79          | 12       | 15.19% | 0           | 0.00%  |
| MB Precinct            | 173                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Provisional            | 173                        | 1        | 4             | 4           | 0        | 0.00%  | 0           | 0.00%  |
| Total                  | 173                        | 5        | 104           | 99          | 12       | 12.12% | 0           | 0.00%  |
| 200016.13-CP 200016.13 |                            |          |               |             |          |        |             |        |
| Precinct TSx           | 331                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Precinct OS            | 331                        | 1        | 50            | 48          | 3        | 6.25%  | 0           | 0.00%  |
| Precinct VBM           | 331                        | 1        | 195           | 179         | 15       | 8.38%  | 0           | 0.00%  |
| MB Precinct            | 331                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Provisional            | 331                        | 1        | 5             | 5           | 0        | 0.00%  | 0           | 0.00%  |
| Total                  | 331                        | 5        | 250           | 232         | 18       | 7.76%  | 0           | 0.00%  |
| 200017.02-CP 200017.02 |                            |          |               |             |          |        |             |        |
| Precinct TSx           | 543                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Precinct OS            | 543                        | 1        | 59            | 58          | 5        | 8.62%  | 0           | 0.00%  |
| Precinct VBM           | 543                        | 1        | 249           | 236         | 15       | 6.36%  | 0           | 0.00%  |
| MB Precinct            | 543                        | 1        | 0             | 0           | 0        | -      | 0           | -      |
| Provisional            | 543                        | 1        | 6             | 6           | 0        | 0.00%  | 0           | 0.00%  |
| Total                  | 543                        | 5        | 314           | 300         | 20       | 6.67%  | 0           | 0.00%  |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | President Republican Party |          |               |             |          |        |             |       |  |
|------------------------|----------------------------|----------|---------------|-------------|----------|--------|-------------|-------|--|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | TED CRUZ |        | JIM GILMORE |       |  |
| 200018.02-CP 200018.02 |                            |          |               |             |          |        |             |       |  |
| Precinct TSx           | 710                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Precinct OS            | 710                        | 1        | 95            | 92          | 4        | 4.35%  | 0           | 0.00% |  |
| Precinct VBM           | 710                        | 1        | 358           | 343         | 26       | 7.58%  | 0           | 0.00% |  |
| MB Precinct            | 710                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Provisional            | 710                        | 1        | 6             | 6           | 0        | 0.00%  | 0           | 0.00% |  |
| Total                  | 710                        | 5        | 459           | 441         | 30       | 6.80%  | 0           | 0.00% |  |
| 200020.02-CP 200020.02 |                            |          |               |             |          |        |             |       |  |
| Precinct TSx           | 595                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Precinct OS            | 595                        | 1        | 79            | 75          | 6        | 8.00%  | 1           | 1.33% |  |
| Precinct VBM           | 595                        | 1        | 278           | 258         | 24       | 9.30%  | 1           | 0.39% |  |
| MB Precinct            | 595                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Provisional            | 595                        | 1        | 7             | 7           | 1        | 14.29% | 0           | 0.00% |  |
| Total                  | 595                        | 5        | 364           | 340         | 31       | 9.12%  | 2           | 0.59% |  |
| 200021.01-CP 200021.01 |                            |          |               |             |          |        |             |       |  |
| Precinct TSx           | 205                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Precinct OS            | 205                        | 1        | 20            | 19          | 2        | 10.53% | 0           | 0.00% |  |
| Precinct VBM           | 205                        | 1        | 105           | 100         | 5        | 5.00%  | 1           | 1.00% |  |
| MB Precinct            | 205                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Provisional            | 205                        | 1        | 4             | 4           | 0        | 0.00%  | 0           | 0.00% |  |
| Total                  | 205                        | 5        | 129           | 123         | 7        | 5.69%  | 1           | 0.81% |  |
| 200021.02-CP 200021.02 |                            |          |               |             |          |        |             |       |  |
| Precinct TSx           | 637                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Precinct OS            | 637                        | 1        | 95            | 94          | 5        | 5.32%  | 0           | 0.00% |  |
| Precinct VBM           | 637                        | 1        | 390           | 376         | 22       | 5.85%  | 3           | 0.80% |  |
| MB Precinct            | 637                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Provisional            | 637                        | 1        | 10            | 10          | 1        | 10.00% | 0           | 0.00% |  |
| Total                  | 637                        | 5        | 495           | 480         | 28       | 5.83%  | 3           | 0.63% |  |
| 200024.02-CP 200024.02 |                            |          |               |             |          |        |             |       |  |
| Precinct TSx           | 270                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Precinct OS            | 270                        | 1        | 37            | 36          | 5        | 13.89% | 0           | 0.00% |  |
| Precinct VBM           | 270                        | 1        | 156           | 153         | 14       | 9.15%  | 0           | 0.00% |  |
| MB Precinct            | 270                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Provisional            | 270                        | 1        | 6             | 6           | 2        | 33.33% | 0           | 0.00% |  |
| Total                  | 270                        | 5        | 199           | 195         | 21       | 10.77% | 0           | 0.00% |  |
| 300001.04-CP 300001.04 |                            |          |               |             |          |        |             |       |  |
| Precinct TSx           | 73                         | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Precinct OS            | 73                         | 1        | 6             | 6           | 1        | 16.67% | 0           | 0.00% |  |
| Precinct VBM           | 73                         | 1        | 30            | 28          | 1        | 3.57%  | 1           | 3.57% |  |
| MB Precinct            | 73                         | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Provisional            | 73                         | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Total                  | 73                         | 5        | 36            | 34          | 2        | 5.88%  | 1           | 2.94% |  |
| 300001.05-CP 300001.05 |                            |          |               |             |          |        |             |       |  |
| Precinct TSx           | 258                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Precinct OS            | 258                        | 1        | 36            | 35          | 2        | 5.71%  | 0           | 0.00% |  |
| Precinct VBM           | 258                        | 1        | 130           | 128         | 11       | 8.59%  | 1           | 0.78% |  |
| MB Precinct            | 258                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Provisional            | 258                        | 1        | 1             | 1           | 0        | 0.00%  | 0           | 0.00% |  |
| Total                  | 258                        | 5        | 167           | 164         | 13       | 7.93%  | 1           | 0.61% |  |
| 300003.05-CP 300003.05 |                            |          |               |             |          |        |             |       |  |
| Precinct TSx           | 686                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Precinct OS            | 686                        | 1        | 78            | 71          | 10       | 14.08% | 0           | 0.00% |  |
| Precinct VBM           | 686                        | 1        | 361           | 351         | 23       | 6.55%  | 0           | 0.00% |  |
| MB Precinct            | 686                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Provisional            | 686                        | 1        | 7             | 6           | 0        | 0.00%  | 0           | 0.00% |  |
| Total                  | 686                        | 5        | 446           | 428         | 33       | 7.71%  | 0           | 0.00% |  |
| 300004.05-CP 300004.05 |                            |          |               |             |          |        |             |       |  |
| Precinct TSx           | 465                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |
| Precinct OS            | 465                        | 1        | 62            | 61          | 4        | 6.56%  | 0           | 0.00% |  |
| Precinct VBM           | 465                        | 1        | 228           | 211         | 16       | 7.58%  | 1           | 0.47% |  |
| MB Precinct            | 465                        | 1        | 0             | 0           | 0        | -      | 0           | -     |  |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | President Republican Party |          |               |             |          |        |             |       |
|------------------------|----------------------------|----------|---------------|-------------|----------|--------|-------------|-------|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | TED CRUZ |        | JIM GILMORE |       |
| Provisional            | 465                        | 1        | 4             | 4           | 2        | 50.00% | 0           | 0.00% |
| Total                  | 465                        | 5        | 294           | 276         | 22       | 7.97%  | 1           | 0.36% |
| 300005.05-CP 300005.05 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 547                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 547                        | 1        | 60            | 57          | 11       | 19.30% | 0           | 0.00% |
| Precinct VBM           | 547                        | 1        | 283           | 261         | 25       | 9.58%  | 2           | 0.77% |
| MB Precinct            | 547                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 547                        | 1        | 8             | 8           | 1        | 12.50% | 0           | 0.00% |
| Total                  | 547                        | 5        | 351           | 326         | 37       | 11.35% | 2           | 0.61% |
| 300008.05-CP 300008.05 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 667                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 667                        | 1        | 75            | 75          | 13       | 17.33% | 0           | 0.00% |
| Precinct VBM           | 667                        | 1        | 380           | 359         | 18       | 5.01%  | 0           | 0.00% |
| MB Precinct            | 667                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 667                        | 1        | 9             | 9           | 1        | 11.11% | 0           | 0.00% |
| Total                  | 667                        | 5        | 464           | 443         | 32       | 7.22%  | 0           | 0.00% |
| 300009.04-CP 300009.04 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 384                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 384                        | 1        | 57            | 56          | 6        | 10.71% | 1           | 1.79% |
| Precinct VBM           | 384                        | 1        | 192           | 184         | 25       | 13.59% | 0           | 0.00% |
| MB Precinct            | 384                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 384                        | 1        | 5             | 5           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 384                        | 5        | 254           | 245         | 31       | 12.65% | 1           | 0.41% |
| 300010.04-CP 300010.04 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 588                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 588                        | 1        | 95            | 91          | 11       | 12.09% | 0           | 0.00% |
| Precinct VBM           | 588                        | 1        | 309           | 302         | 25       | 8.28%  | 1           | 0.33% |
| MB Precinct            | 588                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 588                        | 1        | 3             | 3           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 588                        | 5        | 407           | 396         | 36       | 9.09%  | 1           | 0.25% |
| 300011.04-CP 300011.04 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 678                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 678                        | 1        | 79            | 77          | 5        | 6.49%  | 0           | 0.00% |
| Precinct VBM           | 678                        | 1        | 349           | 338         | 40       | 11.83% | 2           | 0.59% |
| MB Precinct            | 678                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 678                        | 1        | 7             | 7           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 678                        | 5        | 435           | 422         | 45       | 10.66% | 2           | 0.47% |
| 300013.05-CP 300013.05 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 720                        | 1        | 2             | 2           | 0        | 0.00%  | 0           | 0.00% |
| Precinct OS            | 720                        | 1        | 59            | 58          | 6        | 10.34% | 0           | 0.00% |
| Precinct VBM           | 720                        | 1        | 393           | 384         | 23       | 5.99%  | 3           | 0.78% |
| MB Precinct            | 720                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 720                        | 1        | 9             | 8           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 720                        | 5        | 463           | 452         | 29       | 6.42%  | 3           | 0.66% |
| 300015.04-CP 300015.04 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 56                         | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 56                         | 1        | 6             | 6           | 0        | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 56                         | 1        | 32            | 32          | 1        | 3.13%  | 0           | 0.00% |
| MB Precinct            | 56                         | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 56                         | 1        | 1             | 1           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 56                         | 5        | 39            | 39          | 1        | 2.56%  | 0           | 0.00% |
| 300015.05-CP 300015.05 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 209                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 209                        | 1        | 30            | 28          | 1        | 3.57%  | 0           | 0.00% |
| Precinct VBM           | 209                        | 1        | 111           | 102         | 6        | 5.88%  | 0           | 0.00% |
| MB Precinct            | 209                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 209                        | 1        | 1             | 1           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 209                        | 5        | 142           | 131         | 7        | 5.34%  | 0           | 0.00% |
| 300017.05-CP 300017.05 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 604                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 604                        | 1        | 66            | 63          | 6        | 9.52%  | 2           | 3.17% |



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|------------------------|----------------------------|----------|---------------|-------------|----------|--------|-------------|-------|---|---|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | TED CRUZ |        | JIM GILMORE |       |   |   |
| Precinct TSx           | 843                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | - |
| Precinct OS            | 843                        | 1        | 145           | 145         | 13       | 8.97%  | 1           | 0.69% |   |   |
| Precinct VBM           | 843                        | 1        | 380           | 364         | 19       | 5.22%  | 3           | 0.82% |   |   |
| MB Precinct            | 843                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Provisional            | 843                        | 1        | 12            | 12          | 0        | 0.00%  | 0           | 0.00% |   |   |
| Total                  | 843                        | 5        | 537           | 521         | 32       | 6.14%  | 4           | 0.77% |   |   |
| 400004.14-CP 400004.14 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 717                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 717                        | 1        | 98            | 95          | 13       | 13.68% | 1           | 1.05% |   |   |
| Precinct VBM           | 717                        | 1        | 335           | 323         | 27       | 8.36%  | 2           | 0.62% |   |   |
| MB Precinct            | 717                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Provisional            | 717                        | 1        | 10            | 9           | 1        | 11.11% | 0           | 0.00% |   |   |
| Total                  | 717                        | 5        | 443           | 427         | 41       | 9.60%  | 3           | 0.70% |   |   |
| 400005.08-CP 400005.08 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 282                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 282                        | 1        | 37            | 37          | 9        | 24.32% | 0           | 0.00% |   |   |
| Precinct VBM           | 282                        | 1        | 121           | 112         | 7        | 6.25%  | 1           | 0.89% |   |   |
| MB Precinct            | 282                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Provisional            | 282                        | 1        | 5             | 5           | 0        | 0.00%  | 0           | 0.00% |   |   |
| Total                  | 282                        | 5        | 163           | 154         | 16       | 10.39% | 1           | 0.65% |   |   |
| 400006.08-CP 400006.08 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 680                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 680                        | 1        | 107           | 105         | 11       | 10.48% | 1           | 0.95% |   |   |
| Precinct VBM           | 680                        | 1        | 320           | 315         | 17       | 5.40%  | 0           | 0.00% |   |   |
| MB Precinct            | 680                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Provisional            | 680                        | 1        | 9             | 8           | 0        | 0.00%  | 0           | 0.00% |   |   |
| Total                  | 680                        | 5        | 436           | 428         | 28       | 6.54%  | 1           | 0.23% |   |   |
| 400007.08-CP 400007.08 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 838                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 838                        | 1        | 125           | 117         | 5        | 4.27%  | 0           | 0.00% |   |   |
| Precinct VBM           | 838                        | 1        | 401           | 396         | 43       | 10.86% | 2           | 0.51% |   |   |
| MB Precinct            | 838                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Provisional            | 838                        | 1        | 6             | 6           | 0        | 0.00%  | 0           | 0.00% |   |   |
| Total                  | 838                        | 5        | 532           | 519         | 48       | 9.25%  | 2           | 0.39% |   |   |
| 400010.08-CP 400010.08 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 762                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 762                        | 1        | 101           | 98          | 10       | 10.20% | 0           | 0.00% |   |   |
| Precinct VBM           | 762                        | 1        | 373           | 359         | 28       | 7.80%  | 1           | 0.28% |   |   |
| MB Precinct            | 762                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Provisional            | 762                        | 1        | 9             | 8           | 0        | 0.00%  | 0           | 0.00% |   |   |
| Total                  | 762                        | 5        | 483           | 465         | 38       | 8.17%  | 1           | 0.22% |   |   |
| 400012.08-CP 400012.08 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 404                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 404                        | 1        | 49            | 45          | 5        | 11.11% | 1           | 2.22% |   |   |
| Precinct VBM           | 404                        | 1        | 211           | 201         | 12       | 5.97%  | 1           | 0.50% |   |   |
| MB Precinct            | 404                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Provisional            | 404                        | 1        | 8             | 8           | 0        | 0.00%  | 0           | 0.00% |   |   |
| Total                  | 404                        | 5        | 268           | 254         | 17       | 6.69%  | 2           | 0.79% |   |   |
| 400013.08-CP 400013.08 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 355                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 355                        | 1        | 78            | 74          | 5        | 6.76%  | 0           | 0.00% |   |   |
| Precinct VBM           | 355                        | 1        | 160           | 154         | 9        | 5.84%  | 0           | 0.00% |   |   |
| MB Precinct            | 355                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Provisional            | 355                        | 1        | 1             | 1           | 0        | 0.00%  | 0           | 0.00% |   |   |
| Total                  | 355                        | 5        | 239           | 229         | 14       | 6.11%  | 0           | 0.00% |   |   |
| 400014.08-CP 400014.08 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 794                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 794                        | 1        | 119           | 115         | 8        | 6.96%  | 0           | 0.00% |   |   |
| Precinct VBM           | 794                        | 1        | 391           | 377         | 10       | 2.65%  | 0           | 0.00% |   |   |
| MB Precinct            | 794                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Provisional            | 794                        | 1        | 18            | 16          | 0        | 0.00%  | 0           | 0.00% |   |   |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | President Republican Party |          |               |             |          |        |             |       |
|------------------------|----------------------------|----------|---------------|-------------|----------|--------|-------------|-------|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | TED CRUZ |        | JIM GILMORE |       |
| Total                  | 794                        | 5        | 528           | 508         | 18       | 3.54%  | 0           | 0.00% |
| 400017.08-CP 400017.08 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 770                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 770                        | 1        | 96            | 89          | 11       | 12.36% | 0           | 0.00% |
| Precinct VBM           | 770                        | 1        | 409           | 392         | 23       | 5.87%  | 3           | 0.77% |
| MB Precinct            | 770                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 770                        | 1        | 10            | 10          | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 770                        | 5        | 515           | 491         | 34       | 6.92%  | 3           | 0.61% |
| 400018.08-CP 400018.08 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 337                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 337                        | 1        | 48            | 47          | 5        | 10.64% | 0           | 0.00% |
| Precinct VBM           | 337                        | 1        | 165           | 159         | 9        | 5.66%  | 2           | 1.26% |
| MB Precinct            | 337                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 337                        | 1        | 1             | 1           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 337                        | 5        | 214           | 207         | 14       | 6.76%  | 2           | 0.97% |
| 400020.08-CP 400020.08 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 332                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 332                        | 1        | 68            | 67          | 3        | 4.48%  | 0           | 0.00% |
| Precinct VBM           | 332                        | 1        | 167           | 164         | 10       | 6.10%  | 1           | 0.61% |
| MB Precinct            | 332                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 332                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Total                  | 332                        | 5        | 235           | 231         | 13       | 5.63%  | 1           | 0.43% |
| 400021.07-CP 400021.07 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 729                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 729                        | 1        | 72            | 67          | 7        | 10.45% | 0           | 0.00% |
| Precinct VBM           | 729                        | 1        | 442           | 424         | 30       | 7.08%  | 1           | 0.24% |
| MB Precinct            | 729                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 729                        | 1        | 7             | 7           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 729                        | 5        | 521           | 498         | 37       | 7.43%  | 1           | 0.20% |
| 500001.10-CP 500001.10 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 478                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 478                        | 1        | 55            | 52          | 3        | 5.77%  | 0           | 0.00% |
| Precinct VBM           | 478                        | 1        | 188           | 172         | 5        | 2.91%  | 0           | 0.00% |
| MB Precinct            | 478                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 478                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Total                  | 478                        | 5        | 243           | 224         | 8        | 3.57%  | 0           | 0.00% |
| 500002.10-CP 500002.10 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 275                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 275                        | 1        | 35            | 34          | 1        | 2.94%  | 0           | 0.00% |
| Precinct VBM           | 275                        | 1        | 99            | 93          | 7        | 7.53%  | 1           | 1.08% |
| MB Precinct            | 275                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 275                        | 1        | 2             | 2           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 275                        | 5        | 136           | 129         | 8        | 6.20%  | 1           | 0.78% |
| 500003.10-CP 500003.10 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 235                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 235                        | 1        | 22            | 22          | 1        | 4.55%  | 0           | 0.00% |
| Precinct VBM           | 235                        | 1        | 93            | 90          | 5        | 5.56%  | 1           | 1.11% |
| MB Precinct            | 235                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 235                        | 1        | 1             | 1           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 235                        | 5        | 116           | 113         | 6        | 5.31%  | 1           | 0.88% |
| 500004.10-CP 500004.10 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 195                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 195                        | 1        | 27            | 26          | 2        | 7.69%  | 1           | 3.85% |
| Precinct VBM           | 195                        | 1        | 70            | 68          | 6        | 8.82%  | 1           | 1.47% |
| MB Precinct            | 195                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 195                        | 1        | 3             | 3           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 195                        | 5        | 100           | 97          | 8        | 8.25%  | 2           | 2.06% |
| 500005.10-CP 500005.10 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 172                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 172                        | 1        | 22            | 22          | 0        | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 172                        | 1        | 55            | 54          | 4        | 7.41%  | 0           | 0.00% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | President Republican Party |          |               |             |          |        |             |       |   |       |
|------------------------|----------------------------|----------|---------------|-------------|----------|--------|-------------|-------|---|-------|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | TED CRUZ |        | JIM GILMORE |       |   |       |
| MB Precinct            | 172                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Provisional            | 172                        | 1        | 2             | 2           | 0        | 0.00%  | 0           | 0.00% | 0 | 0.00% |
| Total                  | 172                        | 5        | 79            | 78          | 4        | 5.13%  | 0           | 0.00% | 0 | 0.00% |
| 500006.10-CP 500006.10 |                            |          |               |             |          |        |             |       |   |       |
| Precinct TSx           | 273                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Precinct OS            | 273                        | 1        | 29            | 28          | 3        | 10.71% | 0           | 0.00% | 0 | 0.00% |
| Precinct VBM           | 273                        | 1        | 102           | 100         | 6        | 6.00%  | 2           | 2.00% | 0 | 0.00% |
| MB Precinct            | 273                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Provisional            | 273                        | 1        | 6             | 6           | 0        | 0.00%  | 0           | 0.00% | 0 | 0.00% |
| Total                  | 273                        | 5        | 137           | 134         | 9        | 6.72%  | 2           | 1.49% | 0 | 0.00% |
| 500007.10-CP 500007.10 |                            |          |               |             |          |        |             |       |   |       |
| Precinct TSx           | 552                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Precinct OS            | 552                        | 1        | 69            | 68          | 5        | 7.35%  | 0           | 0.00% | 0 | 0.00% |
| Precinct VBM           | 552                        | 1        | 246           | 229         | 12       | 5.24%  | 1           | 0.44% | 0 | 0.00% |
| MB Precinct            | 552                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Provisional            | 552                        | 1        | 4             | 4           | 1        | 25.00% | 0           | 0.00% | 0 | 0.00% |
| Total                  | 552                        | 5        | 319           | 301         | 18       | 5.98%  | 1           | 0.33% | 0 | 0.00% |
| 500009.10-CP 500009.10 |                            |          |               |             |          |        |             |       |   |       |
| Precinct TSx           | 190                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Precinct OS            | 190                        | 1        | 14            | 13          | 0        | 0.00%  | 0           | 0.00% | 0 | 0.00% |
| Precinct VBM           | 190                        | 1        | 78            | 76          | 4        | 5.26%  | 0           | 0.00% | 0 | 0.00% |
| MB Precinct            | 190                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Provisional            | 190                        | 1        | 5             | 5           | 0        | 0.00%  | 0           | 0.00% | 0 | 0.00% |
| Total                  | 190                        | 5        | 97            | 94          | 4        | 4.26%  | 0           | 0.00% | 0 | 0.00% |
| 500011.10-CP 500011.10 |                            |          |               |             |          |        |             |       |   |       |
| Precinct TSx           | 317                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Precinct OS            | 317                        | 1        | 40            | 39          | 3        | 7.69%  | 0           | 0.00% | 0 | 0.00% |
| Precinct VBM           | 317                        | 1        | 94            | 92          | 3        | 3.26%  | 2           | 2.17% | 0 | 0.00% |
| MB Precinct            | 317                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Provisional            | 317                        | 1        | 7             | 7           | 0        | 0.00%  | 0           | 0.00% | 0 | 0.00% |
| Total                  | 317                        | 5        | 141           | 138         | 6        | 4.35%  | 2           | 1.45% | 0 | 0.00% |
| 500012.10-CP 500012.10 |                            |          |               |             |          |        |             |       |   |       |
| Precinct TSx           | 413                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Precinct OS            | 413                        | 1        | 38            | 36          | 3        | 8.33%  | 1           | 2.78% | 0 | 0.00% |
| Precinct VBM           | 413                        | 1        | 192           | 187         | 25       | 13.37% | 2           | 1.07% | 0 | 0.00% |
| MB Precinct            | 413                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Provisional            | 413                        | 1        | 3             | 3           | 0        | 0.00%  | 0           | 0.00% | 0 | 0.00% |
| Total                  | 413                        | 5        | 233           | 226         | 28       | 12.39% | 3           | 1.33% | 0 | 0.00% |
| 500015.10-CP 500015.10 |                            |          |               |             |          |        |             |       |   |       |
| Precinct TSx           | 233                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Precinct OS            | 233                        | 1        | 26            | 25          | 2        | 8.00%  | 1           | 4.00% | 0 | 0.00% |
| Precinct VBM           | 233                        | 1        | 110           | 104         | 6        | 5.77%  | 0           | 0.00% | 0 | 0.00% |
| MB Precinct            | 233                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Provisional            | 233                        | 1        | 3             | 2           | 0        | 0.00%  | 0           | 0.00% | 0 | 0.00% |
| Total                  | 233                        | 5        | 139           | 131         | 8        | 6.11%  | 1           | 0.76% | 0 | 0.00% |
| 500020.10-CP 500020.10 |                            |          |               |             |          |        |             |       |   |       |
| Precinct TSx           | 597                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Precinct OS            | 597                        | 1        | 71            | 67          | 3        | 4.48%  | 1           | 1.49% | 0 | 0.00% |
| Precinct VBM           | 597                        | 1        | 281           | 265         | 17       | 6.42%  | 0           | 0.00% | 0 | 0.00% |
| MB Precinct            | 597                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Provisional            | 597                        | 1        | 7             | 7           | 0        | 0.00%  | 0           | 0.00% | 0 | 0.00% |
| Total                  | 597                        | 5        | 359           | 339         | 20       | 5.90%  | 1           | 0.29% | 0 | 0.00% |
| 500021.10-CP 500021.10 |                            |          |               |             |          |        |             |       |   |       |
| Precinct TSx           | 645                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Precinct OS            | 645                        | 1        | 77            | 77          | 7        | 9.09%  | 0           | 0.00% | 0 | 0.00% |
| Precinct VBM           | 645                        | 1        | 311           | 301         | 31       | 10.30% | 0           | 0.00% | 0 | 0.00% |
| MB Precinct            | 645                        | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |
| Provisional            | 645                        | 1        | 9             | 7           | 0        | 0.00%  | 0           | 0.00% | 0 | 0.00% |
| Total                  | 645                        | 5        | 397           | 385         | 38       | 9.87%  | 0           | 0.00% | 0 | 0.00% |
| 95U201.01-MB 95U201.01 |                            |          |               |             |          |        |             |       |   |       |
| Precinct TSx           | 5                          | 1        | 0             | 0           | 0        | -      | 0           | -     | 0 | -     |

**COUNTY OF EL DORADO**  
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|                        | President Republican Party |          |               |             |          |        |             |       |   |   |
|------------------------|----------------------------|----------|---------------|-------------|----------|--------|-------------|-------|---|---|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | TED CRUZ |        | JIM GILMORE |       |   |   |
| Precinct OS            | 5                          | 1        | 0             | 0           | 0        | 0      | -           | 0     | - | - |
| Precinct VBM           | 5                          | 1        | 0             | 0           | 0        | 0      | -           | 0     | - | - |
| MB Precinct            | 5                          | 1        | 3             | 3           | 2        | 66.67% | 0           | 0.00% |   |   |
| Provisional            | 5                          | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Total                  | 5                          | 5        | 3             | 3           | 2        | 66.67% | 0           | 0.00% |   |   |
| 95U201.02-MB 95U201.02 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 283                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 283                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct VBM           | 283                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| MB Precinct            | 283                        | 1        | 185           | 177         | 11       | 6.21%  | 0           | 0.00% |   |   |
| Provisional            | 283                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Total                  | 283                        | 5        | 185           | 177         | 11       | 6.21%  | 0           | 0.00% |   |   |
| 95U202.02-MB 95U202.02 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 388                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 388                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct VBM           | 388                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| MB Precinct            | 388                        | 1        | 268           | 255         | 9        | 3.53%  | 0           | 0.00% |   |   |
| Provisional            | 388                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Total                  | 388                        | 5        | 268           | 255         | 9        | 3.53%  | 0           | 0.00% |   |   |
| 95U203.02-MB 95U203.02 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct VBM           | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| MB Precinct            | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Provisional            | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Total                  | 0                          | 5        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| 95U204.01-MB 95U204.01 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 461                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 461                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct VBM           | 461                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| MB Precinct            | 461                        | 1        | 351           | 335         | 27       | 8.06%  | 2           | 0.60% |   |   |
| Provisional            | 461                        | 1        | 2             | 2           | 0        | 0.00%  | 0           | 0.00% |   |   |
| Total                  | 461                        | 5        | 353           | 337         | 27       | 8.01%  | 2           | 0.59% |   |   |
| 95U301.05-MB 95U301.05 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 119                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 119                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct VBM           | 119                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| MB Precinct            | 119                        | 1        | 77            | 76          | 2        | 2.63%  | 0           | 0.00% |   |   |
| Provisional            | 119                        | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Total                  | 119                        | 5        | 77            | 76          | 2        | 2.63%  | 0           | 0.00% |   |   |
| 95U302.04-MB 95U302.04 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct VBM           | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| MB Precinct            | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Provisional            | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Total                  | 0                          | 5        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| 95U302.05-MB 95U302.05 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 61                         | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 61                         | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct VBM           | 61                         | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| MB Precinct            | 61                         | 1        | 40            | 39          | 1        | 2.56%  | 0           | 0.00% |   |   |
| Provisional            | 61                         | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Total                  | 61                         | 5        | 40            | 39          | 1        | 2.56%  | 0           | 0.00% |   |   |
| 95U303.03-MB 95U303.03 |                            |          |               |             |          |        |             |       |   |   |
| Precinct TSx           | 60                         | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct OS            | 60                         | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Precinct VBM           | 60                         | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| MB Precinct            | 60                         | 1        | 36            | 36          | 3        | 8.33%  | 0           | 0.00% |   |   |
| Provisional            | 60                         | 1        | 0             | 0           | 0        | -      | 0           | -     |   |   |
| Total                  | 60                         | 5        | 36            | 36          | 3        | 8.33%  | 0           | 0.00% |   |   |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | President Republican Party |          |               |             |          |        |             |       |
|------------------------|----------------------------|----------|---------------|-------------|----------|--------|-------------|-------|
|                        | Reg. Voters                | Vote For | Times Counted | Total Votes | TED CRUZ |        | JIM GILMORE |       |
| 95U303.05-MB 95U303.05 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 76                         | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 76                         | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct VBM           | 76                         | 1        | 0             | 0           | 0        | -      | 0           | -     |
| MB Precinct            | 76                         | 1        | 59            | 56          | 8        | 14.29% | 1           | 1.79% |
| Provisional            | 76                         | 1        | 1             | 1           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 76                         | 5        | 60            | 57          | 8        | 14.04% | 1           | 1.75% |
| 95U304.05-MB 95U304.05 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct VBM           | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| MB Precinct            | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Total                  | 0                          | 5        | 0             | 0           | 0        | -      | 0           | -     |
| 95U305.05-MB 95U305.05 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 6                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 6                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct VBM           | 6                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| MB Precinct            | 6                          | 1        | 5             | 5           | 0        | 0.00%  | 0           | 0.00% |
| Provisional            | 6                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Total                  | 6                          | 5        | 5             | 5           | 0        | 0.00%  | 0           | 0.00% |
| 95U401.08-MB 95U401.08 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 14                         | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 14                         | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct VBM           | 14                         | 1        | 0             | 0           | 0        | -      | 0           | -     |
| MB Precinct            | 14                         | 1        | 5             | 5           | 0        | 0.00%  | 0           | 0.00% |
| Provisional            | 14                         | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Total                  | 14                         | 5        | 5             | 5           | 0        | 0.00%  | 0           | 0.00% |
| 95U402.07-MB 95U402.07 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 197                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 197                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct VBM           | 197                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| MB Precinct            | 197                        | 1        | 133           | 125         | 10       | 8.00%  | 0           | 0.00% |
| Provisional            | 197                        | 1        | 1             | 0           | 0        | -      | 0           | -     |
| Total                  | 197                        | 5        | 134           | 125         | 10       | 8.00%  | 0           | 0.00% |
| 95U402.08-MB 95U402.08 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 468                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 468                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct VBM           | 468                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| MB Precinct            | 468                        | 1        | 319           | 308         | 17       | 5.52%  | 1           | 0.32% |
| Provisional            | 468                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Total                  | 468                        | 5        | 319           | 308         | 17       | 5.52%  | 1           | 0.32% |
| 95U403.08-MB 95U403.08 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 643                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 643                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct VBM           | 643                        | 1        | 0             | 0           | 0        | -      | 0           | -     |
| MB Precinct            | 643                        | 1        | 439           | 425         | 25       | 5.88%  | 2           | 0.47% |
| Provisional            | 643                        | 1        | 6             | 5           | 0        | 0.00%  | 0           | 0.00% |
| Total                  | 643                        | 5        | 445           | 430         | 25       | 5.81%  | 2           | 0.47% |
| 95U404.06-MB 95U404.06 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct VBM           | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| MB Precinct            | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Provisional            | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Total                  | 0                          | 5        | 0             | 0           | 0        | -      | 0           | -     |
| 95U404.08-MB 95U404.08 |                            |          |               |             |          |        |             |       |
| Precinct TSx           | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct OS            | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| Precinct VBM           | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |
| MB Precinct            | 0                          | 1        | 0             | 0           | 0        | -      | 0           | -     |





**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                     | President Republican Party |            |               |              |             |              |             |              |
|-------------------------------------|----------------------------|------------|---------------|--------------|-------------|--------------|-------------|--------------|
|                                     | Reg. Voters                | Vote For   | Times Counted | Total Votes  | TED CRUZ    |              | JIM GILMORE |              |
| Precinct TSx                        | 47243                      | 102        | 4             | 4            | 0           | 0.00%        | 1           | 25.00%       |
| Precinct OS                         | 47243                      | 102        | 5435          | 5222         | 476         | 9.12%        | 27          | 0.52%        |
| Precinct VBM                        | 47243                      | 102        | 21600         | 20627        | 1410        | 6.84%        | 85          | 0.41%        |
| MB Precinct                         | 47243                      | 102        | 2466          | 2376         | 145         | 6.10%        | 10          | 0.42%        |
| Provisional                         | 47243                      | 102        | 505           | 481          | 26          | 5.41%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>47243</b>               | <b>510</b> | <b>30010</b>  | <b>28710</b> | <b>2057</b> | <b>7.16%</b> | <b>123</b>  | <b>0.43%</b> |
| <b>UNITED STATES SENATE</b>         |                            |            |               |              |             |              |             |              |
| <b>UNITED STATES SENATE</b>         |                            |            |               |              |             |              |             |              |
| Precinct TSx                        | 47243                      | 102        | 4             | 4            | 0           | 0.00%        | 1           | 25.00%       |
| Precinct OS                         | 47243                      | 102        | 5435          | 5222         | 476         | 9.12%        | 27          | 0.52%        |
| Precinct VBM                        | 47243                      | 102        | 21600         | 20627        | 1410        | 6.84%        | 85          | 0.41%        |
| MB Precinct                         | 47243                      | 102        | 2466          | 2376         | 145         | 6.10%        | 10          | 0.42%        |
| Provisional                         | 47243                      | 102        | 505           | 481          | 26          | 5.41%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>47243</b>               | <b>510</b> | <b>30010</b>  | <b>28710</b> | <b>2057</b> | <b>7.16%</b> | <b>123</b>  | <b>0.43%</b> |
| <b>Total</b>                        |                            |            |               |              |             |              |             |              |
| Precinct TSx                        | 47243                      | 102        | 4             | 4            | 0           | 0.00%        | 1           | 25.00%       |
| Precinct OS                         | 47243                      | 102        | 5435          | 5222         | 476         | 9.12%        | 27          | 0.52%        |
| Precinct VBM                        | 47243                      | 102        | 21600         | 20627        | 1410        | 6.84%        | 85          | 0.41%        |
| MB Precinct                         | 47243                      | 102        | 2466          | 2376         | 145         | 6.10%        | 10          | 0.42%        |
| Provisional                         | 47243                      | 102        | 505           | 481          | 26          | 5.41%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>47243</b>               | <b>510</b> | <b>30010</b>  | <b>28710</b> | <b>2057</b> | <b>7.16%</b> | <b>123</b>  | <b>0.43%</b> |
| <b>Total</b>                        |                            |            |               |              |             |              |             |              |
| <b>UNITED STATES REPRESENTATIVE</b> |                            |            |               |              |             |              |             |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                            |            |               |              |             |              |             |              |
| Precinct TSx                        | 47243                      | 102        | 4             | 4            | 0           | 0.00%        | 1           | 25.00%       |
| Precinct OS                         | 47243                      | 102        | 5435          | 5222         | 476         | 9.12%        | 27          | 0.52%        |
| Precinct VBM                        | 47243                      | 102        | 21600         | 20627        | 1410        | 6.84%        | 85          | 0.41%        |
| MB Precinct                         | 47243                      | 102        | 2466          | 2376         | 145         | 6.10%        | 10          | 0.42%        |
| Provisional                         | 47243                      | 102        | 505           | 481          | 26          | 5.41%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>47243</b>               | <b>510</b> | <b>30010</b>  | <b>28710</b> | <b>2057</b> | <b>7.16%</b> | <b>123</b>  | <b>0.43%</b> |
| <b>Total</b>                        |                            |            |               |              |             |              |             |              |
| Precinct TSx                        | 47243                      | 102        | 4             | 4            | 0           | 0.00%        | 1           | 25.00%       |
| Precinct OS                         | 47243                      | 102        | 5435          | 5222         | 476         | 9.12%        | 27          | 0.52%        |
| Precinct VBM                        | 47243                      | 102        | 21600         | 20627        | 1410        | 6.84%        | 85          | 0.41%        |
| MB Precinct                         | 47243                      | 102        | 2466          | 2376         | 145         | 6.10%        | 10          | 0.42%        |
| Provisional                         | 47243                      | 102        | 505           | 481          | 26          | 5.41%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>47243</b>               | <b>510</b> | <b>30010</b>  | <b>28710</b> | <b>2057</b> | <b>7.16%</b> | <b>123</b>  | <b>0.43%</b> |
| <b>Total</b>                        |                            |            |               |              |             |              |             |              |
| <b>STATE SENATE</b>                 |                            |            |               |              |             |              |             |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                            |            |               |              |             |              |             |              |
| Precinct TSx                        | 47243                      | 102        | 4             | 4            | 0           | 0.00%        | 1           | 25.00%       |
| Precinct OS                         | 47243                      | 102        | 5435          | 5222         | 476         | 9.12%        | 27          | 0.52%        |
| Precinct VBM                        | 47243                      | 102        | 21600         | 20627        | 1410        | 6.84%        | 85          | 0.41%        |
| MB Precinct                         | 47243                      | 102        | 2466          | 2376         | 145         | 6.10%        | 10          | 0.42%        |
| Provisional                         | 47243                      | 102        | 505           | 481          | 26          | 5.41%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>47243</b>               | <b>510</b> | <b>30010</b>  | <b>28710</b> | <b>2057</b> | <b>7.16%</b> | <b>123</b>  | <b>0.43%</b> |
| <b>Total</b>                        |                            |            |               |              |             |              |             |              |
| Precinct TSx                        | 47243                      | 102        | 4             | 4            | 0           | 0.00%        | 1           | 25.00%       |
| Precinct OS                         | 47243                      | 102        | 5435          | 5222         | 476         | 9.12%        | 27          | 0.52%        |
| Precinct VBM                        | 47243                      | 102        | 21600         | 20627        | 1410        | 6.84%        | 85          | 0.41%        |
| MB Precinct                         | 47243                      | 102        | 2466          | 2376         | 145         | 6.10%        | 10          | 0.42%        |
| Provisional                         | 47243                      | 102        | 505           | 481          | 26          | 5.41%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>47243</b>               | <b>510</b> | <b>30010</b>  | <b>28710</b> | <b>2057</b> | <b>7.16%</b> | <b>123</b>  | <b>0.43%</b> |
| <b>Total</b>                        |                            |            |               |              |             |              |             |              |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                            |            |               |              |             |              |             |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                            |            |               |              |             |              |             |              |
| Precinct TSx                        | 28631                      | 75         | 2             | 2            | 0           | 0.00%        | 0           | 0.00%        |
| Precinct OS                         | 28631                      | 75         | 3328          | 3213         | 287         | 8.93%        | 15          | 0.47%        |
| Precinct VBM                        | 28631                      | 75         | 12668         | 12143        | 848         | 6.98%        | 52          | 0.43%        |
| MB Precinct                         | 28631                      | 75         | 2045          | 1966         | 126         | 6.41%        | 7           | 0.36%        |

**COUNTY OF EL DORADO**  
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|                                   | President Republican Party |          |               |             |          |        |             |        |
|-----------------------------------|----------------------------|----------|---------------|-------------|----------|--------|-------------|--------|
|                                   | Reg. Voters                | Vote For | Times Counted | Total Votes | TED CRUZ |        | JIM GILMORE |        |
| Provisional                       | 28631                      | 75       | 300           | 289         | 16       | 5.54%  | 0           | 0.00%  |
| Total                             | 28631                      | 375      | 18343         | 17613       | 1277     | 7.25%  | 74          | 0.42%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |                            |          |               |             |          |        |             |        |
| Precinct TSx                      | 18612                      | 27       | 2             | 2           | 0        | 0.00%  | 1           | 50.00% |
| Precinct OS                       | 18612                      | 27       | 2107          | 2009        | 189      | 9.41%  | 12          | 0.60%  |
| Precinct VBM                      | 18612                      | 27       | 8932          | 8484        | 562      | 6.62%  | 33          | 0.39%  |
| MB Precinct                       | 18612                      | 27       | 421           | 410         | 19       | 4.63%  | 3           | 0.73%  |
| Provisional                       | 18612                      | 27       | 205           | 192         | 10       | 5.21%  | 0           | 0.00%  |
| Total                             | 18612                      | 135      | 11667         | 11097       | 780      | 7.03%  | 49          | 0.44%  |
| <b>Total</b>                      |                            |          |               |             |          |        |             |        |
| Precinct TSx                      | 47243                      | 102      | 4             | 4           | 0        | 0.00%  | 1           | 25.00% |
| Precinct OS                       | 47243                      | 102      | 5435          | 5222        | 476      | 9.12%  | 27          | 0.52%  |
| Precinct VBM                      | 47243                      | 102      | 21600         | 20627       | 1410     | 6.84%  | 85          | 0.41%  |
| MB Precinct                       | 47243                      | 102      | 2466          | 2376        | 145      | 6.10%  | 10          | 0.42%  |
| Provisional                       | 47243                      | 102      | 505           | 481         | 26       | 5.41%  | 0           | 0.00%  |
| Total                             | 47243                      | 510      | 30010         | 28710       | 2057     | 7.16%  | 123         | 0.43%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |                            |          |               |             |          |        |             |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                            |          |               |             |          |        |             |        |
| Precinct TSx                      | 11033                      | 14       | 0             | 0           | 0        | -      | 0           | -      |
| Precinct OS                       | 11033                      | 14       | 1196          | 1130        | 95       | 8.41%  | 7           | 0.62%  |
| Precinct VBM                      | 11033                      | 14       | 5426          | 5137        | 312      | 6.07%  | 20          | 0.39%  |
| MB Precinct                       | 11033                      | 14       | 233           | 227         | 11       | 4.85%  | 3           | 1.32%  |
| Provisional                       | 11033                      | 14       | 116           | 108         | 4        | 3.70%  | 0           | 0.00%  |
| Total                             | 11033                      | 70       | 6971          | 6602        | 422      | 6.39%  | 30          | 0.45%  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                            |          |               |             |          |        |             |        |
| Precinct TSx                      | 10824                      | 23       | 2             | 2           | 0        | 0.00%  | 1           | 50.00% |
| Precinct OS                       | 10824                      | 23       | 1242          | 1200        | 110      | 9.17%  | 4           | 0.33%  |
| Precinct VBM                      | 10824                      | 23       | 4783          | 4567        | 316      | 6.92%  | 13          | 0.28%  |
| MB Precinct                       | 10824                      | 23       | 807           | 770         | 49       | 6.36%  | 2           | 0.26%  |
| Provisional                       | 10824                      | 23       | 125           | 121         | 9        | 7.44%  | 0           | 0.00%  |
| Total                             | 10824                      | 115      | 6959          | 6660        | 484      | 7.27%  | 20          | 0.30%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                            |          |               |             |          |        |             |        |
| Precinct TSx                      | 9332                       | 26       | 2             | 2           | 0        | 0.00%  | 0           | 0.00%  |
| Precinct OS                       | 9332                       | 26       | 1121          | 1081        | 116      | 10.73% | 7           | 0.65%  |
| Precinct VBM                      | 9332                       | 26       | 4675          | 4466        | 338      | 7.57%  | 18          | 0.40%  |
| MB Precinct                       | 9332                       | 26       | 217           | 212         | 14       | 6.60%  | 1           | 0.47%  |
| Provisional                       | 9332                       | 26       | 93            | 91          | 8        | 8.79%  | 0           | 0.00%  |
| Total                             | 9332                       | 130      | 6108          | 5852        | 476      | 8.13%  | 26          | 0.44%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                            |          |               |             |          |        |             |        |
| Precinct TSx                      | 11200                      | 22       | 0             | 0           | 0        | -      | 0           | -      |
| Precinct OS                       | 11200                      | 22       | 1351          | 1302        | 122      | 9.37%  | 5           | 0.38%  |
| Precinct VBM                      | 11200                      | 22       | 4797          | 4626        | 313      | 6.77%  | 24          | 0.52%  |
| MB Precinct                       | 11200                      | 22       | 1084          | 1046        | 60       | 5.74%  | 3           | 0.29%  |
| Provisional                       | 11200                      | 22       | 118           | 111         | 3        | 2.70%  | 0           | 0.00%  |
| Total                             | 11200                      | 110      | 7350          | 7085        | 498      | 7.03%  | 32          | 0.45%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                            |          |               |             |          |        |             |        |
| Precinct TSx                      | 4854                       | 17       | 0             | 0           | 0        | -      | 0           | -      |
| Precinct OS                       | 4854                       | 17       | 525           | 509         | 33       | 6.48%  | 4           | 0.79%  |
| Precinct VBM                      | 4854                       | 17       | 1919          | 1831        | 131      | 7.15%  | 10          | 0.55%  |
| MB Precinct                       | 4854                       | 17       | 125           | 121         | 11       | 9.09%  | 1           | 0.83%  |
| Provisional                       | 4854                       | 17       | 53            | 50          | 2        | 4.00%  | 0           | 0.00%  |
| Total                             | 4854                       | 85       | 2622          | 2511        | 177      | 7.05%  | 15          | 0.60%  |
| <b>Total</b>                      |                            |          |               |             |          |        |             |        |
| Precinct TSx                      | 47243                      | 102      | 4             | 4           | 0        | 0.00%  | 1           | 25.00% |
| Precinct OS                       | 47243                      | 102      | 5435          | 5222        | 476      | 9.12%  | 27          | 0.52%  |
| Precinct VBM                      | 47243                      | 102      | 21600         | 20627       | 1410     | 6.84%  | 85          | 0.41%  |
| MB Precinct                       | 47243                      | 102      | 2466          | 2376        | 145      | 6.10%  | 10          | 0.42%  |
| Provisional                       | 47243                      | 102      | 505           | 481         | 26       | 5.41%  | 0           | 0.00%  |
| Total                             | 47243                      | 510      | 30010         | 28710       | 2057     | 7.16%  | 123         | 0.43%  |

**COUNTY OF EL DORADO**  
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|                                 | President Republican Party |            |               |              |             |              |             |              |
|---------------------------------|----------------------------|------------|---------------|--------------|-------------|--------------|-------------|--------------|
|                                 | Reg. Voters                | Vote For   | Times Counted | Total Votes  | TED CRUZ    |              | JIM GILMORE |              |
| <b>CITIES</b>                   |                            |            |               |              |             |              |             |              |
| <b>CITY OF PLACERVILLE</b>      |                            |            |               |              |             |              |             |              |
| Precinct TSx                    | 2029                       | 5          | 0             | 0            | 0           | -            | 0           | -            |
| Precinct OS                     | 2029                       | 5          | 242           | 230          | 28          | 12.17%       | 0           | 0.00%        |
| Precinct VBM                    | 2029                       | 5          | 1032          | 979          | 76          | 7.76%        | 5           | 0.51%        |
| MB Precinct                     | 2029                       | 5          | 0             | 0            | 0           | -            | 0           | -            |
| Provisional                     | 2029                       | 5          | 20            | 19           | 3           | 15.79%       | 0           | 0.00%        |
| <b>Total</b>                    | <b>2029</b>                | <b>25</b>  | <b>1294</b>   | <b>1228</b>  | <b>107</b>  | <b>8.71%</b> | <b>5</b>    | <b>0.41%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                            |            |               |              |             |              |             |              |
| Precinct TSx                    | 1974                       | 7          | 0             | 0            | 0           | -            | 0           | -            |
| Precinct OS                     | 1974                       | 7          | 236           | 230          | 14          | 6.09%        | 1           | 0.43%        |
| Precinct VBM                    | 1974                       | 7          | 744           | 712          | 42          | 5.90%        | 7           | 0.98%        |
| MB Precinct                     | 1974                       | 7          | 0             | 0            | 0           | -            | 0           | -            |
| Provisional                     | 1974                       | 7          | 29            | 29           | 1           | 3.45%        | 0           | 0.00%        |
| <b>Total</b>                    | <b>1974</b>                | <b>35</b>  | <b>1009</b>   | <b>971</b>   | <b>57</b>   | <b>5.87%</b> | <b>8</b>    | <b>0.82%</b> |
| <b>UNINCORPORATED AREA</b>      |                            |            |               |              |             |              |             |              |
| Precinct TSx                    | 43240                      | 90         | 4             | 4            | 0           | 0.00%        | 1           | 25.00%       |
| Precinct OS                     | 43240                      | 90         | 4957          | 4762         | 434         | 9.11%        | 26          | 0.55%        |
| Precinct VBM                    | 43240                      | 90         | 19824         | 18936        | 1292        | 6.82%        | 73          | 0.39%        |
| MB Precinct                     | 43240                      | 90         | 2466          | 2376         | 145         | 6.10%        | 10          | 0.42%        |
| Provisional                     | 43240                      | 90         | 456           | 433          | 22          | 5.08%        | 0           | 0.00%        |
| <b>Total</b>                    | <b>43240</b>               | <b>450</b> | <b>27707</b>  | <b>26511</b> | <b>1893</b> | <b>7.14%</b> | <b>110</b>  | <b>0.41%</b> |
| <b>Total</b>                    |                            |            |               |              |             |              |             |              |
| Precinct TSx                    | 47243                      | 102        | 4             | 4            | 0           | 0.00%        | 1           | 25.00%       |
| Precinct OS                     | 47243                      | 102        | 5435          | 5222         | 476         | 9.12%        | 27          | 0.52%        |
| Precinct VBM                    | 47243                      | 102        | 21600         | 20627        | 1410        | 6.84%        | 85          | 0.41%        |
| MB Precinct                     | 47243                      | 102        | 2466          | 2376         | 145         | 6.10%        | 10          | 0.42%        |
| Provisional                     | 47243                      | 102        | 505           | 481          | 26          | 5.41%        | 0           | 0.00%        |
| <b>Total</b>                    | <b>47243</b>               | <b>510</b> | <b>30010</b>  | <b>28710</b> | <b>2057</b> | <b>7.16%</b> | <b>123</b>  | <b>0.43%</b> |

**COUNTY OF EL DORADO**  
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|                        | President Republican Party |        |              |         |            |        |
|------------------------|----------------------------|--------|--------------|---------|------------|--------|
|                        | JOHN R. KASICH             |        | DONALD TRUMP |         | BEN CARSON |        |
| Jurisdiction Wide      |                            |        |              |         |            |        |
| 100001.11-CP 100001.11 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 12                         | 14.46% | 61           | 73.49%  | 2          | 2.41%  |
| Precinct VBM           | 48                         | 12.37% | 292          | 75.26%  | 8          | 2.06%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 25.00% | 3            | 75.00%  | 0          | 0.00%  |
| Total                  | 61                         | 12.84% | 356          | 74.95%  | 10         | 2.11%  |
| 100002.11-CP 100002.11 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 4                          | 13.33% | 22           | 73.33%  | 1          | 3.33%  |
| Precinct VBM           | 33                         | 13.69% | 166          | 68.88%  | 8          | 3.32%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 8            | 88.89%  | 1          | 11.11% |
| Total                  | 37                         | 13.21% | 196          | 70.00%  | 10         | 3.57%  |
| 100003.11-CP 100003.11 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 4                          | 5.97%  | 53           | 79.10%  | 5          | 7.46%  |
| Precinct VBM           | 47                         | 13.35% | 273          | 77.56%  | 4          | 1.14%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 2                          | 25.00% | 4            | 50.00%  | 0          | 0.00%  |
| Total                  | 53                         | 12.41% | 330          | 77.28%  | 9          | 2.11%  |
| 100004.11-CP 100004.11 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 16                         | 16.67% | 69           | 71.88%  | 3          | 3.13%  |
| Precinct VBM           | 46                         | 13.49% | 241          | 70.67%  | 6          | 1.76%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 1            | 100.00% | 0          | 0.00%  |
| Total                  | 62                         | 14.16% | 311          | 71.00%  | 9          | 2.05%  |
| 100005.11-CP 100005.11 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 9                          | 11.39% | 58           | 73.42%  | 2          | 2.53%  |
| Precinct VBM           | 57                         | 10.59% | 429          | 79.74%  | 10         | 1.86%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 8            | 100.00% | 0          | 0.00%  |
| Total                  | 66                         | 10.56% | 495          | 79.20%  | 12         | 1.92%  |
| 100006.11-CP 100006.11 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 12                         | 9.84%  | 90           | 73.77%  | 8          | 6.56%  |
| Precinct VBM           | 78                         | 18.27% | 291          | 68.15%  | 11         | 2.58%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 8.33%  | 10           | 83.33%  | 0          | 0.00%  |
| Total                  | 91                         | 16.22% | 391          | 69.70%  | 19         | 3.39%  |
| 100008.11-CP 100008.11 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 6                          | 7.89%  | 60           | 78.95%  | 3          | 3.95%  |
| Precinct VBM           | 44                         | 12.46% | 272          | 77.05%  | 6          | 1.70%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 2                          | 40.00% | 2            | 40.00%  | 1          | 20.00% |
| Total                  | 52                         | 11.98% | 334          | 76.96%  | 10         | 2.30%  |
| 100009.11-CP 100009.11 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 9                          | 13.43% | 47           | 70.15%  | 2          | 2.99%  |
| Precinct VBM           | 45                         | 13.31% | 257          | 76.04%  | 2          | 0.59%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 10.00% | 8            | 80.00%  | 1          | 10.00% |
| Total                  | 55                         | 13.25% | 312          | 75.18%  | 5          | 1.20%  |
| 100012.11-CP 100012.11 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 15                         | 13.39% | 69           | 61.61%  | 8          | 7.14%  |
| Precinct VBM           | 66                         | 14.16% | 350          | 75.11%  | 10         | 2.15%  |

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|------------------------|----------------------------|--------|--------------|---------|------------|--------|
|                        | JOHN R. KASICH             |        | DONALD TRUMP |         | BEN CARSON |        |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 11           | 84.62%  | 0          | 0.00%  |
| Total                  | 81                         | 13.71% | 430          | 72.76%  | 18         | 3.05%  |
| 100013.11-CP 100013.11 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 10                         | 9.17%  | 79           | 72.48%  | 0          | 0.00%  |
| Precinct VBM           | 47                         | 10.59% | 314          | 70.72%  | 24         | 5.41%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 10.00% | 6            | 60.00%  | 2          | 20.00% |
| Total                  | 58                         | 10.30% | 399          | 70.87%  | 26         | 4.62%  |
| 100014.11-CP 100014.11 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 11                         | 11.11% | 69           | 69.70%  | 7          | 7.07%  |
| Precinct VBM           | 34                         | 9.66%  | 258          | 73.30%  | 22         | 6.25%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 2                          | 15.38% | 10           | 76.92%  | 1          | 7.69%  |
| Total                  | 47                         | 10.13% | 337          | 72.63%  | 30         | 6.47%  |
| 100022.11-CP 100022.11 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 12                         | 16.00% | 51           | 68.00%  | 4          | 5.33%  |
| Precinct VBM           | 48                         | 13.75% | 269          | 77.08%  | 3          | 0.86%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 4            | 100.00% | 0          | 0.00%  |
| Total                  | 60                         | 14.02% | 324          | 75.70%  | 7          | 1.64%  |
| 100023.11-CP 100023.11 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 13                         | 11.30% | 79           | 68.70%  | 3          | 2.61%  |
| Precinct VBM           | 48                         | 8.76%  | 432          | 78.83%  | 21         | 3.83%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 9            | 90.00%  | 0          | 0.00%  |
| Total                  | 61                         | 9.06%  | 520          | 77.27%  | 24         | 3.57%  |
| 200003.12-CP 200003.12 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 4                          | 5.97%  | 50           | 74.63%  | 3          | 4.48%  |
| Precinct VBM           | 22                         | 8.66%  | 189          | 74.41%  | 17         | 6.69%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 10.00% | 7            | 70.00%  | 0          | 0.00%  |
| Total                  | 27                         | 8.16%  | 246          | 74.32%  | 20         | 6.04%  |
| 200004.12-CP 200004.12 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 8                          | 9.09%  | 62           | 70.45%  | 4          | 4.55%  |
| Precinct VBM           | 23                         | 8.07%  | 220          | 77.19%  | 9          | 3.16%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 3            | 60.00%  | 0          | 0.00%  |
| Total                  | 31                         | 8.20%  | 285          | 75.40%  | 13         | 3.44%  |
| 200005.02-CP 200005.02 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 3                          | 7.50%  | 27           | 67.50%  | 1          | 2.50%  |
| Precinct VBM           | 13                         | 6.31%  | 175          | 84.95%  | 7          | 3.40%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 5            | 83.33%  | 0          | 0.00%  |
| Total                  | 16                         | 6.35%  | 207          | 82.14%  | 8          | 3.17%  |
| 200006.12-CP 200006.12 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 5                          | 7.04%  | 53           | 74.65%  | 3          | 4.23%  |
| Precinct VBM           | 13                         | 4.42%  | 244          | 82.99%  | 10         | 3.40%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 9.09%  | 9            | 81.82%  | 0          | 0.00%  |
| Total                  | 19                         | 5.05%  | 306          | 81.38%  | 13         | 3.46%  |
| 200008.12-CP 200008.12 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |

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|                        | JOHN R. KASICH             |        | DONALD TRUMP |         | BEN CARSON |        |
| Precinct OS            | 9                          | 12.33% | 53           | 72.60%  | 3          | 4.11%  |
| Precinct VBM           | 11                         | 6.88%  | 120          | 75.00%  | 7          | 4.38%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 2            | 100.00% | 0          | 0.00%  |
| Total                  | 20                         | 8.51%  | 175          | 74.47%  | 10         | 4.26%  |
| 200010.12-CP 200010.12 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | 0.00%  | 1            | 50.00%  | 0          | 0.00%  |
| Precinct OS            | 7                          | 9.72%  | 52           | 72.22%  | 4          | 5.56%  |
| Precinct VBM           | 37                         | 9.27%  | 306          | 76.69%  | 14         | 3.51%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 11.11% | 8            | 88.89%  | 0          | 0.00%  |
| Total                  | 45                         | 9.34%  | 367          | 76.14%  | 18         | 3.73%  |
| 200012.12-CP 200012.12 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 5                          | 9.09%  | 37           | 67.27%  | 4          | 7.27%  |
| Precinct VBM           | 22                         | 9.82%  | 167          | 74.55%  | 12         | 5.36%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 25.00% | 2            | 50.00%  | 0          | 0.00%  |
| Total                  | 28                         | 9.89%  | 206          | 72.79%  | 16         | 5.65%  |
| 200013.12-CP 200013.12 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 8                          | 7.55%  | 74           | 69.81%  | 4          | 3.77%  |
| Precinct VBM           | 35                         | 9.41%  | 293          | 78.76%  | 11         | 2.96%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 12.50% | 7            | 87.50%  | 0          | 0.00%  |
| Total                  | 44                         | 9.05%  | 374          | 76.95%  | 15         | 3.09%  |
| 200014.12-CP 200014.12 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 11                         | 10.78% | 69           | 67.65%  | 6          | 5.88%  |
| Precinct VBM           | 25                         | 8.87%  | 211          | 74.82%  | 14         | 4.96%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 7            | 87.50%  | 0          | 0.00%  |
| Total                  | 36                         | 9.18%  | 287          | 73.21%  | 20         | 5.10%  |
| 200015.02-CP 200015.02 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 3                          | 3.41%  | 75           | 85.23%  | 7          | 7.95%  |
| Precinct VBM           | 28                         | 7.63%  | 290          | 79.02%  | 14         | 3.81%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 6            | 75.00%  | 2          | 25.00% |
| Total                  | 31                         | 6.70%  | 371          | 80.13%  | 23         | 4.97%  |
| 200016.12-CP 200016.12 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | 0.00%  | 16           | 100.00% | 0          | 0.00%  |
| Precinct VBM           | 7                          | 8.86%  | 56           | 70.89%  | 2          | 2.53%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 25.00% | 2            | 50.00%  | 0          | 0.00%  |
| Total                  | 8                          | 8.08%  | 74           | 74.75%  | 2          | 2.02%  |
| 200016.13-CP 200016.13 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 3                          | 6.25%  | 39           | 81.25%  | 3          | 6.25%  |
| Precinct VBM           | 23                         | 12.85% | 136          | 75.98%  | 2          | 1.12%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 4            | 80.00%  | 1          | 20.00% |
| Total                  | 26                         | 11.21% | 179          | 77.16%  | 6          | 2.59%  |
| 200017.02-CP 200017.02 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 3                          | 5.17%  | 43           | 74.14%  | 3          | 5.17%  |
| Precinct VBM           | 16                         | 6.78%  | 185          | 78.39%  | 12         | 5.08%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 5            | 83.33%  | 0          | 0.00%  |
| Total                  | 19                         | 6.33%  | 233          | 77.67%  | 15         | 5.00%  |

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|                        | JOHN R. KASICH             |        | DONALD TRUMP |         | BEN CARSON |        |
| 200018.02-CP 200018.02 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 5                          | 5.43%  | 76           | 82.61%  | 6          | 6.52%  |
| Precinct VBM           | 24                         | 7.00%  | 277          | 80.76%  | 9          | 2.62%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 5            | 83.33%  | 0          | 0.00%  |
| Total                  | 29                         | 6.58%  | 358          | 81.18%  | 15         | 3.40%  |
| 200020.02-CP 200020.02 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 7                          | 9.33%  | 55           | 73.33%  | 5          | 6.67%  |
| Precinct VBM           | 22                         | 8.53%  | 193          | 74.81%  | 9          | 3.49%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 5            | 71.43%  | 0          | 0.00%  |
| Total                  | 29                         | 8.53%  | 253          | 74.41%  | 14         | 4.12%  |
| 200021.01-CP 200021.01 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 3                          | 15.79% | 14           | 73.68%  | 0          | 0.00%  |
| Precinct VBM           | 3                          | 3.00%  | 88           | 88.00%  | 3          | 3.00%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 4            | 100.00% | 0          | 0.00%  |
| Total                  | 6                          | 4.88%  | 106          | 86.18%  | 3          | 2.44%  |
| 200021.02-CP 200021.02 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 14                         | 14.89% | 70           | 74.47%  | 1          | 1.06%  |
| Precinct VBM           | 32                         | 8.51%  | 300          | 79.79%  | 8          | 2.13%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 2                          | 20.00% | 7            | 70.00%  | 0          | 0.00%  |
| Total                  | 48                         | 10.00% | 377          | 78.54%  | 9          | 1.88%  |
| 200024.02-CP 200024.02 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 1                          | 2.78%  | 25           | 69.44%  | 2          | 5.56%  |
| Precinct VBM           | 3                          | 1.96%  | 127          | 83.01%  | 5          | 3.27%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 4            | 66.67%  | 0          | 0.00%  |
| Total                  | 4                          | 2.05%  | 156          | 80.00%  | 7          | 3.59%  |
| 300001.04-CP 300001.04 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | 0.00%  | 5            | 83.33%  | 0          | 0.00%  |
| Precinct VBM           | 2                          | 7.14%  | 22           | 78.57%  | 1          | 3.57%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 2                          | 5.88%  | 27           | 79.41%  | 1          | 2.94%  |
| 300001.05-CP 300001.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | 0.00%  | 30           | 85.71%  | 3          | 8.57%  |
| Precinct VBM           | 13                         | 10.16% | 94           | 73.44%  | 3          | 2.34%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 1            | 100.00% | 0          | 0.00%  |
| Total                  | 13                         | 7.93%  | 125          | 76.22%  | 6          | 3.66%  |
| 300003.05-CP 300003.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 8                          | 11.27% | 47           | 66.20%  | 5          | 7.04%  |
| Precinct VBM           | 54                         | 15.38% | 241          | 68.66%  | 12         | 3.42%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 16.67% | 4            | 66.67%  | 1          | 16.67% |
| Total                  | 63                         | 14.72% | 292          | 68.22%  | 18         | 4.21%  |
| 300004.05-CP 300004.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 6                          | 9.84%  | 43           | 70.49%  | 3          | 4.92%  |
| Precinct VBM           | 13                         | 6.16%  | 162          | 76.78%  | 10         | 4.74%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |

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|                        | JOHN R. KASICH             |        | DONALD TRUMP |         | BEN CARSON |        |
| Provisional            | 1                          | 25.00% | 0            | 0.00%   | 0          | 0.00%  |
| Total                  | 20                         | 7.25%  | 205          | 74.28%  | 13         | 4.71%  |
| 300005.05-CP 300005.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 8                          | 14.04% | 36           | 63.16%  | 2          | 3.51%  |
| Precinct VBM           | 40                         | 15.33% | 171          | 65.52%  | 13         | 4.98%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 4            | 50.00%  | 3          | 37.50% |
| Total                  | 48                         | 14.72% | 211          | 64.72%  | 18         | 5.52%  |
| 300008.05-CP 300008.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 6                          | 8.00%  | 53           | 70.67%  | 2          | 2.67%  |
| Precinct VBM           | 31                         | 8.64%  | 289          | 80.50%  | 15         | 4.18%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 11.11% | 5            | 55.56%  | 2          | 22.22% |
| Total                  | 38                         | 8.58%  | 347          | 78.33%  | 19         | 4.29%  |
| 300009.04-CP 300009.04 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 2                          | 3.57%  | 45           | 80.36%  | 2          | 3.57%  |
| Precinct VBM           | 16                         | 8.70%  | 132          | 71.74%  | 9          | 4.89%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 20.00% | 3            | 60.00%  | 0          | 0.00%  |
| Total                  | 19                         | 7.76%  | 180          | 73.47%  | 11         | 4.49%  |
| 300010.04-CP 300010.04 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 5                          | 5.49%  | 73           | 80.22%  | 0          | 0.00%  |
| Precinct VBM           | 27                         | 8.94%  | 227          | 75.17%  | 14         | 4.64%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 33.33% | 2            | 66.67%  | 0          | 0.00%  |
| Total                  | 33                         | 8.33%  | 302          | 76.26%  | 14         | 3.54%  |
| 300011.04-CP 300011.04 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 12                         | 15.58% | 56           | 72.73%  | 2          | 2.60%  |
| Precinct VBM           | 20                         | 5.92%  | 253          | 74.85%  | 14         | 4.14%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 7            | 100.00% | 0          | 0.00%  |
| Total                  | 32                         | 7.58%  | 316          | 74.88%  | 16         | 3.79%  |
| 300013.05-CP 300013.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | 0.00%  | 2            | 100.00% | 0          | 0.00%  |
| Precinct OS            | 5                          | 8.62%  | 41           | 70.69%  | 2          | 3.45%  |
| Precinct VBM           | 19                         | 4.95%  | 311          | 80.99%  | 17         | 4.43%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 7            | 87.50%  | 0          | 0.00%  |
| Total                  | 24                         | 5.31%  | 361          | 79.87%  | 19         | 4.20%  |
| 300015.04-CP 300015.04 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | 0.00%  | 5            | 83.33%  | 0          | 0.00%  |
| Precinct VBM           | 2                          | 6.25%  | 26           | 81.25%  | 1          | 3.13%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 1            | 100.00% | 0          | 0.00%  |
| Total                  | 2                          | 5.13%  | 32           | 82.05%  | 1          | 2.56%  |
| 300015.05-CP 300015.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 5                          | 17.86% | 20           | 71.43%  | 1          | 3.57%  |
| Precinct VBM           | 7                          | 6.86%  | 78           | 76.47%  | 4          | 3.92%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 1            | 100.00% | 0          | 0.00%  |
| Total                  | 12                         | 9.16%  | 99           | 75.57%  | 5          | 3.82%  |
| 300017.05-CP 300017.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 4                          | 6.35%  | 47           | 74.60%  | 2          | 3.17%  |



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|                        | JOHN R. KASICH             |        | DONALD TRUMP |         | BEN CARSON |        |
| Precinct VBM           | 18                         | 5.59%  | 254          | 78.88%  | 6          | 1.86%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 2            | 100.00% | 0          | 0.00%  |
| Total                  | 22                         | 5.68%  | 303          | 78.29%  | 8          | 2.07%  |
| 300018.04-CP 300018.04 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 2                          | 2.44%  | 61           | 74.39%  | 2          | 2.44%  |
| Precinct VBM           | 24                         | 10.57% | 159          | 70.04%  | 16         | 7.05%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 9            | 90.00%  | 0          | 0.00%  |
| Total                  | 26                         | 8.15%  | 229          | 71.79%  | 18         | 5.64%  |
| 300020.04-CP 300020.04 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 5                          | 6.94%  | 57           | 79.17%  | 1          | 1.39%  |
| Precinct VBM           | 26                         | 8.90%  | 225          | 77.05%  | 8          | 2.74%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 16.67% | 4            | 66.67%  | 0          | 0.00%  |
| Total                  | 32                         | 8.65%  | 286          | 77.30%  | 9          | 2.43%  |
| 300021.03-CP 300021.03 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 7                          | 11.29% | 42           | 67.74%  | 4          | 6.45%  |
| Precinct VBM           | 19                         | 7.09%  | 203          | 75.75%  | 17         | 6.34%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 5            | 100.00% | 0          | 0.00%  |
| Total                  | 26                         | 7.76%  | 250          | 74.63%  | 21         | 6.27%  |
| 300022.05-CP 300022.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 4                          | 6.67%  | 46           | 76.67%  | 2          | 3.33%  |
| Precinct VBM           | 9                          | 6.34%  | 121          | 85.21%  | 3          | 2.11%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 33.33% | 2            | 66.67%  | 0          | 0.00%  |
| Total                  | 14                         | 6.83%  | 169          | 82.44%  | 5          | 2.44%  |
| 300023.03-CP 300023.03 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 6                          | 5.94%  | 77           | 76.24%  | 6          | 5.94%  |
| Precinct VBM           | 42                         | 9.55%  | 342          | 77.73%  | 22         | 5.00%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 7            | 70.00%  | 1          | 10.00% |
| Total                  | 48                         | 8.71%  | 426          | 77.31%  | 29         | 5.26%  |
| 300023.05-CP 300023.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | 0.00%  | 17           | 85.00%  | 1          | 5.00%  |
| Precinct VBM           | 14                         | 14.74% | 68           | 71.58%  | 2          | 2.11%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 0            | 0.00%   | 0          | 0.00%  |
| Total                  | 14                         | 12.07% | 85           | 73.28%  | 3          | 2.59%  |
| 400001.14-CP 400001.14 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 13                         | 15.12% | 61           | 70.93%  | 3          | 3.49%  |
| Precinct VBM           | 45                         | 9.07%  | 368          | 74.19%  | 12         | 2.42%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 8            | 100.00% | 0          | 0.00%  |
| Total                  | 58                         | 9.83%  | 437          | 74.07%  | 15         | 2.54%  |
| 400002.08-CP 400002.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 7                          | 6.09%  | 87           | 75.65%  | 4          | 3.48%  |
| Precinct VBM           | 26                         | 6.67%  | 302          | 77.44%  | 24         | 6.15%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 4            | 66.67%  | 0          | 0.00%  |
| Total                  | 33                         | 6.46%  | 393          | 76.91%  | 28         | 5.48%  |
| 400003.08-CP 400003.08 |                            |        |              |         |            |        |

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|                        | JOHN R. KASICH             |        | DONALD TRUMP |         | BEN CARSON |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 9                          | 6.21%  | 119          | 82.07%  | 3          | 2.07%  |
| Precinct VBM           | 17                         | 4.67%  | 306          | 84.07%  | 12         | 3.30%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 12           | 100.00% | 0          | 0.00%  |
| Total                  | 26                         | 4.99%  | 437          | 83.88%  | 15         | 2.88%  |
| 400004.14-CP 400004.14 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 6                          | 6.32%  | 62           | 65.26%  | 7          | 7.37%  |
| Precinct VBM           | 26                         | 8.05%  | 247          | 76.47%  | 12         | 3.72%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 2                          | 22.22% | 6            | 66.67%  | 0          | 0.00%  |
| Total                  | 34                         | 7.96%  | 315          | 73.77%  | 19         | 4.45%  |
| 400005.08-CP 400005.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | 0.00%  | 27           | 72.97%  | 1          | 2.70%  |
| Precinct VBM           | 13                         | 11.61% | 82           | 73.21%  | 4          | 3.57%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 5            | 100.00% | 0          | 0.00%  |
| Total                  | 13                         | 8.44%  | 114          | 74.03%  | 5          | 3.25%  |
| 400006.08-CP 400006.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 9                          | 8.57%  | 81           | 77.14%  | 3          | 2.86%  |
| Precinct VBM           | 32                         | 10.16% | 239          | 75.87%  | 11         | 3.49%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 12.50% | 7            | 87.50%  | 0          | 0.00%  |
| Total                  | 42                         | 9.81%  | 327          | 76.40%  | 14         | 3.27%  |
| 400007.08-CP 400007.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 8                          | 6.84%  | 96           | 82.05%  | 5          | 4.27%  |
| Precinct VBM           | 28                         | 7.07%  | 308          | 77.78%  | 8          | 2.02%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 5            | 83.33%  | 0          | 0.00%  |
| Total                  | 36                         | 6.94%  | 409          | 78.81%  | 13         | 2.50%  |
| 400010.08-CP 400010.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 10                         | 10.20% | 69           | 70.41%  | 5          | 5.10%  |
| Precinct VBM           | 22                         | 6.13%  | 292          | 81.34%  | 9          | 2.51%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 6            | 75.00%  | 1          | 12.50% |
| Total                  | 32                         | 6.88%  | 367          | 78.92%  | 15         | 3.23%  |
| 400012.08-CP 400012.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 2                          | 4.44%  | 33           | 73.33%  | 3          | 6.67%  |
| Precinct VBM           | 24                         | 11.94% | 150          | 74.63%  | 5          | 2.49%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 8            | 100.00% | 0          | 0.00%  |
| Total                  | 26                         | 10.24% | 191          | 75.20%  | 8          | 3.15%  |
| 400013.08-CP 400013.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 5                          | 6.76%  | 59           | 79.73%  | 5          | 6.76%  |
| Precinct VBM           | 11                         | 7.14%  | 121          | 78.57%  | 13         | 8.44%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 1            | 100.00% | 0          | 0.00%  |
| Total                  | 16                         | 6.99%  | 181          | 79.04%  | 18         | 7.86%  |
| 400014.08-CP 400014.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 9                          | 7.83%  | 93           | 80.87%  | 3          | 2.61%  |
| Precinct VBM           | 36                         | 9.55%  | 307          | 81.43%  | 16         | 4.24%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 2                          | 12.50% | 14           | 87.50%  | 0          | 0.00%  |

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|------------------------|----------------------------|--------|--------------|---------|------------|--------|
|                        | JOHN R. KASICH             |        | DONALD TRUMP |         | BEN CARSON |        |
| Total                  | 47                         | 9.25%  | 414          | 81.50%  | 19         | 3.74%  |
| 400017.08-CP 400017.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 5                          | 5.62%  | 68           | 76.40%  | 3          | 3.37%  |
| Precinct VBM           | 28                         | 7.14%  | 323          | 82.40%  | 10         | 2.55%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 9            | 90.00%  | 1          | 10.00% |
| Total                  | 33                         | 6.72%  | 400          | 81.47%  | 14         | 2.85%  |
| 400018.08-CP 400018.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 1                          | 2.13%  | 37           | 78.72%  | 3          | 6.38%  |
| Precinct VBM           | 12                         | 7.55%  | 133          | 83.65%  | 2          | 1.26%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 1            | 100.00% | 0          | 0.00%  |
| Total                  | 13                         | 6.28%  | 171          | 82.61%  | 5          | 2.42%  |
| 400020.08-CP 400020.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 5                          | 7.46%  | 52           | 77.61%  | 6          | 8.96%  |
| Precinct VBM           | 10                         | 6.10%  | 136          | 82.93%  | 5          | 3.05%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 15                         | 6.49%  | 188          | 81.39%  | 11         | 4.76%  |
| 400021.07-CP 400021.07 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 12                         | 17.91% | 46           | 68.66%  | 2          | 2.99%  |
| Precinct VBM           | 54                         | 12.74% | 311          | 73.35%  | 13         | 3.07%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 7            | 100.00% | 0          | 0.00%  |
| Total                  | 66                         | 13.25% | 364          | 73.09%  | 15         | 3.01%  |
| 500001.10-CP 500001.10 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 6                          | 11.54% | 37           | 71.15%  | 5          | 9.62%  |
| Precinct VBM           | 20                         | 11.63% | 137          | 79.65%  | 4          | 2.33%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 26                         | 11.61% | 174          | 77.68%  | 9          | 4.02%  |
| 500002.10-CP 500002.10 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 6                          | 17.65% | 23           | 67.65%  | 3          | 8.82%  |
| Precinct VBM           | 8                          | 8.60%  | 70           | 75.27%  | 3          | 3.23%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 1            | 50.00%  | 0          | 0.00%  |
| Total                  | 14                         | 10.85% | 94           | 72.87%  | 6          | 4.65%  |
| 500003.10-CP 500003.10 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 4                          | 18.18% | 15           | 68.18%  | 0          | 0.00%  |
| Precinct VBM           | 11                         | 12.22% | 68           | 75.56%  | 2          | 2.22%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 1            | 100.00% | 0          | 0.00%  |
| Total                  | 15                         | 13.27% | 84           | 74.34%  | 2          | 1.77%  |
| 500004.10-CP 500004.10 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 3                          | 11.54% | 17           | 65.38%  | 2          | 7.69%  |
| Precinct VBM           | 7                          | 10.29% | 50           | 73.53%  | 1          | 1.47%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 2            | 66.67%  | 1          | 33.33% |
| Total                  | 10                         | 10.31% | 69           | 71.13%  | 4          | 4.12%  |
| 500005.10-CP 500005.10 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 3                          | 13.64% | 17           | 77.27%  | 2          | 9.09%  |
| Precinct VBM           | 1                          | 1.85%  | 42           | 77.78%  | 1          | 1.85%  |

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|------------------------|----------------------------|--------|--------------|---------|------------|--------|
|                        | JOHN R. KASICH             |        | DONALD TRUMP |         | BEN CARSON |        |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 2            | 100.00% | 0          | 0.00%  |
| Total                  | 4                          | 5.13%  | 61           | 78.21%  | 3          | 3.85%  |
| 500006.10-CP 500006.10 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 6                          | 21.43% | 15           | 53.57%  | 1          | 3.57%  |
| Precinct VBM           | 11                         | 11.00% | 75           | 75.00%  | 3          | 3.00%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 6            | 100.00% | 0          | 0.00%  |
| Total                  | 17                         | 12.69% | 96           | 71.64%  | 4          | 2.99%  |
| 500007.10-CP 500007.10 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 4                          | 5.88%  | 53           | 77.94%  | 5          | 7.35%  |
| Precinct VBM           | 26                         | 11.35% | 174          | 75.98%  | 9          | 3.93%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 2            | 50.00%  | 0          | 0.00%  |
| Total                  | 30                         | 9.97%  | 229          | 76.08%  | 14         | 4.65%  |
| 500009.10-CP 500009.10 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | 0.00%  | 13           | 100.00% | 0          | 0.00%  |
| Precinct VBM           | 8                          | 10.53% | 59           | 77.63%  | 3          | 3.95%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 4            | 80.00%  | 1          | 20.00% |
| Total                  | 8                          | 8.51%  | 76           | 80.85%  | 4          | 4.26%  |
| 500011.10-CP 500011.10 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 1                          | 2.56%  | 32           | 82.05%  | 2          | 5.13%  |
| Precinct VBM           | 11                         | 11.96% | 70           | 76.09%  | 0          | 0.00%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 2                          | 28.57% | 4            | 57.14%  | 1          | 14.29% |
| Total                  | 14                         | 10.14% | 106          | 76.81%  | 3          | 2.17%  |
| 500012.10-CP 500012.10 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 6                          | 16.67% | 23           | 63.89%  | 2          | 5.56%  |
| Precinct VBM           | 15                         | 8.02%  | 125          | 66.84%  | 8          | 4.28%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 33.33% | 2            | 66.67%  | 0          | 0.00%  |
| Total                  | 22                         | 9.73%  | 150          | 66.37%  | 10         | 4.42%  |
| 500015.10-CP 500015.10 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 4                          | 16.00% | 17           | 68.00%  | 0          | 0.00%  |
| Precinct VBM           | 21                         | 20.19% | 65           | 62.50%  | 2          | 1.92%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 50.00% | 1            | 50.00%  | 0          | 0.00%  |
| Total                  | 26                         | 19.85% | 83           | 63.36%  | 2          | 1.53%  |
| 500020.10-CP 500020.10 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 8                          | 11.94% | 52           | 77.61%  | 3          | 4.48%  |
| Precinct VBM           | 20                         | 7.55%  | 212          | 80.00%  | 7          | 2.64%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 1                          | 14.29% | 6            | 85.71%  | 0          | 0.00%  |
| Total                  | 29                         | 8.55%  | 270          | 79.65%  | 10         | 2.95%  |
| 500021.10-CP 500021.10 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 2                          | 2.60%  | 59           | 76.62%  | 7          | 9.09%  |
| Precinct VBM           | 21                         | 6.98%  | 228          | 75.75%  | 14         | 4.65%  |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | 0.00%  | 5            | 71.43%  | 1          | 14.29% |
| Total                  | 23                         | 5.97%  | 292          | 75.84%  | 22         | 5.71%  |
| 95U201.01-MB 95U201.01 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |

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|                        | JOHN R. KASICH             |        | DONALD TRUMP |         | BEN CARSON |        |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 0                          | 0.00%  | 1            | 33.33%  | 0          | 0.00%  |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 0                          | 0.00%  | 1            | 33.33%  | 0          | 0.00%  |
| 95U201.02-MB 95U201.02 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 22                         | 12.43% | 127          | 71.75%  | 9          | 5.08%  |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 22                         | 12.43% | 127          | 71.75%  | 9          | 5.08%  |
| 95U202.02-MB 95U202.02 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 11                         | 4.31%  | 221          | 86.67%  | 8          | 3.14%  |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 11                         | 4.31%  | 221          | 86.67%  | 8          | 3.14%  |
| 95U203.02-MB 95U203.02 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 0                          | -      | 0            | -       | 0          | -      |
| 95U204.01-MB 95U204.01 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 24                         | 7.16%  | 262          | 78.21%  | 14         | 4.18%  |
| Provisional            | 0                          | 0.00%  | 2            | 100.00% | 0          | 0.00%  |
| Total                  | 24                         | 7.12%  | 264          | 78.34%  | 14         | 4.15%  |
| 95U301.05-MB 95U301.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 6                          | 7.89%  | 63           | 82.89%  | 2          | 2.63%  |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 6                          | 7.89%  | 63           | 82.89%  | 2          | 2.63%  |
| 95U302.04-MB 95U302.04 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 0                          | -      | 0            | -       | 0          | -      |
| 95U302.05-MB 95U302.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 4                          | 10.26% | 28           | 71.79%  | 3          | 7.69%  |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 4                          | 10.26% | 28           | 71.79%  | 3          | 7.69%  |
| 95U303.03-MB 95U303.03 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 1                          | 2.78%  | 26           | 72.22%  | 4          | 11.11% |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 1                          | 2.78%  | 26           | 72.22%  | 4          | 11.11% |

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|                        | JOHN R. KASICH             |        | DONALD TRUMP |         | BEN CARSON |        |
| 95U303.05-MB 95U303.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 9                          | 16.07% | 36           | 64.29%  | 0          | 0.00%  |
| Provisional            | 0                          | 0.00%  | 1            | 100.00% | 0          | 0.00%  |
| Total                  | 9                          | 15.79% | 37           | 64.91%  | 0          | 0.00%  |
| 95U304.05-MB 95U304.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 0                          | -      | 0            | -       | 0          | -      |
| 95U305.05-MB 95U305.05 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 0                          | 0.00%  | 3            | 60.00%  | 2          | 40.00% |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 0                          | 0.00%  | 3            | 60.00%  | 2          | 40.00% |
| 95U401.08-MB 95U401.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 1                          | 20.00% | 4            | 80.00%  | 0          | 0.00%  |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 1                          | 20.00% | 4            | 80.00%  | 0          | 0.00%  |
| 95U402.07-MB 95U402.07 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 15                         | 12.00% | 91           | 72.80%  | 2          | 1.60%  |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 15                         | 12.00% | 91           | 72.80%  | 2          | 1.60%  |
| 95U402.08-MB 95U402.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 26                         | 8.44%  | 239          | 77.60%  | 13         | 4.22%  |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 26                         | 8.44%  | 239          | 77.60%  | 13         | 4.22%  |
| 95U403.08-MB 95U403.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 30                         | 7.06%  | 342          | 80.47%  | 10         | 2.35%  |
| Provisional            | 0                          | 0.00%  | 5            | 100.00% | 0          | 0.00%  |
| Total                  | 30                         | 6.98%  | 347          | 80.70%  | 10         | 2.33%  |
| 95U404.06-MB 95U404.06 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |
| Provisional            | 0                          | -      | 0            | -       | 0          | -      |
| Total                  | 0                          | -      | 0            | -       | 0          | -      |
| 95U404.08-MB 95U404.08 |                            |        |              |         |            |        |
| Precinct TSx           | 0                          | -      | 0            | -       | 0          | -      |
| Precinct OS            | 0                          | -      | 0            | -       | 0          | -      |
| Precinct VBM           | 0                          | -      | 0            | -       | 0          | -      |
| MB Precinct            | 0                          | -      | 0            | -       | 0          | -      |

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|                               | JOHN R. KASICH             |        | DONALD TRUMP |         | BEN CARSON |       |
| Provisional                   | 0                          | -      | 0            | -       | 0          | -     |
| Total                         | 0                          | -      | 0            | -       | 0          | -     |
| 95U501.09-MB 95U501.09        |                            |        |              |         |            |       |
| Precinct TSx                  | 0                          | -      | 0            | -       | 0          | -     |
| Precinct OS                   | 0                          | -      | 0            | -       | 0          | -     |
| Precinct VBM                  | 0                          | -      | 0            | -       | 0          | -     |
| MB Precinct                   | 0                          | -      | 0            | -       | 0          | -     |
| Provisional                   | 0                          | -      | 0            | -       | 0          | -     |
| Total                         | 0                          | -      | 0            | -       | 0          | -     |
| 95U501.10-MB 95U501.10        |                            |        |              |         |            |       |
| Precinct TSx                  | 0                          | -      | 0            | -       | 0          | -     |
| Precinct OS                   | 0                          | -      | 0            | -       | 0          | -     |
| Precinct VBM                  | 0                          | -      | 0            | -       | 0          | -     |
| MB Precinct                   | 5                          | 7.69%  | 47           | 72.31%  | 1          | 1.54% |
| Provisional                   | 0                          | -      | 0            | -       | 0          | -     |
| Total                         | 5                          | 7.69%  | 47           | 72.31%  | 1          | 1.54% |
| 95U598.10-MB 95U598.10        |                            |        |              |         |            |       |
| Precinct TSx                  | 0                          | -      | 0            | -       | 0          | -     |
| Precinct OS                   | 0                          | -      | 0            | -       | 0          | -     |
| Precinct VBM                  | 0                          | -      | 0            | -       | 0          | -     |
| MB Precinct                   | 10                         | 23.81% | 29           | 69.05%  | 0          | 0.00% |
| Provisional                   | 0                          | 0.00%  | 0            | 0.00%   | 0          | 0.00% |
| Total                         | 10                         | 23.26% | 29           | 67.44%  | 0          | 0.00% |
| 95U599.10-MB 95U599.10        |                            |        |              |         |            |       |
| Precinct TSx                  | 0                          | -      | 0            | -       | 0          | -     |
| Precinct OS                   | 0                          | -      | 0            | -       | 0          | -     |
| Precinct VBM                  | 0                          | -      | 0            | -       | 0          | -     |
| MB Precinct                   | 4                          | 28.57% | 8            | 57.14%  | 0          | 0.00% |
| Provisional                   | 0                          | -      | 0            | -       | 0          | -     |
| Total                         | 4                          | 28.57% | 8            | 57.14%  | 0          | 0.00% |
| 96U101.11-MB 96U101.11        |                            |        |              |         |            |       |
| Precinct TSx                  | 0                          | -      | 0            | -       | 0          | -     |
| Precinct OS                   | 0                          | -      | 0            | -       | 0          | -     |
| Precinct VBM                  | 0                          | -      | 0            | -       | 0          | -     |
| MB Precinct                   | 24                         | 10.57% | 174          | 76.65%  | 10         | 4.41% |
| Provisional                   | 0                          | 0.00%  | 1            | 100.00% | 0          | 0.00% |
| Total                         | 24                         | 10.53% | 175          | 76.75%  | 10         | 4.39% |
| 96U401.14-MB 96U401.14        |                            |        |              |         |            |       |
| Precinct TSx                  | 0                          | -      | 0            | -       | 0          | -     |
| Precinct OS                   | 0                          | -      | 0            | -       | 0          | -     |
| Precinct VBM                  | 0                          | -      | 0            | -       | 0          | -     |
| MB Precinct                   | 15                         | 8.20%  | 147          | 80.33%  | 4          | 2.19% |
| Provisional                   | 0                          | 0.00%  | 0            | 0.00%   | 0          | 0.00% |
| Total                         | 15                         | 8.15%  | 147          | 79.89%  | 4          | 2.17% |
| Total                         |                            |        |              |         |            |       |
| Precinct TSx                  | 0                          | 0.00%  | 3            | 75.00%  | 0          | 0.00% |
| Precinct OS                   | 471                        | 9.02%  | 3861         | 73.94%  | 235        | 4.50% |
| Precinct VBM                  | 1960                       | 9.50%  | 15799        | 76.59%  | 700        | 3.39% |
| MB Precinct                   | 207                        | 8.71%  | 1848         | 77.78%  | 82         | 3.45% |
| Provisional                   | 35                         | 7.28%  | 378          | 78.59%  | 22         | 4.57% |
| Total                         | 2673                       | 9.31%  | 21889        | 76.24%  | 1039       | 3.62% |
| STATE BOARD OF EQUALIZATION   |                            |        |              |         |            |       |
| STATE BOARD OF EQUALIZATION 1 |                            |        |              |         |            |       |
| Precinct TSx                  | 0                          | 0.00%  | 3            | 75.00%  | 0          | 0.00% |
| Precinct OS                   | 471                        | 9.02%  | 3861         | 73.94%  | 235        | 4.50% |
| Precinct VBM                  | 1960                       | 9.50%  | 15799        | 76.59%  | 700        | 3.39% |
| MB Precinct                   | 207                        | 8.71%  | 1848         | 77.78%  | 82         | 3.45% |
| Provisional                   | 35                         | 7.28%  | 378          | 78.59%  | 22         | 4.57% |
| Total                         | 2673                       | 9.31%  | 21889        | 76.24%  | 1039       | 3.62% |
| Total                         |                            |        |              |         |            |       |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                     | President Republican Party |              |              |               |             |              |
|-------------------------------------|----------------------------|--------------|--------------|---------------|-------------|--------------|
|                                     | JOHN R. KASICH             |              | DONALD TRUMP |               | BEN CARSON  |              |
| Precinct TSx                        | 0                          | 0.00%        | 3            | 75.00%        | 0           | 0.00%        |
| Precinct OS                         | 471                        | 9.02%        | 3861         | 73.94%        | 235         | 4.50%        |
| Precinct VBM                        | 1960                       | 9.50%        | 15799        | 76.59%        | 700         | 3.39%        |
| MB Precinct                         | 207                        | 8.71%        | 1848         | 77.78%        | 82          | 3.45%        |
| Provisional                         | 35                         | 7.28%        | 378          | 78.59%        | 22          | 4.57%        |
| <b>Total</b>                        | <b>2673</b>                | <b>9.31%</b> | <b>21889</b> | <b>76.24%</b> | <b>1039</b> | <b>3.62%</b> |
| <b>UNITED STATES SENATE</b>         |                            |              |              |               |             |              |
| <b>UNITED STATES SENATE</b>         |                            |              |              |               |             |              |
| Precinct TSx                        | 0                          | 0.00%        | 3            | 75.00%        | 0           | 0.00%        |
| Precinct OS                         | 471                        | 9.02%        | 3861         | 73.94%        | 235         | 4.50%        |
| Precinct VBM                        | 1960                       | 9.50%        | 15799        | 76.59%        | 700         | 3.39%        |
| MB Precinct                         | 207                        | 8.71%        | 1848         | 77.78%        | 82          | 3.45%        |
| Provisional                         | 35                         | 7.28%        | 378          | 78.59%        | 22          | 4.57%        |
| <b>Total</b>                        | <b>2673</b>                | <b>9.31%</b> | <b>21889</b> | <b>76.24%</b> | <b>1039</b> | <b>3.62%</b> |
| <b>Total</b>                        |                            |              |              |               |             |              |
| Precinct TSx                        | 0                          | 0.00%        | 3            | 75.00%        | 0           | 0.00%        |
| Precinct OS                         | 471                        | 9.02%        | 3861         | 73.94%        | 235         | 4.50%        |
| Precinct VBM                        | 1960                       | 9.50%        | 15799        | 76.59%        | 700         | 3.39%        |
| MB Precinct                         | 207                        | 8.71%        | 1848         | 77.78%        | 82          | 3.45%        |
| Provisional                         | 35                         | 7.28%        | 378          | 78.59%        | 22          | 4.57%        |
| <b>Total</b>                        | <b>2673</b>                | <b>9.31%</b> | <b>21889</b> | <b>76.24%</b> | <b>1039</b> | <b>3.62%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |                            |              |              |               |             |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                            |              |              |               |             |              |
| Precinct TSx                        | 0                          | 0.00%        | 3            | 75.00%        | 0           | 0.00%        |
| Precinct OS                         | 471                        | 9.02%        | 3861         | 73.94%        | 235         | 4.50%        |
| Precinct VBM                        | 1960                       | 9.50%        | 15799        | 76.59%        | 700         | 3.39%        |
| MB Precinct                         | 207                        | 8.71%        | 1848         | 77.78%        | 82          | 3.45%        |
| Provisional                         | 35                         | 7.28%        | 378          | 78.59%        | 22          | 4.57%        |
| <b>Total</b>                        | <b>2673</b>                | <b>9.31%</b> | <b>21889</b> | <b>76.24%</b> | <b>1039</b> | <b>3.62%</b> |
| <b>Total</b>                        |                            |              |              |               |             |              |
| Precinct TSx                        | 0                          | 0.00%        | 3            | 75.00%        | 0           | 0.00%        |
| Precinct OS                         | 471                        | 9.02%        | 3861         | 73.94%        | 235         | 4.50%        |
| Precinct VBM                        | 1960                       | 9.50%        | 15799        | 76.59%        | 700         | 3.39%        |
| MB Precinct                         | 207                        | 8.71%        | 1848         | 77.78%        | 82          | 3.45%        |
| Provisional                         | 35                         | 7.28%        | 378          | 78.59%        | 22          | 4.57%        |
| <b>Total</b>                        | <b>2673</b>                | <b>9.31%</b> | <b>21889</b> | <b>76.24%</b> | <b>1039</b> | <b>3.62%</b> |
| <b>STATE SENATE</b>                 |                            |              |              |               |             |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                            |              |              |               |             |              |
| Precinct TSx                        | 0                          | 0.00%        | 3            | 75.00%        | 0           | 0.00%        |
| Precinct OS                         | 471                        | 9.02%        | 3861         | 73.94%        | 235         | 4.50%        |
| Precinct VBM                        | 1960                       | 9.50%        | 15799        | 76.59%        | 700         | 3.39%        |
| MB Precinct                         | 207                        | 8.71%        | 1848         | 77.78%        | 82          | 3.45%        |
| Provisional                         | 35                         | 7.28%        | 378          | 78.59%        | 22          | 4.57%        |
| <b>Total</b>                        | <b>2673</b>                | <b>9.31%</b> | <b>21889</b> | <b>76.24%</b> | <b>1039</b> | <b>3.62%</b> |
| <b>Total</b>                        |                            |              |              |               |             |              |
| Precinct TSx                        | 0                          | 0.00%        | 3            | 75.00%        | 0           | 0.00%        |
| Precinct OS                         | 471                        | 9.02%        | 3861         | 73.94%        | 235         | 4.50%        |
| Precinct VBM                        | 1960                       | 9.50%        | 15799        | 76.59%        | 700         | 3.39%        |
| MB Precinct                         | 207                        | 8.71%        | 1848         | 77.78%        | 82          | 3.45%        |
| Provisional                         | 35                         | 7.28%        | 378          | 78.59%        | 22          | 4.57%        |
| <b>Total</b>                        | <b>2673</b>                | <b>9.31%</b> | <b>21889</b> | <b>76.24%</b> | <b>1039</b> | <b>3.62%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                            |              |              |               |             |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                            |              |              |               |             |              |
| Precinct TSx                        | 0                          | 0.00%        | 2            | 100.00%       | 0           | 0.00%        |
| Precinct OS                         | 259                        | 8.06%        | 2426         | 75.51%        | 143         | 4.45%        |
| Precinct VBM                        | 1030                       | 8.48%        | 9398         | 77.39%        | 443         | 3.65%        |
| MB Precinct                         | 168                        | 8.55%        | 1527         | 77.67%        | 68          | 3.46%        |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                   | President Republican Party |        |              |         |            |       |
|-----------------------------------|----------------------------|--------|--------------|---------|------------|-------|
|                                   | JOHN R. KASICH             |        | DONALD TRUMP |         | BEN CARSON |       |
| Provisional                       | 17                         | 5.88%  | 228          | 78.89%  | 15         | 5.19% |
| Total                             | 1474                       | 8.37%  | 13581        | 77.11%  | 669        | 3.80% |
| <b>6TH STATE ASSEMBLY DIST</b>    |                            |        |              |         |            |       |
| Precinct TSx                      | 0                          | 0.00%  | 1            | 50.00%  | 0          | 0.00% |
| Precinct OS                       | 212                        | 10.55% | 1435         | 71.43%  | 92         | 4.58% |
| Precinct VBM                      | 930                        | 10.96% | 6401         | 75.45%  | 257        | 3.03% |
| MB Precinct                       | 39                         | 9.51%  | 321          | 78.29%  | 14         | 3.41% |
| Provisional                       | 18                         | 9.38%  | 150          | 78.13%  | 7          | 3.65% |
| Total                             | 1199                       | 10.80% | 8308         | 74.87%  | 370        | 3.33% |
| <b>Total</b>                      |                            |        |              |         |            |       |
| Precinct TSx                      | 0                          | 0.00%  | 3            | 75.00%  | 0          | 0.00% |
| Precinct OS                       | 471                        | 9.02%  | 3861         | 73.94%  | 235        | 4.50% |
| Precinct VBM                      | 1960                       | 9.50%  | 15799        | 76.59%  | 700        | 3.39% |
| MB Precinct                       | 207                        | 8.71%  | 1848         | 77.78%  | 82         | 3.45% |
| Provisional                       | 35                         | 7.28%  | 378          | 78.59%  | 22         | 4.57% |
| Total                             | 2673                       | 9.31%  | 21889        | 76.24%  | 1039       | 3.62% |
| <b>SUPERVISORIAL DISTRICTS</b>    |                            |        |              |         |            |       |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                            |        |              |         |            |       |
| Precinct TSx                      | 0                          | -      | 0            | -       | 0          | -     |
| Precinct OS                       | 133                        | 11.77% | 807          | 71.42%  | 48         | 4.25% |
| Precinct VBM                      | 641                        | 12.48% | 3844         | 74.83%  | 135        | 2.63% |
| MB Precinct                       | 24                         | 10.57% | 174          | 76.65%  | 10         | 4.41% |
| Provisional                       | 10                         | 9.26%  | 85           | 78.70%  | 6          | 5.56% |
| Total                             | 808                        | 12.24% | 4910         | 74.37%  | 199        | 3.01% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                            |        |              |         |            |       |
| Precinct TSx                      | 0                          | 0.00%  | 1            | 50.00%  | 0          | 0.00% |
| Precinct OS                       | 99                         | 8.25%  | 890          | 74.17%  | 59         | 4.92% |
| Precinct VBM                      | 359                        | 7.86%  | 3577         | 78.32%  | 165        | 3.61% |
| MB Precinct                       | 57                         | 7.40%  | 611          | 79.35%  | 31         | 4.03% |
| Provisional                       | 8                          | 6.61%  | 94           | 77.69%  | 3          | 2.48% |
| Total                             | 523                        | 7.85%  | 5173         | 77.67%  | 258        | 3.87% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                            |        |              |         |            |       |
| Precinct TSx                      | 0                          | 0.00%  | 2            | 100.00% | 0          | 0.00% |
| Precinct OS                       | 85                         | 7.86%  | 801          | 74.10%  | 40         | 3.70% |
| Precinct VBM                      | 396                        | 8.87%  | 3378         | 75.64%  | 187        | 4.19% |
| MB Precinct                       | 20                         | 9.43%  | 156          | 73.58%  | 11         | 5.19% |
| Provisional                       | 7                          | 7.69%  | 65           | 71.43%  | 7          | 7.69% |
| Total                             | 508                        | 8.68%  | 4402         | 75.22%  | 245        | 4.19% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                            |        |              |         |            |       |
| Precinct TSx                      | 0                          | -      | 0            | -       | 0          | -     |
| Precinct OS                       | 101                        | 7.76%  | 990          | 76.04%  | 56         | 4.30% |
| Precinct VBM                      | 384                        | 8.30%  | 3625         | 78.36%  | 156        | 3.37% |
| MB Precinct                       | 87                         | 8.32%  | 823          | 78.68%  | 29         | 2.77% |
| Provisional                       | 5                          | 4.50%  | 98           | 88.29%  | 2          | 1.80% |
| Total                             | 577                        | 8.14%  | 5536         | 78.14%  | 243        | 3.43% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                            |        |              |         |            |       |
| Precinct TSx                      | 0                          | -      | 0            | -       | 0          | -     |
| Precinct OS                       | 53                         | 10.41% | 373          | 73.28%  | 32         | 6.29% |
| Precinct VBM                      | 180                        | 9.83%  | 1375         | 75.10%  | 57         | 3.11% |
| MB Precinct                       | 19                         | 15.70% | 84           | 69.42%  | 1          | 0.83% |
| Provisional                       | 5                          | 10.00% | 36           | 72.00%  | 4          | 8.00% |
| Total                             | 257                        | 10.23% | 1868         | 74.39%  | 94         | 3.74% |
| <b>Total</b>                      |                            |        |              |         |            |       |
| Precinct TSx                      | 0                          | 0.00%  | 3            | 75.00%  | 0          | 0.00% |
| Precinct OS                       | 471                        | 9.02%  | 3861         | 73.94%  | 235        | 4.50% |
| Precinct VBM                      | 1960                       | 9.50%  | 15799        | 76.59%  | 700        | 3.39% |
| MB Precinct                       | 207                        | 8.71%  | 1848         | 77.78%  | 82         | 3.45% |
| Provisional                       | 35                         | 7.28%  | 378          | 78.59%  | 22         | 4.57% |
| Total                             | 2673                       | 9.31%  | 21889        | 76.24%  | 1039       | 3.62% |

**COUNTY OF EL DORADO**  
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|                                 | President Republican Party |        |              |        |            |        |
|---------------------------------|----------------------------|--------|--------------|--------|------------|--------|
|                                 | JOHN R. KASICH             |        | DONALD TRUMP |        | BEN CARSON |        |
| <b>CITIES</b>                   |                            |        |              |        |            |        |
| <b>CITY OF PLACERVILLE</b>      |                            |        |              |        |            |        |
| Precinct TSx                    | 0                          | -      | 0            | -      | 0          | -      |
| Precinct OS                     | 22                         | 9.57%  | 161          | 70.00% | 13         | 5.65%  |
| Precinct VBM                    | 122                        | 12.46% | 690          | 70.48% | 39         | 3.98%  |
| MB Precinct                     | 0                          | -      | 0            | -      | 0          | -      |
| Provisional                     | 2                          | 10.53% | 9            | 47.37% | 4          | 21.05% |
| Total                           | 146                        | 11.89% | 860          | 70.03% | 56         | 4.56%  |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                            |        |              |        |            |        |
| Precinct TSx                    | 0                          | -      | 0            | -      | 0          | -      |
| Precinct OS                     | 23                         | 10.00% | 170          | 73.91% | 15         | 6.52%  |
| Precinct VBM                    | 72                         | 10.11% | 540          | 75.84% | 20         | 2.81%  |
| MB Precinct                     | 0                          | -      | 0            | -      | 0          | -      |
| Provisional                     | 2                          | 6.90%  | 21           | 72.41% | 3          | 10.34% |
| Total                           | 97                         | 9.99%  | 731          | 75.28% | 38         | 3.91%  |
| <b>UNINCORPORATED AREA</b>      |                            |        |              |        |            |        |
| Precinct TSx                    | 0                          | 0.00%  | 3            | 75.00% | 0          | 0.00%  |
| Precinct OS                     | 426                        | 8.95%  | 3530         | 74.13% | 207        | 4.35%  |
| Precinct VBM                    | 1766                       | 9.33%  | 14569        | 76.94% | 641        | 3.39%  |
| MB Precinct                     | 207                        | 8.71%  | 1848         | 77.78% | 82         | 3.45%  |
| Provisional                     | 31                         | 7.16%  | 348          | 80.37% | 15         | 3.46%  |
| Total                           | 2430                       | 9.17%  | 20298        | 76.56% | 945        | 3.56%  |
| <b>Total</b>                    |                            |        |              |        |            |        |
| Precinct TSx                    | 0                          | 0.00%  | 3            | 75.00% | 0          | 0.00%  |
| Precinct OS                     | 471                        | 9.02%  | 3861         | 73.94% | 235        | 4.50%  |
| Precinct VBM                    | 1960                       | 9.50%  | 15799        | 76.59% | 700        | 3.39%  |
| MB Precinct                     | 207                        | 8.71%  | 1848         | 77.78% | 82         | 3.45%  |
| Provisional                     | 35                         | 7.28%  | 378          | 78.59% | 22         | 4.57%  |
| Total                           | 2673                       | 9.31%  | 21889        | 76.24% | 1039       | 3.62%  |

**COUNTY OF EL DORADO**  
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|                        | President American Independent Party |          |               |             |             |               |
|------------------------|--------------------------------------|----------|---------------|-------------|-------------|---------------|
|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | WILEY DRAKE | ARTHUR HARRIS |
| Jurisdiction Wide      |                                      |          |               |             |             |               |
| 100001.11-CP 100001.11 |                                      |          |               |             |             |               |
| Precinct TSx           | 49                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Precinct OS            | 49                                   | 1        | 7             | 2           | 0 0.00%     | 0 0.00%       |
| Precinct VBM           | 49                                   | 1        | 21            | 12          | 0 0.00%     | 0 0.00%       |
| MB Precinct            | 49                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Provisional            | 49                                   | 1        | 1             | 0           | 0 -         | 0 -           |
| Total                  | 49                                   | 5        | 29            | 14          | 0 0.00%     | 0 0.00%       |
| 100002.11-CP 100002.11 |                                      |          |               |             |             |               |
| Precinct TSx           | 26                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Precinct OS            | 26                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Precinct VBM           | 26                                   | 1        | 9             | 4           | 0 0.00%     | 0 0.00%       |
| MB Precinct            | 26                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Provisional            | 26                                   | 1        | 3             | 2           | 0 0.00%     | 0 0.00%       |
| Total                  | 26                                   | 5        | 12            | 6           | 0 0.00%     | 0 0.00%       |
| 100003.11-CP 100003.11 |                                      |          |               |             |             |               |
| Precinct TSx           | 36                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Precinct OS            | 36                                   | 1        | 2             | 1           | 0 0.00%     | 0 0.00%       |
| Precinct VBM           | 36                                   | 1        | 19            | 6           | 0 0.00%     | 0 0.00%       |
| MB Precinct            | 36                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Provisional            | 36                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Total                  | 36                                   | 5        | 21            | 7           | 0 0.00%     | 0 0.00%       |
| 100004.11-CP 100004.11 |                                      |          |               |             |             |               |
| Precinct TSx           | 50                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Precinct OS            | 50                                   | 1        | 2             | 2           | 1 50.00%    | 1 50.00%      |
| Precinct VBM           | 50                                   | 1        | 22            | 11          | 1 9.09%     | 0 0.00%       |
| MB Precinct            | 50                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Provisional            | 50                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Total                  | 50                                   | 5        | 24            | 13          | 2 15.38%    | 1 7.69%       |
| 100005.11-CP 100005.11 |                                      |          |               |             |             |               |
| Precinct TSx           | 36                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Precinct OS            | 36                                   | 1        | 3             | 0           | 0 -         | 0 -           |
| Precinct VBM           | 36                                   | 1        | 15            | 8           | 1 12.50%    | 1 12.50%      |
| MB Precinct            | 36                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Provisional            | 36                                   | 1        | 1             | 0           | 0 -         | 0 -           |
| Total                  | 36                                   | 5        | 19            | 8           | 1 12.50%    | 1 12.50%      |
| 100006.11-CP 100006.11 |                                      |          |               |             |             |               |
| Precinct TSx           | 59                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Precinct OS            | 59                                   | 1        | 4             | 3           | 0 0.00%     | 1 33.33%      |
| Precinct VBM           | 59                                   | 1        | 28            | 15          | 0 0.00%     | 1 6.67%       |
| MB Precinct            | 59                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Provisional            | 59                                   | 1        | 3             | 0           | 0 -         | 0 -           |
| Total                  | 59                                   | 5        | 35            | 18          | 0 0.00%     | 2 11.11%      |
| 100008.11-CP 100008.11 |                                      |          |               |             |             |               |
| Precinct TSx           | 50                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Precinct OS            | 50                                   | 1        | 5             | 3           | 0 0.00%     | 0 0.00%       |
| Precinct VBM           | 50                                   | 1        | 16            | 10          | 0 0.00%     | 1 10.00%      |
| MB Precinct            | 50                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Provisional            | 50                                   | 1        | 1             | 1           | 0 0.00%     | 0 0.00%       |
| Total                  | 50                                   | 5        | 22            | 14          | 0 0.00%     | 1 7.14%       |
| 100009.11-CP 100009.11 |                                      |          |               |             |             |               |
| Precinct TSx           | 67                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Precinct OS            | 67                                   | 1        | 7             | 6           | 0 0.00%     | 0 0.00%       |
| Precinct VBM           | 67                                   | 1        | 25            | 19          | 0 0.00%     | 1 5.26%       |
| MB Precinct            | 67                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Provisional            | 67                                   | 1        | 1             | 0           | 0 -         | 0 -           |
| Total                  | 67                                   | 5        | 33            | 25          | 0 0.00%     | 1 4.00%       |
| 100012.11-CP 100012.11 |                                      |          |               |             |             |               |
| Precinct TSx           | 72                                   | 1        | 0             | 0           | 0 -         | 0 -           |
| Precinct OS            | 72                                   | 1        | 6             | 1           | 0 0.00%     | 0 0.00%       |
| Precinct VBM           | 72                                   | 1        | 29            | 17          | 0 0.00%     | 1 5.88%       |

**COUNTY OF EL DORADO**  
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|------------------------|--------------------------------------|----------|---------------|-------------|-------------|---------------|---|--------|
|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | WILEY DRAKE | ARTHUR HARRIS |   |        |
| MB Precinct            | 72                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Provisional            | 72                                   | 1        | 1             | 0           | 0           | -             | 0 | -      |
| Total                  | 72                                   | 5        | 36            | 18          | 0           | 0.00%         | 1 | 5.56%  |
| 100013.11-CP 100013.11 |                                      |          |               |             |             |               |   |        |
| Precinct TSx           | 69                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Precinct OS            | 69                                   | 1        | 4             | 1           | 0           | 0.00%         | 0 | 0.00%  |
| Precinct VBM           | 69                                   | 1        | 33            | 18          | 1           | 5.56%         | 1 | 5.56%  |
| MB Precinct            | 69                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Provisional            | 69                                   | 1        | 1             | 0           | 0           | -             | 0 | -      |
| Total                  | 69                                   | 5        | 38            | 19          | 1           | 5.26%         | 1 | 5.26%  |
| 100014.11-CP 100014.11 |                                      |          |               |             |             |               |   |        |
| Precinct TSx           | 48                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Precinct OS            | 48                                   | 1        | 7             | 3           | 0           | 0.00%         | 0 | 0.00%  |
| Precinct VBM           | 48                                   | 1        | 15            | 10          | 0           | 0.00%         | 0 | 0.00%  |
| MB Precinct            | 48                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Provisional            | 48                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Total                  | 48                                   | 5        | 22            | 13          | 0           | 0.00%         | 0 | 0.00%  |
| 100022.11-CP 100022.11 |                                      |          |               |             |             |               |   |        |
| Precinct TSx           | 39                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Precinct OS            | 39                                   | 1        | 5             | 2           | 0           | 0.00%         | 1 | 50.00% |
| Precinct VBM           | 39                                   | 1        | 13            | 6           | 0           | 0.00%         | 0 | 0.00%  |
| MB Precinct            | 39                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Provisional            | 39                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Total                  | 39                                   | 5        | 18            | 8           | 0           | 0.00%         | 1 | 12.50% |
| 100023.11-CP 100023.11 |                                      |          |               |             |             |               |   |        |
| Precinct TSx           | 83                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Precinct OS            | 83                                   | 1        | 4             | 2           | 0           | 0.00%         | 0 | 0.00%  |
| Precinct VBM           | 83                                   | 1        | 32            | 14          | 0           | 0.00%         | 0 | 0.00%  |
| MB Precinct            | 83                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Provisional            | 83                                   | 1        | 4             | 3           | 0           | 0.00%         | 1 | 33.33% |
| Total                  | 83                                   | 5        | 40            | 19          | 0           | 0.00%         | 1 | 5.26%  |
| 200003.12-CP 200003.12 |                                      |          |               |             |             |               |   |        |
| Precinct TSx           | 59                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Precinct OS            | 59                                   | 1        | 3             | 3           | 0           | 0.00%         | 1 | 33.33% |
| Precinct VBM           | 59                                   | 1        | 12            | 6           | 0           | 0.00%         | 1 | 16.67% |
| MB Precinct            | 59                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Provisional            | 59                                   | 1        | 2             | 1           | 0           | 0.00%         | 0 | 0.00%  |
| Total                  | 59                                   | 5        | 17            | 10          | 0           | 0.00%         | 2 | 20.00% |
| 200004.12-CP 200004.12 |                                      |          |               |             |             |               |   |        |
| Precinct TSx           | 63                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Precinct OS            | 63                                   | 1        | 2             | 2           | 0           | 0.00%         | 0 | 0.00%  |
| Precinct VBM           | 63                                   | 1        | 24            | 15          | 0           | 0.00%         | 0 | 0.00%  |
| MB Precinct            | 63                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Provisional            | 63                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Total                  | 63                                   | 5        | 26            | 17          | 0           | 0.00%         | 0 | 0.00%  |
| 200005.02-CP 200005.02 |                                      |          |               |             |             |               |   |        |
| Precinct TSx           | 43                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Precinct OS            | 43                                   | 1        | 2             | 2           | 0           | 0.00%         | 0 | 0.00%  |
| Precinct VBM           | 43                                   | 1        | 22            | 16          | 0           | 0.00%         | 0 | 0.00%  |
| MB Precinct            | 43                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Provisional            | 43                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Total                  | 43                                   | 5        | 24            | 18          | 0           | 0.00%         | 0 | 0.00%  |
| 200006.12-CP 200006.12 |                                      |          |               |             |             |               |   |        |
| Precinct TSx           | 37                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Precinct OS            | 37                                   | 1        | 3             | 1           | 0           | 0.00%         | 0 | 0.00%  |
| Precinct VBM           | 37                                   | 1        | 12            | 7           | 0           | 0.00%         | 1 | 14.29% |
| MB Precinct            | 37                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Provisional            | 37                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |
| Total                  | 37                                   | 5        | 15            | 8           | 0           | 0.00%         | 1 | 12.50% |
| 200008.12-CP 200008.12 |                                      |          |               |             |             |               |   |        |
| Precinct TSx           | 37                                   | 1        | 0             | 0           | 0           | -             | 0 | -      |

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|------------------------|--------------------------------------|----------|---------------|-------------|---|--------|-------------|---------------|
|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes |   |        | WILEY DRAKE | ARTHUR HARRIS |
| Precinct OS            | 37                                   | 1        | 3             | 2           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 37                                   | 1        | 7             | 4           | 0 | 0.00%  | 0           | 0.00%         |
| MB Precinct            | 37                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 37                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 37                                   | 5        | 10            | 6           | 0 | 0.00%  | 0           | 0.00%         |
| 200010.12-CP 200010.12 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 65                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 65                                   | 1        | 1             | 0           | 0 | -      | 0           | -             |
| Precinct VBM           | 65                                   | 1        | 21            | 8           | 0 | 0.00%  | 1           | 12.50%        |
| MB Precinct            | 65                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 65                                   | 1        | 2             | 1           | 0 | 0.00%  | 0           | 0.00%         |
| Total                  | 65                                   | 5        | 24            | 9           | 0 | 0.00%  | 1           | 11.11%        |
| 200012.12-CP 200012.12 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 37                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 37                                   | 1        | 4             | 2           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 37                                   | 1        | 9             | 4           | 0 | 0.00%  | 0           | 0.00%         |
| MB Precinct            | 37                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 37                                   | 1        | 2             | 2           | 0 | 0.00%  | 0           | 0.00%         |
| Total                  | 37                                   | 5        | 15            | 8           | 0 | 0.00%  | 0           | 0.00%         |
| 200013.12-CP 200013.12 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 60                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 60                                   | 1        | 6             | 2           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 60                                   | 1        | 21            | 11          | 1 | 9.09%  | 0           | 0.00%         |
| MB Precinct            | 60                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 60                                   | 1        | 2             | 1           | 0 | 0.00%  | 0           | 0.00%         |
| Total                  | 60                                   | 5        | 29            | 14          | 1 | 7.14%  | 0           | 0.00%         |
| 200014.12-CP 200014.12 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 51                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 51                                   | 1        | 5             | 2           | 0 | 0.00%  | 1           | 50.00%        |
| Precinct VBM           | 51                                   | 1        | 13            | 10          | 0 | 0.00%  | 1           | 10.00%        |
| MB Precinct            | 51                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 51                                   | 1        | 3             | 2           | 0 | 0.00%  | 0           | 0.00%         |
| Total                  | 51                                   | 5        | 21            | 14          | 0 | 0.00%  | 2           | 14.29%        |
| 200015.02-CP 200015.02 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 88                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 88                                   | 1        | 5             | 4           | 0 | 0.00%  | 1           | 25.00%        |
| Precinct VBM           | 88                                   | 1        | 32            | 17          | 0 | 0.00%  | 0           | 0.00%         |
| MB Precinct            | 88                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 88                                   | 1        | 3             | 2           | 0 | 0.00%  | 0           | 0.00%         |
| Total                  | 88                                   | 5        | 40            | 23          | 0 | 0.00%  | 1           | 4.35%         |
| 200016.12-CP 200016.12 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 9                                    | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 9                                    | 1        | 1             | 1           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 9                                    | 1        | 5             | 5           | 1 | 20.00% | 1           | 20.00%        |
| MB Precinct            | 9                                    | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 9                                    | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 9                                    | 5        | 6             | 6           | 1 | 16.67% | 1           | 16.67%        |
| 200016.13-CP 200016.13 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 17                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 17                                   | 1        | 2             | 1           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 17                                   | 1        | 11            | 5           | 0 | 0.00%  | 0           | 0.00%         |
| MB Precinct            | 17                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 17                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 17                                   | 5        | 13            | 6           | 0 | 0.00%  | 0           | 0.00%         |
| 200017.02-CP 200017.02 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 48                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 48                                   | 1        | 5             | 4           | 1 | 25.00% | 0           | 0.00%         |
| Precinct VBM           | 48                                   | 1        | 21            | 19          | 1 | 5.26%  | 0           | 0.00%         |
| MB Precinct            | 48                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 48                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 48                                   | 5        | 26            | 23          | 2 | 8.70%  | 0           | 0.00%         |

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|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | WILEY DRAKE | ARTHUR HARRIS |
| 200018.02-CP 200018.02 |                                      |          |               |             |             |               |
| Precinct TSx           | 70                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 70                                   | 1        | 7             | 5           | 0           | 0.00%         |
| Precinct VBM           | 70                                   | 1        | 22            | 14          | 0           | 0.00%         |
| MB Precinct            | 70                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 70                                   | 1        | 0             | 0           | 0           | -             |
| Total                  | 70                                   | 5        | 29            | 19          | 0           | 0.00%         |
| 200020.02-CP 200020.02 |                                      |          |               |             |             |               |
| Precinct TSx           | 52                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 52                                   | 1        | 3             | 2           | 0           | 0.00%         |
| Precinct VBM           | 52                                   | 1        | 21            | 13          | 0           | 0.00%         |
| MB Precinct            | 52                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 52                                   | 1        | 1             | 0           | 0           | -             |
| Total                  | 52                                   | 5        | 25            | 15          | 0           | 0.00%         |
| 200021.01-CP 200021.01 |                                      |          |               |             |             |               |
| Precinct TSx           | 28                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 28                                   | 1        | 2             | 0           | 0           | -             |
| Precinct VBM           | 28                                   | 1        | 9             | 5           | 0           | 0.00%         |
| MB Precinct            | 28                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 28                                   | 1        | 0             | 0           | 0           | -             |
| Total                  | 28                                   | 5        | 11            | 5           | 0           | 0.00%         |
| 200021.02-CP 200021.02 |                                      |          |               |             |             |               |
| Precinct TSx           | 41                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 41                                   | 1        | 5             | 4           | 0           | 0.00%         |
| Precinct VBM           | 41                                   | 1        | 13            | 5           | 0           | 0.00%         |
| MB Precinct            | 41                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 41                                   | 1        | 1             | 0           | 0           | -             |
| Total                  | 41                                   | 5        | 19            | 9           | 0           | 0.00%         |
| 200024.02-CP 200024.02 |                                      |          |               |             |             |               |
| Precinct TSx           | 23                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 23                                   | 1        | 1             | 1           | 0           | 0.00%         |
| Precinct VBM           | 23                                   | 1        | 6             | 4           | 0           | 0.00%         |
| MB Precinct            | 23                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 23                                   | 1        | 1             | 0           | 0           | -             |
| Total                  | 23                                   | 5        | 8             | 5           | 0           | 0.00%         |
| 300001.04-CP 300001.04 |                                      |          |               |             |             |               |
| Precinct TSx           | 12                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 12                                   | 1        | 0             | 0           | 0           | -             |
| Precinct VBM           | 12                                   | 1        | 4             | 2           | 0           | 0.00%         |
| MB Precinct            | 12                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 12                                   | 1        | 0             | 0           | 0           | -             |
| Total                  | 12                                   | 5        | 4             | 2           | 0           | 0.00%         |
| 300001.05-CP 300001.05 |                                      |          |               |             |             |               |
| Precinct TSx           | 22                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 22                                   | 1        | 1             | 0           | 0           | -             |
| Precinct VBM           | 22                                   | 1        | 9             | 7           | 0           | 0.00%         |
| MB Precinct            | 22                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 22                                   | 1        | 1             | 1           | 0           | 0.00%         |
| Total                  | 22                                   | 5        | 11            | 8           | 0           | 0.00%         |
| 300003.05-CP 300003.05 |                                      |          |               |             |             |               |
| Precinct TSx           | 83                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 83                                   | 1        | 2             | 2           | 0           | 0.00%         |
| Precinct VBM           | 83                                   | 1        | 27            | 18          | 1           | 5.56%         |
| MB Precinct            | 83                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 83                                   | 1        | 1             | 1           | 0           | 0.00%         |
| Total                  | 83                                   | 5        | 30            | 21          | 1           | 4.76%         |
| 300004.05-CP 300004.05 |                                      |          |               |             |             |               |
| Precinct TSx           | 61                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 61                                   | 1        | 3             | 1           | 0           | 0.00%         |
| Precinct VBM           | 61                                   | 1        | 18            | 12          | 0           | 0.00%         |
| MB Precinct            | 61                                   | 1        | 0             | 0           | 0           | -             |

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|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes |   |        | WILEY DRAKE | ARTHUR HARRIS |
| Provisional            | 61                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 61                                   | 5        | 21            | 13          | 0 | 0.00%  | 2           | 15.38%        |
| 300005.05-CP 300005.05 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 45                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 45                                   | 1        | 6             | 2           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 45                                   | 1        | 10            | 9           | 0 | 0.00%  | 0           | 0.00%         |
| MB Precinct            | 45                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 45                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 45                                   | 5        | 16            | 11          | 0 | 0.00%  | 0           | 0.00%         |
| 300008.05-CP 300008.05 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 54                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 54                                   | 1        | 3             | 1           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 54                                   | 1        | 23            | 11          | 0 | 0.00%  | 0           | 0.00%         |
| MB Precinct            | 54                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 54                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 54                                   | 5        | 26            | 12          | 0 | 0.00%  | 0           | 0.00%         |
| 300009.04-CP 300009.04 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 47                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 47                                   | 1        | 4             | 4           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 47                                   | 1        | 12            | 7           | 1 | 14.29% | 0           | 0.00%         |
| MB Precinct            | 47                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 47                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 47                                   | 5        | 16            | 11          | 1 | 9.09%  | 0           | 0.00%         |
| 300010.04-CP 300010.04 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 60                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 60                                   | 1        | 2             | 1           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 60                                   | 1        | 30            | 21          | 1 | 4.76%  | 0           | 0.00%         |
| MB Precinct            | 60                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 60                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 60                                   | 5        | 32            | 22          | 1 | 4.55%  | 0           | 0.00%         |
| 300011.04-CP 300011.04 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 57                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 57                                   | 1        | 3             | 2           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 57                                   | 1        | 21            | 11          | 0 | 0.00%  | 1           | 9.09%         |
| MB Precinct            | 57                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 57                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 57                                   | 5        | 24            | 13          | 0 | 0.00%  | 1           | 7.69%         |
| 300013.05-CP 300013.05 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 49                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 49                                   | 1        | 1             | 1           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 49                                   | 1        | 16            | 9           | 0 | 0.00%  | 0           | 0.00%         |
| MB Precinct            | 49                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 49                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 49                                   | 5        | 17            | 10          | 0 | 0.00%  | 0           | 0.00%         |
| 300015.04-CP 300015.04 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 8                                    | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 8                                    | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct VBM           | 8                                    | 1        | 3             | 2           | 0 | 0.00%  | 0           | 0.00%         |
| MB Precinct            | 8                                    | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 8                                    | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 8                                    | 5        | 3             | 2           | 0 | 0.00%  | 0           | 0.00%         |
| 300015.05-CP 300015.05 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 13                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 13                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct VBM           | 13                                   | 1        | 1             | 1           | 0 | 0.00%  | 0           | 0.00%         |
| MB Precinct            | 13                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 13                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 13                                   | 5        | 1             | 1           | 0 | 0.00%  | 0           | 0.00%         |
| 300017.05-CP 300017.05 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 40                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 40                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |





**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|------------------------|--------------------------------------|----------|---------------|-------------|---|--------|-------------|---------------|
|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes |   |        | WILEY DRAKE | ARTHUR HARRIS |
| Precinct TSx           | 74                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 74                                   | 1        | 6             | 5           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 74                                   | 1        | 25            | 14          | 0 | 0.00%  | 0           | 0.00%         |
| MB Precinct            | 74                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 74                                   | 1        | 3             | 2           | 0 | 0.00%  | 0           | 0.00%         |
| Total                  | 74                                   | 5        | 34            | 21          | 0 | 0.00%  | 0           | 0.00%         |
| 400004.14-CP 400004.14 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 62                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 62                                   | 1        | 2             | 2           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 62                                   | 1        | 16            | 9           | 0 | 0.00%  | 1           | 11.11%        |
| MB Precinct            | 62                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 62                                   | 1        | 1             | 0           | 0 | -      | 0           | -             |
| Total                  | 62                                   | 5        | 19            | 11          | 0 | 0.00%  | 1           | 9.09%         |
| 400005.08-CP 400005.08 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 38                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 38                                   | 1        | 5             | 3           | 0 | 0.00%  | 1           | 33.33%        |
| Precinct VBM           | 38                                   | 1        | 9             | 5           | 2 | 40.00% | 0           | 0.00%         |
| MB Precinct            | 38                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 38                                   | 1        | 2             | 0           | 0 | -      | 0           | -             |
| Total                  | 38                                   | 5        | 16            | 8           | 2 | 25.00% | 1           | 12.50%        |
| 400006.08-CP 400006.08 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 58                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 58                                   | 1        | 10            | 9           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 58                                   | 1        | 23            | 14          | 0 | 0.00%  | 0           | 0.00%         |
| MB Precinct            | 58                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 58                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 58                                   | 5        | 33            | 23          | 0 | 0.00%  | 0           | 0.00%         |
| 400007.08-CP 400007.08 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 92                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 92                                   | 1        | 4             | 3           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 92                                   | 1        | 25            | 17          | 1 | 5.88%  | 0           | 0.00%         |
| MB Precinct            | 92                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 92                                   | 1        | 2             | 1           | 0 | 0.00%  | 0           | 0.00%         |
| Total                  | 92                                   | 5        | 31            | 21          | 1 | 4.76%  | 0           | 0.00%         |
| 400010.08-CP 400010.08 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 74                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 74                                   | 1        | 10            | 5           | 1 | 20.00% | 1           | 20.00%        |
| Precinct VBM           | 74                                   | 1        | 24            | 18          | 0 | 0.00%  | 0           | 0.00%         |
| MB Precinct            | 74                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 74                                   | 1        | 1             | 0           | 0 | -      | 0           | -             |
| Total                  | 74                                   | 5        | 35            | 23          | 1 | 4.35%  | 1           | 4.35%         |
| 400012.08-CP 400012.08 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 31                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 31                                   | 1        | 1             | 0           | 0 | -      | 0           | -             |
| Precinct VBM           | 31                                   | 1        | 12            | 5           | 1 | 20.00% | 0           | 0.00%         |
| MB Precinct            | 31                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 31                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 31                                   | 5        | 13            | 5           | 1 | 20.00% | 0           | 0.00%         |
| 400013.08-CP 400013.08 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 19                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 19                                   | 1        | 1             | 1           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 19                                   | 1        | 5             | 4           | 0 | 0.00%  | 0           | 0.00%         |
| MB Precinct            | 19                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 19                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Total                  | 19                                   | 5        | 6             | 5           | 0 | 0.00%  | 0           | 0.00%         |
| 400014.08-CP 400014.08 |                                      |          |               |             |   |        |             |               |
| Precinct TSx           | 52                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Precinct OS            | 52                                   | 1        | 1             | 1           | 0 | 0.00%  | 0           | 0.00%         |
| Precinct VBM           | 52                                   | 1        | 19            | 13          | 0 | 0.00%  | 0           | 0.00%         |
| MB Precinct            | 52                                   | 1        | 0             | 0           | 0 | -      | 0           | -             |
| Provisional            | 52                                   | 1        | 3             | 1           | 0 | 0.00%  | 0           | 0.00%         |

**COUNTY OF EL DORADO**  
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|------------------------|--------------------------------------|----------|---------------|-------------|-------------|--------|---------------|--------|--|--|
|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | WILEY DRAKE |        | ARTHUR HARRIS |        |  |  |
| Total                  | 52                                   | 5        | 23            | 15          | 0           | 0.00%  | 0             | 0.00%  |  |  |
| 400017.08-CP 400017.08 |                                      |          |               |             |             |        |               |        |  |  |
| Precinct TSx           | 59                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Precinct OS            | 59                                   | 1        | 2             | 0           | 0           | -      | 0             | -      |  |  |
| Precinct VBM           | 59                                   | 1        | 21            | 12          | 0           | 0.00%  | 1             | 8.33%  |  |  |
| MB Precinct            | 59                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Provisional            | 59                                   | 1        | 1             | 0           | 0           | -      | 0             | -      |  |  |
| Total                  | 59                                   | 5        | 24            | 12          | 0           | 0.00%  | 1             | 8.33%  |  |  |
| 400018.08-CP 400018.08 |                                      |          |               |             |             |        |               |        |  |  |
| Precinct TSx           | 13                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Precinct OS            | 13                                   | 1        | 3             | 2           | 0           | 0.00%  | 0             | 0.00%  |  |  |
| Precinct VBM           | 13                                   | 1        | 2             | 1           | 0           | 0.00%  | 0             | 0.00%  |  |  |
| MB Precinct            | 13                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Provisional            | 13                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Total                  | 13                                   | 5        | 5             | 3           | 0           | 0.00%  | 0             | 0.00%  |  |  |
| 400020.08-CP 400020.08 |                                      |          |               |             |             |        |               |        |  |  |
| Precinct TSx           | 35                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Precinct OS            | 35                                   | 1        | 5             | 5           | 0           | 0.00%  | 0             | 0.00%  |  |  |
| Precinct VBM           | 35                                   | 1        | 14            | 10          | 0           | 0.00%  | 0             | 0.00%  |  |  |
| MB Precinct            | 35                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Provisional            | 35                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Total                  | 35                                   | 5        | 19            | 15          | 0           | 0.00%  | 0             | 0.00%  |  |  |
| 400021.07-CP 400021.07 |                                      |          |               |             |             |        |               |        |  |  |
| Precinct TSx           | 41                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Precinct OS            | 41                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Precinct VBM           | 41                                   | 1        | 18            | 9           | 1           | 11.11% | 0             | 0.00%  |  |  |
| MB Precinct            | 41                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Provisional            | 41                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Total                  | 41                                   | 5        | 18            | 9           | 1           | 11.11% | 0             | 0.00%  |  |  |
| 500001.10-CP 500001.10 |                                      |          |               |             |             |        |               |        |  |  |
| Precinct TSx           | 48                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Precinct OS            | 48                                   | 1        | 3             | 2           | 0           | 0.00%  | 0             | 0.00%  |  |  |
| Precinct VBM           | 48                                   | 1        | 10            | 8           | 0           | 0.00%  | 0             | 0.00%  |  |  |
| MB Precinct            | 48                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Provisional            | 48                                   | 1        | 1             | 1           | 0           | 0.00%  | 0             | 0.00%  |  |  |
| Total                  | 48                                   | 5        | 14            | 11          | 0           | 0.00%  | 0             | 0.00%  |  |  |
| 500002.10-CP 500002.10 |                                      |          |               |             |             |        |               |        |  |  |
| Precinct TSx           | 56                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Precinct OS            | 56                                   | 1        | 1             | 1           | 0           | 0.00%  | 0             | 0.00%  |  |  |
| Precinct VBM           | 56                                   | 1        | 13            | 11          | 0           | 0.00%  | 2             | 18.18% |  |  |
| MB Precinct            | 56                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Provisional            | 56                                   | 1        | 2             | 1           | 0           | 0.00%  | 0             | 0.00%  |  |  |
| Total                  | 56                                   | 5        | 16            | 13          | 0           | 0.00%  | 2             | 15.38% |  |  |
| 500003.10-CP 500003.10 |                                      |          |               |             |             |        |               |        |  |  |
| Precinct TSx           | 36                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Precinct OS            | 36                                   | 1        | 2             | 2           | 0           | 0.00%  | 0             | 0.00%  |  |  |
| Precinct VBM           | 36                                   | 1        | 9             | 6           | 0           | 0.00%  | 1             | 16.67% |  |  |
| MB Precinct            | 36                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Provisional            | 36                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Total                  | 36                                   | 5        | 11            | 8           | 0           | 0.00%  | 1             | 12.50% |  |  |
| 500004.10-CP 500004.10 |                                      |          |               |             |             |        |               |        |  |  |
| Precinct TSx           | 58                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Precinct OS            | 58                                   | 1        | 3             | 1           | 0           | 0.00%  | 0             | 0.00%  |  |  |
| Precinct VBM           | 58                                   | 1        | 14            | 10          | 1           | 10.00% | 2             | 20.00% |  |  |
| MB Precinct            | 58                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Provisional            | 58                                   | 1        | 2             | 1           | 0           | 0.00%  | 0             | 0.00%  |  |  |
| Total                  | 58                                   | 5        | 19            | 12          | 1           | 8.33%  | 2             | 16.67% |  |  |
| 500005.10-CP 500005.10 |                                      |          |               |             |             |        |               |        |  |  |
| Precinct TSx           | 49                                   | 1        | 0             | 0           | 0           | -      | 0             | -      |  |  |
| Precinct OS            | 49                                   | 1        | 2             | 2           | 0           | 0.00%  | 1             | 50.00% |  |  |
| Precinct VBM           | 49                                   | 1        | 6             | 6           | 0           | 0.00%  | 0             | 0.00%  |  |  |

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|                        | President American Independent Party |          |               |             |             |               |
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|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | WILEY DRAKE | ARTHUR HARRIS |
| MB Precinct            | 49                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 49                                   | 1        | 2             | 1           | 0           | 0.00%         |
| Total                  | 49                                   | 5        | 10            | 9           | 0           | 0.00%         |
| 500006.10-CP 500006.10 |                                      |          |               |             |             |               |
| Precinct TSx           | 75                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 75                                   | 1        | 6             | 5           | 0           | 0.00%         |
| Precinct VBM           | 75                                   | 1        | 15            | 13          | 0           | 0.00%         |
| MB Precinct            | 75                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 75                                   | 1        | 2             | 0           | 0           | -             |
| Total                  | 75                                   | 5        | 23            | 18          | 0           | 0.00%         |
| 500007.10-CP 500007.10 |                                      |          |               |             |             |               |
| Precinct TSx           | 92                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 92                                   | 1        | 3             | 2           | 0           | 0.00%         |
| Precinct VBM           | 92                                   | 1        | 25            | 11          | 0           | 0.00%         |
| MB Precinct            | 92                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 92                                   | 1        | 0             | 0           | 0           | -             |
| Total                  | 92                                   | 5        | 28            | 13          | 0           | 0.00%         |
| 500009.10-CP 500009.10 |                                      |          |               |             |             |               |
| Precinct TSx           | 47                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 47                                   | 1        | 4             | 2           | 0           | 0.00%         |
| Precinct VBM           | 47                                   | 1        | 10            | 2           | 0           | 0.00%         |
| MB Precinct            | 47                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 47                                   | 1        | 2             | 1           | 0           | 0.00%         |
| Total                  | 47                                   | 5        | 16            | 5           | 0           | 0.00%         |
| 500011.10-CP 500011.10 |                                      |          |               |             |             |               |
| Precinct TSx           | 63                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 63                                   | 1        | 2             | 0           | 0           | -             |
| Precinct VBM           | 63                                   | 1        | 10            | 5           | 0           | 0.00%         |
| MB Precinct            | 63                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 63                                   | 1        | 1             | 0           | 0           | -             |
| Total                  | 63                                   | 5        | 13            | 5           | 0           | 0.00%         |
| 500012.10-CP 500012.10 |                                      |          |               |             |             |               |
| Precinct TSx           | 47                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 47                                   | 1        | 7             | 4           | 0           | 0.00%         |
| Precinct VBM           | 47                                   | 1        | 8             | 5           | 1           | 20.00%        |
| MB Precinct            | 47                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 47                                   | 1        | 2             | 1           | 0           | 0.00%         |
| Total                  | 47                                   | 5        | 17            | 10          | 1           | 10.00%        |
| 500015.10-CP 500015.10 |                                      |          |               |             |             |               |
| Precinct TSx           | 26                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 26                                   | 1        | 1             | 1           | 0           | 0.00%         |
| Precinct VBM           | 26                                   | 1        | 8             | 4           | 0           | 0.00%         |
| MB Precinct            | 26                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 26                                   | 1        | 0             | 0           | 0           | -             |
| Total                  | 26                                   | 5        | 9             | 5           | 0           | 0.00%         |
| 500020.10-CP 500020.10 |                                      |          |               |             |             |               |
| Precinct TSx           | 84                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 84                                   | 1        | 3             | 3           | 0           | 0.00%         |
| Precinct VBM           | 84                                   | 1        | 18            | 12          | 0           | 0.00%         |
| MB Precinct            | 84                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 84                                   | 1        | 5             | 1           | 0           | 0.00%         |
| Total                  | 84                                   | 5        | 26            | 16          | 0           | 0.00%         |
| 500021.10-CP 500021.10 |                                      |          |               |             |             |               |
| Precinct TSx           | 62                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 62                                   | 1        | 6             | 4           | 0           | 0.00%         |
| Precinct VBM           | 62                                   | 1        | 28            | 17          | 0           | 0.00%         |
| MB Precinct            | 62                                   | 1        | 0             | 0           | 0           | -             |
| Provisional            | 62                                   | 1        | 0             | 0           | 0           | -             |
| Total                  | 62                                   | 5        | 34            | 21          | 0           | 0.00%         |
| 95U201.01-MB 95U201.01 |                                      |          |               |             |             |               |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0           | -             |

**COUNTY OF EL DORADO**  
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|                        | President American Independent Party |          |               |             |             |               |   |       |
|------------------------|--------------------------------------|----------|---------------|-------------|-------------|---------------|---|-------|
|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | WILEY DRAKE | ARTHUR HARRIS |   |       |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Provisional            | 0                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Total                  | 0                                    | 5        | 0             | 0           | 0           | -             | 0 | -     |
| 95U201.02-MB 95U201.02 |                                      |          |               |             |             |               |   |       |
| Precinct TSx           | 18                                   | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct OS            | 18                                   | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct VBM           | 18                                   | 1        | 0             | 0           | 0           | -             | 0 | -     |
| MB Precinct            | 18                                   | 1        | 13            | 10          | 0           | 0.00%         | 0 | 0.00% |
| Provisional            | 18                                   | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Total                  | 18                                   | 5        | 13            | 10          | 0           | 0.00%         | 0 | 0.00% |
| 95U202.02-MB 95U202.02 |                                      |          |               |             |             |               |   |       |
| Precinct TSx           | 41                                   | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct OS            | 41                                   | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct VBM           | 41                                   | 1        | 0             | 0           | 0           | -             | 0 | -     |
| MB Precinct            | 41                                   | 1        | 22            | 16          | 0           | 0.00%         | 0 | 0.00% |
| Provisional            | 41                                   | 1        | 2             | 0           | 0           | -             | 0 | -     |
| Total                  | 41                                   | 5        | 24            | 16          | 0           | 0.00%         | 0 | 0.00% |
| 95U203.02-MB 95U203.02 |                                      |          |               |             |             |               |   |       |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Provisional            | 0                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Total                  | 0                                    | 5        | 0             | 0           | 0           | -             | 0 | -     |
| 95U204.01-MB 95U204.01 |                                      |          |               |             |             |               |   |       |
| Precinct TSx           | 41                                   | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct OS            | 41                                   | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct VBM           | 41                                   | 1        | 0             | 0           | 0           | -             | 0 | -     |
| MB Precinct            | 41                                   | 1        | 23            | 15          | 0           | 0.00%         | 0 | 0.00% |
| Provisional            | 41                                   | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Total                  | 41                                   | 5        | 23            | 15          | 0           | 0.00%         | 0 | 0.00% |
| 95U301.05-MB 95U301.05 |                                      |          |               |             |             |               |   |       |
| Precinct TSx           | 8                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct OS            | 8                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct VBM           | 8                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| MB Precinct            | 8                                    | 1        | 4             | 3           | 0           | 0.00%         | 0 | 0.00% |
| Provisional            | 8                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Total                  | 8                                    | 5        | 4             | 3           | 0           | 0.00%         | 0 | 0.00% |
| 95U302.04-MB 95U302.04 |                                      |          |               |             |             |               |   |       |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Provisional            | 0                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Total                  | 0                                    | 5        | 0             | 0           | 0           | -             | 0 | -     |
| 95U302.05-MB 95U302.05 |                                      |          |               |             |             |               |   |       |
| Precinct TSx           | 7                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct OS            | 7                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct VBM           | 7                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| MB Precinct            | 7                                    | 1        | 4             | 2           | 0           | 0.00%         | 0 | 0.00% |
| Provisional            | 7                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Total                  | 7                                    | 5        | 4             | 2           | 0           | 0.00%         | 0 | 0.00% |
| 95U303.03-MB 95U303.03 |                                      |          |               |             |             |               |   |       |
| Precinct TSx           | 9                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct OS            | 9                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Precinct VBM           | 9                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| MB Precinct            | 9                                    | 1        | 2             | 2           | 0           | 0.00%         | 0 | 0.00% |
| Provisional            | 9                                    | 1        | 0             | 0           | 0           | -             | 0 | -     |
| Total                  | 9                                    | 5        | 2             | 2           | 0           | 0.00%         | 0 | 0.00% |

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|                        | President American Independent Party |          |               |             |             |               |
|------------------------|--------------------------------------|----------|---------------|-------------|-------------|---------------|
|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | WILEY DRAKE | ARTHUR HARRIS |
| 95U303.05-MB 95U303.05 |                                      |          |               |             |             |               |
| Precinct TSx           | 8                                    | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 8                                    | 1        | 0             | 0           | 0           | -             |
| Precinct VBM           | 8                                    | 1        | 0             | 0           | 0           | -             |
| MB Precinct            | 8                                    | 1        | 8             | 5           | 0           | 0.00%         |
| Provisional            | 8                                    | 1        | 0             | 0           | 0           | -             |
| Total                  | 8                                    | 5        | 8             | 5           | 0           | 0.00%         |
| 95U304.05-MB 95U304.05 |                                      |          |               |             |             |               |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0           | -             |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0           | -             |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0           | -             |
| Provisional            | 0                                    | 1        | 0             | 0           | 0           | -             |
| Total                  | 0                                    | 5        | 0             | 0           | 0           | -             |
| 95U305.05-MB 95U305.05 |                                      |          |               |             |             |               |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0           | -             |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0           | -             |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0           | -             |
| Provisional            | 0                                    | 1        | 0             | 0           | 0           | -             |
| Total                  | 0                                    | 5        | 0             | 0           | 0           | -             |
| 95U401.08-MB 95U401.08 |                                      |          |               |             |             |               |
| Precinct TSx           | 2                                    | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 2                                    | 1        | 0             | 0           | 0           | -             |
| Precinct VBM           | 2                                    | 1        | 0             | 0           | 0           | -             |
| MB Precinct            | 2                                    | 1        | 0             | 0           | 0           | -             |
| Provisional            | 2                                    | 1        | 0             | 0           | 0           | -             |
| Total                  | 2                                    | 5        | 0             | 0           | 0           | -             |
| 95U402.07-MB 95U402.07 |                                      |          |               |             |             |               |
| Precinct TSx           | 13                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 13                                   | 1        | 0             | 0           | 0           | -             |
| Precinct VBM           | 13                                   | 1        | 0             | 0           | 0           | -             |
| MB Precinct            | 13                                   | 1        | 1             | 1           | 1           | 100.00%       |
| Provisional            | 13                                   | 1        | 0             | 0           | 0           | -             |
| Total                  | 13                                   | 5        | 1             | 1           | 1           | 100.00%       |
| 95U402.08-MB 95U402.08 |                                      |          |               |             |             |               |
| Precinct TSx           | 34                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 34                                   | 1        | 0             | 0           | 0           | -             |
| Precinct VBM           | 34                                   | 1        | 0             | 0           | 0           | -             |
| MB Precinct            | 34                                   | 1        | 13            | 8           | 0           | 0.00%         |
| Provisional            | 34                                   | 1        | 0             | 0           | 0           | -             |
| Total                  | 34                                   | 5        | 13            | 8           | 0           | 0.00%         |
| 95U403.08-MB 95U403.08 |                                      |          |               |             |             |               |
| Precinct TSx           | 62                                   | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 62                                   | 1        | 0             | 0           | 0           | -             |
| Precinct VBM           | 62                                   | 1        | 0             | 0           | 0           | -             |
| MB Precinct            | 62                                   | 1        | 31            | 14          | 0           | 0.00%         |
| Provisional            | 62                                   | 1        | 1             | 0           | 0           | -             |
| Total                  | 62                                   | 5        | 32            | 14          | 0           | 0.00%         |
| 95U404.06-MB 95U404.06 |                                      |          |               |             |             |               |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0           | -             |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0           | -             |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0           | -             |
| Provisional            | 0                                    | 1        | 0             | 0           | 0           | -             |
| Total                  | 0                                    | 5        | 0             | 0           | 0           | -             |
| 95U404.08-MB 95U404.08 |                                      |          |               |             |             |               |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0           | -             |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0           | -             |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0           | -             |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0           | -             |



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|-------------------------------------|--------------------------------------|------------|---------------|-------------|-------------|---------------|-----------|--------------|
|                                     | Reg. Voters                          | Vote For   | Times Counted | Total Votes | WILEY DRAKE | ARTHUR HARRIS |           |              |
| Precinct TSx                        | 4167                                 | 102        | 0             | 0           | 0           | -             | 0         | -            |
| Precinct OS                         | 4167                                 | 102        | 272           | 167         | 4           | 2.40%         | 11        | 6.59%        |
| Precinct VBM                        | 4167                                 | 102        | 1249          | 752         | 19          | 2.53%         | 27        | 3.59%        |
| MB Precinct                         | 4167                                 | 102        | 158           | 100         | 3           | 3.00%         | 3         | 3.00%        |
| Provisional                         | 4167                                 | 102        | 82            | 30          | 0           | 0.00%         | 1         | 3.33%        |
| <b>Total</b>                        | <b>4167</b>                          | <b>510</b> | <b>1761</b>   | <b>1049</b> | <b>26</b>   | <b>2.48%</b>  | <b>42</b> | <b>4.00%</b> |
| <b>UNITED STATES SENATE</b>         |                                      |            |               |             |             |               |           |              |
| <b>UNITED STATES SENATE</b>         |                                      |            |               |             |             |               |           |              |
| Precinct TSx                        | 4167                                 | 102        | 0             | 0           | 0           | -             | 0         | -            |
| Precinct OS                         | 4167                                 | 102        | 272           | 167         | 4           | 2.40%         | 11        | 6.59%        |
| Precinct VBM                        | 4167                                 | 102        | 1249          | 752         | 19          | 2.53%         | 27        | 3.59%        |
| MB Precinct                         | 4167                                 | 102        | 158           | 100         | 3           | 3.00%         | 3         | 3.00%        |
| Provisional                         | 4167                                 | 102        | 82            | 30          | 0           | 0.00%         | 1         | 3.33%        |
| <b>Total</b>                        | <b>4167</b>                          | <b>510</b> | <b>1761</b>   | <b>1049</b> | <b>26</b>   | <b>2.48%</b>  | <b>42</b> | <b>4.00%</b> |
| <b>Total</b>                        |                                      |            |               |             |             |               |           |              |
| Precinct TSx                        | 4167                                 | 102        | 0             | 0           | 0           | -             | 0         | -            |
| Precinct OS                         | 4167                                 | 102        | 272           | 167         | 4           | 2.40%         | 11        | 6.59%        |
| Precinct VBM                        | 4167                                 | 102        | 1249          | 752         | 19          | 2.53%         | 27        | 3.59%        |
| MB Precinct                         | 4167                                 | 102        | 158           | 100         | 3           | 3.00%         | 3         | 3.00%        |
| Provisional                         | 4167                                 | 102        | 82            | 30          | 0           | 0.00%         | 1         | 3.33%        |
| <b>Total</b>                        | <b>4167</b>                          | <b>510</b> | <b>1761</b>   | <b>1049</b> | <b>26</b>   | <b>2.48%</b>  | <b>42</b> | <b>4.00%</b> |
| <b>Total</b>                        |                                      |            |               |             |             |               |           |              |
| <b>UNITED STATES REPRESENTATIVE</b> |                                      |            |               |             |             |               |           |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                                      |            |               |             |             |               |           |              |
| Precinct TSx                        | 4167                                 | 102        | 0             | 0           | 0           | -             | 0         | -            |
| Precinct OS                         | 4167                                 | 102        | 272           | 167         | 4           | 2.40%         | 11        | 6.59%        |
| Precinct VBM                        | 4167                                 | 102        | 1249          | 752         | 19          | 2.53%         | 27        | 3.59%        |
| MB Precinct                         | 4167                                 | 102        | 158           | 100         | 3           | 3.00%         | 3         | 3.00%        |
| Provisional                         | 4167                                 | 102        | 82            | 30          | 0           | 0.00%         | 1         | 3.33%        |
| <b>Total</b>                        | <b>4167</b>                          | <b>510</b> | <b>1761</b>   | <b>1049</b> | <b>26</b>   | <b>2.48%</b>  | <b>42</b> | <b>4.00%</b> |
| <b>Total</b>                        |                                      |            |               |             |             |               |           |              |
| Precinct TSx                        | 4167                                 | 102        | 0             | 0           | 0           | -             | 0         | -            |
| Precinct OS                         | 4167                                 | 102        | 272           | 167         | 4           | 2.40%         | 11        | 6.59%        |
| Precinct VBM                        | 4167                                 | 102        | 1249          | 752         | 19          | 2.53%         | 27        | 3.59%        |
| MB Precinct                         | 4167                                 | 102        | 158           | 100         | 3           | 3.00%         | 3         | 3.00%        |
| Provisional                         | 4167                                 | 102        | 82            | 30          | 0           | 0.00%         | 1         | 3.33%        |
| <b>Total</b>                        | <b>4167</b>                          | <b>510</b> | <b>1761</b>   | <b>1049</b> | <b>26</b>   | <b>2.48%</b>  | <b>42</b> | <b>4.00%</b> |
| <b>Total</b>                        |                                      |            |               |             |             |               |           |              |
| <b>STATE SENATE</b>                 |                                      |            |               |             |             |               |           |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                                      |            |               |             |             |               |           |              |
| Precinct TSx                        | 4167                                 | 102        | 0             | 0           | 0           | -             | 0         | -            |
| Precinct OS                         | 4167                                 | 102        | 272           | 167         | 4           | 2.40%         | 11        | 6.59%        |
| Precinct VBM                        | 4167                                 | 102        | 1249          | 752         | 19          | 2.53%         | 27        | 3.59%        |
| MB Precinct                         | 4167                                 | 102        | 158           | 100         | 3           | 3.00%         | 3         | 3.00%        |
| Provisional                         | 4167                                 | 102        | 82            | 30          | 0           | 0.00%         | 1         | 3.33%        |
| <b>Total</b>                        | <b>4167</b>                          | <b>510</b> | <b>1761</b>   | <b>1049</b> | <b>26</b>   | <b>2.48%</b>  | <b>42</b> | <b>4.00%</b> |
| <b>Total</b>                        |                                      |            |               |             |             |               |           |              |
| Precinct TSx                        | 4167                                 | 102        | 0             | 0           | 0           | -             | 0         | -            |
| Precinct OS                         | 4167                                 | 102        | 272           | 167         | 4           | 2.40%         | 11        | 6.59%        |
| Precinct VBM                        | 4167                                 | 102        | 1249          | 752         | 19          | 2.53%         | 27        | 3.59%        |
| MB Precinct                         | 4167                                 | 102        | 158           | 100         | 3           | 3.00%         | 3         | 3.00%        |
| Provisional                         | 4167                                 | 102        | 82            | 30          | 0           | 0.00%         | 1         | 3.33%        |
| <b>Total</b>                        | <b>4167</b>                          | <b>510</b> | <b>1761</b>   | <b>1049</b> | <b>26</b>   | <b>2.48%</b>  | <b>42</b> | <b>4.00%</b> |
| <b>Total</b>                        |                                      |            |               |             |             |               |           |              |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                                      |            |               |             |             |               |           |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                                      |            |               |             |             |               |           |              |
| Precinct TSx                        | 2874                                 | 75         | 0             | 0           | 0           | -             | 0         | -            |
| Precinct OS                         | 2874                                 | 75         | 177           | 119         | 3           | 2.52%         | 6         | 5.04%        |
| Precinct VBM                        | 2874                                 | 75         | 798           | 501         | 13          | 2.59%         | 15        | 2.99%        |
| MB Precinct                         | 2874                                 | 75         | 134           | 85          | 1           | 1.18%         | 3         | 3.53%        |

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|                                   | Reg. Voters                          | Vote For | Times Counted | Total Votes | WILEY DRAKE |        | ARTHUR HARRIS |        |
| Provisional                       | 2874                                 | 75       | 52            | 17          | 0           | 0.00%  | 0             | 0.00%  |
| Total                             | 2874                                 | 375      | 1161          | 722         | 17          | 2.35%  | 24            | 3.32%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |                                      |          |               |             |             |        |               |        |
| Precinct TSx                      | 1293                                 | 27       | 0             | 0           | 0           | -      | 0             | -      |
| Precinct OS                       | 1293                                 | 27       | 95            | 48          | 1           | 2.08%  | 5             | 10.42% |
| Precinct VBM                      | 1293                                 | 27       | 451           | 251         | 6           | 2.39%  | 12            | 4.78%  |
| MB Precinct                       | 1293                                 | 27       | 24            | 15          | 2           | 13.33% | 0             | 0.00%  |
| Provisional                       | 1293                                 | 27       | 30            | 13          | 0           | 0.00%  | 1             | 7.69%  |
| Total                             | 1293                                 | 135      | 600           | 327         | 9           | 2.75%  | 18            | 5.50%  |
| <b>Total</b>                      |                                      |          |               |             |             |        |               |        |
| Precinct TSx                      | 4167                                 | 102      | 0             | 0           | 0           | -      | 0             | -      |
| Precinct OS                       | 4167                                 | 102      | 272           | 167         | 4           | 2.40%  | 11            | 6.59%  |
| Precinct VBM                      | 4167                                 | 102      | 1249          | 752         | 19          | 2.53%  | 27            | 3.59%  |
| MB Precinct                       | 4167                                 | 102      | 158           | 100         | 3           | 3.00%  | 3             | 3.00%  |
| Provisional                       | 4167                                 | 102      | 82            | 30          | 0           | 0.00%  | 1             | 3.33%  |
| Total                             | 4167                                 | 510      | 1761          | 1049        | 26          | 2.48%  | 42            | 4.00%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |                                      |          |               |             |             |        |               |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                                      |          |               |             |             |        |               |        |
| Precinct TSx                      | 713                                  | 14       | 0             | 0           | 0           | -      | 0             | -      |
| Precinct OS                       | 713                                  | 14       | 56            | 26          | 1           | 3.85%  | 3             | 11.54% |
| Precinct VBM                      | 713                                  | 14       | 277           | 150         | 3           | 2.00%  | 6             | 4.00%  |
| MB Precinct                       | 713                                  | 14       | 15            | 9           | 1           | 11.11% | 0             | 0.00%  |
| Provisional                       | 713                                  | 14       | 16            | 6           | 0           | 0.00%  | 1             | 16.67% |
| Total                             | 713                                  | 70       | 364           | 191         | 5           | 2.62%  | 10            | 5.24%  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                                      |          |               |             |             |        |               |        |
| Precinct TSx                      | 928                                  | 23       | 0             | 0           | 0           | -      | 0             | -      |
| Precinct OS                       | 928                                  | 23       | 60            | 38          | 1           | 2.63%  | 3             | 7.89%  |
| Precinct VBM                      | 928                                  | 23       | 281           | 168         | 3           | 1.79%  | 8             | 4.76%  |
| MB Precinct                       | 928                                  | 23       | 58            | 41          | 0           | 0.00%  | 0             | 0.00%  |
| Provisional                       | 928                                  | 23       | 19            | 9           | 0           | 0.00%  | 0             | 0.00%  |
| Total                             | 928                                  | 115      | 418           | 256         | 4           | 1.56%  | 11            | 4.30%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                                      |          |               |             |             |        |               |        |
| Precinct TSx                      | 861                                  | 26       | 0             | 0           | 0           | -      | 0             | -      |
| Precinct OS                       | 861                                  | 26       | 53            | 32          | 1           | 3.13%  | 2             | 6.25%  |
| Precinct VBM                      | 861                                  | 26       | 265           | 166         | 5           | 3.01%  | 4             | 2.41%  |
| MB Precinct                       | 861                                  | 26       | 18            | 12          | 0           | 0.00%  | 0             | 0.00%  |
| Provisional                       | 861                                  | 26       | 12            | 4           | 0           | 0.00%  | 0             | 0.00%  |
| Total                             | 861                                  | 130      | 348           | 214         | 6           | 2.80%  | 6             | 2.80%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                                      |          |               |             |             |        |               |        |
| Precinct TSx                      | 880                                  | 22       | 0             | 0           | 0           | -      | 0             | -      |
| Precinct OS                       | 880                                  | 22       | 60            | 42          | 1           | 2.38%  | 2             | 4.76%  |
| Precinct VBM                      | 880                                  | 22       | 252           | 158         | 6           | 3.80%  | 2             | 1.27%  |
| MB Precinct                       | 880                                  | 22       | 54            | 29          | 2           | 6.90%  | 2             | 6.90%  |
| Provisional                       | 880                                  | 22       | 16            | 4           | 0           | 0.00%  | 0             | 0.00%  |
| Total                             | 880                                  | 110      | 382           | 233         | 9           | 3.86%  | 6             | 2.58%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                                      |          |               |             |             |        |               |        |
| Precinct TSx                      | 785                                  | 17       | 0             | 0           | 0           | -      | 0             | -      |
| Precinct OS                       | 785                                  | 17       | 43            | 29          | 0           | 0.00%  | 1             | 3.45%  |
| Precinct VBM                      | 785                                  | 17       | 174           | 110         | 2           | 1.82%  | 7             | 6.36%  |
| MB Precinct                       | 785                                  | 17       | 13            | 9           | 0           | 0.00%  | 1             | 11.11% |
| Provisional                       | 785                                  | 17       | 19            | 7           | 0           | 0.00%  | 0             | 0.00%  |
| Total                             | 785                                  | 85       | 249           | 155         | 2           | 1.29%  | 9             | 5.81%  |
| <b>Total</b>                      |                                      |          |               |             |             |        |               |        |
| Precinct TSx                      | 4167                                 | 102      | 0             | 0           | 0           | -      | 0             | -      |
| Precinct OS                       | 4167                                 | 102      | 272           | 167         | 4           | 2.40%  | 11            | 6.59%  |
| Precinct VBM                      | 4167                                 | 102      | 1249          | 752         | 19          | 2.53%  | 27            | 3.59%  |
| MB Precinct                       | 4167                                 | 102      | 158           | 100         | 3           | 3.00%  | 3             | 3.00%  |
| Provisional                       | 4167                                 | 102      | 82            | 30          | 0           | 0.00%  | 1             | 3.33%  |
| Total                             | 4167                                 | 510      | 1761          | 1049        | 26          | 2.48%  | 42            | 4.00%  |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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|                                 | President American Independent Party |            |               |             |             |               |           |              |
|---------------------------------|--------------------------------------|------------|---------------|-------------|-------------|---------------|-----------|--------------|
|                                 | Reg. Voters                          | Vote For   | Times Counted | Total Votes | WILEY DRAKE | ARTHUR HARRIS |           |              |
| <b>CITIES</b>                   |                                      |            |               |             |             |               |           |              |
| <b>CITY OF PLACERVILLE</b>      |                                      |            |               |             |             |               |           |              |
| Precinct TSx                    | 223                                  | 5          | 0             | 0           | 0           | -             | 0         | -            |
| Precinct OS                     | 223                                  | 5          | 12            | 5           | 0           | 0.00%         | 2         | 40.00%       |
| Precinct VBM                    | 223                                  | 5          | 68            | 48          | 1           | 2.08%         | 1         | 2.08%        |
| MB Precinct                     | 223                                  | 5          | 0             | 0           | 0           | -             | 0         | -            |
| Provisional                     | 223                                  | 5          | 2             | 2           | 0           | 0.00%         | 0         | 0.00%        |
| <b>Total</b>                    | <b>223</b>                           | <b>25</b>  | <b>82</b>     | <b>55</b>   | <b>1</b>    | <b>1.82%</b>  | <b>3</b>  | <b>5.45%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                                      |            |               |             |             |               |           |              |
| Precinct TSx                    | 440                                  | 7          | 0             | 0           | 0           | -             | 0         | -            |
| Precinct OS                     | 440                                  | 7          | 21            | 13          | 0           | 0.00%         | 1         | 7.69%        |
| Precinct VBM                    | 440                                  | 7          | 93            | 58          | 1           | 1.72%         | 5         | 8.62%        |
| MB Precinct                     | 440                                  | 7          | 0             | 0           | 0           | -             | 0         | -            |
| Provisional                     | 440                                  | 7          | 11            | 4           | 0           | 0.00%         | 0         | 0.00%        |
| <b>Total</b>                    | <b>440</b>                           | <b>35</b>  | <b>125</b>    | <b>75</b>   | <b>1</b>    | <b>1.33%</b>  | <b>6</b>  | <b>8.00%</b> |
| <b>UNINCORPORATED AREA</b>      |                                      |            |               |             |             |               |           |              |
| Precinct TSx                    | 3504                                 | 90         | 0             | 0           | 0           | -             | 0         | -            |
| Precinct OS                     | 3504                                 | 90         | 239           | 149         | 4           | 2.68%         | 8         | 5.37%        |
| Precinct VBM                    | 3504                                 | 90         | 1088          | 646         | 17          | 2.63%         | 21        | 3.25%        |
| MB Precinct                     | 3504                                 | 90         | 158           | 100         | 3           | 3.00%         | 3         | 3.00%        |
| Provisional                     | 3504                                 | 90         | 69            | 24          | 0           | 0.00%         | 1         | 4.17%        |
| <b>Total</b>                    | <b>3504</b>                          | <b>450</b> | <b>1554</b>   | <b>919</b>  | <b>24</b>   | <b>2.61%</b>  | <b>33</b> | <b>3.59%</b> |
| <b>Total</b>                    |                                      |            |               |             |             |               |           |              |
| Precinct TSx                    | 4167                                 | 102        | 0             | 0           | 0           | -             | 0         | -            |
| Precinct OS                     | 4167                                 | 102        | 272           | 167         | 4           | 2.40%         | 11        | 6.59%        |
| Precinct VBM                    | 4167                                 | 102        | 1249          | 752         | 19          | 2.53%         | 27        | 3.59%        |
| MB Precinct                     | 4167                                 | 102        | 158           | 100         | 3           | 3.00%         | 3         | 3.00%        |
| Provisional                     | 4167                                 | 102        | 82            | 30          | 0           | 0.00%         | 1         | 3.33%        |
| <b>Total</b>                    | <b>4167</b>                          | <b>510</b> | <b>1761</b>   | <b>1049</b> | <b>26</b>   | <b>2.48%</b>  | <b>42</b> | <b>4.00%</b> |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                        | President American Independent Party |        |                 |        |            |        |                |         |             |        |
|------------------------|--------------------------------------|--------|-----------------|--------|------------|--------|----------------|---------|-------------|--------|
|                        | JAMES HEDGES                         |        | THOMAS HOEFLING |        | J.R. MYERS |        | ROBERT ORNELAS |         | ALAN SPEARS |        |
| Jurisdiction Wide      |                                      |        |                 |        |            |        |                |         |             |        |
| 100001.11-CP 100001.11 |                                      |        |                 |        |            |        |                |         |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| 100002.11-CP 100002.11 |                                      |        |                 |        |            |        |                |         |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Precinct OS            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Precinct VBM           | 0                                    | 0.00%  | 1               | 25.00% | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Provisional            | 1                                    | 50.00% | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| Total                  | 1                                    | 16.67% | 1               | 16.67% | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| 100003.11-CP 100003.11 |                                      |        |                 |        |            |        |                |         |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| 100004.11-CP 100004.11 |                                      |        |                 |        |            |        |                |         |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| 100005.11-CP 100005.11 |                                      |        |                 |        |            |        |                |         |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Precinct OS            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| 100006.11-CP 100006.11 |                                      |        |                 |        |            |        |                |         |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 33.33% | 0              | 0.00%   | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 1               | 6.67%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Total                  | 0                                    | 0.00%  | 1               | 5.56%  | 1          | 5.56%  | 0              | 0.00%   | 0           | 0.00%  |
| 100008.11-CP 100008.11 |                                      |        |                 |        |            |        |                |         |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 1           | 10.00% |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Provisional            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 1           | 7.14%  |
| 100009.11-CP 100009.11 |                                      |        |                 |        |            |        |                |         |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 1               | 16.67% | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 1              | 5.26%   | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Total                  | 0                                    | 0.00%  | 1               | 4.00%  | 0          | 0.00%  | 1              | 4.00%   | 0           | 0.00%  |
| 100012.11-CP 100012.11 |                                      |        |                 |        |            |        |                |         |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -       | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 1              | 100.00% | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%   | 1           | 5.88%  |

**COUNTY OF EL DORADO**  
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|                        | President American Independent Party |       |                 |        |            |         |                |        |             |        |
|------------------------|--------------------------------------|-------|-----------------|--------|------------|---------|----------------|--------|-------------|--------|
|                        | JAMES HEDGES                         |       | THOMAS HOEFLING |        | J.R. MYERS |         | ROBERT ORNELAS |        | ALAN SPEARS |        |
| MB Precinct            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00%  | 0          | 0.00%   | 1              | 5.56%  | 1           | 5.56%  |
| 100013.11-CP 100013.11 |                                      |       |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00% | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00% | 1               | 5.56%  | 1          | 5.56%   | 1              | 5.56%  | 1           | 5.56%  |
| MB Precinct            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 1               | 5.26%  | 1          | 5.26%   | 1              | 5.26%  | 1           | 5.26%  |
| 100014.11-CP 100014.11 |                                      |       |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00% | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00% | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| 100022.11-CP 100022.11 |                                      |       |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00% | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00% | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| 100023.11-CP 100023.11 |                                      |       |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00% | 1               | 50.00% | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00% | 0               | 0.00%  | 0          | 0.00%   | 1              | 7.14%  | 2           | 14.29% |
| MB Precinct            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | 0.00% | 1               | 33.33% | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| Total                  | 0                                    | 0.00% | 2               | 10.53% | 0          | 0.00%   | 1              | 5.26%  | 2           | 10.53% |
| 200003.12-CP 200003.12 |                                      |       |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00% | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 1           | 33.33% |
| Precinct VBM           | 0                                    | 0.00% | 0               | 0.00%  | 1          | 16.67%  | 1              | 16.67% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | 0.00% | 0               | 0.00%  | 1          | 100.00% | 0              | 0.00%  | 0           | 0.00%  |
| Total                  | 0                                    | 0.00% | 0               | 0.00%  | 2          | 20.00%  | 1              | 10.00% | 1           | 10.00% |
| 200004.12-CP 200004.12 |                                      |       |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00% | 0               | 0.00%  | 1          | 50.00%  | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00% | 1               | 6.67%  | 0          | 0.00%   | 0              | 0.00%  | 1           | 6.67%  |
| MB Precinct            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 1               | 5.88%  | 1          | 5.88%   | 0              | 0.00%  | 1           | 5.88%  |
| 200005.02-CP 200005.02 |                                      |       |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00% | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 1           | 50.00% |
| Precinct VBM           | 0                                    | 0.00% | 1               | 6.25%  | 2          | 12.50%  | 2              | 12.50% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 1               | 5.56%  | 2          | 11.11%  | 2              | 11.11% | 1           | 5.56%  |
| 200006.12-CP 200006.12 |                                      |       |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00% | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00% | 2               | 28.57% | 1          | 14.29%  | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 2               | 25.00% | 1          | 12.50%  | 0              | 0.00%  | 0           | 0.00%  |
| 200008.12-CP 200008.12 |                                      |       |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -      | 0          | -       | 0              | -      | 0           | -      |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|------------------------|--------------------------------------|---------|-----------------|---------|------------|--------|----------------|-------|-------------|--------|
|                        | JAMES HEDGES                         |         | THOMAS HOEFLING |         | J.R. MYERS |        | ROBERT ORNELAS |       | ALAN SPEARS |        |
| Precinct OS            | 0                                    | 0.00%   | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 1                                    | 25.00%  | 0               | 0.00%   | 2          | 50.00% | 0              | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Total                  | 1                                    | 16.67%  | 0               | 0.00%   | 2          | 33.33% | 0              | 0.00% | 0           | 0.00%  |
| 200010.12-CP 200010.12 |                                      |         |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct VBM           | 0                                    | 0.00%   | 0               | 0.00%   | 1          | 12.50% | 0              | 0.00% | 1           | 12.50% |
| MB Precinct            | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | 0.00%   | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Total                  | 0                                    | 0.00%   | 0               | 0.00%   | 1          | 11.11% | 0              | 0.00% | 1           | 11.11% |
| 200012.12-CP 200012.12 |                                      |         |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | 0.00%   | 2               | 100.00% | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%   | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | 0.00%   | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Total                  | 0                                    | 0.00%   | 2               | 25.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| 200013.12-CP 200013.12 |                                      |         |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | 0.00%   | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 1                                    | 9.09%   | 1               | 9.09%   | 1          | 9.09%  | 0              | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | 0.00%   | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Total                  | 1                                    | 7.14%   | 1               | 7.14%   | 1          | 7.14%  | 0              | 0.00% | 0           | 0.00%  |
| 200014.12-CP 200014.12 |                                      |         |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | 0.00%   | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%   | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | 0.00%   | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Total                  | 0                                    | 0.00%   | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| 200015.02-CP 200015.02 |                                      |         |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | 0.00%   | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%   | 1               | 5.88%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | 0.00%   | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Total                  | 0                                    | 0.00%   | 1               | 4.35%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| 200016.12-CP 200016.12 |                                      |         |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 1                                    | 100.00% | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%   | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 1           | 20.00% |
| MB Precinct            | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Total                  | 1                                    | 16.67%  | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 1           | 16.67% |
| 200016.13-CP 200016.13 |                                      |         |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | 0.00%   | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%   | 1               | 20.00%  | 0          | 0.00%  | 0              | 0.00% | 1           | 20.00% |
| MB Precinct            | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Total                  | 0                                    | 0.00%   | 1               | 16.67%  | 0          | 0.00%  | 0              | 0.00% | 1           | 16.67% |
| 200017.02-CP 200017.02 |                                      |         |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 1                                    | 25.00%  | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%   | 1               | 5.26%   | 0          | 0.00%  | 0              | 0.00% | 1           | 5.26%  |
| MB Precinct            | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | -       | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Total                  | 1                                    | 4.35%   | 1               | 4.35%   | 0          | 0.00%  | 0              | 0.00% | 1           | 4.35%  |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|------------------------|--------------------------------------|-------|-----------------|---------|------------|--------|----------------|-------|-------------|--------|
|                        | JAMES HEDGES                         |       | THOMAS HOEFLING |         | J.R. MYERS |        | ROBERT ORNELAS |       | ALAN SPEARS |        |
| 200018.02-CP 200018.02 |                                      |       |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | 0.00% | 0               | 0.00%   | 1          | 20.00% | 0              | 0.00% | 1           | 20.00% |
| Precinct VBM           | 0                                    | 0.00% | 1               | 7.14%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Total                  | 0                                    | 0.00% | 1               | 5.26%   | 1          | 5.26%  | 0              | 0.00% | 1           | 5.26%  |
| 200020.02-CP 200020.02 |                                      |       |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | 0.00% | 0               | 0.00%   | 1          | 50.00% | 0              | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00% | 3               | 23.08%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Total                  | 0                                    | 0.00% | 3               | 20.00%  | 1          | 6.67%  | 0              | 0.00% | 0           | 0.00%  |
| 200021.01-CP 200021.01 |                                      |       |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct VBM           | 0                                    | 0.00% | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| 200021.02-CP 200021.02 |                                      |       |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | 0.00% | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00% | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| 200024.02-CP 200024.02 |                                      |       |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | 0.00% | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00% | 1               | 25.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Total                  | 0                                    | 0.00% | 1               | 20.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| 300001.04-CP 300001.04 |                                      |       |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct VBM           | 0                                    | 0.00% | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| 300001.05-CP 300001.05 |                                      |       |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct VBM           | 0                                    | 0.00% | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | 0.00% | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Total                  | 0                                    | 0.00% | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| 300003.05-CP 300003.05 |                                      |       |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | 0.00% | 0               | 0.00%   | 1          | 50.00% | 0              | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00% | 2               | 11.11%  | 0          | 0.00%  | 1              | 5.56% | 0           | 0.00%  |
| MB Precinct            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Provisional            | 0                                    | 0.00% | 1               | 100.00% | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Total                  | 0                                    | 0.00% | 3               | 14.29%  | 1          | 4.76%  | 1              | 4.76% | 0           | 0.00%  |
| 300004.05-CP 300004.05 |                                      |       |                 |         |            |        |                |       |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS            | 0                                    | 0.00% | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00% | 1               | 8.33%   | 2          | 16.67% | 0              | 0.00% | 1           | 8.33%  |
| MB Precinct            | 0                                    | -     | 0               | -       | 0          | -      | 0              | -     | 0           | -      |

**COUNTY OF EL DORADO**  
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|------------------------|--------------------------------------|--------|-----------------|--------|------------|---------|----------------|--------|-------------|--------|
|                        | JAMES HEDGES                         |        | THOMAS HOEFLING |        | J.R. MYERS |         | ROBERT ORNELAS |        | ALAN SPEARS |        |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00%  | 1               | 7.69%  | 2          | 15.38%  | 0              | 0.00%  | 1           | 7.69%  |
| 300005.05-CP 300005.05 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 1                                    | 11.11% | 1               | 11.11% | 0          | 0.00%   | 0              | 0.00%  | 1           | 11.11% |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 1                                    | 9.09%  | 1               | 9.09%  | 0          | 0.00%   | 0              | 0.00%  | 1           | 9.09%  |
| 300008.05-CP 300008.05 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 1               | 9.09%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00%  | 1               | 8.33%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| 300009.04-CP 300009.04 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 1               | 25.00% | 1          | 25.00%  | 0              | 0.00%  | 1           | 25.00% |
| Precinct VBM           | 0                                    | 0.00%  | 1               | 14.29% | 1          | 14.29%  | 0              | 0.00%  | 1           | 14.29% |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00%  | 2               | 18.18% | 2          | 18.18%  | 0              | 0.00%  | 2           | 18.18% |
| 300010.04-CP 300010.04 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 3                                    | 14.29% | 1               | 4.76%  | 0          | 0.00%   | 0              | 0.00%  | 2           | 9.52%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 3                                    | 13.64% | 1               | 4.55%  | 0          | 0.00%   | 0              | 0.00%  | 2           | 9.09%  |
| 300011.04-CP 300011.04 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 1              | 50.00% | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 1              | 7.69%  | 0           | 0.00%  |
| 300013.05-CP 300013.05 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 100.00% | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 10.00%  | 0              | 0.00%  | 0           | 0.00%  |
| 300015.04-CP 300015.04 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| 300015.05-CP 300015.05 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| 300017.05-CP 300017.05 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |

**COUNTY OF EL DORADO**  
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|------------------------|--------------------------------------|--------|-----------------|--------|------------|---------|----------------|--------|-------------|--------|
|                        | JAMES HEDGES                         |        | THOMAS HOEFLING |        | J.R. MYERS |         | ROBERT ORNELAS |        | ALAN SPEARS |        |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| 300018.04-CP 300018.04 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 12.50%  | 0              | 0.00%  | 1           | 12.50% |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 10.00%  | 0              | 0.00%  | 1           | 10.00% |
| 300020.04-CP 300020.04 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 1                                    | 20.00% | 1               | 20.00% | 0          | 0.00%   | 1              | 20.00% | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 9.09%   | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 1                                    | 6.25%  | 1               | 6.25%  | 1          | 6.25%   | 1              | 6.25%  | 0           | 0.00%  |
| 300021.03-CP 300021.03 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 100.00% | 0              | 0.00%  | 0           | 0.00%  |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 10.00%  | 0              | 0.00%  | 0           | 0.00%  |
| 300022.05-CP 300022.05 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 50.00%  | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 14.29%  | 0              | 0.00%  | 0           | 0.00%  |
| 300023.03-CP 300023.03 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 1           | 25.00% |
| Precinct VBM           | 0                                    | 0.00%  | 1               | 6.67%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00%  | 1               | 5.26%  | 0          | 0.00%   | 0              | 0.00%  | 1           | 5.26%  |
| 300023.05-CP 300023.05 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 25.00%  | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 25.00%  | 0              | 0.00%  | 0           | 0.00%  |
| 400001.14-CP 400001.14 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 1               | 5.88%  | 1          | 5.88%   | 0              | 0.00%  | 1           | 5.88%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00%  | 1               | 4.76%  | 1          | 4.76%   | 0              | 0.00%  | 1           | 4.76%  |
| 400002.08-CP 400002.08 |                                      |        |                 |        |            |         |                |        |             |        |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 1           | 50.00% |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -       | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%   | 0              | 0.00%  | 1           | 8.33%  |
| 400003.08-CP 400003.08 |                                      |        |                 |        |            |         |                |        |             |        |

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|------------------------|--------------------------------------|--------|-----------------|--------|------------|--------|----------------|--------|-------------|---------|
|                        | JAMES HEDGES                         |        | THOMAS HOEFLING |        | J.R. MYERS |        | ROBERT ORNELAS |        | ALAN SPEARS |         |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 2           | 40.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 7.14%  | 1              | 7.14%  | 1           | 7.14%   |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Provisional            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%   |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 4.76%  | 1              | 4.76%  | 3           | 14.29%  |
| 400004.14-CP 400004.14 |                                      |        |                 |        |            |        |                |        |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%   |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 1              | 11.11% | 2           | 22.22%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 1              | 9.09%  | 2           | 18.18%  |
| 400005.08-CP 400005.08 |                                      |        |                 |        |            |        |                |        |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Precinct OS            | 1                                    | 33.33% | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 1           | 33.33%  |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%   |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Total                  | 1                                    | 12.50% | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 1           | 12.50%  |
| 400006.08-CP 400006.08 |                                      |        |                 |        |            |        |                |        |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 2          | 22.22% | 0              | 0.00%  | 0           | 0.00%   |
| Precinct VBM           | 1                                    | 7.14%  | 1               | 7.14%  | 0          | 0.00%  | 0              | 0.00%  | 1           | 7.14%   |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Total                  | 1                                    | 4.35%  | 1               | 4.35%  | 2          | 8.70%  | 0              | 0.00%  | 1           | 4.35%   |
| 400007.08-CP 400007.08 |                                      |        |                 |        |            |        |                |        |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Precinct OS            | 0                                    | 0.00%  | 1               | 33.33% | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%   |
| Precinct VBM           | 0                                    | 0.00%  | 2               | 11.76% | 1          | 5.88%  | 0              | 0.00%  | 1           | 5.88%   |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Provisional            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%   |
| Total                  | 0                                    | 0.00%  | 3               | 14.29% | 1          | 4.76%  | 0              | 0.00%  | 1           | 4.76%   |
| 400010.08-CP 400010.08 |                                      |        |                 |        |            |        |                |        |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 20.00% | 0              | 0.00%  | 0           | 0.00%   |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 5.56%  | 1              | 5.56%  | 2           | 11.11%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 2          | 8.70%  | 1              | 4.35%  | 2           | 8.70%   |
| 400012.08-CP 400012.08 |                                      |        |                 |        |            |        |                |        |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Precinct OS            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%   |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%   |
| 400013.08-CP 400013.08 |                                      |        |                 |        |            |        |                |        |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%   |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 25.00% | 0              | 0.00%  | 0           | 0.00%   |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 20.00% | 0              | 0.00%  | 0           | 0.00%   |
| 400014.08-CP 400014.08 |                                      |        |                 |        |            |        |                |        |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 1           | 100.00% |
| Precinct VBM           | 1                                    | 7.69%  | 1               | 7.69%  | 0          | 0.00%  | 0              | 0.00%  | 2           | 15.38%  |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -       |
| Provisional            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%   |



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|------------------------|--------------------------------------|--------|-----------------|---------|------------|--------|----------------|---------|-------------|---------|
|                        | JAMES HEDGES                         |        | THOMAS HOEFLING |         | J.R. MYERS |        | ROBERT ORNELAS |         | ALAN SPEARS |         |
| Total                  | 1                                    | 6.67%  | 1               | 6.67%   | 0          | 0.00%  | 0              | 0.00%   | 3           | 20.00%  |
| 400017.08-CP 400017.08 |                                      |        |                 |         |            |        |                |         |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Precinct OS            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Precinct VBM           | 1                                    | 8.33%  | 0               | 0.00%   | 0          | 0.00%  | 1              | 8.33%   | 1           | 8.33%   |
| MB Precinct            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Provisional            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Total                  | 1                                    | 8.33%  | 0               | 0.00%   | 0          | 0.00%  | 1              | 8.33%   | 1           | 8.33%   |
| 400018.08-CP 400018.08 |                                      |        |                 |         |            |        |                |         |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%   | 1          | 50.00% | 0              | 0.00%   | 0           | 0.00%   |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%   | 0          | 0.00%  | 1              | 100.00% | 0           | 0.00%   |
| MB Precinct            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Provisional            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%   | 1          | 33.33% | 1              | 33.33%  | 0           | 0.00%   |
| 400020.08-CP 400020.08 |                                      |        |                 |         |            |        |                |         |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00%   | 1           | 20.00%  |
| Precinct VBM           | 0                                    | 0.00%  | 1               | 10.00%  | 0          | 0.00%  | 1              | 10.00%  | 1           | 10.00%  |
| MB Precinct            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Provisional            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Total                  | 0                                    | 0.00%  | 1               | 6.67%   | 0          | 0.00%  | 1              | 6.67%   | 2           | 13.33%  |
| 400021.07-CP 400021.07 |                                      |        |                 |         |            |        |                |         |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Precinct OS            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Precinct VBM           | 1                                    | 11.11% | 0               | 0.00%   | 0          | 0.00%  | 1              | 11.11%  | 0           | 0.00%   |
| MB Precinct            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Provisional            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Total                  | 1                                    | 11.11% | 0               | 0.00%   | 0          | 0.00%  | 1              | 11.11%  | 0           | 0.00%   |
| 500001.10-CP 500001.10 |                                      |        |                 |         |            |        |                |         |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%   |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%   |
| MB Precinct            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Provisional            | 0                                    | 0.00%  | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%   |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%   |
| 500002.10-CP 500002.10 |                                      |        |                 |         |            |        |                |         |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Precinct OS            | 0                                    | 0.00%  | 1               | 100.00% | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%   |
| Precinct VBM           | 0                                    | 0.00%  | 1               | 9.09%   | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%   |
| MB Precinct            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Provisional            | 0                                    | 0.00%  | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%   |
| Total                  | 0                                    | 0.00%  | 2               | 15.38%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%   |
| 500003.10-CP 500003.10 |                                      |        |                 |         |            |        |                |         |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%   | 1          | 50.00% | 0              | 0.00%   | 0           | 0.00%   |
| Precinct VBM           | 1                                    | 16.67% | 0               | 0.00%   | 0          | 0.00%  | 1              | 16.67%  | 1           | 16.67%  |
| MB Precinct            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Provisional            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Total                  | 1                                    | 12.50% | 0               | 0.00%   | 1          | 12.50% | 1              | 12.50%  | 1           | 12.50%  |
| 500004.10-CP 500004.10 |                                      |        |                 |         |            |        |                |         |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%   |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%   | 1          | 10.00% | 0              | 0.00%   | 0           | 0.00%   |
| MB Precinct            | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Provisional            | 0                                    | 0.00%  | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00%   | 1           | 100.00% |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%   | 1          | 8.33%  | 0              | 0.00%   | 1           | 8.33%   |
| 500005.10-CP 500005.10 |                                      |        |                 |         |            |        |                |         |             |         |
| Precinct TSx           | 0                                    | -      | 0               | -       | 0          | -      | 0              | -       | 0           | -       |
| Precinct OS            | 0                                    | 0.00%  | 1               | 50.00%  | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%   |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%   | 0          | 0.00%  | 0              | 0.00%   | 0           | 0.00%   |

**COUNTY OF EL DORADO**  
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|                        | JAMES HEDGES                         |        | THOMAS HOEFLING |        | J.R. MYERS |        | ROBERT ORNELAS |       | ALAN SPEARS |       |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Provisional            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| Total                  | 0                                    | 0.00%  | 1               | 11.11% | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| 500006.10-CP 500006.10 |                                      |        |                 |        |            |        |                |       |             |       |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 20.00% | 0              | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                                    | 0.00%  | 1               | 7.69%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Total                  | 0                                    | 0.00%  | 1               | 5.56%  | 1          | 5.56%  | 0              | 0.00% | 0           | 0.00% |
| 500007.10-CP 500007.10 |                                      |        |                 |        |            |        |                |       |             |       |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Precinct OS            | 0                                    | 0.00%  | 1               | 50.00% | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Total                  | 0                                    | 0.00%  | 1               | 7.69%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| 500009.10-CP 500009.10 |                                      |        |                 |        |            |        |                |       |             |       |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Provisional            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| 500011.10-CP 500011.10 |                                      |        |                 |        |            |        |                |       |             |       |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Precinct OS            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Precinct VBM           | 0                                    | 0.00%  | 1               | 20.00% | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Total                  | 0                                    | 0.00%  | 1               | 20.00% | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| 500012.10-CP 500012.10 |                                      |        |                 |        |            |        |                |       |             |       |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Provisional            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| 500015.10-CP 500015.10 |                                      |        |                 |        |            |        |                |       |             |       |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Total                  | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| 500020.10-CP 500020.10 |                                      |        |                 |        |            |        |                |       |             |       |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Precinct OS            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| Precinct VBM           | 2                                    | 16.67% | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 1           | 8.33% |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Provisional            | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| Total                  | 2                                    | 12.50% | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 1           | 6.25% |
| 500021.10-CP 500021.10 |                                      |        |                 |        |            |        |                |       |             |       |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Precinct OS            | 1                                    | 25.00% | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| Precinct VBM           | 4                                    | 23.53% | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Provisional            | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |
| Total                  | 5                                    | 23.81% | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00% |
| 95U201.01-MB 95U201.01 |                                      |        |                 |        |            |        |                |       |             |       |
| Precinct TSx           | 0                                    | -      | 0               | -      | 0          | -      | 0              | -     | 0           | -     |

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|------------------------|--------------------------------------|-------|-----------------|-------|------------|--------|----------------|--------|-------------|--------|
|                        | JAMES HEDGES                         |       | THOMAS HOEFLING |       | J.R. MYERS |        | ROBERT ORNELAS |        | ALAN SPEARS |        |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Total                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| 95U201.02-MB 95U201.02 |                                      |       |                 |       |            |        |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%  |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%  |
| 95U202.02-MB 95U202.02 |                                      |       |                 |       |            |        |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00%  | 2              | 12.50% | 0           | 0.00%  |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00%  | 2              | 12.50% | 0           | 0.00%  |
| 95U203.02-MB 95U203.02 |                                      |       |                 |       |            |        |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Total                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| 95U204.01-MB 95U204.01 |                                      |       |                 |       |            |        |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | 0.00% | 0               | 0.00% | 1          | 6.67%  | 1              | 6.67%  | 0           | 0.00%  |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00% | 1          | 6.67%  | 1              | 6.67%  | 0           | 0.00%  |
| 95U301.05-MB 95U301.05 |                                      |       |                 |       |            |        |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | 0.00% | 0               | 0.00% | 1          | 33.33% | 1              | 33.33% | 1           | 33.33% |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00% | 1          | 33.33% | 1              | 33.33% | 1           | 33.33% |
| 95U302.04-MB 95U302.04 |                                      |       |                 |       |            |        |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Total                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| 95U302.05-MB 95U302.05 |                                      |       |                 |       |            |        |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | 0.00% | 0               | 0.00% | 1          | 50.00% | 0              | 0.00%  | 0           | 0.00%  |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00% | 1          | 50.00% | 0              | 0.00%  | 0           | 0.00%  |
| 95U303.03-MB 95U303.03 |                                      |       |                 |       |            |        |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%  |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -      | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%  |

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|                        | President American Independent Party |       |                 |       |            |       |                |        |             |        |
|------------------------|--------------------------------------|-------|-----------------|-------|------------|-------|----------------|--------|-------------|--------|
|                        | JAMES HEDGES                         |       | THOMAS HOEFLING |       | J.R. MYERS |       | ROBERT ORNELAS |        | ALAN SPEARS |        |
| 95U303.05-MB 95U303.05 |                                      |       |                 |       |            |       |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00% | 1              | 20.00% | 1           | 20.00% |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00% | 1              | 20.00% | 1           | 20.00% |
| 95U304.05-MB 95U304.05 |                                      |       |                 |       |            |       |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Total                  | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| 95U305.05-MB 95U305.05 |                                      |       |                 |       |            |       |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Total                  | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| 95U401.08-MB 95U401.08 |                                      |       |                 |       |            |       |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Total                  | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| 95U402.07-MB 95U402.07 |                                      |       |                 |       |            |       |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00% | 0              | 0.00%  | 0           | 0.00%  |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00% | 0              | 0.00%  | 0           | 0.00%  |
| 95U402.08-MB 95U402.08 |                                      |       |                 |       |            |       |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00% | 0              | 0.00%  | 0           | 0.00%  |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00% | 0              | 0.00%  | 0           | 0.00%  |
| 95U403.08-MB 95U403.08 |                                      |       |                 |       |            |       |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00% | 0              | 0.00%  | 0           | 0.00%  |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Total                  | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00% | 0              | 0.00%  | 0           | 0.00%  |
| 95U404.06-MB 95U404.06 |                                      |       |                 |       |            |       |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Provisional            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Total                  | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| 95U404.08-MB 95U404.08 |                                      |       |                 |       |            |       |                |        |             |        |
| Precinct TSx           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct OS            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| Precinct VBM           | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |
| MB Precinct            | 0                                    | -     | 0               | -     | 0          | -     | 0              | -      | 0           | -      |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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 Time:08:08:08  
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|                               | President American Independent Party |       |                 |       |            |        |                |       |             |        |
|-------------------------------|--------------------------------------|-------|-----------------|-------|------------|--------|----------------|-------|-------------|--------|
|                               | JAMES HEDGES                         |       | THOMAS HOEFLING |       | J.R. MYERS |        | ROBERT ORNELAS |       | ALAN SPEARS |        |
| Provisional                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Total                         | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| 95U501.09-MB 95U501.09        |                                      |       |                 |       |            |        |                |       |             |        |
| Precinct TSx                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Precinct VBM                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| MB Precinct                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Provisional                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Total                         | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| 95U501.10-MB 95U501.10        |                                      |       |                 |       |            |        |                |       |             |        |
| Precinct TSx                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Precinct VBM                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| MB Precinct                   | 0                                    | 0.00% | 0               | 0.00% | 1          | 14.29% | 0              | 0.00% | 0           | 0.00%  |
| Provisional                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Total                         | 0                                    | 0.00% | 0               | 0.00% | 1          | 14.29% | 0              | 0.00% | 0           | 0.00%  |
| 95U598.10-MB 95U598.10        |                                      |       |                 |       |            |        |                |       |             |        |
| Precinct TSx                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Precinct VBM                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| MB Precinct                   | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00%  | 0              | 0.00% | 1           | 50.00% |
| Provisional                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Total                         | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00%  | 0              | 0.00% | 1           | 50.00% |
| 95U599.10-MB 95U599.10        |                                      |       |                 |       |            |        |                |       |             |        |
| Precinct TSx                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Precinct VBM                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| MB Precinct                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Provisional                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Total                         | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| 96U101.11-MB 96U101.11        |                                      |       |                 |       |            |        |                |       |             |        |
| Precinct TSx                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Precinct VBM                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| MB Precinct                   | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Provisional                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Total                         | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| 96U401.14-MB 96U401.14        |                                      |       |                 |       |            |        |                |       |             |        |
| Precinct TSx                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Precinct VBM                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| MB Precinct                   | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Provisional                   | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Total                         | 0                                    | 0.00% | 0               | 0.00% | 0          | 0.00%  | 0              | 0.00% | 0           | 0.00%  |
| Total                         |                                      |       |                 |       |            |        |                |       |             |        |
| Precinct TSx                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS                   | 5                                    | 2.99% | 10              | 5.99% | 13         | 7.78%  | 3              | 1.80% | 11          | 6.59%  |
| Precinct VBM                  | 17                                   | 2.26% | 33              | 4.39% | 22         | 2.93%  | 15             | 1.99% | 30          | 3.99%  |
| MB Precinct                   | 0                                    | 0.00% | 0               | 0.00% | 4          | 4.00%  | 5              | 5.00% | 3           | 3.00%  |
| Provisional                   | 1                                    | 3.33% | 2               | 6.67% | 2          | 6.67%  | 0              | 0.00% | 1           | 3.33%  |
| Total                         | 23                                   | 2.19% | 45              | 4.29% | 41         | 3.91%  | 23             | 2.19% | 45          | 4.29%  |
| STATE BOARD OF EQUALIZATION   |                                      |       |                 |       |            |        |                |       |             |        |
| STATE BOARD OF EQUALIZATION 1 |                                      |       |                 |       |            |        |                |       |             |        |
| Precinct TSx                  | 0                                    | -     | 0               | -     | 0          | -      | 0              | -     | 0           | -      |
| Precinct OS                   | 5                                    | 2.99% | 10              | 5.99% | 13         | 7.78%  | 3              | 1.80% | 11          | 6.59%  |
| Precinct VBM                  | 17                                   | 2.26% | 33              | 4.39% | 22         | 2.93%  | 15             | 1.99% | 30          | 3.99%  |
| MB Precinct                   | 0                                    | 0.00% | 0               | 0.00% | 4          | 4.00%  | 5              | 5.00% | 3           | 3.00%  |
| Provisional                   | 1                                    | 3.33% | 2               | 6.67% | 2          | 6.67%  | 0              | 0.00% | 1           | 3.33%  |
| Total                         | 23                                   | 2.19% | 45              | 4.29% | 41         | 3.91%  | 23             | 2.19% | 45          | 4.29%  |
| Total                         |                                      |       |                 |       |            |        |                |       |             |        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

Date:07/05/16  
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|                                     | President American Independent Party |              |                 |              |            |              |                |              |             |              |
|-------------------------------------|--------------------------------------|--------------|-----------------|--------------|------------|--------------|----------------|--------------|-------------|--------------|
|                                     | JAMES HEDGES                         |              | THOMAS HOEFLING |              | J.R. MYERS |              | ROBERT ORNELAS |              | ALAN SPEARS |              |
| Precinct TSx                        | 0                                    | -            | 0               | -            | 0          | -            | 0              | -            | 0           | -            |
| Precinct OS                         | 5                                    | 2.99%        | 10              | 5.99%        | 13         | 7.78%        | 3              | 1.80%        | 11          | 6.59%        |
| Precinct VBM                        | 17                                   | 2.26%        | 33              | 4.39%        | 22         | 2.93%        | 15             | 1.99%        | 30          | 3.99%        |
| MB Precinct                         | 0                                    | 0.00%        | 0               | 0.00%        | 4          | 4.00%        | 5              | 5.00%        | 3           | 3.00%        |
| Provisional                         | 1                                    | 3.33%        | 2               | 6.67%        | 2          | 6.67%        | 0              | 0.00%        | 1           | 3.33%        |
| <b>Total</b>                        | <b>23</b>                            | <b>2.19%</b> | <b>45</b>       | <b>4.29%</b> | <b>41</b>  | <b>3.91%</b> | <b>23</b>      | <b>2.19%</b> | <b>45</b>   | <b>4.29%</b> |
| <b>UNITED STATES SENATE</b>         |                                      |              |                 |              |            |              |                |              |             |              |
| <b>UNITED STATES SENATE</b>         |                                      |              |                 |              |            |              |                |              |             |              |
| Precinct TSx                        | 0                                    | -            | 0               | -            | 0          | -            | 0              | -            | 0           | -            |
| Precinct OS                         | 5                                    | 2.99%        | 10              | 5.99%        | 13         | 7.78%        | 3              | 1.80%        | 11          | 6.59%        |
| Precinct VBM                        | 17                                   | 2.26%        | 33              | 4.39%        | 22         | 2.93%        | 15             | 1.99%        | 30          | 3.99%        |
| MB Precinct                         | 0                                    | 0.00%        | 0               | 0.00%        | 4          | 4.00%        | 5              | 5.00%        | 3           | 3.00%        |
| Provisional                         | 1                                    | 3.33%        | 2               | 6.67%        | 2          | 6.67%        | 0              | 0.00%        | 1           | 3.33%        |
| <b>Total</b>                        | <b>23</b>                            | <b>2.19%</b> | <b>45</b>       | <b>4.29%</b> | <b>41</b>  | <b>3.91%</b> | <b>23</b>      | <b>2.19%</b> | <b>45</b>   | <b>4.29%</b> |
| <b>Total</b>                        | <b>0</b>                             | <b>-</b>     | <b>0</b>        | <b>-</b>     | <b>0</b>   | <b>-</b>     | <b>0</b>       | <b>-</b>     | <b>0</b>    | <b>-</b>     |
| Precinct OS                         | 5                                    | 2.99%        | 10              | 5.99%        | 13         | 7.78%        | 3              | 1.80%        | 11          | 6.59%        |
| Precinct VBM                        | 17                                   | 2.26%        | 33              | 4.39%        | 22         | 2.93%        | 15             | 1.99%        | 30          | 3.99%        |
| MB Precinct                         | 0                                    | 0.00%        | 0               | 0.00%        | 4          | 4.00%        | 5              | 5.00%        | 3           | 3.00%        |
| Provisional                         | 1                                    | 3.33%        | 2               | 6.67%        | 2          | 6.67%        | 0              | 0.00%        | 1           | 3.33%        |
| <b>Total</b>                        | <b>23</b>                            | <b>2.19%</b> | <b>45</b>       | <b>4.29%</b> | <b>41</b>  | <b>3.91%</b> | <b>23</b>      | <b>2.19%</b> | <b>45</b>   | <b>4.29%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |                                      |              |                 |              |            |              |                |              |             |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                                      |              |                 |              |            |              |                |              |             |              |
| Precinct TSx                        | 0                                    | -            | 0               | -            | 0          | -            | 0              | -            | 0           | -            |
| Precinct OS                         | 5                                    | 2.99%        | 10              | 5.99%        | 13         | 7.78%        | 3              | 1.80%        | 11          | 6.59%        |
| Precinct VBM                        | 17                                   | 2.26%        | 33              | 4.39%        | 22         | 2.93%        | 15             | 1.99%        | 30          | 3.99%        |
| MB Precinct                         | 0                                    | 0.00%        | 0               | 0.00%        | 4          | 4.00%        | 5              | 5.00%        | 3           | 3.00%        |
| Provisional                         | 1                                    | 3.33%        | 2               | 6.67%        | 2          | 6.67%        | 0              | 0.00%        | 1           | 3.33%        |
| <b>Total</b>                        | <b>23</b>                            | <b>2.19%</b> | <b>45</b>       | <b>4.29%</b> | <b>41</b>  | <b>3.91%</b> | <b>23</b>      | <b>2.19%</b> | <b>45</b>   | <b>4.29%</b> |
| <b>Total</b>                        | <b>0</b>                             | <b>-</b>     | <b>0</b>        | <b>-</b>     | <b>0</b>   | <b>-</b>     | <b>0</b>       | <b>-</b>     | <b>0</b>    | <b>-</b>     |
| Precinct OS                         | 5                                    | 2.99%        | 10              | 5.99%        | 13         | 7.78%        | 3              | 1.80%        | 11          | 6.59%        |
| Precinct VBM                        | 17                                   | 2.26%        | 33              | 4.39%        | 22         | 2.93%        | 15             | 1.99%        | 30          | 3.99%        |
| MB Precinct                         | 0                                    | 0.00%        | 0               | 0.00%        | 4          | 4.00%        | 5              | 5.00%        | 3           | 3.00%        |
| Provisional                         | 1                                    | 3.33%        | 2               | 6.67%        | 2          | 6.67%        | 0              | 0.00%        | 1           | 3.33%        |
| <b>Total</b>                        | <b>23</b>                            | <b>2.19%</b> | <b>45</b>       | <b>4.29%</b> | <b>41</b>  | <b>3.91%</b> | <b>23</b>      | <b>2.19%</b> | <b>45</b>   | <b>4.29%</b> |
| <b>STATE SENATE</b>                 |                                      |              |                 |              |            |              |                |              |             |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                                      |              |                 |              |            |              |                |              |             |              |
| Precinct TSx                        | 0                                    | -            | 0               | -            | 0          | -            | 0              | -            | 0           | -            |
| Precinct OS                         | 5                                    | 2.99%        | 10              | 5.99%        | 13         | 7.78%        | 3              | 1.80%        | 11          | 6.59%        |
| Precinct VBM                        | 17                                   | 2.26%        | 33              | 4.39%        | 22         | 2.93%        | 15             | 1.99%        | 30          | 3.99%        |
| MB Precinct                         | 0                                    | 0.00%        | 0               | 0.00%        | 4          | 4.00%        | 5              | 5.00%        | 3           | 3.00%        |
| Provisional                         | 1                                    | 3.33%        | 2               | 6.67%        | 2          | 6.67%        | 0              | 0.00%        | 1           | 3.33%        |
| <b>Total</b>                        | <b>23</b>                            | <b>2.19%</b> | <b>45</b>       | <b>4.29%</b> | <b>41</b>  | <b>3.91%</b> | <b>23</b>      | <b>2.19%</b> | <b>45</b>   | <b>4.29%</b> |
| <b>Total</b>                        | <b>0</b>                             | <b>-</b>     | <b>0</b>        | <b>-</b>     | <b>0</b>   | <b>-</b>     | <b>0</b>       | <b>-</b>     | <b>0</b>    | <b>-</b>     |
| Precinct OS                         | 5                                    | 2.99%        | 10              | 5.99%        | 13         | 7.78%        | 3              | 1.80%        | 11          | 6.59%        |
| Precinct VBM                        | 17                                   | 2.26%        | 33              | 4.39%        | 22         | 2.93%        | 15             | 1.99%        | 30          | 3.99%        |
| MB Precinct                         | 0                                    | 0.00%        | 0               | 0.00%        | 4          | 4.00%        | 5              | 5.00%        | 3           | 3.00%        |
| Provisional                         | 1                                    | 3.33%        | 2               | 6.67%        | 2          | 6.67%        | 0              | 0.00%        | 1           | 3.33%        |
| <b>Total</b>                        | <b>23</b>                            | <b>2.19%</b> | <b>45</b>       | <b>4.29%</b> | <b>41</b>  | <b>3.91%</b> | <b>23</b>      | <b>2.19%</b> | <b>45</b>   | <b>4.29%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                                      |              |                 |              |            |              |                |              |             |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                                      |              |                 |              |            |              |                |              |             |              |
| Precinct TSx                        | 0                                    | -            | 0               | -            | 0          | -            | 0              | -            | 0           | -            |
| Precinct OS                         | 4                                    | 3.36%        | 6               | 5.04%        | 11         | 9.24%        | 2              | 1.68%        | 10          | 8.40%        |
| Precinct VBM                        | 15                                   | 2.99%        | 24              | 4.79%        | 14         | 2.79%        | 10             | 2.00%        | 18          | 3.59%        |
| MB Precinct                         | 0                                    | 0.00%        | 0               | 0.00%        | 4          | 4.71%        | 5              | 5.88%        | 3           | 3.53%        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                                   | President American Independent Party |        |                 |        |            |        |                |        |             |        |
|-----------------------------------|--------------------------------------|--------|-----------------|--------|------------|--------|----------------|--------|-------------|--------|
|                                   | JAMES HEDGES                         |        | THOMAS HOEFLING |        | J.R. MYERS |        | ROBERT ORNELAS |        | ALAN SPEARS |        |
| Provisional                       | 0                                    | 0.00%  | 1               | 5.88%  | 1          | 5.88%  | 0              | 0.00%  | 1           | 5.88%  |
| Total                             | 19                                   | 2.63%  | 31              | 4.29%  | 30         | 4.16%  | 17             | 2.35%  | 32          | 4.43%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |                                      |        |                 |        |            |        |                |        |             |        |
| Precinct TSx                      | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS                       | 1                                    | 2.08%  | 4               | 8.33%  | 2          | 4.17%  | 1              | 2.08%  | 1           | 2.08%  |
| Precinct VBM                      | 2                                    | 0.80%  | 9               | 3.59%  | 8          | 3.19%  | 5              | 1.99%  | 12          | 4.78%  |
| MB Precinct                       | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%  |
| Provisional                       | 1                                    | 7.69%  | 1               | 7.69%  | 1          | 7.69%  | 0              | 0.00%  | 0           | 0.00%  |
| Total                             | 4                                    | 1.22%  | 14              | 4.28%  | 11         | 3.36%  | 6              | 1.83%  | 13          | 3.98%  |
| <b>Total</b>                      |                                      |        |                 |        |            |        |                |        |             |        |
| Precinct TSx                      | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS                       | 5                                    | 2.99%  | 10              | 5.99%  | 13         | 7.78%  | 3              | 1.80%  | 11          | 6.59%  |
| Precinct VBM                      | 17                                   | 2.26%  | 33              | 4.39%  | 22         | 2.93%  | 15             | 1.99%  | 30          | 3.99%  |
| MB Precinct                       | 0                                    | 0.00%  | 0               | 0.00%  | 4          | 4.00%  | 5              | 5.00%  | 3           | 3.00%  |
| Provisional                       | 1                                    | 3.33%  | 2               | 6.67%  | 2          | 6.67%  | 0              | 0.00%  | 1           | 3.33%  |
| Total                             | 23                                   | 2.19%  | 45              | 4.29%  | 41         | 3.91%  | 23             | 2.19%  | 45          | 4.29%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |                                      |        |                 |        |            |        |                |        |             |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                                      |        |                 |        |            |        |                |        |             |        |
| Precinct TSx                      | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS                       | 0                                    | 0.00%  | 2               | 7.69%  | 1          | 3.85%  | 1              | 3.85%  | 0           | 0.00%  |
| Precinct VBM                      | 0                                    | 0.00%  | 3               | 2.00%  | 1          | 0.67%  | 3              | 2.00%  | 5           | 3.33%  |
| MB Precinct                       | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%  |
| Provisional                       | 1                                    | 16.67% | 1               | 16.67% | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%  |
| Total                             | 1                                    | 0.52%  | 6               | 3.14%  | 2          | 1.05%  | 4              | 2.09%  | 5           | 2.62%  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                                      |        |                 |        |            |        |                |        |             |        |
| Precinct TSx                      | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS                       | 2                                    | 5.26%  | 2               | 5.26%  | 3          | 7.89%  | 0              | 0.00%  | 3           | 7.89%  |
| Precinct VBM                      | 2                                    | 1.19%  | 13              | 7.74%  | 8          | 4.76%  | 3              | 1.79%  | 5           | 2.98%  |
| MB Precinct                       | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 2.44%  | 3              | 7.32%  | 0           | 0.00%  |
| Provisional                       | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 11.11% | 0              | 0.00%  | 0           | 0.00%  |
| Total                             | 4                                    | 1.56%  | 15              | 5.86%  | 13         | 5.08%  | 6              | 2.34%  | 8           | 3.13%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                                      |        |                 |        |            |        |                |        |             |        |
| Precinct TSx                      | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS                       | 1                                    | 3.13%  | 2               | 6.25%  | 3          | 9.38%  | 2              | 6.25%  | 2           | 6.25%  |
| Precinct VBM                      | 4                                    | 2.41%  | 8               | 4.82%  | 7          | 4.22%  | 1              | 0.60%  | 6           | 3.61%  |
| MB Precinct                       | 0                                    | 0.00%  | 0               | 0.00%  | 2          | 16.67% | 2              | 16.67% | 2           | 16.67% |
| Provisional                       | 0                                    | 0.00%  | 1               | 25.00% | 1          | 25.00% | 0              | 0.00%  | 0           | 0.00%  |
| Total                             | 5                                    | 2.34%  | 11              | 5.14%  | 13         | 6.07%  | 5              | 2.34%  | 10          | 4.67%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                                      |        |                 |        |            |        |                |        |             |        |
| Precinct TSx                      | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS                       | 1                                    | 2.38%  | 1               | 2.38%  | 4          | 9.52%  | 0              | 0.00%  | 6           | 14.29% |
| Precinct VBM                      | 4                                    | 2.53%  | 6               | 3.80%  | 5          | 3.16%  | 7              | 4.43%  | 12          | 7.59%  |
| MB Precinct                       | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%  |
| Provisional                       | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 0           | 0.00%  |
| Total                             | 5                                    | 2.15%  | 7               | 3.00%  | 9          | 3.86%  | 7              | 3.00%  | 18          | 7.73%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                                      |        |                 |        |            |        |                |        |             |        |
| Precinct TSx                      | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS                       | 1                                    | 3.45%  | 3               | 10.34% | 2          | 6.90%  | 0              | 0.00%  | 0           | 0.00%  |
| Precinct VBM                      | 7                                    | 6.36%  | 3               | 2.73%  | 1          | 0.91%  | 1              | 0.91%  | 2           | 1.82%  |
| MB Precinct                       | 0                                    | 0.00%  | 0               | 0.00%  | 1          | 11.11% | 0              | 0.00%  | 1           | 11.11% |
| Provisional                       | 0                                    | 0.00%  | 0               | 0.00%  | 0          | 0.00%  | 0              | 0.00%  | 1           | 14.29% |
| Total                             | 8                                    | 5.16%  | 6               | 3.87%  | 4          | 2.58%  | 1              | 0.65%  | 4           | 2.58%  |
| <b>Total</b>                      |                                      |        |                 |        |            |        |                |        |             |        |
| Precinct TSx                      | 0                                    | -      | 0               | -      | 0          | -      | 0              | -      | 0           | -      |
| Precinct OS                       | 5                                    | 2.99%  | 10              | 5.99%  | 13         | 7.78%  | 3              | 1.80%  | 11          | 6.59%  |
| Precinct VBM                      | 17                                   | 2.26%  | 33              | 4.39%  | 22         | 2.93%  | 15             | 1.99%  | 30          | 3.99%  |
| MB Precinct                       | 0                                    | 0.00%  | 0               | 0.00%  | 4          | 4.00%  | 5              | 5.00%  | 3           | 3.00%  |
| Provisional                       | 1                                    | 3.33%  | 2               | 6.67%  | 2          | 6.67%  | 0              | 0.00%  | 1           | 3.33%  |
| Total                             | 23                                   | 2.19%  | 45              | 4.29%  | 41         | 3.91%  | 23             | 2.19%  | 45          | 4.29%  |

**COUNTY OF EL DORADO**  
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|                                 | President American Independent Party |              |                 |              |            |              |                |              |             |              |
|---------------------------------|--------------------------------------|--------------|-----------------|--------------|------------|--------------|----------------|--------------|-------------|--------------|
|                                 | JAMES HEDGES                         |              | THOMAS HOEFLING |              | J.R. MYERS |              | ROBERT ORNELAS |              | ALAN SPEARS |              |
| <b>CITIES</b>                   |                                      |              |                 |              |            |              |                |              |             |              |
| <b>CITY OF PLACERVILLE</b>      |                                      |              |                 |              |            |              |                |              |             |              |
| Precinct TSx                    | 0                                    | -            | 0               | -            | 0          | -            | 0              | -            | 0           | -            |
| Precinct OS                     | 0                                    | 0.00%        | 0               | 0.00%        | 1          | 20.00%       | 0              | 0.00%        | 0           | 0.00%        |
| Precinct VBM                    | 1                                    | 2.08%        | 4               | 8.33%        | 2          | 4.17%        | 1              | 2.08%        | 2           | 4.17%        |
| MB Precinct                     | 0                                    | -            | 0               | -            | 0          | -            | 0              | -            | 0           | -            |
| Provisional                     | 0                                    | 0.00%        | 1               | 50.00%       | 0          | 0.00%        | 0              | 0.00%        | 0           | 0.00%        |
| <b>Total</b>                    | <b>1</b>                             | <b>1.82%</b> | <b>5</b>        | <b>9.09%</b> | <b>3</b>   | <b>5.45%</b> | <b>1</b>       | <b>1.82%</b> | <b>2</b>    | <b>3.64%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                                      |              |                 |              |            |              |                |              |             |              |
| Precinct TSx                    | 0                                    | -            | 0               | -            | 0          | -            | 0              | -            | 0           | -            |
| Precinct OS                     | 0                                    | 0.00%        | 3               | 23.08%       | 1          | 7.69%        | 0              | 0.00%        | 0           | 0.00%        |
| Precinct VBM                    | 0                                    | 0.00%        | 3               | 5.17%        | 1          | 1.72%        | 0              | 0.00%        | 0           | 0.00%        |
| MB Precinct                     | 0                                    | -            | 0               | -            | 0          | -            | 0              | -            | 0           | -            |
| Provisional                     | 0                                    | 0.00%        | 0               | 0.00%        | 0          | 0.00%        | 0              | 0.00%        | 1           | 25.00%       |
| <b>Total</b>                    | <b>0</b>                             | <b>0.00%</b> | <b>6</b>        | <b>8.00%</b> | <b>2</b>   | <b>2.67%</b> | <b>0</b>       | <b>0.00%</b> | <b>1</b>    | <b>1.33%</b> |
| <b>UNINCORPORATED AREA</b>      |                                      |              |                 |              |            |              |                |              |             |              |
| Precinct TSx                    | 0                                    | -            | 0               | -            | 0          | -            | 0              | -            | 0           | -            |
| Precinct OS                     | 5                                    | 3.36%        | 7               | 4.70%        | 11         | 7.38%        | 3              | 2.01%        | 11          | 7.38%        |
| Precinct VBM                    | 16                                   | 2.48%        | 26              | 4.02%        | 19         | 2.94%        | 14             | 2.17%        | 28          | 4.33%        |
| MB Precinct                     | 0                                    | 0.00%        | 0               | 0.00%        | 4          | 4.00%        | 5              | 5.00%        | 3           | 3.00%        |
| Provisional                     | 1                                    | 4.17%        | 1               | 4.17%        | 2          | 8.33%        | 0              | 0.00%        | 0           | 0.00%        |
| <b>Total</b>                    | <b>22</b>                            | <b>2.39%</b> | <b>34</b>       | <b>3.70%</b> | <b>36</b>  | <b>3.92%</b> | <b>22</b>      | <b>2.39%</b> | <b>42</b>   | <b>4.57%</b> |
| <b>Total</b>                    |                                      |              |                 |              |            |              |                |              |             |              |
| Precinct TSx                    | 0                                    | -            | 0               | -            | 0          | -            | 0              | -            | 0           | -            |
| Precinct OS                     | 5                                    | 2.99%        | 10              | 5.99%        | 13         | 7.78%        | 3              | 1.80%        | 11          | 6.59%        |
| Precinct VBM                    | 17                                   | 2.26%        | 33              | 4.39%        | 22         | 2.93%        | 15             | 1.99%        | 30          | 3.99%        |
| MB Precinct                     | 0                                    | 0.00%        | 0               | 0.00%        | 4          | 4.00%        | 5              | 5.00%        | 3           | 3.00%        |
| Provisional                     | 1                                    | 3.33%        | 2               | 6.67%        | 2          | 6.67%        | 0              | 0.00%        | 1           | 3.33%        |
| <b>Total</b>                    | <b>23</b>                            | <b>2.19%</b> | <b>45</b>       | <b>4.29%</b> | <b>41</b>  | <b>3.91%</b> | <b>23</b>      | <b>2.19%</b> | <b>45</b>   | <b>4.29%</b> |



**COUNTY OF EL DORADO**  
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|                        | President Green Party |          |               |             |                |         |               |       |              |       |
|------------------------|-----------------------|----------|---------------|-------------|----------------|---------|---------------|-------|--------------|-------|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | DARRYL CHERNEY |         | WILLIAM KREML |       | KENT MESPLAY |       |
| Jurisdiction Wide      |                       |          |               |             |                |         |               |       |              |       |
| 100001.11-CP 100001.11 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 7                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 7                     | 1        | 1             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| Precinct VBM           | 7                     | 1        | 1             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 7                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 7                     | 1        | 1             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Total                  | 7                     | 5        | 3             | 2           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| 100002.11-CP 100002.11 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 5                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 5                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct VBM           | 5                     | 1        | 2             | 2           | 1              | 50.00%  | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 5                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 5                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Total                  | 5                     | 5        | 2             | 2           | 1              | 50.00%  | 0             | 0.00% | 0            | 0.00% |
| 100003.11-CP 100003.11 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 1                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 1                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct VBM           | 1                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| MB Precinct            | 1                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 1                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Total                  | 1                     | 5        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| 100004.11-CP 100004.11 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 4                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 4                     | 1        | 1             | 1           | 1              | 100.00% | 0             | 0.00% | 0            | 0.00% |
| Precinct VBM           | 4                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| MB Precinct            | 4                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 4                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Total                  | 4                     | 5        | 1             | 1           | 1              | 100.00% | 0             | 0.00% | 0            | 0.00% |
| 100005.11-CP 100005.11 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 2                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 2                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct VBM           | 2                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| MB Precinct            | 2                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 2                     | 1        | 1             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| Total                  | 2                     | 5        | 1             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| 100006.11-CP 100006.11 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 5                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 5                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct VBM           | 5                     | 1        | 2             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 5                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 5                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Total                  | 5                     | 5        | 2             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| 100008.11-CP 100008.11 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 3                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 3                     | 1        | 1             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| Precinct VBM           | 3                     | 1        | 1             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 3                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 3                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Total                  | 3                     | 5        | 2             | 2           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| 100009.11-CP 100009.11 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 0                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Total                  | 0                     | 5        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| 100012.11-CP 100012.11 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 8                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 8                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct VBM           | 8                     | 1        | 3             | 3           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |

**COUNTY OF EL DORADO**  
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|                        | President Green Party |          |               |             |                |               |              |       |   |       |
|------------------------|-----------------------|----------|---------------|-------------|----------------|---------------|--------------|-------|---|-------|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | DARRYL CHERNEY | WILLIAM KREML | KENT MESPLAY |       |   |       |
| MB Precinct            | 8                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 8                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 8                     | 5        | 3             | 3           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| 100013.11-CP 100013.11 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 3                     | 1        | 1             | 0           | 0              | -             | 0            | -     | 0 | -     |
| MB Precinct            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 3                     | 5        | 1             | 0           | 0              | -             | 0            | -     | 0 | -     |
| 100014.11-CP 100014.11 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 3                     | 1        | 2             | 2           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| MB Precinct            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 3                     | 5        | 2             | 2           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| 100022.11-CP 100022.11 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| MB Precinct            | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 2                     | 5        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| 100023.11-CP 100023.11 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 4                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 4                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 4                     | 1        | 3             | 0           | 0              | -             | 0            | -     | 0 | -     |
| MB Precinct            | 4                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 4                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 4                     | 5        | 3             | 0           | 0              | -             | 0            | -     | 0 | -     |
| 200003.12-CP 200003.12 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 5                     | 1        | 1             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| MB Precinct            | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 5                     | 5        | 1             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| 200004.12-CP 200004.12 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 3                     | 1        | 1             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| Precinct VBM           | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| MB Precinct            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 3                     | 5        | 1             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| 200005.02-CP 200005.02 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 5                     | 1        | 2             | 2           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| MB Precinct            | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 5                     | 5        | 2             | 2           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| 200006.12-CP 200006.12 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 4                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 4                     | 1        | 1             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| Precinct VBM           | 4                     | 1        | 2             | 0           | 0              | -             | 0            | -     | 0 | -     |
| MB Precinct            | 4                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 4                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 4                     | 5        | 3             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| 200008.12-CP 200008.12 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |

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|------------------------|-----------------------|----------|---------------|-------------|----------------|---------------|--------------|-------|---|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | DARRYL CHERNEY | WILLIAM KREML | KENT MESPLAY |       |   |
| Precinct OS            | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct VBM           | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| MB Precinct            | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Provisional            | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Total                  | 5                     | 5        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| 200010.12-CP 200010.12 |                       |          |               |             |                |               |              |       |   |
| Precinct TSx           | 4                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct OS            | 4                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct VBM           | 4                     | 1        | 1             | 0           | 0              | -             | 0            | -     | 0 |
| MB Precinct            | 4                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Provisional            | 4                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Total                  | 4                     | 5        | 1             | 0           | 0              | -             | 0            | -     | 0 |
| 200012.12-CP 200012.12 |                       |          |               |             |                |               |              |       |   |
| Precinct TSx           | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct OS            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct VBM           | 3                     | 1        | 1             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 |
| MB Precinct            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Provisional            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Total                  | 3                     | 5        | 1             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 |
| 200013.12-CP 200013.12 |                       |          |               |             |                |               |              |       |   |
| Precinct TSx           | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct OS            | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct VBM           | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| MB Precinct            | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Provisional            | 5                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Total                  | 5                     | 5        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| 200014.12-CP 200014.12 |                       |          |               |             |                |               |              |       |   |
| Precinct TSx           | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct OS            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct VBM           | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| MB Precinct            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Provisional            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Total                  | 3                     | 5        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| 200015.02-CP 200015.02 |                       |          |               |             |                |               |              |       |   |
| Precinct TSx           | 11                    | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct OS            | 11                    | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct VBM           | 11                    | 1        | 5             | 5           | 0              | 0.00%         | 0            | 0.00% | 0 |
| MB Precinct            | 11                    | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Provisional            | 11                    | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Total                  | 11                    | 5        | 5             | 5           | 0              | 0.00%         | 0            | 0.00% | 0 |
| 200016.12-CP 200016.12 |                       |          |               |             |                |               |              |       |   |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Provisional            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Total                  | 0                     | 5        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| 200016.13-CP 200016.13 |                       |          |               |             |                |               |              |       |   |
| Precinct TSx           | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct OS            | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct VBM           | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| MB Precinct            | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Provisional            | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Total                  | 1                     | 5        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| 200017.02-CP 200017.02 |                       |          |               |             |                |               |              |       |   |
| Precinct TSx           | 13                    | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Precinct OS            | 13                    | 1        | 1             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 |
| Precinct VBM           | 13                    | 1        | 5             | 4           | 0              | 0.00%         | 0            | 0.00% | 0 |
| MB Precinct            | 13                    | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Provisional            | 13                    | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 |
| Total                  | 13                    | 5        | 6             | 5           | 0              | 0.00%         | 0            | 0.00% | 0 |

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|------------------------|-----------------------|----------|---------------|-------------|----------------|-------|---------------|-------|--------------|-------|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | DARRYL CHERNEY |       | WILLIAM KREML |       | KENT MESPLAY |       |
| 200018.02-CP 200018.02 |                       |          |               |             |                |       |               |       |              |       |
| Precinct TSx           | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct OS            | 6                     | 1        | 1             | 1           | 0              | 0.00% | 0             | 0.00% | 0            | 0.00% |
| Precinct VBM           | 6                     | 1        | 1             | 1           | 0              | 0.00% | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Provisional            | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Total                  | 6                     | 5        | 2             | 2           | 0              | 0.00% | 0             | 0.00% | 0            | 0.00% |
| 200020.02-CP 200020.02 |                       |          |               |             |                |       |               |       |              |       |
| Precinct TSx           | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct OS            | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct VBM           | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| MB Precinct            | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Provisional            | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Total                  | 1                     | 5        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| 200021.01-CP 200021.01 |                       |          |               |             |                |       |               |       |              |       |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Provisional            | 0                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Total                  | 0                     | 5        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| 200021.02-CP 200021.02 |                       |          |               |             |                |       |               |       |              |       |
| Precinct TSx           | 7                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct OS            | 7                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct VBM           | 7                     | 1        | 2             | 2           | 0              | 0.00% | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 7                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Provisional            | 7                     | 1        | 2             | 1           | 0              | 0.00% | 0             | 0.00% | 0            | 0.00% |
| Total                  | 7                     | 5        | 4             | 3           | 0              | 0.00% | 0             | 0.00% | 0            | 0.00% |
| 200024.02-CP 200024.02 |                       |          |               |             |                |       |               |       |              |       |
| Precinct TSx           | 3                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct OS            | 3                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct VBM           | 3                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| MB Precinct            | 3                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Provisional            | 3                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Total                  | 3                     | 5        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| 300001.04-CP 300001.04 |                       |          |               |             |                |       |               |       |              |       |
| Precinct TSx           | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct OS            | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct VBM           | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| MB Precinct            | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Provisional            | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Total                  | 1                     | 5        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| 300001.05-CP 300001.05 |                       |          |               |             |                |       |               |       |              |       |
| Precinct TSx           | 2                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct OS            | 2                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct VBM           | 2                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| MB Precinct            | 2                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Provisional            | 2                     | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Total                  | 2                     | 5        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| 300003.05-CP 300003.05 |                       |          |               |             |                |       |               |       |              |       |
| Precinct TSx           | 13                    | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct OS            | 13                    | 1        | 3             | 2           | 0              | 0.00% | 0             | 0.00% | 0            | 0.00% |
| Precinct VBM           | 13                    | 1        | 2             | 1           | 0              | 0.00% | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 13                    | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Provisional            | 13                    | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Total                  | 13                    | 5        | 5             | 3           | 0              | 0.00% | 0             | 0.00% | 0            | 0.00% |
| 300004.05-CP 300004.05 |                       |          |               |             |                |       |               |       |              |       |
| Precinct TSx           | 17                    | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |
| Precinct OS            | 17                    | 1        | 2             | 2           | 0              | 0.00% | 0             | 0.00% | 0            | 0.00% |
| Precinct VBM           | 17                    | 1        | 6             | 6           | 0              | 0.00% | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 17                    | 1        | 0             | 0           | 0              | -     | 0             | -     | 0            | -     |

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|------------------------|-----------------------|----------|---------------|-------------|----------------|-------|---------------|---------|--------------|-------|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | DARRYL CHERNEY |       | WILLIAM KREML |         | KENT MESPLAY |       |
| Provisional            | 17                    | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 17                    | 5        | 8             | 8           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| 300005.05-CP 300005.05 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 8                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 8                     | 1        | 1             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct VBM           | 8                     | 1        | 3             | 3           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| MB Precinct            | 8                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 8                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 8                     | 5        | 4             | 3           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| 300008.05-CP 300008.05 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct VBM           | 6                     | 1        | 2             | 2           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| MB Precinct            | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 6                     | 5        | 2             | 2           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| 300009.04-CP 300009.04 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 4                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 4                     | 1        | 1             | 1           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| Precinct VBM           | 4                     | 1        | 1             | 1           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| MB Precinct            | 4                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 4                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 4                     | 5        | 2             | 2           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| 300010.04-CP 300010.04 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 5                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 5                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct VBM           | 5                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| MB Precinct            | 5                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 5                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 5                     | 5        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| 300011.04-CP 300011.04 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 7                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 7                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct VBM           | 7                     | 1        | 1             | 1           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| MB Precinct            | 7                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 7                     | 1        | 1             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 7                     | 5        | 2             | 1           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| 300013.05-CP 300013.05 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 12                    | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 12                    | 1        | 2             | 1           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| Precinct VBM           | 12                    | 1        | 3             | 1           | 0              | 0.00% | 1             | 100.00% | 0            | 0.00% |
| MB Precinct            | 12                    | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 12                    | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 12                    | 5        | 5             | 2           | 0              | 0.00% | 1             | 50.00%  | 0            | 0.00% |
| 300015.04-CP 300015.04 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 0                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 0                     | 5        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| 300015.05-CP 300015.05 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 2                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 2                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct VBM           | 2                     | 1        | 1             | 1           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| MB Precinct            | 2                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 2                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 2                     | 5        | 1             | 1           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| 300017.05-CP 300017.05 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 5                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 5                     | 1        | 1             | 0           | 0              | -     | 0             | -       | 0            | -     |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | President Green Party |          |               |             |                |       |               |         |              |       |
|------------------------|-----------------------|----------|---------------|-------------|----------------|-------|---------------|---------|--------------|-------|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | DARRYL CHERNEY |       | WILLIAM KREML |         | KENT MESPLAY |       |
| Precinct TSx           | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct VBM           | 6                     | 1        | 3             | 3           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| MB Precinct            | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 6                     | 5        | 3             | 3           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| 400004.14-CP 400004.14 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 9                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 9                     | 1        | 2             | 2           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| Precinct VBM           | 9                     | 1        | 4             | 4           | 0              | 0.00% | 1             | 25.00%  | 0            | 0.00% |
| MB Precinct            | 9                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 9                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 9                     | 5        | 6             | 6           | 0              | 0.00% | 1             | 16.67%  | 0            | 0.00% |
| 400005.08-CP 400005.08 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 5                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 5                     | 1        | 1             | 1           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| Precinct VBM           | 5                     | 1        | 2             | 1           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| MB Precinct            | 5                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 5                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 5                     | 5        | 3             | 2           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| 400006.08-CP 400006.08 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 1                     | 1        | 1             | 1           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| Precinct VBM           | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| MB Precinct            | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 1                     | 5        | 1             | 1           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| 400007.08-CP 400007.08 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 12                    | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 12                    | 1        | 3             | 3           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| Precinct VBM           | 12                    | 1        | 2             | 1           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| MB Precinct            | 12                    | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 12                    | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 12                    | 5        | 5             | 4           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| 400010.08-CP 400010.08 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 15                    | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 15                    | 1        | 5             | 3           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| Precinct VBM           | 15                    | 1        | 4             | 3           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| MB Precinct            | 15                    | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 15                    | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 15                    | 5        | 9             | 6           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| 400012.08-CP 400012.08 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct VBM           | 6                     | 1        | 2             | 2           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| MB Precinct            | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 6                     | 5        | 2             | 2           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| 400013.08-CP 400013.08 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 7                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 7                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct VBM           | 7                     | 1        | 3             | 3           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| MB Precinct            | 7                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 7                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Total                  | 7                     | 5        | 3             | 3           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| 400014.08-CP 400014.08 |                       |          |               |             |                |       |               |         |              |       |
| Precinct TSx           | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct OS            | 6                     | 1        | 2             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Precinct VBM           | 6                     | 1        | 1             | 1           | 0              | 0.00% | 0             | 0.00%   | 0            | 0.00% |
| MB Precinct            | 6                     | 1        | 0             | 0           | 0              | -     | 0             | -       | 0            | -     |
| Provisional            | 6                     | 1        | 1             | 1           | 0              | 0.00% | 1             | 100.00% | 0            | 0.00% |

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|------------------------|-----------------------|----------|---------------|-------------|----------------|-------|---------------|--------|--------------|-------|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | DARRYL CHERNEY |       | WILLIAM KREML |        | KENT MESPLAY |       |
| Total                  | 6                     | 5        | 4             | 2           | 0              | 0.00% | 1             | 50.00% | 0            | 0.00% |
| 400017.08-CP 400017.08 |                       |          |               |             |                |       |               |        |              |       |
| Precinct TSx           | 3                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct OS            | 3                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct VBM           | 3                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| MB Precinct            | 3                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Provisional            | 3                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Total                  | 3                     | 5        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| 400018.08-CP 400018.08 |                       |          |               |             |                |       |               |        |              |       |
| Precinct TSx           | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct OS            | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct VBM           | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| MB Precinct            | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Provisional            | 1                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Total                  | 1                     | 5        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| 400020.08-CP 400020.08 |                       |          |               |             |                |       |               |        |              |       |
| Precinct TSx           | 3                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct OS            | 3                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct VBM           | 3                     | 1        | 1             | 1           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |
| MB Precinct            | 3                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Provisional            | 3                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Total                  | 3                     | 5        | 1             | 1           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |
| 400021.07-CP 400021.07 |                       |          |               |             |                |       |               |        |              |       |
| Precinct TSx           | 5                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct OS            | 5                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct VBM           | 5                     | 1        | 1             | 1           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |
| MB Precinct            | 5                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Provisional            | 5                     | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Total                  | 5                     | 5        | 1             | 1           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |
| 500001.10-CP 500001.10 |                       |          |               |             |                |       |               |        |              |       |
| Precinct TSx           | 16                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct OS            | 16                    | 1        | 2             | 1           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |
| Precinct VBM           | 16                    | 1        | 4             | 2           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |
| MB Precinct            | 16                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Provisional            | 16                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Total                  | 16                    | 5        | 6             | 3           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |
| 500002.10-CP 500002.10 |                       |          |               |             |                |       |               |        |              |       |
| Precinct TSx           | 10                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct OS            | 10                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct VBM           | 10                    | 1        | 2             | 1           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |
| MB Precinct            | 10                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Provisional            | 10                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Total                  | 10                    | 5        | 2             | 1           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |
| 500003.10-CP 500003.10 |                       |          |               |             |                |       |               |        |              |       |
| Precinct TSx           | 16                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct OS            | 16                    | 1        | 1             | 1           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |
| Precinct VBM           | 16                    | 1        | 3             | 1           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |
| MB Precinct            | 16                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Provisional            | 16                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Total                  | 16                    | 5        | 4             | 2           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |
| 500004.10-CP 500004.10 |                       |          |               |             |                |       |               |        |              |       |
| Precinct TSx           | 22                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct OS            | 22                    | 1        | 1             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct VBM           | 22                    | 1        | 2             | 1           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |
| MB Precinct            | 22                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Provisional            | 22                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Total                  | 22                    | 5        | 3             | 1           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |
| 500005.10-CP 500005.10 |                       |          |               |             |                |       |               |        |              |       |
| Precinct TSx           | 17                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct OS            | 17                    | 1        | 0             | 0           | 0              | -     | 0             | -      | 0            | -     |
| Precinct VBM           | 17                    | 1        | 2             | 2           | 0              | 0.00% | 0             | 0.00%  | 0            | 0.00% |



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|------------------------|-----------------------|----------|---------------|-------------|----------------|---------|---------------|-------|--------------|-------|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | DARRYL CHERNEY |         | WILLIAM KREML |       | KENT MESPLAY |       |
| MB Precinct            | 17                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 17                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Total                  | 17                    | 5        | 2             | 2           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| 500006.10-CP 500006.10 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 35                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 35                    | 1        | 3             | 2           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| Precinct VBM           | 35                    | 1        | 5             | 3           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 35                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 35                    | 1        | 1             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Total                  | 35                    | 5        | 9             | 5           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| 500007.10-CP 500007.10 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 21                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 21                    | 1        | 1             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| Precinct VBM           | 21                    | 1        | 1             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 21                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 21                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Total                  | 21                    | 5        | 2             | 2           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| 500009.10-CP 500009.10 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 11                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 11                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct VBM           | 11                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| MB Precinct            | 11                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 11                    | 1        | 2             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| Total                  | 11                    | 5        | 2             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| 500011.10-CP 500011.10 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 16                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 16                    | 1        | 2             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| Precinct VBM           | 16                    | 1        | 1             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 16                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 16                    | 1        | 1             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Total                  | 16                    | 5        | 4             | 2           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| 500012.10-CP 500012.10 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 27                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 27                    | 1        | 4             | 3           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| Precinct VBM           | 27                    | 1        | 5             | 2           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 27                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 27                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Total                  | 27                    | 5        | 9             | 5           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| 500015.10-CP 500015.10 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 8                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 8                     | 1        | 1             | 1           | 1              | 100.00% | 0             | 0.00% | 0            | 0.00% |
| Precinct VBM           | 8                     | 1        | 2             | 2           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 8                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 8                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Total                  | 8                     | 5        | 3             | 3           | 1              | 33.33%  | 0             | 0.00% | 0            | 0.00% |
| 500020.10-CP 500020.10 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 13                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 13                    | 1        | 2             | 2           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| Precinct VBM           | 13                    | 1        | 1             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 13                    | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 13                    | 1        | 1             | 1           | 1              | 100.00% | 0             | 0.00% | 0            | 0.00% |
| Total                  | 13                    | 5        | 4             | 4           | 1              | 25.00%  | 0             | 0.00% | 0            | 0.00% |
| 500021.10-CP 500021.10 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 4                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct OS            | 4                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Precinct VBM           | 4                     | 1        | 2             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| MB Precinct            | 4                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Provisional            | 4                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |
| Total                  | 4                     | 5        | 2             | 1           | 0              | 0.00%   | 0             | 0.00% | 0            | 0.00% |
| 95U201.01-MB 95U201.01 |                       |          |               |             |                |         |               |       |              |       |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0              | -       | 0             | -     | 0            | -     |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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|                        | President Green Party |          |               |             |                |               |              |       |   |       |
|------------------------|-----------------------|----------|---------------|-------------|----------------|---------------|--------------|-------|---|-------|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | DARRYL CHERNEY | WILLIAM KREML | KENT MESPLAY |       |   |       |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 0                     | 5        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| 95U201.02-MB 95U201.02 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| MB Precinct            | 1                     | 1        | 1             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| Provisional            | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 1                     | 5        | 1             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| 95U202.02-MB 95U202.02 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 7                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 7                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 7                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| MB Precinct            | 7                     | 1        | 1             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 7                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 7                     | 5        | 1             | 0           | 0              | -             | 0            | -     | 0 | -     |
| 95U203.02-MB 95U203.02 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 0                     | 5        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| 95U204.01-MB 95U204.01 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| MB Precinct            | 2                     | 1        | 1             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| Provisional            | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 2                     | 5        | 1             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| 95U301.05-MB 95U301.05 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| MB Precinct            | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 2                     | 5        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| 95U302.04-MB 95U302.04 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 0                     | 5        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| 95U302.05-MB 95U302.05 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| MB Precinct            | 1                     | 1        | 1             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| Provisional            | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 1                     | 5        | 1             | 1           | 0              | 0.00%         | 0            | 0.00% | 0 | 0.00% |
| 95U303.03-MB 95U303.03 |                       |          |               |             |                |               |              |       |   |       |
| Precinct TSx           | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct OS            | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Precinct VBM           | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| MB Precinct            | 2                     | 1        | 1             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Provisional            | 2                     | 1        | 0             | 0           | 0              | -             | 0            | -     | 0 | -     |
| Total                  | 2                     | 5        | 1             | 0           | 0              | -             | 0            | -     | 0 | -     |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | President Green Party |          |               |             |                |               |              |        |   |       |
|------------------------|-----------------------|----------|---------------|-------------|----------------|---------------|--------------|--------|---|-------|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | DARRYL CHERNEY | WILLIAM KREML | KENT MESPLAY |        |   |       |
| 95U303.05-MB 95U303.05 |                       |          |               |             |                |               |              |        |   |       |
| Precinct TSx           | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct OS            | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct VBM           | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| MB Precinct            | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Provisional            | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Total                  | 1                     | 5        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| 95U304.05-MB 95U304.05 |                       |          |               |             |                |               |              |        |   |       |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Provisional            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Total                  | 0                     | 5        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| 95U305.05-MB 95U305.05 |                       |          |               |             |                |               |              |        |   |       |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Provisional            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Total                  | 0                     | 5        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| 95U401.08-MB 95U401.08 |                       |          |               |             |                |               |              |        |   |       |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Provisional            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Total                  | 0                     | 5        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| 95U402.07-MB 95U402.07 |                       |          |               |             |                |               |              |        |   |       |
| Precinct TSx           | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct OS            | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct VBM           | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| MB Precinct            | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Provisional            | 1                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Total                  | 1                     | 5        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| 95U402.08-MB 95U402.08 |                       |          |               |             |                |               |              |        |   |       |
| Precinct TSx           | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct OS            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct VBM           | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| MB Precinct            | 3                     | 1        | 2             | 2           | 0              | 0.00%         | 1            | 50.00% | 0 | 0.00% |
| Provisional            | 3                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Total                  | 3                     | 5        | 2             | 2           | 0              | 0.00%         | 1            | 50.00% | 0 | 0.00% |
| 95U403.08-MB 95U403.08 |                       |          |               |             |                |               |              |        |   |       |
| Precinct TSx           | 7                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct OS            | 7                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct VBM           | 7                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| MB Precinct            | 7                     | 1        | 2             | 1           | 0              | 0.00%         | 0            | 0.00%  | 0 | 0.00% |
| Provisional            | 7                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Total                  | 7                     | 5        | 2             | 1           | 0              | 0.00%         | 0            | 0.00%  | 0 | 0.00% |
| 95U404.06-MB 95U404.06 |                       |          |               |             |                |               |              |        |   |       |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Provisional            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Total                  | 0                     | 5        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| 95U404.08-MB 95U404.08 |                       |          |               |             |                |               |              |        |   |       |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0              | -             | 0            | -      | 0 | -     |



**COUNTY OF EL DORADO**  
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President Green Party

|                                     | Reg. Voters | Vote For   | Times Counted | Total Votes | DARRYL CHERNEY |              | WILLIAM KREML |              | KENT MESPLAY |              |
|-------------------------------------|-------------|------------|---------------|-------------|----------------|--------------|---------------|--------------|--------------|--------------|
| Precinct TSx                        | 597         | 102        | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| Precinct OS                         | 597         | 102        | 57            | 39          | 2              | 5.13%        | 0             | 0.00%        | 0            | 0.00%        |
| Precinct VBM                        | 597         | 102        | 123           | 89          | 2              | 2.25%        | 2             | 2.25%        | 0            | 0.00%        |
| MB Precinct                         | 597         | 102        | 14            | 11          | 2              | 18.18%       | 1             | 9.09%        | 0            | 0.00%        |
| Provisional                         | 597         | 102        | 13            | 6           | 1              | 16.67%       | 1             | 16.67%       | 0            | 0.00%        |
| <b>Total</b>                        | <b>597</b>  | <b>510</b> | <b>207</b>    | <b>145</b>  | <b>7</b>       | <b>4.83%</b> | <b>4</b>      | <b>2.76%</b> | <b>0</b>     | <b>0.00%</b> |
| <b>UNITED STATES SENATE</b>         |             |            |               |             |                |              |               |              |              |              |
| <b>UNITED STATES SENATE</b>         |             |            |               |             |                |              |               |              |              |              |
| Precinct TSx                        | 597         | 102        | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| Precinct OS                         | 597         | 102        | 57            | 39          | 2              | 5.13%        | 0             | 0.00%        | 0            | 0.00%        |
| Precinct VBM                        | 597         | 102        | 123           | 89          | 2              | 2.25%        | 2             | 2.25%        | 0            | 0.00%        |
| MB Precinct                         | 597         | 102        | 14            | 11          | 2              | 18.18%       | 1             | 9.09%        | 0            | 0.00%        |
| Provisional                         | 597         | 102        | 13            | 6           | 1              | 16.67%       | 1             | 16.67%       | 0            | 0.00%        |
| <b>Total</b>                        | <b>597</b>  | <b>510</b> | <b>207</b>    | <b>145</b>  | <b>7</b>       | <b>4.83%</b> | <b>4</b>      | <b>2.76%</b> | <b>0</b>     | <b>0.00%</b> |
| <b>Total</b>                        |             |            |               |             |                |              |               |              |              |              |
| Precinct TSx                        | 597         | 102        | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| Precinct OS                         | 597         | 102        | 57            | 39          | 2              | 5.13%        | 0             | 0.00%        | 0            | 0.00%        |
| Precinct VBM                        | 597         | 102        | 123           | 89          | 2              | 2.25%        | 2             | 2.25%        | 0            | 0.00%        |
| MB Precinct                         | 597         | 102        | 14            | 11          | 2              | 18.18%       | 1             | 9.09%        | 0            | 0.00%        |
| Provisional                         | 597         | 102        | 13            | 6           | 1              | 16.67%       | 1             | 16.67%       | 0            | 0.00%        |
| <b>Total</b>                        | <b>597</b>  | <b>510</b> | <b>207</b>    | <b>145</b>  | <b>7</b>       | <b>4.83%</b> | <b>4</b>      | <b>2.76%</b> | <b>0</b>     | <b>0.00%</b> |
| <b>Total</b>                        |             |            |               |             |                |              |               |              |              |              |
| <b>UNITED STATES REPRESENTATIVE</b> |             |            |               |             |                |              |               |              |              |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |             |            |               |             |                |              |               |              |              |              |
| Precinct TSx                        | 597         | 102        | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| Precinct OS                         | 597         | 102        | 57            | 39          | 2              | 5.13%        | 0             | 0.00%        | 0            | 0.00%        |
| Precinct VBM                        | 597         | 102        | 123           | 89          | 2              | 2.25%        | 2             | 2.25%        | 0            | 0.00%        |
| MB Precinct                         | 597         | 102        | 14            | 11          | 2              | 18.18%       | 1             | 9.09%        | 0            | 0.00%        |
| Provisional                         | 597         | 102        | 13            | 6           | 1              | 16.67%       | 1             | 16.67%       | 0            | 0.00%        |
| <b>Total</b>                        | <b>597</b>  | <b>510</b> | <b>207</b>    | <b>145</b>  | <b>7</b>       | <b>4.83%</b> | <b>4</b>      | <b>2.76%</b> | <b>0</b>     | <b>0.00%</b> |
| <b>Total</b>                        |             |            |               |             |                |              |               |              |              |              |
| Precinct TSx                        | 597         | 102        | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| Precinct OS                         | 597         | 102        | 57            | 39          | 2              | 5.13%        | 0             | 0.00%        | 0            | 0.00%        |
| Precinct VBM                        | 597         | 102        | 123           | 89          | 2              | 2.25%        | 2             | 2.25%        | 0            | 0.00%        |
| MB Precinct                         | 597         | 102        | 14            | 11          | 2              | 18.18%       | 1             | 9.09%        | 0            | 0.00%        |
| Provisional                         | 597         | 102        | 13            | 6           | 1              | 16.67%       | 1             | 16.67%       | 0            | 0.00%        |
| <b>Total</b>                        | <b>597</b>  | <b>510</b> | <b>207</b>    | <b>145</b>  | <b>7</b>       | <b>4.83%</b> | <b>4</b>      | <b>2.76%</b> | <b>0</b>     | <b>0.00%</b> |
| <b>Total</b>                        |             |            |               |             |                |              |               |              |              |              |
| <b>STATE SENATE</b>                 |             |            |               |             |                |              |               |              |              |              |
| <b>1ST STATE SENATE DISTRICT</b>    |             |            |               |             |                |              |               |              |              |              |
| Precinct TSx                        | 597         | 102        | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| Precinct OS                         | 597         | 102        | 57            | 39          | 2              | 5.13%        | 0             | 0.00%        | 0            | 0.00%        |
| Precinct VBM                        | 597         | 102        | 123           | 89          | 2              | 2.25%        | 2             | 2.25%        | 0            | 0.00%        |
| MB Precinct                         | 597         | 102        | 14            | 11          | 2              | 18.18%       | 1             | 9.09%        | 0            | 0.00%        |
| Provisional                         | 597         | 102        | 13            | 6           | 1              | 16.67%       | 1             | 16.67%       | 0            | 0.00%        |
| <b>Total</b>                        | <b>597</b>  | <b>510</b> | <b>207</b>    | <b>145</b>  | <b>7</b>       | <b>4.83%</b> | <b>4</b>      | <b>2.76%</b> | <b>0</b>     | <b>0.00%</b> |
| <b>Total</b>                        |             |            |               |             |                |              |               |              |              |              |
| Precinct TSx                        | 597         | 102        | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| Precinct OS                         | 597         | 102        | 57            | 39          | 2              | 5.13%        | 0             | 0.00%        | 0            | 0.00%        |
| Precinct VBM                        | 597         | 102        | 123           | 89          | 2              | 2.25%        | 2             | 2.25%        | 0            | 0.00%        |
| MB Precinct                         | 597         | 102        | 14            | 11          | 2              | 18.18%       | 1             | 9.09%        | 0            | 0.00%        |
| Provisional                         | 597         | 102        | 13            | 6           | 1              | 16.67%       | 1             | 16.67%       | 0            | 0.00%        |
| <b>Total</b>                        | <b>597</b>  | <b>510</b> | <b>207</b>    | <b>145</b>  | <b>7</b>       | <b>4.83%</b> | <b>4</b>      | <b>2.76%</b> | <b>0</b>     | <b>0.00%</b> |
| <b>Total</b>                        |             |            |               |             |                |              |               |              |              |              |
| <b>STATE ASSEMBLY DISTRICTS</b>     |             |            |               |             |                |              |               |              |              |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |             |            |               |             |                |              |               |              |              |              |
| Precinct TSx                        | 500         | 75         | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| Precinct OS                         | 500         | 75         | 49            | 32          | 1              | 3.13%        | 0             | 0.00%        | 0            | 0.00%        |
| Precinct VBM                        | 500         | 75         | 97            | 72          | 1              | 1.39%        | 1             | 1.39%        | 0            | 0.00%        |
| MB Precinct                         | 500         | 75         | 13            | 10          | 2              | 20.00%       | 1             | 10.00%       | 0            | 0.00%        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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President Green Party

|                                   | Reg. Voters | Vote For | Times Counted | Total Votes | DARRYL CHERNEY |        | WILLIAM KREML |        | KENT MESPLAY |       |
|-----------------------------------|-------------|----------|---------------|-------------|----------------|--------|---------------|--------|--------------|-------|
| Provisional                       | 500         | 75       | 11            | 5           | 1              | 20.00% | 1             | 20.00% | 0            | 0.00% |
| Total                             | 500         | 375      | 170           | 119         | 5              | 4.20%  | 3             | 2.52%  | 0            | 0.00% |
| <b>6TH STATE ASSEMBLY DIST</b>    |             |          |               |             |                |        |               |        |              |       |
| Precinct TSx                      | 97          | 27       | 0             | 0           | 0              | -      | 0             | -      | 0            | -     |
| Precinct OS                       | 97          | 27       | 8             | 7           | 1              | 14.29% | 0             | 0.00%  | 0            | 0.00% |
| Precinct VBM                      | 97          | 27       | 26            | 17          | 1              | 5.88%  | 1             | 5.88%  | 0            | 0.00% |
| MB Precinct                       | 97          | 27       | 1             | 1           | 0              | 0.00%  | 0             | 0.00%  | 0            | 0.00% |
| Provisional                       | 97          | 27       | 2             | 1           | 0              | 0.00%  | 0             | 0.00%  | 0            | 0.00% |
| Total                             | 97          | 135      | 37            | 26          | 2              | 7.69%  | 1             | 3.85%  | 0            | 0.00% |
| <b>Total</b>                      |             |          |               |             |                |        |               |        |              |       |
| Precinct TSx                      | 597         | 102      | 0             | 0           | 0              | -      | 0             | -      | 0            | -     |
| Precinct OS                       | 597         | 102      | 57            | 39          | 2              | 5.13%  | 0             | 0.00%  | 0            | 0.00% |
| Precinct VBM                      | 597         | 102      | 123           | 89          | 2              | 2.25%  | 2             | 2.25%  | 0            | 0.00% |
| MB Precinct                       | 597         | 102      | 14            | 11          | 2              | 18.18% | 1             | 9.09%  | 0            | 0.00% |
| Provisional                       | 597         | 102      | 13            | 6           | 1              | 16.67% | 1             | 16.67% | 0            | 0.00% |
| Total                             | 597         | 510      | 207           | 145         | 7              | 4.83%  | 4             | 2.76%  | 0            | 0.00% |
| <b>SUPERVISORIAL DISTRICTS</b>    |             |          |               |             |                |        |               |        |              |       |
| <b>1ST SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |               |        |              |       |
| Precinct TSx                      | 48          | 14       | 0             | 0           | 0              | -      | 0             | -      | 0            | -     |
| Precinct OS                       | 48          | 14       | 3             | 3           | 1              | 33.33% | 0             | 0.00%  | 0            | 0.00% |
| Precinct VBM                      | 48          | 14       | 15            | 10          | 1              | 10.00% | 0             | 0.00%  | 0            | 0.00% |
| MB Precinct                       | 48          | 14       | 1             | 1           | 0              | 0.00%  | 0             | 0.00%  | 0            | 0.00% |
| Provisional                       | 48          | 14       | 2             | 1           | 0              | 0.00%  | 0             | 0.00%  | 0            | 0.00% |
| Total                             | 48          | 70       | 21            | 15          | 2              | 13.33% | 0             | 0.00%  | 0            | 0.00% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |               |        |              |       |
| Precinct TSx                      | 89          | 23       | 0             | 0           | 0              | -      | 0             | -      | 0            | -     |
| Precinct OS                       | 89          | 23       | 4             | 4           | 0              | 0.00%  | 0             | 0.00%  | 0            | 0.00% |
| Precinct VBM                      | 89          | 23       | 20            | 16          | 0              | 0.00%  | 0             | 0.00%  | 0            | 0.00% |
| MB Precinct                       | 89          | 23       | 3             | 2           | 0              | 0.00%  | 0             | 0.00%  | 0            | 0.00% |
| Provisional                       | 89          | 23       | 2             | 1           | 0              | 0.00%  | 0             | 0.00%  | 0            | 0.00% |
| Total                             | 89          | 115      | 29            | 23          | 0              | 0.00%  | 0             | 0.00%  | 0            | 0.00% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |               |        |              |       |
| Precinct TSx                      | 131         | 26       | 0             | 0           | 0              | -      | 0             | -      | 0            | -     |
| Precinct OS                       | 131         | 26       | 16            | 8           | 0              | 0.00%  | 0             | 0.00%  | 0            | 0.00% |
| Precinct VBM                      | 131         | 26       | 32            | 24          | 1              | 4.17%  | 1             | 4.17%  | 0            | 0.00% |
| MB Precinct                       | 131         | 26       | 2             | 1           | 0              | 0.00%  | 0             | 0.00%  | 0            | 0.00% |
| Provisional                       | 131         | 26       | 1             | 0           | 0              | -      | 0             | -      | 0            | -     |
| Total                             | 131         | 130      | 51            | 33          | 1              | 3.03%  | 1             | 3.03%  | 0            | 0.00% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |               |        |              |       |
| Precinct TSx                      | 104         | 22       | 0             | 0           | 0              | -      | 0             | -      | 0            | -     |
| Precinct OS                       | 104         | 22       | 17            | 12          | 0              | 0.00%  | 0             | 0.00%  | 0            | 0.00% |
| Precinct VBM                      | 104         | 22       | 26            | 21          | 0              | 0.00%  | 1             | 4.76%  | 0            | 0.00% |
| MB Precinct                       | 104         | 22       | 4             | 3           | 0              | 0.00%  | 1             | 33.33% | 0            | 0.00% |
| Provisional                       | 104         | 22       | 2             | 2           | 0              | 0.00%  | 1             | 50.00% | 0            | 0.00% |
| Total                             | 104         | 110      | 49            | 38          | 0              | 0.00%  | 3             | 7.89%  | 0            | 0.00% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |               |        |              |       |
| Precinct TSx                      | 225         | 17       | 0             | 0           | 0              | -      | 0             | -      | 0            | -     |
| Precinct OS                       | 225         | 17       | 17            | 12          | 1              | 8.33%  | 0             | 0.00%  | 0            | 0.00% |
| Precinct VBM                      | 225         | 17       | 30            | 18          | 0              | 0.00%  | 0             | 0.00%  | 0            | 0.00% |
| MB Precinct                       | 225         | 17       | 4             | 4           | 2              | 50.00% | 0             | 0.00%  | 0            | 0.00% |
| Provisional                       | 225         | 17       | 6             | 2           | 1              | 50.00% | 0             | 0.00%  | 0            | 0.00% |
| Total                             | 225         | 85       | 57            | 36          | 4              | 11.11% | 0             | 0.00%  | 0            | 0.00% |
| <b>Total</b>                      |             |          |               |             |                |        |               |        |              |       |
| Precinct TSx                      | 597         | 102      | 0             | 0           | 0              | -      | 0             | -      | 0            | -     |
| Precinct OS                       | 597         | 102      | 57            | 39          | 2              | 5.13%  | 0             | 0.00%  | 0            | 0.00% |
| Precinct VBM                      | 597         | 102      | 123           | 89          | 2              | 2.25%  | 2             | 2.25%  | 0            | 0.00% |
| MB Precinct                       | 597         | 102      | 14            | 11          | 2              | 18.18% | 1             | 9.09%  | 0            | 0.00% |
| Provisional                       | 597         | 102      | 13            | 6           | 1              | 16.67% | 1             | 16.67% | 0            | 0.00% |
| Total                             | 597         | 510      | 207           | 145         | 7              | 4.83%  | 4             | 2.76%  | 0            | 0.00% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                 | President Green Party |            |               |             |                |              |               |              |              |              |
|---------------------------------|-----------------------|------------|---------------|-------------|----------------|--------------|---------------|--------------|--------------|--------------|
|                                 | Reg. Voters           | Vote For   | Times Counted | Total Votes | DARRYL CHERNEY |              | WILLIAM KREML |              | KENT MESPLAY |              |
| <b>CITIES</b>                   |                       |            |               |             |                |              |               |              |              |              |
| <b>CITY OF PLACERVILLE</b>      |                       |            |               |             |                |              |               |              |              |              |
| Precinct TSx                    | 41                    | 5          | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| Precinct OS                     | 41                    | 5          | 6             | 4           | 0              | 0.00%        | 0             | 0.00%        | 0            | 0.00%        |
| Precinct VBM                    | 41                    | 5          | 11            | 10          | 0              | 0.00%        | 0             | 0.00%        | 0            | 0.00%        |
| MB Precinct                     | 41                    | 5          | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| Provisional                     | 41                    | 5          | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| <b>Total</b>                    | <b>41</b>             | <b>25</b>  | <b>17</b>     | <b>14</b>   | <b>0</b>       | <b>0.00%</b> | <b>0</b>      | <b>0.00%</b> | <b>0</b>     | <b>0.00%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                       |            |               |             |                |              |               |              |              |              |
| Precinct TSx                    | 132                   | 7          | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| Precinct OS                     | 132                   | 7          | 7             | 4           | 0              | 0.00%        | 0             | 0.00%        | 0            | 0.00%        |
| Precinct VBM                    | 132                   | 7          | 13            | 9           | 0              | 0.00%        | 0             | 0.00%        | 0            | 0.00%        |
| MB Precinct                     | 132                   | 7          | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| Provisional                     | 132                   | 7          | 4             | 1           | 0              | 0.00%        | 0             | 0.00%        | 0            | 0.00%        |
| <b>Total</b>                    | <b>132</b>            | <b>35</b>  | <b>24</b>     | <b>14</b>   | <b>0</b>       | <b>0.00%</b> | <b>0</b>      | <b>0.00%</b> | <b>0</b>     | <b>0.00%</b> |
| <b>UNINCORPORATED AREA</b>      |                       |            |               |             |                |              |               |              |              |              |
| Precinct TSx                    | 424                   | 90         | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| Precinct OS                     | 424                   | 90         | 44            | 31          | 2              | 6.45%        | 0             | 0.00%        | 0            | 0.00%        |
| Precinct VBM                    | 424                   | 90         | 99            | 70          | 2              | 2.86%        | 2             | 2.86%        | 0            | 0.00%        |
| MB Precinct                     | 424                   | 90         | 14            | 11          | 2              | 18.18%       | 1             | 9.09%        | 0            | 0.00%        |
| Provisional                     | 424                   | 90         | 9             | 5           | 1              | 20.00%       | 1             | 20.00%       | 0            | 0.00%        |
| <b>Total</b>                    | <b>424</b>            | <b>450</b> | <b>166</b>    | <b>117</b>  | <b>7</b>       | <b>5.98%</b> | <b>4</b>      | <b>3.42%</b> | <b>0</b>     | <b>0.00%</b> |
| <b>Total</b>                    |                       |            |               |             |                |              |               |              |              |              |
| Precinct TSx                    | 597                   | 102        | 0             | 0           | 0              | -            | 0             | -            | 0            | -            |
| Precinct OS                     | 597                   | 102        | 57            | 39          | 2              | 5.13%        | 0             | 0.00%        | 0            | 0.00%        |
| Precinct VBM                    | 597                   | 102        | 123           | 89          | 2              | 2.25%        | 2             | 2.25%        | 0            | 0.00%        |
| MB Precinct                     | 597                   | 102        | 14            | 11          | 2              | 18.18%       | 1             | 9.09%        | 0            | 0.00%        |
| Provisional                     | 597                   | 102        | 13            | 6           | 1              | 16.67%       | 1             | 16.67%       | 0            | 0.00%        |
| <b>Total</b>                    | <b>597</b>            | <b>510</b> | <b>207</b>    | <b>145</b>  | <b>7</b>       | <b>4.83%</b> | <b>4</b>      | <b>2.76%</b> | <b>0</b>     | <b>0.00%</b> |

**COUNTY OF EL DORADO**  
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|------------------------|-----------------------|------------|
|                        | S.MOYOWASIFS.         | JILL STEIN |
| Jurisdiction Wide      |                       |            |
| 100001.11-CP 100001.11 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 1 100.00%             | 0 0.00%    |
| Precinct VBM           | 0 0.00%               | 1 100.00%  |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 1 50.00%              | 1 50.00%   |
| 100002.11-CP 100002.11 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 -                   | 0 -        |
| Precinct VBM           | 0 0.00%               | 0 0.00%    |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 0 0.00%               | 0 0.00%    |
| 100003.11-CP 100003.11 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 -                   | 0 -        |
| Precinct VBM           | 0 -                   | 0 -        |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 0 -                   | 0 -        |
| 100004.11-CP 100004.11 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 0.00%               | 0 0.00%    |
| Precinct VBM           | 0 -                   | 0 -        |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 0 0.00%               | 0 0.00%    |
| 100005.11-CP 100005.11 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 -                   | 0 -        |
| Precinct VBM           | 0 -                   | 0 -        |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 0.00%               | 1 100.00%  |
| Total                  | 0 0.00%               | 1 100.00%  |
| 100006.11-CP 100006.11 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 -                   | 0 -        |
| Precinct VBM           | 0 0.00%               | 0 0.00%    |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 0 0.00%               | 0 0.00%    |
| 100008.11-CP 100008.11 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 0.00%               | 1 100.00%  |
| Precinct VBM           | 0 0.00%               | 1 100.00%  |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 0 0.00%               | 2 100.00%  |
| 100009.11-CP 100009.11 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 -                   | 0 -        |
| Precinct VBM           | 0 -                   | 0 -        |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 0 -                   | 0 -        |
| 100012.11-CP 100012.11 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 -                   | 0 -        |
| Precinct VBM           | 0 0.00%               | 1 33.33%   |



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|------------------------|-----------------------|------------|---|---------|
|                        | S.MOYOWASIFS.         | JILL STEIN |   |         |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 1 | 33.33%  |
| 100013.11-CP 100013.11 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | -          | 0 | -       |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | -          | 0 | -       |
| 100014.11-CP 100014.11 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | 0.00%      | 2 | 100.00% |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 2 | 100.00% |
| 100022.11-CP 100022.11 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | -          | 0 | -       |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | -          | 0 | -       |
| 100023.11-CP 100023.11 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | -          | 0 | -       |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | -          | 0 | -       |
| 200003.12-CP 200003.12 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | 0.00%      | 1 | 100.00% |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 1 | 100.00% |
| 200004.12-CP 200004.12 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | 0.00%      | 1 | 100.00% |
| Precinct VBM           | 0                     | -          | 0 | -       |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 1 | 100.00% |
| 200005.02-CP 200005.02 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | 0.00%      | 1 | 50.00%  |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 1 | 50.00%  |
| 200006.12-CP 200006.12 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | 0.00%      | 1 | 100.00% |
| Precinct VBM           | 0                     | -          | 0 | -       |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 1 | 100.00% |
| 200008.12-CP 200008.12 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |

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|------------------------|-----------------------|------------|---|---------|
|                        | S.MOYOWASIFS.         | JILL STEIN |   |         |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | -          | 0 | -       |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | -          | 0 | -       |
| 200010.12-CP 200010.12 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | -          | 0 | -       |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | -          | 0 | -       |
| 200012.12-CP 200012.12 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | 0.00%      | 0 | 0.00%   |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 0 | 0.00%   |
| 200013.12-CP 200013.12 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | -          | 0 | -       |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | -          | 0 | -       |
| 200014.12-CP 200014.12 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | -          | 0 | -       |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | -          | 0 | -       |
| 200015.02-CP 200015.02 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 1                     | 20.00%     | 3 | 60.00%  |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 1                     | 20.00%     | 3 | 60.00%  |
| 200016.12-CP 200016.12 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | -          | 0 | -       |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | -          | 0 | -       |
| 200016.13-CP 200016.13 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | -          | 0 | -       |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | -          | 0 | -       |
| 200017.02-CP 200017.02 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | 0.00%      | 1 | 100.00% |
| Precinct VBM           | 0                     | 0.00%      | 3 | 75.00%  |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 4 | 80.00%  |

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|                        | S.MOYOWASIFS.         |       | JILL STEIN |         |
| 200018.02-CP 200018.02 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | 0.00% | 0          | 0.00%   |
| Precinct VBM           | 0                     | 0.00% | 0          | 0.00%   |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | 0.00% | 0          | 0.00%   |
| 200020.02-CP 200020.02 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | -     | 0          | -       |
| Precinct VBM           | 0                     | -     | 0          | -       |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | -     | 0          | -       |
| 200021.01-CP 200021.01 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | -     | 0          | -       |
| Precinct VBM           | 0                     | -     | 0          | -       |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | -     | 0          | -       |
| 200021.02-CP 200021.02 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | -     | 0          | -       |
| Precinct VBM           | 0                     | 0.00% | 1          | 50.00%  |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | 0.00% | 1          | 100.00% |
| Total                  | 0                     | 0.00% | 2          | 66.67%  |
| 200024.02-CP 200024.02 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | -     | 0          | -       |
| Precinct VBM           | 0                     | -     | 0          | -       |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | -     | 0          | -       |
| 300001.04-CP 300001.04 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | -     | 0          | -       |
| Precinct VBM           | 0                     | -     | 0          | -       |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | -     | 0          | -       |
| 300001.05-CP 300001.05 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | -     | 0          | -       |
| Precinct VBM           | 0                     | -     | 0          | -       |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | -     | 0          | -       |
| 300003.05-CP 300003.05 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | 0.00% | 2          | 100.00% |
| Precinct VBM           | 0                     | 0.00% | 0          | 0.00%   |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | 0.00% | 2          | 66.67%  |
| 300004.05-CP 300004.05 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | 0.00% | 2          | 100.00% |
| Precinct VBM           | 0                     | 0.00% | 3          | 50.00%  |
| MB Precinct            | 0                     | -     | 0          | -       |

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|                        | S.MOYOWASIFS.         | JILL STEIN |   |         |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 5 | 62.50%  |
| 300005.05-CP 300005.05 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | 0.00%      | 2 | 66.67%  |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 2 | 66.67%  |
| 300008.05-CP 300008.05 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | 0.00%      | 0 | 0.00%   |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 0 | 0.00%   |
| 300009.04-CP 300009.04 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | 0.00%      | 0 | 0.00%   |
| Precinct VBM           | 0                     | 0.00%      | 1 | 100.00% |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 1 | 50.00%  |
| 300010.04-CP 300010.04 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | -          | 0 | -       |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | -          | 0 | -       |
| 300011.04-CP 300011.04 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | 0.00%      | 0 | 0.00%   |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 0 | 0.00%   |
| 300013.05-CP 300013.05 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | 0.00%      | 1 | 100.00% |
| Precinct VBM           | 0                     | 0.00%      | 0 | 0.00%   |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 1 | 50.00%  |
| 300015.04-CP 300015.04 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | -          | 0 | -       |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | -          | 0 | -       |
| 300015.05-CP 300015.05 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |
| Precinct VBM           | 0                     | 0.00%      | 1 | 100.00% |
| MB Precinct            | 0                     | -          | 0 | -       |
| Provisional            | 0                     | -          | 0 | -       |
| Total                  | 0                     | 0.00%      | 1 | 100.00% |
| 300017.05-CP 300017.05 |                       |            |   |         |
| Precinct TSx           | 0                     | -          | 0 | -       |
| Precinct OS            | 0                     | -          | 0 | -       |

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|                        | S.MOYOWASIFS.         | JILL STEIN |
| Precinct VBM           | 0 0.00%               | 1 100.00%  |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 0 0.00%               | 1 100.00%  |
| 300018.04-CP 300018.04 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 -                   | 0 -        |
| Precinct VBM           | 0 0.00%               | 1 100.00%  |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 0 0.00%               | 1 100.00%  |
| 300020.04-CP 300020.04 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 -                   | 0 -        |
| Precinct VBM           | 0 0.00%               | 0 0.00%    |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 0 0.00%               | 0 0.00%    |
| 300021.03-CP 300021.03 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 -                   | 0 -        |
| Precinct VBM           | 0 0.00%               | 1 50.00%   |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 0 0.00%               | 1 50.00%   |
| 300022.05-CP 300022.05 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 -                   | 0 -        |
| Precinct VBM           | 0 0.00%               | 1 50.00%   |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 0 0.00%               | 1 50.00%   |
| 300023.03-CP 300023.03 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 0.00%               | 2 100.00%  |
| Precinct VBM           | 0 0.00%               | 0 0.00%    |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 0 0.00%               | 2 66.67%   |
| 300023.05-CP 300023.05 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 -                   | 0 -        |
| Precinct VBM           | 0 -                   | 0 -        |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 0 -                   | 0 -        |
| 400001.14-CP 400001.14 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 -                   | 0 -        |
| Precinct VBM           | 0 0.00%               | 1 100.00%  |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 -                   | 0 -        |
| Total                  | 0 0.00%               | 1 100.00%  |
| 400002.08-CP 400002.08 |                       |            |
| Precinct TSx           | 0 -                   | 0 -        |
| Precinct OS            | 0 0.00%               | 0 0.00%    |
| Precinct VBM           | 0 -                   | 0 -        |
| MB Precinct            | 0 -                   | 0 -        |
| Provisional            | 0 0.00%               | 0 0.00%    |
| Total                  | 0 0.00%               | 0 0.00%    |
| 400003.08-CP 400003.08 |                       |            |

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|                        | S.MOYOWASIFS.         | JILL STEIN |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0                     | 0          |
| Precinct VBM           | 0 0.00%               | 2 66.67%   |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 0 0.00%               | 2 66.67%   |
| 400004.14-CP 400004.14 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0 0.00%               | 1 50.00%   |
| Precinct VBM           | 0 0.00%               | 3 75.00%   |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 0 0.00%               | 4 66.67%   |
| 400005.08-CP 400005.08 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0 0.00%               | 1 100.00%  |
| Precinct VBM           | 0 0.00%               | 1 100.00%  |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 0 0.00%               | 2 100.00%  |
| 400006.08-CP 400006.08 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0 0.00%               | 0 0.00%    |
| Precinct VBM           | 0                     | 0          |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 0 0.00%               | 0 0.00%    |
| 400007.08-CP 400007.08 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0 0.00%               | 1 33.33%   |
| Precinct VBM           | 0 0.00%               | 1 100.00%  |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 0 0.00%               | 2 50.00%   |
| 400010.08-CP 400010.08 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 1 33.33%              | 1 33.33%   |
| Precinct VBM           | 0 0.00%               | 2 66.67%   |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 1 16.67%              | 3 50.00%   |
| 400012.08-CP 400012.08 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0                     | 0          |
| Precinct VBM           | 0 0.00%               | 0 0.00%    |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 0 0.00%               | 0 0.00%    |
| 400013.08-CP 400013.08 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0                     | 0          |
| Precinct VBM           | 0 0.00%               | 2 66.67%   |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 0 0.00%               | 2 66.67%   |
| 400014.08-CP 400014.08 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0                     | 0          |
| Precinct VBM           | 0 0.00%               | 1 100.00%  |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0 0.00%               | 0 0.00%    |

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|                        | S.MOYOWASIFS.         |       | JILL STEIN |         |
| Total                  | 0                     | 0.00% | 1          | 50.00%  |
| 400017.08-CP 400017.08 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | -     | 0          | -       |
| Precinct VBM           | 0                     | -     | 0          | -       |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | -     | 0          | -       |
| 400018.08-CP 400018.08 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | -     | 0          | -       |
| Precinct VBM           | 0                     | -     | 0          | -       |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | -     | 0          | -       |
| 400020.08-CP 400020.08 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | -     | 0          | -       |
| Precinct VBM           | 0                     | 0.00% | 1          | 100.00% |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | 0.00% | 1          | 100.00% |
| 400021.07-CP 400021.07 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | -     | 0          | -       |
| Precinct VBM           | 0                     | 0.00% | 0          | 0.00%   |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | 0.00% | 0          | 0.00%   |
| 500001.10-CP 500001.10 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | 0.00% | 1          | 100.00% |
| Precinct VBM           | 0                     | 0.00% | 0          | 0.00%   |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | 0.00% | 1          | 33.33%  |
| 500002.10-CP 500002.10 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | -     | 0          | -       |
| Precinct VBM           | 0                     | 0.00% | 1          | 100.00% |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | 0.00% | 1          | 100.00% |
| 500003.10-CP 500003.10 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | 0.00% | 0          | 0.00%   |
| Precinct VBM           | 0                     | 0.00% | 0          | 0.00%   |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | 0.00% | 0          | 0.00%   |
| 500004.10-CP 500004.10 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | -     | 0          | -       |
| Precinct VBM           | 0                     | 0.00% | 1          | 100.00% |
| MB Precinct            | 0                     | -     | 0          | -       |
| Provisional            | 0                     | -     | 0          | -       |
| Total                  | 0                     | 0.00% | 1          | 100.00% |
| 500005.10-CP 500005.10 |                       |       |            |         |
| Precinct TSx           | 0                     | -     | 0          | -       |
| Precinct OS            | 0                     | -     | 0          | -       |
| Precinct VBM           | 0                     | 0.00% | 1          | 50.00%  |

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|------------------------|-----------------------|------------|
|                        | S.MOYOWASIFS.         | JILL STEIN |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 0 0.00%               | 1 50.00%   |
| 500006.10-CP 500006.10 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0 0.00%               | 1 50.00%   |
| Precinct VBM           | 0 0.00%               | 3 100.00%  |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 0 0.00%               | 4 80.00%   |
| 500007.10-CP 500007.10 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0 0.00%               | 0 0.00%    |
| Precinct VBM           | 0 0.00%               | 0 0.00%    |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 0 0.00%               | 0 0.00%    |
| 500009.10-CP 500009.10 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0                     | 0          |
| Precinct VBM           | 0                     | 0          |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0 0.00%               | 1 100.00%  |
| Total                  | 0 0.00%               | 1 100.00%  |
| 500011.10-CP 500011.10 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0 0.00%               | 1 100.00%  |
| Precinct VBM           | 0 0.00%               | 1 100.00%  |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 0 0.00%               | 2 100.00%  |
| 500012.10-CP 500012.10 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0 0.00%               | 3 100.00%  |
| Precinct VBM           | 0 0.00%               | 1 50.00%   |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 0 0.00%               | 4 80.00%   |
| 500015.10-CP 500015.10 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0 0.00%               | 0 0.00%    |
| Precinct VBM           | 0 0.00%               | 2 100.00%  |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 0 0.00%               | 2 66.67%   |
| 500020.10-CP 500020.10 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0 0.00%               | 1 50.00%   |
| Precinct VBM           | 0 0.00%               | 0 0.00%    |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0 0.00%               | 0 0.00%    |
| Total                  | 0 0.00%               | 1 25.00%   |
| 500021.10-CP 500021.10 |                       |            |
| Precinct TSx           | 0                     | 0          |
| Precinct OS            | 0                     | 0          |
| Precinct VBM           | 0 0.00%               | 0 0.00%    |
| MB Precinct            | 0                     | 0          |
| Provisional            | 0                     | 0          |
| Total                  | 0 0.00%               | 0 0.00%    |
| 95U201.01-MB 95U201.01 |                       |            |
| Precinct TSx           | 0                     | 0          |



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|                        | S.MOYOWASIFS.         | JILL STEIN |   |       |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | -          | 0 | -     |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | -          | 0 | -     |
| 95U201.02-MB 95U201.02 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | 0.00%      | 0 | 0.00% |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | 0.00%      | 0 | 0.00% |
| 95U202.02-MB 95U202.02 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | -          | 0 | -     |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | -          | 0 | -     |
| 95U203.02-MB 95U203.02 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | -          | 0 | -     |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | -          | 0 | -     |
| 95U204.01-MB 95U204.01 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | 0.00%      | 0 | 0.00% |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | 0.00%      | 0 | 0.00% |
| 95U301.05-MB 95U301.05 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | -          | 0 | -     |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | -          | 0 | -     |
| 95U302.04-MB 95U302.04 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | -          | 0 | -     |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | -          | 0 | -     |
| 95U302.05-MB 95U302.05 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | 0.00%      | 0 | 0.00% |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | 0.00%      | 0 | 0.00% |
| 95U303.03-MB 95U303.03 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | -          | 0 | -     |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | -          | 0 | -     |

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|                        | S.MOYOWASIFS.         | JILL STEIN |   |       |
| 95U303.05-MB 95U303.05 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | -          | 0 | -     |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | -          | 0 | -     |
| 95U304.05-MB 95U304.05 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | -          | 0 | -     |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | -          | 0 | -     |
| 95U305.05-MB 95U305.05 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | -          | 0 | -     |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | -          | 0 | -     |
| 95U401.08-MB 95U401.08 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | -          | 0 | -     |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | -          | 0 | -     |
| 95U402.07-MB 95U402.07 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | -          | 0 | -     |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | -          | 0 | -     |
| 95U402.08-MB 95U402.08 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | 0.00%      | 0 | 0.00% |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | 0.00%      | 0 | 0.00% |
| 95U403.08-MB 95U403.08 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | 0.00%      | 0 | 0.00% |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | 0.00%      | 0 | 0.00% |
| 95U404.06-MB 95U404.06 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | -          | 0 | -     |
| Provisional            | 0                     | -          | 0 | -     |
| Total                  | 0                     | -          | 0 | -     |
| 95U404.08-MB 95U404.08 |                       |            |   |       |
| Precinct TSx           | 0                     | -          | 0 | -     |
| Precinct OS            | 0                     | -          | 0 | -     |
| Precinct VBM           | 0                     | -          | 0 | -     |
| MB Precinct            | 0                     | -          | 0 | -     |

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|-------------------------------|-----------------------|------------|----|--------|
|                               | S.MOYOWASIFS.         | JILL STEIN |    |        |
| Provisional                   | 0                     | -          | 0  | -      |
| Total                         | 0                     | -          | 0  | -      |
| 95U501.09-MB 95U501.09        |                       |            |    |        |
| Precinct TSx                  | 0                     | -          | 0  | -      |
| Precinct OS                   | 0                     | -          | 0  | -      |
| Precinct VBM                  | 0                     | -          | 0  | -      |
| MB Precinct                   | 0                     | -          | 0  | -      |
| Provisional                   | 0                     | -          | 0  | -      |
| Total                         | 0                     | -          | 0  | -      |
| 95U501.10-MB 95U501.10        |                       |            |    |        |
| Precinct TSx                  | 0                     | -          | 0  | -      |
| Precinct OS                   | 0                     | -          | 0  | -      |
| Precinct VBM                  | 0                     | -          | 0  | -      |
| MB Precinct                   | 0                     | 0.00%      | 0  | 0.00%  |
| Provisional                   | 0                     | -          | 0  | -      |
| Total                         | 0                     | 0.00%      | 0  | 0.00%  |
| 95U598.10-MB 95U598.10        |                       |            |    |        |
| Precinct TSx                  | 0                     | -          | 0  | -      |
| Precinct OS                   | 0                     | -          | 0  | -      |
| Precinct VBM                  | 0                     | -          | 0  | -      |
| MB Precinct                   | 0                     | 0.00%      | 0  | 0.00%  |
| Provisional                   | 0                     | -          | 0  | -      |
| Total                         | 0                     | 0.00%      | 0  | 0.00%  |
| 95U599.10-MB 95U599.10        |                       |            |    |        |
| Precinct TSx                  | 0                     | -          | 0  | -      |
| Precinct OS                   | 0                     | -          | 0  | -      |
| Precinct VBM                  | 0                     | -          | 0  | -      |
| MB Precinct                   | 0                     | -          | 0  | -      |
| Provisional                   | 0                     | -          | 0  | -      |
| Total                         | 0                     | -          | 0  | -      |
| 96U101.11-MB 96U101.11        |                       |            |    |        |
| Precinct TSx                  | 0                     | -          | 0  | -      |
| Precinct OS                   | 0                     | -          | 0  | -      |
| Precinct VBM                  | 0                     | -          | 0  | -      |
| MB Precinct                   | 0                     | 0.00%      | 0  | 0.00%  |
| Provisional                   | 0                     | -          | 0  | -      |
| Total                         | 0                     | 0.00%      | 0  | 0.00%  |
| 96U401.14-MB 96U401.14        |                       |            |    |        |
| Precinct TSx                  | 0                     | -          | 0  | -      |
| Precinct OS                   | 0                     | -          | 0  | -      |
| Precinct VBM                  | 0                     | -          | 0  | -      |
| MB Precinct                   | 0                     | -          | 0  | -      |
| Provisional                   | 0                     | -          | 0  | -      |
| Total                         | 0                     | -          | 0  | -      |
| Total                         |                       |            |    |        |
| Precinct TSx                  | 0                     | -          | 0  | -      |
| Precinct OS                   | 2                     | 5.13%      | 22 | 56.41% |
| Precinct VBM                  | 1                     | 1.12%      | 49 | 55.06% |
| MB Precinct                   | 0                     | 0.00%      | 0  | 0.00%  |
| Provisional                   | 0                     | 0.00%      | 3  | 50.00% |
| Total                         | 3                     | 2.07%      | 74 | 51.03% |
| STATE BOARD OF EQUALIZATION   |                       |            |    |        |
| STATE BOARD OF EQUALIZATION 1 |                       |            |    |        |
| Precinct TSx                  | 0                     | -          | 0  | -      |
| Precinct OS                   | 2                     | 5.13%      | 22 | 56.41% |
| Precinct VBM                  | 1                     | 1.12%      | 49 | 55.06% |
| MB Precinct                   | 0                     | 0.00%      | 0  | 0.00%  |
| Provisional                   | 0                     | 0.00%      | 3  | 50.00% |
| Total                         | 3                     | 2.07%      | 74 | 51.03% |
| Total                         |                       |            |    |        |

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|                                     | S.MOYOWASIFS.         |              | JILL STEIN |               |
| Precinct TSx                        | 0                     | -            | 0          | -             |
| Precinct OS                         | 2                     | 5.13%        | 22         | 56.41%        |
| Precinct VBM                        | 1                     | 1.12%        | 49         | 55.06%        |
| MB Precinct                         | 0                     | 0.00%        | 0          | 0.00%         |
| Provisional                         | 0                     | 0.00%        | 3          | 50.00%        |
| <b>Total</b>                        | <b>3</b>              | <b>2.07%</b> | <b>74</b>  | <b>51.03%</b> |
| <b>UNITED STATES SENATE</b>         |                       |              |            |               |
| <b>UNITED STATES SENATE</b>         |                       |              |            |               |
| Precinct TSx                        | 0                     | -            | 0          | -             |
| Precinct OS                         | 2                     | 5.13%        | 22         | 56.41%        |
| Precinct VBM                        | 1                     | 1.12%        | 49         | 55.06%        |
| MB Precinct                         | 0                     | 0.00%        | 0          | 0.00%         |
| Provisional                         | 0                     | 0.00%        | 3          | 50.00%        |
| <b>Total</b>                        | <b>3</b>              | <b>2.07%</b> | <b>74</b>  | <b>51.03%</b> |
| <b>Total</b>                        |                       |              |            |               |
| Precinct TSx                        | 0                     | -            | 0          | -             |
| Precinct OS                         | 2                     | 5.13%        | 22         | 56.41%        |
| Precinct VBM                        | 1                     | 1.12%        | 49         | 55.06%        |
| MB Precinct                         | 0                     | 0.00%        | 0          | 0.00%         |
| Provisional                         | 0                     | 0.00%        | 3          | 50.00%        |
| <b>Total</b>                        | <b>3</b>              | <b>2.07%</b> | <b>74</b>  | <b>51.03%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |                       |              |            |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                       |              |            |               |
| Precinct TSx                        | 0                     | -            | 0          | -             |
| Precinct OS                         | 2                     | 5.13%        | 22         | 56.41%        |
| Precinct VBM                        | 1                     | 1.12%        | 49         | 55.06%        |
| MB Precinct                         | 0                     | 0.00%        | 0          | 0.00%         |
| Provisional                         | 0                     | 0.00%        | 3          | 50.00%        |
| <b>Total</b>                        | <b>3</b>              | <b>2.07%</b> | <b>74</b>  | <b>51.03%</b> |
| <b>Total</b>                        |                       |              |            |               |
| Precinct TSx                        | 0                     | -            | 0          | -             |
| Precinct OS                         | 2                     | 5.13%        | 22         | 56.41%        |
| Precinct VBM                        | 1                     | 1.12%        | 49         | 55.06%        |
| MB Precinct                         | 0                     | 0.00%        | 0          | 0.00%         |
| Provisional                         | 0                     | 0.00%        | 3          | 50.00%        |
| <b>Total</b>                        | <b>3</b>              | <b>2.07%</b> | <b>74</b>  | <b>51.03%</b> |
| <b>STATE SENATE</b>                 |                       |              |            |               |
| <b>1ST STATE SENATE DISTRICT</b>    |                       |              |            |               |
| Precinct TSx                        | 0                     | -            | 0          | -             |
| Precinct OS                         | 2                     | 5.13%        | 22         | 56.41%        |
| Precinct VBM                        | 1                     | 1.12%        | 49         | 55.06%        |
| MB Precinct                         | 0                     | 0.00%        | 0          | 0.00%         |
| Provisional                         | 0                     | 0.00%        | 3          | 50.00%        |
| <b>Total</b>                        | <b>3</b>              | <b>2.07%</b> | <b>74</b>  | <b>51.03%</b> |
| <b>Total</b>                        |                       |              |            |               |
| Precinct TSx                        | 0                     | -            | 0          | -             |
| Precinct OS                         | 2                     | 5.13%        | 22         | 56.41%        |
| Precinct VBM                        | 1                     | 1.12%        | 49         | 55.06%        |
| MB Precinct                         | 0                     | 0.00%        | 0          | 0.00%         |
| Provisional                         | 0                     | 0.00%        | 3          | 50.00%        |
| <b>Total</b>                        | <b>3</b>              | <b>2.07%</b> | <b>74</b>  | <b>51.03%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                       |              |            |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |                       |              |            |               |
| Precinct TSx                        | 0                     | -            | 0          | -             |
| Precinct OS                         | 1                     | 3.13%        | 18         | 56.25%        |
| Precinct VBM                        | 1                     | 1.39%        | 39         | 54.17%        |
| MB Precinct                         | 0                     | 0.00%        | 0          | 0.00%         |

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|                                   | S.MOYOWASIFS.         |        | JILL STEIN |         |
| Provisional                       | 0                     | 0.00%  | 2          | 40.00%  |
| Total                             | 2                     | 1.68%  | 59         | 49.58%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |                       |        |            |         |
| Precinct TSx                      | 0                     | -      | 0          | -       |
| Precinct OS                       | 1                     | 14.29% | 4          | 57.14%  |
| Precinct VBM                      | 0                     | 0.00%  | 10         | 58.82%  |
| MB Precinct                       | 0                     | 0.00%  | 0          | 0.00%   |
| Provisional                       | 0                     | 0.00%  | 1          | 100.00% |
| Total                             | 1                     | 3.85%  | 15         | 57.69%  |
| <b>Total</b>                      |                       |        |            |         |
| Precinct TSx                      | 0                     | -      | 0          | -       |
| Precinct OS                       | 2                     | 5.13%  | 22         | 56.41%  |
| Precinct VBM                      | 1                     | 1.12%  | 49         | 55.06%  |
| MB Precinct                       | 0                     | 0.00%  | 0          | 0.00%   |
| Provisional                       | 0                     | 0.00%  | 3          | 50.00%  |
| Total                             | 3                     | 2.07%  | 74         | 51.03%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |                       |        |            |         |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                       |        |            |         |
| Precinct TSx                      | 0                     | -      | 0          | -       |
| Precinct OS                       | 1                     | 33.33% | 1          | 33.33%  |
| Precinct VBM                      | 0                     | 0.00%  | 5          | 50.00%  |
| MB Precinct                       | 0                     | 0.00%  | 0          | 0.00%   |
| Provisional                       | 0                     | 0.00%  | 1          | 100.00% |
| Total                             | 1                     | 6.67%  | 7          | 46.67%  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                       |        |            |         |
| Precinct TSx                      | 0                     | -      | 0          | -       |
| Precinct OS                       | 0                     | 0.00%  | 3          | 75.00%  |
| Precinct VBM                      | 1                     | 6.25%  | 9          | 56.25%  |
| MB Precinct                       | 0                     | 0.00%  | 0          | 0.00%   |
| Provisional                       | 0                     | 0.00%  | 1          | 100.00% |
| Total                             | 1                     | 4.35%  | 13         | 56.52%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                       |        |            |         |
| Precinct TSx                      | 0                     | -      | 0          | -       |
| Precinct OS                       | 0                     | 0.00%  | 7          | 87.50%  |
| Precinct VBM                      | 0                     | 0.00%  | 11         | 45.83%  |
| MB Precinct                       | 0                     | 0.00%  | 0          | 0.00%   |
| Provisional                       | 0                     | -      | 0          | -       |
| Total                             | 0                     | 0.00%  | 18         | 54.55%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                       |        |            |         |
| Precinct TSx                      | 0                     | -      | 0          | -       |
| Precinct OS                       | 1                     | 8.33%  | 4          | 33.33%  |
| Precinct VBM                      | 0                     | 0.00%  | 14         | 66.67%  |
| MB Precinct                       | 0                     | 0.00%  | 0          | 0.00%   |
| Provisional                       | 0                     | 0.00%  | 0          | 0.00%   |
| Total                             | 1                     | 2.63%  | 18         | 47.37%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                       |        |            |         |
| Precinct TSx                      | 0                     | -      | 0          | -       |
| Precinct OS                       | 0                     | 0.00%  | 7          | 58.33%  |
| Precinct VBM                      | 0                     | 0.00%  | 10         | 55.56%  |
| MB Precinct                       | 0                     | 0.00%  | 0          | 0.00%   |
| Provisional                       | 0                     | 0.00%  | 1          | 50.00%  |
| Total                             | 0                     | 0.00%  | 18         | 50.00%  |
| <b>Total</b>                      |                       |        |            |         |
| Precinct TSx                      | 0                     | -      | 0          | -       |
| Precinct OS                       | 2                     | 5.13%  | 22         | 56.41%  |
| Precinct VBM                      | 1                     | 1.12%  | 49         | 55.06%  |
| MB Precinct                       | 0                     | 0.00%  | 0          | 0.00%   |
| Provisional                       | 0                     | 0.00%  | 3          | 50.00%  |
| Total                             | 3                     | 2.07%  | 74         | 51.03%  |

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|                                 | President Green Party |       |            |         |
|---------------------------------|-----------------------|-------|------------|---------|
|                                 | S.MOYOWASIFS.         |       | JILL STEIN |         |
| <b>CITIES</b>                   |                       |       |            |         |
| <b>CITY OF PLACERVILLE</b>      |                       |       |            |         |
| Precinct TSx                    | 0                     | -     | 0          | -       |
| Precinct OS                     | 0                     | 0.00% | 4          | 100.00% |
| Precinct VBM                    | 0                     | 0.00% | 5          | 50.00%  |
| MB Precinct                     | 0                     | -     | 0          | -       |
| Provisional                     | 0                     | -     | 0          | -       |
| Total                           | 0                     | 0.00% | 9          | 64.29%  |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                       |       |            |         |
| Precinct TSx                    | 0                     | -     | 0          | -       |
| Precinct OS                     | 0                     | 0.00% | 2          | 50.00%  |
| Precinct VBM                    | 0                     | 0.00% | 7          | 77.78%  |
| MB Precinct                     | 0                     | -     | 0          | -       |
| Provisional                     | 0                     | 0.00% | 1          | 100.00% |
| Total                           | 0                     | 0.00% | 10         | 71.43%  |
| <b>UNINCORPORATED AREA</b>      |                       |       |            |         |
| Precinct TSx                    | 0                     | -     | 0          | -       |
| Precinct OS                     | 2                     | 6.45% | 16         | 51.61%  |
| Precinct VBM                    | 1                     | 1.43% | 37         | 52.86%  |
| MB Precinct                     | 0                     | 0.00% | 0          | 0.00%   |
| Provisional                     | 0                     | 0.00% | 2          | 40.00%  |
| Total                           | 3                     | 2.56% | 55         | 47.01%  |
| <b>Total</b>                    |                       |       |            |         |
| Precinct TSx                    | 0                     | -     | 0          | -       |
| Precinct OS                     | 2                     | 5.13% | 22         | 56.41%  |
| Precinct VBM                    | 1                     | 1.12% | 49         | 55.06%  |
| MB Precinct                     | 0                     | 0.00% | 0          | 0.00%   |
| Provisional                     | 0                     | 0.00% | 3          | 50.00%  |
| Total                           | 3                     | 2.07% | 74         | 51.03%  |

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|                        | President Libertarian Party |          |               |             |              |       |           |         |
|------------------------|-----------------------------|----------|---------------|-------------|--------------|-------|-----------|---------|
|                        | Reg. Voters                 | Vote For | Times Counted | Total Votes | MARC FELDMAN |       | JOHN HALE |         |
| Jurisdiction Wide      |                             |          |               |             |              |       |           |         |
| 100001.11-CP 100001.11 |                             |          |               |             |              |       |           |         |
| Precinct TSx           | 19                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Precinct OS            | 19                          | 1        | 4             | 3           | 0            | 0.00% | 0         | 0.00%   |
| Precinct VBM           | 19                          | 1        | 7             | 5           | 0            | 0.00% | 0         | 0.00%   |
| MB Precinct            | 19                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Provisional            | 19                          | 1        | 1             | 1           | 0            | 0.00% | 1         | 100.00% |
| Total                  | 19                          | 5        | 12            | 9           | 0            | 0.00% | 1         | 11.11%  |
| 100002.11-CP 100002.11 |                             |          |               |             |              |       |           |         |
| Precinct TSx           | 12                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Precinct OS            | 12                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Precinct VBM           | 12                          | 1        | 3             | 3           | 0            | 0.00% | 0         | 0.00%   |
| MB Precinct            | 12                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Provisional            | 12                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Total                  | 12                          | 5        | 3             | 3           | 0            | 0.00% | 0         | 0.00%   |
| 100003.11-CP 100003.11 |                             |          |               |             |              |       |           |         |
| Precinct TSx           | 11                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Precinct OS            | 11                          | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%   |
| Precinct VBM           | 11                          | 1        | 8             | 7           | 0            | 0.00% | 0         | 0.00%   |
| MB Precinct            | 11                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Provisional            | 11                          | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%   |
| Total                  | 11                          | 5        | 11            | 10          | 0            | 0.00% | 0         | 0.00%   |
| 100004.11-CP 100004.11 |                             |          |               |             |              |       |           |         |
| Precinct TSx           | 16                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Precinct OS            | 16                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Precinct VBM           | 16                          | 1        | 4             | 2           | 0            | 0.00% | 0         | 0.00%   |
| MB Precinct            | 16                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Provisional            | 16                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Total                  | 16                          | 5        | 4             | 2           | 0            | 0.00% | 0         | 0.00%   |
| 100005.11-CP 100005.11 |                             |          |               |             |              |       |           |         |
| Precinct TSx           | 13                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Precinct OS            | 13                          | 1        | 4             | 3           | 0            | 0.00% | 0         | 0.00%   |
| Precinct VBM           | 13                          | 1        | 3             | 3           | 0            | 0.00% | 1         | 33.33%  |
| MB Precinct            | 13                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Provisional            | 13                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Total                  | 13                          | 5        | 7             | 6           | 0            | 0.00% | 1         | 16.67%  |
| 100006.11-CP 100006.11 |                             |          |               |             |              |       |           |         |
| Precinct TSx           | 25                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Precinct OS            | 25                          | 1        | 1             | 0           | 0            | -     | 0         | -       |
| Precinct VBM           | 25                          | 1        | 12            | 9           | 0            | 0.00% | 0         | 0.00%   |
| MB Precinct            | 25                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Provisional            | 25                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Total                  | 25                          | 5        | 13            | 9           | 0            | 0.00% | 0         | 0.00%   |
| 100008.11-CP 100008.11 |                             |          |               |             |              |       |           |         |
| Precinct TSx           | 21                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Precinct OS            | 21                          | 1        | 6             | 4           | 0            | 0.00% | 1         | 25.00%  |
| Precinct VBM           | 21                          | 1        | 7             | 4           | 0            | 0.00% | 0         | 0.00%   |
| MB Precinct            | 21                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Provisional            | 21                          | 1        | 1             | 0           | 0            | -     | 0         | -       |
| Total                  | 21                          | 5        | 14            | 8           | 0            | 0.00% | 1         | 12.50%  |
| 100009.11-CP 100009.11 |                             |          |               |             |              |       |           |         |
| Precinct TSx           | 18                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Precinct OS            | 18                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Precinct VBM           | 18                          | 1        | 6             | 5           | 0            | 0.00% | 0         | 0.00%   |
| MB Precinct            | 18                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Provisional            | 18                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Total                  | 18                          | 5        | 6             | 5           | 0            | 0.00% | 0         | 0.00%   |
| 100012.11-CP 100012.11 |                             |          |               |             |              |       |           |         |
| Precinct TSx           | 28                          | 1        | 0             | 0           | 0            | -     | 0         | -       |
| Precinct OS            | 28                          | 1        | 4             | 2           | 0            | 0.00% | 0         | 0.00%   |
| Precinct VBM           | 28                          | 1        | 9             | 7           | 0            | 0.00% | 1         | 14.29%  |

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|------------------------|-----------------------------|----------|---------------|-------------|--------------|-----------|---|---------|
|                        | Reg. Voters                 | Vote For | Times Counted | Total Votes | MARC FELDMAN | JOHN HALE |   |         |
| MB Precinct            | 28                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Provisional            | 28                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Total                  | 28                          | 5        | 13            | 9           | 0            | 0.00%     | 1 | 11.11%  |
| 100013.11-CP 100013.11 |                             |          |               |             |              |           |   |         |
| Precinct TSx           | 30                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Precinct OS            | 30                          | 1        | 4             | 4           | 0            | 0.00%     | 0 | 0.00%   |
| Precinct VBM           | 30                          | 1        | 14            | 11          | 0            | 0.00%     | 0 | 0.00%   |
| MB Precinct            | 30                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Provisional            | 30                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Total                  | 30                          | 5        | 18            | 15          | 0            | 0.00%     | 0 | 0.00%   |
| 100014.11-CP 100014.11 |                             |          |               |             |              |           |   |         |
| Precinct TSx           | 21                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Precinct OS            | 21                          | 1        | 4             | 2           | 0            | 0.00%     | 0 | 0.00%   |
| Precinct VBM           | 21                          | 1        | 4             | 4           | 0            | 0.00%     | 0 | 0.00%   |
| MB Precinct            | 21                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Provisional            | 21                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Total                  | 21                          | 5        | 8             | 6           | 0            | 0.00%     | 0 | 0.00%   |
| 100022.11-CP 100022.11 |                             |          |               |             |              |           |   |         |
| Precinct TSx           | 12                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Precinct OS            | 12                          | 1        | 1             | 1           | 0            | 0.00%     | 0 | 0.00%   |
| Precinct VBM           | 12                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| MB Precinct            | 12                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Provisional            | 12                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Total                  | 12                          | 5        | 1             | 1           | 0            | 0.00%     | 0 | 0.00%   |
| 100023.11-CP 100023.11 |                             |          |               |             |              |           |   |         |
| Precinct TSx           | 15                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Precinct OS            | 15                          | 1        | 3             | 3           | 0            | 0.00%     | 0 | 0.00%   |
| Precinct VBM           | 15                          | 1        | 2             | 2           | 0            | 0.00%     | 0 | 0.00%   |
| MB Precinct            | 15                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Provisional            | 15                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Total                  | 15                          | 5        | 5             | 5           | 0            | 0.00%     | 0 | 0.00%   |
| 200003.12-CP 200003.12 |                             |          |               |             |              |           |   |         |
| Precinct TSx           | 15                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Precinct OS            | 15                          | 1        | 1             | 1           | 0            | 0.00%     | 0 | 0.00%   |
| Precinct VBM           | 15                          | 1        | 2             | 2           | 0            | 0.00%     | 0 | 0.00%   |
| MB Precinct            | 15                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Provisional            | 15                          | 1        | 1             | 1           | 0            | 0.00%     | 0 | 0.00%   |
| Total                  | 15                          | 5        | 4             | 4           | 0            | 0.00%     | 0 | 0.00%   |
| 200004.12-CP 200004.12 |                             |          |               |             |              |           |   |         |
| Precinct TSx           | 17                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Precinct OS            | 17                          | 1        | 5             | 5           | 0            | 0.00%     | 0 | 0.00%   |
| Precinct VBM           | 17                          | 1        | 5             | 5           | 0            | 0.00%     | 0 | 0.00%   |
| MB Precinct            | 17                          | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Provisional            | 17                          | 1        | 1             | 1           | 0            | 0.00%     | 1 | 100.00% |
| Total                  | 17                          | 5        | 11            | 11          | 0            | 0.00%     | 1 | 9.09%   |
| 200005.02-CP 200005.02 |                             |          |               |             |              |           |   |         |
| Precinct TSx           | 4                           | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Precinct OS            | 4                           | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Precinct VBM           | 4                           | 1        | 5             | 4           | 0            | 0.00%     | 2 | 50.00%  |
| MB Precinct            | 4                           | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Provisional            | 4                           | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Total                  | 4                           | 5        | 5             | 4           | 0            | 0.00%     | 2 | 50.00%  |
| 200006.12-CP 200006.12 |                             |          |               |             |              |           |   |         |
| Precinct TSx           | 9                           | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Precinct OS            | 9                           | 1        | 1             | 1           | 0            | 0.00%     | 0 | 0.00%   |
| Precinct VBM           | 9                           | 1        | 4             | 3           | 0            | 0.00%     | 0 | 0.00%   |
| MB Precinct            | 9                           | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Provisional            | 9                           | 1        | 0             | 0           | 0            | -         | 0 | -       |
| Total                  | 9                           | 5        | 5             | 4           | 0            | 0.00%     | 0 | 0.00%   |
| 200008.12-CP 200008.12 |                             |          |               |             |              |           |   |         |
| Precinct TSx           | 14                          | 1        | 0             | 0           | 0            | -         | 0 | -       |



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|                        | President Libertarian Party |          |               |             |              |       |           |        |  |
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|                        | Reg. Voters                 | Vote For | Times Counted | Total Votes | MARC FELDMAN |       | JOHN HALE |        |  |
| Precinct OS            | 14                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct VBM           | 14                          | 1        | 6             | 4           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 14                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 14                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Total                  | 14                          | 5        | 6             | 4           | 0            | 0.00% | 0         | 0.00%  |  |
| 200010.12-CP 200010.12 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 20                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 20                          | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%  |  |
| Precinct VBM           | 20                          | 1        | 4             | 3           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 20                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 20                          | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |  |
| Total                  | 20                          | 5        | 7             | 6           | 0            | 0.00% | 0         | 0.00%  |  |
| 200012.12-CP 200012.12 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 13                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 13                          | 1        | 3             | 2           | 0            | 0.00% | 0         | 0.00%  |  |
| Precinct VBM           | 13                          | 1        | 7             | 7           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 13                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 13                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Total                  | 13                          | 5        | 10            | 9           | 0            | 0.00% | 0         | 0.00%  |  |
| 200013.12-CP 200013.12 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 18                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 18                          | 1        | 7             | 6           | 0            | 0.00% | 0         | 0.00%  |  |
| Precinct VBM           | 18                          | 1        | 4             | 3           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 18                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 18                          | 1        | 1             | 0           | 0            | -     | 0         | -      |  |
| Total                  | 18                          | 5        | 12            | 9           | 0            | 0.00% | 0         | 0.00%  |  |
| 200014.12-CP 200014.12 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 17                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 17                          | 1        | 3             | 3           | 0            | 0.00% | 0         | 0.00%  |  |
| Precinct VBM           | 17                          | 1        | 5             | 5           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 17                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 17                          | 1        | 3             | 3           | 0            | 0.00% | 1         | 33.33% |  |
| Total                  | 17                          | 5        | 11            | 11          | 0            | 0.00% | 1         | 9.09%  |  |
| 200015.02-CP 200015.02 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 23                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 23                          | 1        | 3             | 3           | 0            | 0.00% | 0         | 0.00%  |  |
| Precinct VBM           | 23                          | 1        | 8             | 7           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 23                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 23                          | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%  |  |
| Total                  | 23                          | 5        | 13            | 12          | 0            | 0.00% | 0         | 0.00%  |  |
| 200016.12-CP 200016.12 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 2                           | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 2                           | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |  |
| Precinct VBM           | 2                           | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 2                           | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 2                           | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |  |
| Total                  | 2                           | 5        | 3             | 3           | 0            | 0.00% | 0         | 0.00%  |  |
| 200016.13-CP 200016.13 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 5                           | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 5                           | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |  |
| Precinct VBM           | 5                           | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 5                           | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 5                           | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Total                  | 5                           | 5        | 3             | 3           | 0            | 0.00% | 0         | 0.00%  |  |
| 200017.02-CP 200017.02 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 23                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 23                          | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%  |  |
| Precinct VBM           | 23                          | 1        | 12            | 11          | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 23                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 23                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Total                  | 23                          | 5        | 14            | 13          | 0            | 0.00% | 0         | 0.00%  |  |

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|                        | President Libertarian Party |          |               |             |              |       |           |       |
|------------------------|-----------------------------|----------|---------------|-------------|--------------|-------|-----------|-------|
|                        | Reg. Voters                 | Vote For | Times Counted | Total Votes | MARC FELDMAN |       | JOHN HALE |       |
| 200018.02-CP 200018.02 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 20                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 20                          | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00% |
| Precinct VBM           | 20                          | 1        | 4             | 3           | 0            | 0.00% | 0         | 0.00% |
| MB Precinct            | 20                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Provisional            | 20                          | 1        | 2             | 1           | 0            | 0.00% | 0         | 0.00% |
| Total                  | 20                          | 5        | 8             | 6           | 0            | 0.00% | 0         | 0.00% |
| 200020.02-CP 200020.02 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 14                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 14                          | 1        | 5             | 5           | 0            | 0.00% | 0         | 0.00% |
| Precinct VBM           | 14                          | 1        | 3             | 3           | 0            | 0.00% | 0         | 0.00% |
| MB Precinct            | 14                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Provisional            | 14                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 14                          | 5        | 8             | 8           | 0            | 0.00% | 0         | 0.00% |
| 200021.01-CP 200021.01 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 9                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 9                           | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00% |
| Precinct VBM           | 9                           | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00% |
| MB Precinct            | 9                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Provisional            | 9                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 9                           | 5        | 3             | 3           | 0            | 0.00% | 0         | 0.00% |
| 200021.02-CP 200021.02 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 18                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 18                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct VBM           | 18                          | 1        | 10            | 8           | 0            | 0.00% | 0         | 0.00% |
| MB Precinct            | 18                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Provisional            | 18                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 18                          | 5        | 10            | 8           | 0            | 0.00% | 0         | 0.00% |
| 200024.02-CP 200024.02 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 7                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 7                           | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00% |
| Precinct VBM           | 7                           | 1        | 4             | 4           | 0            | 0.00% | 0         | 0.00% |
| MB Precinct            | 7                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Provisional            | 7                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 7                           | 5        | 5             | 5           | 0            | 0.00% | 0         | 0.00% |
| 300001.04-CP 300001.04 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 5                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 5                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct VBM           | 5                           | 1        | 2             | 1           | 0            | 0.00% | 0         | 0.00% |
| MB Precinct            | 5                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Provisional            | 5                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 5                           | 5        | 2             | 1           | 0            | 0.00% | 0         | 0.00% |
| 300001.05-CP 300001.05 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 7                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 7                           | 1        | 2             | 1           | 0            | 0.00% | 0         | 0.00% |
| Precinct VBM           | 7                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| MB Precinct            | 7                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Provisional            | 7                           | 1        | 1             | 0           | 0            | -     | 0         | -     |
| Total                  | 7                           | 5        | 3             | 1           | 0            | 0.00% | 0         | 0.00% |
| 300003.05-CP 300003.05 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 15                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 15                          | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00% |
| Precinct VBM           | 15                          | 1        | 7             | 6           | 0            | 0.00% | 0         | 0.00% |
| MB Precinct            | 15                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Provisional            | 15                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 15                          | 5        | 8             | 7           | 0            | 0.00% | 0         | 0.00% |
| 300004.05-CP 300004.05 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 10                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 10                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct VBM           | 10                          | 1        | 4             | 4           | 0            | 0.00% | 0         | 0.00% |
| MB Precinct            | 10                          | 1        | 0             | 0           | 0            | -     | 0         | -     |

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|------------------------|-----------------------------|----------|---------------|-------------|--------------|--------|-----------|-------|
|                        | Reg. Voters                 | Vote For | Times Counted | Total Votes | MARC FELDMAN |        | JOHN HALE |       |
| Provisional            | 10                          | 1        | 1             | 0           | 0            | -      | 0         | -     |
| Total                  | 10                          | 5        | 5             | 4           | 0            | 0.00%  | 0         | 0.00% |
| 300005.05-CP 300005.05 |                             |          |               |             |              |        |           |       |
| Precinct TSx           | 10                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Precinct OS            | 10                          | 1        | 1             | 1           | 0            | 0.00%  | 0         | 0.00% |
| Precinct VBM           | 10                          | 1        | 4             | 3           | 0            | 0.00%  | 0         | 0.00% |
| MB Precinct            | 10                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Provisional            | 10                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Total                  | 10                          | 5        | 5             | 4           | 0            | 0.00%  | 0         | 0.00% |
| 300008.05-CP 300008.05 |                             |          |               |             |              |        |           |       |
| Precinct TSx           | 16                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Precinct OS            | 16                          | 1        | 1             | 1           | 0            | 0.00%  | 0         | 0.00% |
| Precinct VBM           | 16                          | 1        | 4             | 4           | 0            | 0.00%  | 0         | 0.00% |
| MB Precinct            | 16                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Provisional            | 16                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Total                  | 16                          | 5        | 5             | 5           | 0            | 0.00%  | 0         | 0.00% |
| 300009.04-CP 300009.04 |                             |          |               |             |              |        |           |       |
| Precinct TSx           | 11                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Precinct OS            | 11                          | 1        | 2             | 2           | 0            | 0.00%  | 0         | 0.00% |
| Precinct VBM           | 11                          | 1        | 2             | 2           | 0            | 0.00%  | 0         | 0.00% |
| MB Precinct            | 11                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Provisional            | 11                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Total                  | 11                          | 5        | 4             | 4           | 0            | 0.00%  | 0         | 0.00% |
| 300010.04-CP 300010.04 |                             |          |               |             |              |        |           |       |
| Precinct TSx           | 10                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Precinct OS            | 10                          | 1        | 1             | 0           | 0            | -      | 0         | -     |
| Precinct VBM           | 10                          | 1        | 2             | 2           | 0            | 0.00%  | 0         | 0.00% |
| MB Precinct            | 10                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Provisional            | 10                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Total                  | 10                          | 5        | 3             | 2           | 0            | 0.00%  | 0         | 0.00% |
| 300011.04-CP 300011.04 |                             |          |               |             |              |        |           |       |
| Precinct TSx           | 13                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Precinct OS            | 13                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Precinct VBM           | 13                          | 1        | 3             | 3           | 1            | 33.33% | 0         | 0.00% |
| MB Precinct            | 13                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Provisional            | 13                          | 1        | 1             | 1           | 0            | 0.00%  | 0         | 0.00% |
| Total                  | 13                          | 5        | 4             | 4           | 1            | 25.00% | 0         | 0.00% |
| 300013.05-CP 300013.05 |                             |          |               |             |              |        |           |       |
| Precinct TSx           | 22                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Precinct OS            | 22                          | 1        | 1             | 1           | 0            | 0.00%  | 0         | 0.00% |
| Precinct VBM           | 22                          | 1        | 3             | 3           | 1            | 33.33% | 0         | 0.00% |
| MB Precinct            | 22                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Provisional            | 22                          | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Total                  | 22                          | 5        | 4             | 4           | 1            | 25.00% | 0         | 0.00% |
| 300015.04-CP 300015.04 |                             |          |               |             |              |        |           |       |
| Precinct TSx           | 1                           | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Precinct OS            | 1                           | 1        | 1             | 1           | 0            | 0.00%  | 0         | 0.00% |
| Precinct VBM           | 1                           | 1        | 0             | 0           | 0            | -      | 0         | -     |
| MB Precinct            | 1                           | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Provisional            | 1                           | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Total                  | 1                           | 5        | 1             | 1           | 0            | 0.00%  | 0         | 0.00% |
| 300015.05-CP 300015.05 |                             |          |               |             |              |        |           |       |
| Precinct TSx           | 5                           | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Precinct OS            | 5                           | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Precinct VBM           | 5                           | 1        | 3             | 1           | 0            | 0.00%  | 0         | 0.00% |
| MB Precinct            | 5                           | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Provisional            | 5                           | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Total                  | 5                           | 5        | 3             | 1           | 0            | 0.00%  | 0         | 0.00% |
| 300017.05-CP 300017.05 |                             |          |               |             |              |        |           |       |
| Precinct TSx           | 8                           | 1        | 0             | 0           | 0            | -      | 0         | -     |
| Precinct OS            | 8                           | 1        | 3             | 3           | 0            | 0.00%  | 0         | 0.00% |



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|                        | Reg. Voters                 | Vote For | Times Counted | Total Votes | MARC FELDMAN |       | JOHN HALE |        |
| Precinct TSx           | 30                          | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS            | 30                          | 1        | 2             | 2           | 0            | 0.00% | 1         | 50.00% |
| Precinct VBM           | 30                          | 1        | 11            | 8           | 0            | 0.00% | 0         | 0.00%  |
| MB Precinct            | 30                          | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Provisional            | 30                          | 1        | 2             | 0           | 0            | -     | 0         | -      |
| Total                  | 30                          | 5        | 15            | 10          | 0            | 0.00% | 1         | 10.00% |
| 400004.14-CP 400004.14 |                             |          |               |             |              |       |           |        |
| Precinct TSx           | 13                          | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS            | 13                          | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |
| Precinct VBM           | 13                          | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%  |
| MB Precinct            | 13                          | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Provisional            | 13                          | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |
| Total                  | 13                          | 5        | 4             | 4           | 0            | 0.00% | 0         | 0.00%  |
| 400005.08-CP 400005.08 |                             |          |               |             |              |       |           |        |
| Precinct TSx           | 5                           | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS            | 5                           | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |
| Precinct VBM           | 5                           | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |
| MB Precinct            | 5                           | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Provisional            | 5                           | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Total                  | 5                           | 5        | 2             | 2           | 0            | 0.00% | 0         | 0.00%  |
| 400006.08-CP 400006.08 |                             |          |               |             |              |       |           |        |
| Precinct TSx           | 27                          | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS            | 27                          | 1        | 6             | 6           | 0            | 0.00% | 0         | 0.00%  |
| Precinct VBM           | 27                          | 1        | 12            | 10          | 0            | 0.00% | 0         | 0.00%  |
| MB Precinct            | 27                          | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Provisional            | 27                          | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Total                  | 27                          | 5        | 18            | 16          | 0            | 0.00% | 0         | 0.00%  |
| 400007.08-CP 400007.08 |                             |          |               |             |              |       |           |        |
| Precinct TSx           | 18                          | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS            | 18                          | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%  |
| Precinct VBM           | 18                          | 1        | 7             | 6           | 0            | 0.00% | 0         | 0.00%  |
| MB Precinct            | 18                          | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Provisional            | 18                          | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |
| Total                  | 18                          | 5        | 10            | 9           | 0            | 0.00% | 0         | 0.00%  |
| 400010.08-CP 400010.08 |                             |          |               |             |              |       |           |        |
| Precinct TSx           | 19                          | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS            | 19                          | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |
| Precinct VBM           | 19                          | 1        | 4             | 3           | 0            | 0.00% | 0         | 0.00%  |
| MB Precinct            | 19                          | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Provisional            | 19                          | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%  |
| Total                  | 19                          | 5        | 7             | 6           | 0            | 0.00% | 0         | 0.00%  |
| 400012.08-CP 400012.08 |                             |          |               |             |              |       |           |        |
| Precinct TSx           | 7                           | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS            | 7                           | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Precinct VBM           | 7                           | 1        | 1             | 0           | 0            | -     | 0         | -      |
| MB Precinct            | 7                           | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Provisional            | 7                           | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Total                  | 7                           | 5        | 1             | 0           | 0            | -     | 0         | -      |
| 400013.08-CP 400013.08 |                             |          |               |             |              |       |           |        |
| Precinct TSx           | 7                           | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS            | 7                           | 1        | 2             | 1           | 0            | 0.00% | 0         | 0.00%  |
| Precinct VBM           | 7                           | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |
| MB Precinct            | 7                           | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Provisional            | 7                           | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Total                  | 7                           | 5        | 3             | 2           | 0            | 0.00% | 0         | 0.00%  |
| 400014.08-CP 400014.08 |                             |          |               |             |              |       |           |        |
| Precinct TSx           | 15                          | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS            | 15                          | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%  |
| Precinct VBM           | 15                          | 1        | 6             | 6           | 0            | 0.00% | 0         | 0.00%  |
| MB Precinct            | 15                          | 1        | 0             | 0           | 0            | -     | 0         | -      |
| Provisional            | 15                          | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%  |

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|                        | Reg. Voters                 | Vote For | Times Counted | Total Votes | MARC FELDMAN |       | JOHN HALE |         |  |
| Total                  | 15                          | 5        | 10            | 10          | 0            | 0.00% | 0         | 0.00%   |  |
| 400017.08-CP 400017.08 |                             |          |               |             |              |       |           |         |  |
| Precinct TSx           | 15                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Precinct OS            | 15                          | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%   |  |
| Precinct VBM           | 15                          | 1        | 5             | 3           | 0            | 0.00% | 0         | 0.00%   |  |
| MB Precinct            | 15                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Provisional            | 15                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Total                  | 15                          | 5        | 7             | 5           | 0            | 0.00% | 0         | 0.00%   |  |
| 400018.08-CP 400018.08 |                             |          |               |             |              |       |           |         |  |
| Precinct TSx           | 8                           | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Precinct OS            | 8                           | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%   |  |
| Precinct VBM           | 8                           | 1        | 3             | 2           | 0            | 0.00% | 0         | 0.00%   |  |
| MB Precinct            | 8                           | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Provisional            | 8                           | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Total                  | 8                           | 5        | 5             | 4           | 0            | 0.00% | 0         | 0.00%   |  |
| 400020.08-CP 400020.08 |                             |          |               |             |              |       |           |         |  |
| Precinct TSx           | 12                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Precinct OS            | 12                          | 1        | 3             | 1           | 0            | 0.00% | 1         | 100.00% |  |
| Precinct VBM           | 12                          | 1        | 3             | 2           | 0            | 0.00% | 0         | 0.00%   |  |
| MB Precinct            | 12                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Provisional            | 12                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Total                  | 12                          | 5        | 6             | 3           | 0            | 0.00% | 1         | 33.33%  |  |
| 400021.07-CP 400021.07 |                             |          |               |             |              |       |           |         |  |
| Precinct TSx           | 13                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Precinct OS            | 13                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Precinct VBM           | 13                          | 1        | 9             | 8           | 0            | 0.00% | 0         | 0.00%   |  |
| MB Precinct            | 13                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Provisional            | 13                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Total                  | 13                          | 5        | 9             | 8           | 0            | 0.00% | 0         | 0.00%   |  |
| 500001.10-CP 500001.10 |                             |          |               |             |              |       |           |         |  |
| Precinct TSx           | 22                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Precinct OS            | 22                          | 1        | 4             | 4           | 0            | 0.00% | 0         | 0.00%   |  |
| Precinct VBM           | 22                          | 1        | 6             | 4           | 0            | 0.00% | 0         | 0.00%   |  |
| MB Precinct            | 22                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Provisional            | 22                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Total                  | 22                          | 5        | 10            | 8           | 0            | 0.00% | 0         | 0.00%   |  |
| 500002.10-CP 500002.10 |                             |          |               |             |              |       |           |         |  |
| Precinct TSx           | 15                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Precinct OS            | 15                          | 1        | 1             | 1           | 0            | 0.00% | 1         | 100.00% |  |
| Precinct VBM           | 15                          | 1        | 3             | 3           | 0            | 0.00% | 0         | 0.00%   |  |
| MB Precinct            | 15                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Provisional            | 15                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Total                  | 15                          | 5        | 4             | 4           | 0            | 0.00% | 1         | 25.00%  |  |
| 500003.10-CP 500003.10 |                             |          |               |             |              |       |           |         |  |
| Precinct TSx           | 8                           | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Precinct OS            | 8                           | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%   |  |
| Precinct VBM           | 8                           | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%   |  |
| MB Precinct            | 8                           | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Provisional            | 8                           | 1        | 1             | 0           | 0            | -     | 0         | -       |  |
| Total                  | 8                           | 5        | 3             | 2           | 0            | 0.00% | 0         | 0.00%   |  |
| 500004.10-CP 500004.10 |                             |          |               |             |              |       |           |         |  |
| Precinct TSx           | 16                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Precinct OS            | 16                          | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%   |  |
| Precinct VBM           | 16                          | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%   |  |
| MB Precinct            | 16                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Provisional            | 16                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Total                  | 16                          | 5        | 3             | 3           | 0            | 0.00% | 0         | 0.00%   |  |
| 500005.10-CP 500005.10 |                             |          |               |             |              |       |           |         |  |
| Precinct TSx           | 19                          | 1        | 0             | 0           | 0            | -     | 0         | -       |  |
| Precinct OS            | 19                          | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%   |  |
| Precinct VBM           | 19                          | 1        | 3             | 3           | 0            | 0.00% | 0         | 0.00%   |  |

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|------------------------|-----------------------------|----------|---------------|-------------|--------------|-------|-----------|--------|--|
|                        | Reg. Voters                 | Vote For | Times Counted | Total Votes | MARC FELDMAN |       | JOHN HALE |        |  |
| MB Precinct            | 19                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 19                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Total                  | 19                          | 5        | 5             | 5           | 0            | 0.00% | 0         | 0.00%  |  |
| 500006.10-CP 500006.10 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 22                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 22                          | 1        | 4             | 4           | 0            | 0.00% | 0         | 0.00%  |  |
| Precinct VBM           | 22                          | 1        | 4             | 4           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 22                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 22                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Total                  | 22                          | 5        | 8             | 8           | 0            | 0.00% | 0         | 0.00%  |  |
| 500007.10-CP 500007.10 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 14                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 14                          | 1        | 4             | 4           | 0            | 0.00% | 0         | 0.00%  |  |
| Precinct VBM           | 14                          | 1        | 3             | 3           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 14                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 14                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Total                  | 14                          | 5        | 7             | 7           | 0            | 0.00% | 0         | 0.00%  |  |
| 500009.10-CP 500009.10 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 17                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 17                          | 1        | 2             | 1           | 0            | 0.00% | 0         | 0.00%  |  |
| Precinct VBM           | 17                          | 1        | 2             | 2           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 17                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 17                          | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |  |
| Total                  | 17                          | 5        | 5             | 4           | 0            | 0.00% | 0         | 0.00%  |  |
| 500011.10-CP 500011.10 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 25                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 25                          | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |  |
| Precinct VBM           | 25                          | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 25                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 25                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Total                  | 25                          | 5        | 2             | 2           | 0            | 0.00% | 0         | 0.00%  |  |
| 500012.10-CP 500012.10 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 16                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 16                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct VBM           | 16                          | 1        | 3             | 2           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 16                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 16                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Total                  | 16                          | 5        | 3             | 2           | 0            | 0.00% | 0         | 0.00%  |  |
| 500015.10-CP 500015.10 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 8                           | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 8                           | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct VBM           | 8                           | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| MB Precinct            | 8                           | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 8                           | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |  |
| Total                  | 8                           | 5        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |  |
| 500020.10-CP 500020.10 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 24                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 24                          | 1        | 3             | 3           | 0            | 0.00% | 0         | 0.00%  |  |
| Precinct VBM           | 24                          | 1        | 5             | 5           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 24                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 24                          | 1        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |  |
| Total                  | 24                          | 5        | 9             | 9           | 0            | 0.00% | 0         | 0.00%  |  |
| 500021.10-CP 500021.10 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 17                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Precinct OS            | 17                          | 1        | 10            | 10          | 0            | 0.00% | 0         | 0.00%  |  |
| Precinct VBM           | 17                          | 1        | 6             | 3           | 0            | 0.00% | 0         | 0.00%  |  |
| MB Precinct            | 17                          | 1        | 0             | 0           | 0            | -     | 0         | -      |  |
| Provisional            | 17                          | 1        | 3             | 2           | 0            | 0.00% | 1         | 50.00% |  |
| Total                  | 17                          | 5        | 19            | 15          | 0            | 0.00% | 1         | 6.67%  |  |
| 95U201.01-MB 95U201.01 |                             |          |               |             |              |       |           |        |  |
| Precinct TSx           | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -      |  |

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|------------------------|-----------------------------|----------|---------------|-------------|--------------|-------|-----------|-------|
|                        | Reg. Voters                 | Vote For | Times Counted | Total Votes | MARC FELDMAN |       | JOHN HALE |       |
| Precinct OS            | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct VBM           | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| MB Precinct            | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Provisional            | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 0                           | 5        | 0             | 0           | 0            | -     | 0         | -     |
| 95U201.02-MB 95U201.02 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 4                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 4                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct VBM           | 4                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| MB Precinct            | 4                           | 1        | 2             | 1           | 0            | 0.00% | 0         | 0.00% |
| Provisional            | 4                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 4                           | 5        | 2             | 1           | 0            | 0.00% | 0         | 0.00% |
| 95U202.02-MB 95U202.02 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 10                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 10                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct VBM           | 10                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| MB Precinct            | 10                          | 1        | 4             | 4           | 0            | 0.00% | 0         | 0.00% |
| Provisional            | 10                          | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 10                          | 5        | 4             | 4           | 0            | 0.00% | 0         | 0.00% |
| 95U203.02-MB 95U203.02 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct VBM           | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| MB Precinct            | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Provisional            | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 0                           | 5        | 0             | 0           | 0            | -     | 0         | -     |
| 95U204.01-MB 95U204.01 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 8                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 8                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct VBM           | 8                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| MB Precinct            | 8                           | 1        | 5             | 4           | 0            | 0.00% | 0         | 0.00% |
| Provisional            | 8                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 8                           | 5        | 5             | 4           | 0            | 0.00% | 0         | 0.00% |
| 95U301.05-MB 95U301.05 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 5                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 5                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct VBM           | 5                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| MB Precinct            | 5                           | 1        | 3             | 2           | 0            | 0.00% | 0         | 0.00% |
| Provisional            | 5                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 5                           | 5        | 3             | 2           | 0            | 0.00% | 0         | 0.00% |
| 95U302.04-MB 95U302.04 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct VBM           | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| MB Precinct            | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Provisional            | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 0                           | 5        | 0             | 0           | 0            | -     | 0         | -     |
| 95U302.05-MB 95U302.05 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct VBM           | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| MB Precinct            | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Provisional            | 0                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 0                           | 5        | 0             | 0           | 0            | -     | 0         | -     |
| 95U303.03-MB 95U303.03 |                             |          |               |             |              |       |           |       |
| Precinct TSx           | 2                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct OS            | 2                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Precinct VBM           | 2                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| MB Precinct            | 2                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Provisional            | 2                           | 1        | 0             | 0           | 0            | -     | 0         | -     |
| Total                  | 2                           | 5        | 0             | 0           | 0            | -     | 0         | -     |



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|------------------------|-----------------------------|----------|---------------|-------------|--------------|-----------|---|-------|
|                        | Reg. Voters                 | Vote For | Times Counted | Total Votes | MARC FELDMAN | JOHN HALE |   |       |
| 95U303.05-MB 95U303.05 |                             |          |               |             |              |           |   |       |
| Precinct TSx           | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct OS            | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct VBM           | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| MB Precinct            | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Provisional            | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Total                  | 0                           | 5        | 0             | 0           | 0            | -         | 0 | -     |
| 95U304.05-MB 95U304.05 |                             |          |               |             |              |           |   |       |
| Precinct TSx           | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct OS            | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct VBM           | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| MB Precinct            | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Provisional            | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Total                  | 0                           | 5        | 0             | 0           | 0            | -         | 0 | -     |
| 95U305.05-MB 95U305.05 |                             |          |               |             |              |           |   |       |
| Precinct TSx           | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct OS            | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct VBM           | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| MB Precinct            | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Provisional            | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Total                  | 0                           | 5        | 0             | 0           | 0            | -         | 0 | -     |
| 95U401.08-MB 95U401.08 |                             |          |               |             |              |           |   |       |
| Precinct TSx           | 1                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct OS            | 1                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct VBM           | 1                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| MB Precinct            | 1                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Provisional            | 1                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Total                  | 1                           | 5        | 0             | 0           | 0            | -         | 0 | -     |
| 95U402.07-MB 95U402.07 |                             |          |               |             |              |           |   |       |
| Precinct TSx           | 3                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct OS            | 3                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct VBM           | 3                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| MB Precinct            | 3                           | 1        | 2             | 2           | 0            | 0.00%     | 0 | 0.00% |
| Provisional            | 3                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Total                  | 3                           | 5        | 2             | 2           | 0            | 0.00%     | 0 | 0.00% |
| 95U402.08-MB 95U402.08 |                             |          |               |             |              |           |   |       |
| Precinct TSx           | 8                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct OS            | 8                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct VBM           | 8                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| MB Precinct            | 8                           | 1        | 3             | 3           | 0            | 0.00%     | 0 | 0.00% |
| Provisional            | 8                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Total                  | 8                           | 5        | 3             | 3           | 0            | 0.00%     | 0 | 0.00% |
| 95U403.08-MB 95U403.08 |                             |          |               |             |              |           |   |       |
| Precinct TSx           | 17                          | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct OS            | 17                          | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct VBM           | 17                          | 1        | 0             | 0           | 0            | -         | 0 | -     |
| MB Precinct            | 17                          | 1        | 12            | 9           | 0            | 0.00%     | 0 | 0.00% |
| Provisional            | 17                          | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Total                  | 17                          | 5        | 12            | 9           | 0            | 0.00%     | 0 | 0.00% |
| 95U404.06-MB 95U404.06 |                             |          |               |             |              |           |   |       |
| Precinct TSx           | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct OS            | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct VBM           | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| MB Precinct            | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Provisional            | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Total                  | 0                           | 5        | 0             | 0           | 0            | -         | 0 | -     |
| 95U404.08-MB 95U404.08 |                             |          |               |             |              |           |   |       |
| Precinct TSx           | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct OS            | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| Precinct VBM           | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |
| MB Precinct            | 0                           | 1        | 0             | 0           | 0            | -         | 0 | -     |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                     | President Libertarian Party |            |               |             |              |              |           |              |
|-------------------------------------|-----------------------------|------------|---------------|-------------|--------------|--------------|-----------|--------------|
|                                     | Reg. Voters                 | Vote For   | Times Counted | Total Votes | MARC FELDMAN |              | JOHN HALE |              |
| Precinct TSx                        | 1222                        | 102        | 0             | 0           | 0            | -            | 0         | -            |
| Precinct OS                         | 1222                        | 102        | 150           | 132         | 0            | 0.00%        | 5         | 3.79%        |
| Precinct VBM                        | 1222                        | 102        | 352           | 290         | 2            | 0.69%        | 4         | 1.38%        |
| MB Precinct                         | 1222                        | 102        | 38            | 31          | 0            | 0.00%        | 0         | 0.00%        |
| Provisional                         | 1222                        | 102        | 37            | 26          | 0            | 0.00%        | 4         | 15.38%       |
| <b>Total</b>                        | <b>1222</b>                 | <b>510</b> | <b>577</b>    | <b>479</b>  | <b>2</b>     | <b>0.42%</b> | <b>13</b> | <b>2.71%</b> |
| <b>UNITED STATES SENATE</b>         |                             |            |               |             |              |              |           |              |
| <b>UNITED STATES SENATE</b>         |                             |            |               |             |              |              |           |              |
| Precinct TSx                        | 1222                        | 102        | 0             | 0           | 0            | -            | 0         | -            |
| Precinct OS                         | 1222                        | 102        | 150           | 132         | 0            | 0.00%        | 5         | 3.79%        |
| Precinct VBM                        | 1222                        | 102        | 352           | 290         | 2            | 0.69%        | 4         | 1.38%        |
| MB Precinct                         | 1222                        | 102        | 38            | 31          | 0            | 0.00%        | 0         | 0.00%        |
| Provisional                         | 1222                        | 102        | 37            | 26          | 0            | 0.00%        | 4         | 15.38%       |
| <b>Total</b>                        | <b>1222</b>                 | <b>510</b> | <b>577</b>    | <b>479</b>  | <b>2</b>     | <b>0.42%</b> | <b>13</b> | <b>2.71%</b> |
| <b>Total</b>                        |                             |            |               |             |              |              |           |              |
| Precinct TSx                        | 1222                        | 102        | 0             | 0           | 0            | -            | 0         | -            |
| Precinct OS                         | 1222                        | 102        | 150           | 132         | 0            | 0.00%        | 5         | 3.79%        |
| Precinct VBM                        | 1222                        | 102        | 352           | 290         | 2            | 0.69%        | 4         | 1.38%        |
| MB Precinct                         | 1222                        | 102        | 38            | 31          | 0            | 0.00%        | 0         | 0.00%        |
| Provisional                         | 1222                        | 102        | 37            | 26          | 0            | 0.00%        | 4         | 15.38%       |
| <b>Total</b>                        | <b>1222</b>                 | <b>510</b> | <b>577</b>    | <b>479</b>  | <b>2</b>     | <b>0.42%</b> | <b>13</b> | <b>2.71%</b> |
| <b>Total</b>                        |                             |            |               |             |              |              |           |              |
| <b>UNITED STATES REPRESENTATIVE</b> |                             |            |               |             |              |              |           |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                             |            |               |             |              |              |           |              |
| Precinct TSx                        | 1222                        | 102        | 0             | 0           | 0            | -            | 0         | -            |
| Precinct OS                         | 1222                        | 102        | 150           | 132         | 0            | 0.00%        | 5         | 3.79%        |
| Precinct VBM                        | 1222                        | 102        | 352           | 290         | 2            | 0.69%        | 4         | 1.38%        |
| MB Precinct                         | 1222                        | 102        | 38            | 31          | 0            | 0.00%        | 0         | 0.00%        |
| Provisional                         | 1222                        | 102        | 37            | 26          | 0            | 0.00%        | 4         | 15.38%       |
| <b>Total</b>                        | <b>1222</b>                 | <b>510</b> | <b>577</b>    | <b>479</b>  | <b>2</b>     | <b>0.42%</b> | <b>13</b> | <b>2.71%</b> |
| <b>Total</b>                        |                             |            |               |             |              |              |           |              |
| Precinct TSx                        | 1222                        | 102        | 0             | 0           | 0            | -            | 0         | -            |
| Precinct OS                         | 1222                        | 102        | 150           | 132         | 0            | 0.00%        | 5         | 3.79%        |
| Precinct VBM                        | 1222                        | 102        | 352           | 290         | 2            | 0.69%        | 4         | 1.38%        |
| MB Precinct                         | 1222                        | 102        | 38            | 31          | 0            | 0.00%        | 0         | 0.00%        |
| Provisional                         | 1222                        | 102        | 37            | 26          | 0            | 0.00%        | 4         | 15.38%       |
| <b>Total</b>                        | <b>1222</b>                 | <b>510</b> | <b>577</b>    | <b>479</b>  | <b>2</b>     | <b>0.42%</b> | <b>13</b> | <b>2.71%</b> |
| <b>Total</b>                        |                             |            |               |             |              |              |           |              |
| <b>STATE SENATE</b>                 |                             |            |               |             |              |              |           |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                             |            |               |             |              |              |           |              |
| Precinct TSx                        | 1222                        | 102        | 0             | 0           | 0            | -            | 0         | -            |
| Precinct OS                         | 1222                        | 102        | 150           | 132         | 0            | 0.00%        | 5         | 3.79%        |
| Precinct VBM                        | 1222                        | 102        | 352           | 290         | 2            | 0.69%        | 4         | 1.38%        |
| MB Precinct                         | 1222                        | 102        | 38            | 31          | 0            | 0.00%        | 0         | 0.00%        |
| Provisional                         | 1222                        | 102        | 37            | 26          | 0            | 0.00%        | 4         | 15.38%       |
| <b>Total</b>                        | <b>1222</b>                 | <b>510</b> | <b>577</b>    | <b>479</b>  | <b>2</b>     | <b>0.42%</b> | <b>13</b> | <b>2.71%</b> |
| <b>Total</b>                        |                             |            |               |             |              |              |           |              |
| Precinct TSx                        | 1222                        | 102        | 0             | 0           | 0            | -            | 0         | -            |
| Precinct OS                         | 1222                        | 102        | 150           | 132         | 0            | 0.00%        | 5         | 3.79%        |
| Precinct VBM                        | 1222                        | 102        | 352           | 290         | 2            | 0.69%        | 4         | 1.38%        |
| MB Precinct                         | 1222                        | 102        | 38            | 31          | 0            | 0.00%        | 0         | 0.00%        |
| Provisional                         | 1222                        | 102        | 37            | 26          | 0            | 0.00%        | 4         | 15.38%       |
| <b>Total</b>                        | <b>1222</b>                 | <b>510</b> | <b>577</b>    | <b>479</b>  | <b>2</b>     | <b>0.42%</b> | <b>13</b> | <b>2.71%</b> |
| <b>Total</b>                        |                             |            |               |             |              |              |           |              |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                             |            |               |             |              |              |           |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                             |            |               |             |              |              |           |              |
| Precinct TSx                        | 805                         | 75         | 0             | 0           | 0            | -            | 0         | -            |
| Precinct OS                         | 805                         | 75         | 91            | 84          | 0            | 0.00%        | 4         | 4.76%        |
| Precinct VBM                        | 805                         | 75         | 226           | 187         | 2            | 1.07%        | 2         | 1.07%        |
| MB Precinct                         | 805                         | 75         | 35            | 29          | 0            | 0.00%        | 0         | 0.00%        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                   | President Libertarian Party |          |               |             |              |       |           |        |
|-----------------------------------|-----------------------------|----------|---------------|-------------|--------------|-------|-----------|--------|
|                                   | Reg. Voters                 | Vote For | Times Counted | Total Votes | MARC FELDMAN |       | JOHN HALE |        |
| Provisional                       | 805                         | 75       | 23            | 14          | 0            | 0.00% | 1         | 7.14%  |
| Total                             | 805                         | 375      | 375           | 314         | 2            | 0.64% | 7         | 2.23%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |                             |          |               |             |              |       |           |        |
| Precinct TSx                      | 417                         | 27       | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS                       | 417                         | 27       | 59            | 48          | 0            | 0.00% | 1         | 2.08%  |
| Precinct VBM                      | 417                         | 27       | 126           | 103         | 0            | 0.00% | 2         | 1.94%  |
| MB Precinct                       | 417                         | 27       | 3             | 2           | 0            | 0.00% | 0         | 0.00%  |
| Provisional                       | 417                         | 27       | 14            | 12          | 0            | 0.00% | 3         | 25.00% |
| Total                             | 417                         | 135      | 202           | 165         | 0            | 0.00% | 6         | 3.64%  |
| <b>Total</b>                      |                             |          |               |             |              |       |           |        |
| Precinct TSx                      | 1222                        | 102      | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS                       | 1222                        | 102      | 150           | 132         | 0            | 0.00% | 5         | 3.79%  |
| Precinct VBM                      | 1222                        | 102      | 352           | 290         | 2            | 0.69% | 4         | 1.38%  |
| MB Precinct                       | 1222                        | 102      | 38            | 31          | 0            | 0.00% | 0         | 0.00%  |
| Provisional                       | 1222                        | 102      | 37            | 26          | 0            | 0.00% | 4         | 15.38% |
| Total                             | 1222                        | 510      | 577           | 479         | 2            | 0.42% | 13        | 2.71%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |                             |          |               |             |              |       |           |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                             |          |               |             |              |       |           |        |
| Precinct TSx                      | 245                         | 14       | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS                       | 245                         | 14       | 32            | 23          | 0            | 0.00% | 1         | 4.35%  |
| Precinct VBM                      | 245                         | 14       | 79            | 62          | 0            | 0.00% | 2         | 3.23%  |
| MB Precinct                       | 245                         | 14       | 2             | 2           | 0            | 0.00% | 0         | 0.00%  |
| Provisional                       | 245                         | 14       | 4             | 3           | 0            | 0.00% | 1         | 33.33% |
| Total                             | 245                         | 70       | 117           | 90          | 0            | 0.00% | 4         | 4.44%  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                             |          |               |             |              |       |           |        |
| Precinct TSx                      | 270                         | 23       | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS                       | 270                         | 23       | 38            | 36          | 0            | 0.00% | 0         | 0.00%  |
| Precinct VBM                      | 270                         | 23       | 88            | 77          | 0            | 0.00% | 2         | 2.60%  |
| MB Precinct                       | 270                         | 23       | 11            | 9           | 0            | 0.00% | 0         | 0.00%  |
| Provisional                       | 270                         | 23       | 12            | 10          | 0            | 0.00% | 2         | 20.00% |
| Total                             | 270                         | 115      | 149           | 132         | 0            | 0.00% | 4         | 3.03%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                             |          |               |             |              |       |           |        |
| Precinct TSx                      | 209                         | 26       | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS                       | 209                         | 26       | 21            | 18          | 0            | 0.00% | 1         | 5.56%  |
| Precinct VBM                      | 209                         | 26       | 67            | 53          | 2            | 3.77% | 0         | 0.00%  |
| MB Precinct                       | 209                         | 26       | 3             | 2           | 0            | 0.00% | 0         | 0.00%  |
| Provisional                       | 209                         | 26       | 5             | 1           | 0            | 0.00% | 0         | 0.00%  |
| Total                             | 209                         | 130      | 96            | 74          | 2            | 2.70% | 1         | 1.35%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                             |          |               |             |              |       |           |        |
| Precinct TSx                      | 265                         | 22       | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS                       | 265                         | 22       | 26            | 23          | 0            | 0.00% | 2         | 8.70%  |
| Precinct VBM                      | 265                         | 22       | 79            | 65          | 0            | 0.00% | 0         | 0.00%  |
| MB Precinct                       | 265                         | 22       | 18            | 14          | 0            | 0.00% | 0         | 0.00%  |
| Provisional                       | 265                         | 22       | 9             | 7           | 0            | 0.00% | 0         | 0.00%  |
| Total                             | 265                         | 110      | 132           | 109         | 0            | 0.00% | 2         | 1.83%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                             |          |               |             |              |       |           |        |
| Precinct TSx                      | 233                         | 17       | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS                       | 233                         | 17       | 33            | 32          | 0            | 0.00% | 1         | 3.13%  |
| Precinct VBM                      | 233                         | 17       | 39            | 33          | 0            | 0.00% | 0         | 0.00%  |
| MB Precinct                       | 233                         | 17       | 4             | 4           | 0            | 0.00% | 0         | 0.00%  |
| Provisional                       | 233                         | 17       | 7             | 5           | 0            | 0.00% | 1         | 20.00% |
| Total                             | 233                         | 85       | 83            | 74          | 0            | 0.00% | 2         | 2.70%  |
| <b>Total</b>                      |                             |          |               |             |              |       |           |        |
| Precinct TSx                      | 1222                        | 102      | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS                       | 1222                        | 102      | 150           | 132         | 0            | 0.00% | 5         | 3.79%  |
| Precinct VBM                      | 1222                        | 102      | 352           | 290         | 2            | 0.69% | 4         | 1.38%  |
| MB Precinct                       | 1222                        | 102      | 38            | 31          | 0            | 0.00% | 0         | 0.00%  |
| Provisional                       | 1222                        | 102      | 37            | 26          | 0            | 0.00% | 4         | 15.38% |
| Total                             | 1222                        | 510      | 577           | 479         | 2            | 0.42% | 13        | 2.71%  |

**COUNTY OF EL DORADO**  
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|                                 | President Libertarian Party |          |               |             |              |       |           |        |
|---------------------------------|-----------------------------|----------|---------------|-------------|--------------|-------|-----------|--------|
|                                 | Reg. Voters                 | Vote For | Times Counted | Total Votes | MARC FELDMAN |       | JOHN HALE |        |
| <b>CITIES</b>                   |                             |          |               |             |              |       |           |        |
| <b>CITY OF PLACERVILLE</b>      |                             |          |               |             |              |       |           |        |
| Precinct TSx                    | 47                          | 5        | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS                     | 47                          | 5        | 4             | 3           | 0            | 0.00% | 0         | 0.00%  |
| Precinct VBM                    | 47                          | 5        | 17            | 14          | 0            | 0.00% | 0         | 0.00%  |
| MB Precinct                     | 47                          | 5        | 0             | 0           | 0            | -     | 0         | -      |
| Provisional                     | 47                          | 5        | 2             | 0           | 0            | -     | 0         | -      |
| Total                           | 47                          | 25       | 23            | 17          | 0            | 0.00% | 0         | 0.00%  |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                             |          |               |             |              |       |           |        |
| Precinct TSx                    | 128                         | 7        | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS                     | 128                         | 7        | 15            | 14          | 0            | 0.00% | 1         | 7.14%  |
| Precinct VBM                    | 128                         | 7        | 18            | 18          | 0            | 0.00% | 0         | 0.00%  |
| MB Precinct                     | 128                         | 7        | 0             | 0           | 0            | -     | 0         | -      |
| Provisional                     | 128                         | 7        | 1             | 1           | 0            | 0.00% | 0         | 0.00%  |
| Total                           | 128                         | 35       | 34            | 33          | 0            | 0.00% | 1         | 3.03%  |
| <b>UNINCORPORATED AREA</b>      |                             |          |               |             |              |       |           |        |
| Precinct TSx                    | 1047                        | 90       | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS                     | 1047                        | 90       | 131           | 115         | 0            | 0.00% | 4         | 3.48%  |
| Precinct VBM                    | 1047                        | 90       | 317           | 258         | 2            | 0.78% | 4         | 1.55%  |
| MB Precinct                     | 1047                        | 90       | 38            | 31          | 0            | 0.00% | 0         | 0.00%  |
| Provisional                     | 1047                        | 90       | 34            | 25          | 0            | 0.00% | 4         | 16.00% |
| Total                           | 1047                        | 450      | 520           | 429         | 2            | 0.47% | 12        | 2.80%  |
| <b>Total</b>                    |                             |          |               |             |              |       |           |        |
| Precinct TSx                    | 1222                        | 102      | 0             | 0           | 0            | -     | 0         | -      |
| Precinct OS                     | 1222                        | 102      | 150           | 132         | 0            | 0.00% | 5         | 3.79%  |
| Precinct VBM                    | 1222                        | 102      | 352           | 290         | 2            | 0.69% | 4         | 1.38%  |
| MB Precinct                     | 1222                        | 102      | 38            | 31          | 0            | 0.00% | 0         | 0.00%  |
| Provisional                     | 1222                        | 102      | 37            | 26          | 0            | 0.00% | 4         | 15.38% |
| Total                           | 1222                        | 510      | 577           | 479         | 2            | 0.42% | 13        | 2.71%  |

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|------------------------|-----------------------------|-------|--------------|---------|--------------|-------|--------------|--------|-----------------|-------|
|                        | CECIL INCE                  |       | GARY JOHNSON |         | STEVE KERBEL |       | JOHN MCAFFEE |        | DARRYL W. PERRY |       |
| Jurisdiction Wide      |                             |       |              |         |              |       |              |        |                 |       |
| 100001.11-CP 100001.11 |                             |       |              |         |              |       |              |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS            | 0                           | 0.00% | 3            | 100.00% | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 5            | 100.00% | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Provisional            | 0                           | 0.00% | 0            | 0.00%   | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Total                  | 0                           | 0.00% | 8            | 88.89%  | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| 100002.11-CP 100002.11 |                             |       |              |         |              |       |              |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct VBM           | 0                           | 0.00% | 3            | 100.00% | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Provisional            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Total                  | 0                           | 0.00% | 3            | 100.00% | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| 100003.11-CP 100003.11 |                             |       |              |         |              |       |              |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS            | 0                           | 0.00% | 1            | 100.00% | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 3            | 42.86%  | 0            | 0.00% | 1            | 14.29% | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Provisional            | 0                           | 0.00% | 1            | 50.00%  | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Total                  | 0                           | 0.00% | 5            | 50.00%  | 0            | 0.00% | 1            | 10.00% | 0               | 0.00% |
| 100004.11-CP 100004.11 |                             |       |              |         |              |       |              |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct VBM           | 0                           | 0.00% | 0            | 0.00%   | 0            | 0.00% | 1            | 50.00% | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Provisional            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Total                  | 0                           | 0.00% | 0            | 0.00%   | 0            | 0.00% | 1            | 50.00% | 0               | 0.00% |
| 100005.11-CP 100005.11 |                             |       |              |         |              |       |              |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS            | 0                           | 0.00% | 3            | 100.00% | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0            | 0.00%   | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Provisional            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Total                  | 0                           | 0.00% | 3            | 50.00%  | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| 100006.11-CP 100006.11 |                             |       |              |         |              |       |              |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct VBM           | 0                           | 0.00% | 4            | 44.44%  | 0            | 0.00% | 1            | 11.11% | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Provisional            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Total                  | 0                           | 0.00% | 4            | 44.44%  | 0            | 0.00% | 1            | 11.11% | 0               | 0.00% |
| 100008.11-CP 100008.11 |                             |       |              |         |              |       |              |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS            | 0                           | 0.00% | 0            | 0.00%   | 0            | 0.00% | 1            | 25.00% | 0               | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 1            | 25.00%  | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Provisional            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Total                  | 0                           | 0.00% | 1            | 12.50%  | 0            | 0.00% | 1            | 12.50% | 0               | 0.00% |
| 100009.11-CP 100009.11 |                             |       |              |         |              |       |              |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct VBM           | 0                           | 0.00% | 5            | 100.00% | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Provisional            | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Total                  | 0                           | 0.00% | 5            | 100.00% | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| 100012.11-CP 100012.11 |                             |       |              |         |              |       |              |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS            | 0                           | 0.00% | 1            | 50.00%  | 0            | 0.00% | 1            | 50.00% | 0               | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 4            | 57.14%  | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | CECIL INCE | GARY JOHNSON | STEVE KERBEL | JOHN MCAFEE | DARRYL W. PERRY |
|------------------------|------------|--------------|--------------|-------------|-----------------|
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 5 55.56%     | 0 0.00%      | 1 11.11%    | 0 0.00%         |
| 100013.11-CP 100013.11 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 1 25.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 8 72.73%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 9 60.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 100014.11-CP 100014.11 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 0 0.00%      | 0 0.00%      | 1 50.00%    | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 2 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 2 33.33%     | 0 0.00%      | 1 16.67%    | 0 0.00%         |
| 100022.11-CP 100022.11 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 0 0.00%      | 0 0.00%      | 1 100.00%   | 0 0.00%         |
| Precinct VBM           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 0 0.00%      | 0 0.00%      | 1 100.00%   | 0 0.00%         |
| 100023.11-CP 100023.11 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 3 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 3 60.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 200003.12-CP 200003.12 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 1 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Total                  | 0 0.00%    | 1 25.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 200004.12-CP 200004.12 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 3 60.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Total                  | 0 0.00%    | 3 27.27%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 200005.02-CP 200005.02 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct VBM           | 0 0.00%    | 2 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 2 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 200006.12-CP 200006.12 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 1 33.33%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 2 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 200008.12-CP 200008.12 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |

**COUNTY OF EL DORADO**  
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|                        | CECIL INCE | GARY JOHNSON | STEVE KERBEL | JOHN MCAFEE | DARRYL W. PERRY |
|------------------------|------------|--------------|--------------|-------------|-----------------|
| Precinct OS            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct VBM           | 1 25.00%   | 1 25.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 1 25.00%   | 1 25.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 200010.12-CP 200010.12 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 2 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 1 33.33%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Total                  | 0 0.00%    | 3 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 200012.12-CP 200012.12 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 2 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 4 57.14%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 6 66.67%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 200013.12-CP 200013.12 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 3 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 2 66.67%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 5 55.56%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 200014.12-CP 200014.12 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 2 66.67%     | 0 0.00%      | 1 33.33%    | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 1 20.00%     | 0 0.00%      | 1 20.00%    | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Total                  | 0 0.00%    | 3 27.27%     | 0 0.00%      | 2 18.18%    | 0 0.00%         |
| 200015.02-CP 200015.02 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 1 33.33%   | 0 0.00%      | 0 0.00%      | 1 33.33%    | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 5 71.43%     | 0 0.00%      | 1 14.29%    | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 0.00%    | 1 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Total                  | 1 8.33%    | 6 50.00%     | 0 0.00%      | 2 16.67%    | 0 0.00%         |
| 200016.12-CP 200016.12 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Total                  | 0 0.00%    | 3 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 200016.13-CP 200016.13 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 1 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 1 33.33%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 200017.02-CP 200017.02 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 0 0.00%      | 0 0.00%      | 1 50.00%    | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 5 45.45%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 5 38.46%     | 0 0.00%      | 1 7.69%     | 0 0.00%         |



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|                        | CECIL INCE | GARY JOHNSON | STEVE KERBEL | JOHN MCAFEE | DARRYL W. PERRY |
|------------------------|------------|--------------|--------------|-------------|-----------------|
| 200018.02-CP 200018.02 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 1 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 3 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Total                  | 0 0.00%    | 5 83.33%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 200020.02-CP 200020.02 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 1 20.00%     | 1 20.00%     | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 1 33.33%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 2 25.00%     | 1 12.50%     | 0 0.00%     | 0 0.00%         |
| 200021.01-CP 200021.01 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 2 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 2 66.67%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 200021.02-CP 200021.02 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct VBM           | 0 0.00%    | 3 37.50%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 3 37.50%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 200024.02-CP 200024.02 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 0 0.00%      | 0 0.00%      | 1 25.00%    | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 1 20.00%     | 0 0.00%      | 1 20.00%    | 0 0.00%         |
| 300001.04-CP 300001.04 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct VBM           | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 300001.05-CP 300001.05 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 300003.05-CP 300003.05 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 2 33.33%     | 0 0.00%      | 2 33.33%    | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 2 28.57%     | 0 0.00%      | 2 28.57%    | 0 0.00%         |
| 300004.05-CP 300004.05 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct VBM           | 0 0.00%    | 2 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |

**COUNTY OF EL DORADO**  
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|                        | CECIL INCE | GARY JOHNSON | STEVE KERBEL | JOHN MCAFEE | DARRYL W. PERRY |
|------------------------|------------|--------------|--------------|-------------|-----------------|
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 2 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 300005.05-CP 300005.05 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 2 66.67%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 3 75.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 300008.05-CP 300008.05 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 0 0.00%      | 0 0.00%      | 1 25.00%    | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 1 20.00%     | 0 0.00%      | 1 20.00%    | 0 0.00%         |
| 300009.04-CP 300009.04 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 1 50.00%   | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 1 25.00%   | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 300010.04-CP 300010.04 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct VBM           | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 300011.04-CP 300011.04 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct VBM           | 0 0.00%    | 1 33.33%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Total                  | 0 0.00%    | 1 25.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 300013.05-CP 300013.05 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 1 33.33%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 2 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 300015.04-CP 300015.04 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 300015.05-CP 300015.05 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct VBM           | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Provisional            | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Total                  | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 300017.05-CP 300017.05 |            |              |              |             |                 |
| Precinct TSx           | 0 -        | 0 -          | 0 -          | 0 -         | 0 -             |
| Precinct OS            | 0 0.00%    | 1 33.33%     | 0 0.00%      | 1 33.33%    | 0 0.00%         |

**COUNTY OF EL DORADO**  
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|                        | President Libertarian Party |       |              |         |              |       |             |        |                 |       |
|------------------------|-----------------------------|-------|--------------|---------|--------------|-------|-------------|--------|-----------------|-------|
|                        | CECIL INCE                  |       | GARY JOHNSON |         | STEVE KERBEL |       | JOHN MCAFEE |        | DARRYL W. PERRY |       |
| Precinct VBM           | 0                           | 0.00% | 0            | 0.00%   | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Provisional            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Total                  | 0                           | 0.00% | 1            | 25.00%  | 0            | 0.00% | 1           | 25.00% | 0               | 0.00% |
| 300018.04-CP 300018.04 |                             |       |              |         |              |       |             |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Precinct OS            | 0                           | 0.00% | 1            | 50.00%  | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 1            | 50.00%  | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Provisional            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Total                  | 0                           | 0.00% | 2            | 50.00%  | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| 300020.04-CP 300020.04 |                             |       |              |         |              |       |             |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Precinct OS            | 0                           | 0.00% | 1            | 100.00% | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 1            | 16.67%  | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Provisional            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Total                  | 0                           | 0.00% | 2            | 28.57%  | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| 300021.03-CP 300021.03 |                             |       |              |         |              |       |             |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Precinct OS            | 0                           | 0.00% | 1            | 50.00%  | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 3            | 100.00% | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Provisional            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Total                  | 0                           | 0.00% | 4            | 80.00%  | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| 300022.05-CP 300022.05 |                             |       |              |         |              |       |             |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Precinct OS            | 0                           | 0.00% | 1            | 100.00% | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 2            | 50.00%  | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Provisional            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Total                  | 0                           | 0.00% | 3            | 60.00%  | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| 300023.03-CP 300023.03 |                             |       |              |         |              |       |             |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Precinct OS            | 0                           | 0.00% | 1            | 100.00% | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 3            | 50.00%  | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Provisional            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Total                  | 0                           | 0.00% | 4            | 57.14%  | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| 300023.05-CP 300023.05 |                             |       |              |         |              |       |             |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Precinct VBM           | 0                           | 0.00% | 1            | 50.00%  | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Provisional            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Total                  | 0                           | 0.00% | 1            | 50.00%  | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| 400001.14-CP 400001.14 |                             |       |              |         |              |       |             |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Precinct OS            | 0                           | 0.00% | 1            | 50.00%  | 0            | 0.00% | 1           | 50.00% | 0               | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 2            | 50.00%  | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Provisional            | 0                           | 0.00% | 0            | 0.00%   | 0            | 0.00% | 0           | 0.00%  | 0               | 0.00% |
| Total                  | 0                           | 0.00% | 3            | 42.86%  | 0            | 0.00% | 1           | 14.29% | 0               | 0.00% |
| 400002.08-CP 400002.08 |                             |       |              |         |              |       |             |        |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Precinct VBM           | 0                           | 0.00% | 3            | 33.33%  | 0            | 0.00% | 2           | 22.22% | 0               | 0.00% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Provisional            | 0                           | -     | 0            | -       | 0            | -     | 0           | -      | 0               | -     |
| Total                  | 0                           | 0.00% | 3            | 33.33%  | 0            | 0.00% | 2           | 22.22% | 0               | 0.00% |
| 400003.08-CP 400003.08 |                             |       |              |         |              |       |             |        |                 |       |

**COUNTY OF EL DORADO**  
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|------------------------|-----------------------------|-------|--------------|---------|--------------|--------|-------------|--------|-----------------|--------|
|                        | CECIL INCE                  |       | GARY JOHNSON |         | STEVE KERBEL |        | JOHN MCAFEE |        | DARRYL W. PERRY |        |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Precinct OS            | 0                           | 0.00% | 0            | 0.00%   | 1            | 50.00% | 0           | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 4            | 50.00%  | 0            | 0.00%  | 2           | 25.00% | 1               | 12.50% |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Provisional            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Total                  | 0                           | 0.00% | 4            | 40.00%  | 1            | 10.00% | 2           | 20.00% | 1               | 10.00% |
| 400004.14-CP 400004.14 |                             |       |              |         |              |        |             |        |                 |        |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Precinct OS            | 0                           | 0.00% | 1            | 100.00% | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 1            | 50.00%  | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Provisional            | 0                           | 0.00% | 1            | 100.00% | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| Total                  | 0                           | 0.00% | 3            | 75.00%  | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| 400005.08-CP 400005.08 |                             |       |              |         |              |        |             |        |                 |        |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Precinct OS            | 0                           | 0.00% | 1            | 100.00% | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 1            | 100.00% | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Provisional            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Total                  | 0                           | 0.00% | 2            | 100.00% | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| 400006.08-CP 400006.08 |                             |       |              |         |              |        |             |        |                 |        |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Precinct OS            | 0                           | 0.00% | 4            | 66.67%  | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 5            | 50.00%  | 0            | 0.00%  | 1           | 10.00% | 0               | 0.00%  |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Provisional            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Total                  | 0                           | 0.00% | 9            | 56.25%  | 0            | 0.00%  | 1           | 6.25%  | 0               | 0.00%  |
| 400007.08-CP 400007.08 |                             |       |              |         |              |        |             |        |                 |        |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Precinct OS            | 0                           | 0.00% | 2            | 100.00% | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 5            | 83.33%  | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Provisional            | 0                           | 0.00% | 0            | 0.00%   | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| Total                  | 0                           | 0.00% | 7            | 77.78%  | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| 400010.08-CP 400010.08 |                             |       |              |         |              |        |             |        |                 |        |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Precinct OS            | 0                           | 0.00% | 0            | 0.00%   | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 2            | 66.67%  | 0            | 0.00%  | 1           | 33.33% | 0               | 0.00%  |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Provisional            | 0                           | 0.00% | 2            | 100.00% | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| Total                  | 0                           | 0.00% | 4            | 66.67%  | 0            | 0.00%  | 1           | 16.67% | 0               | 0.00%  |
| 400012.08-CP 400012.08 |                             |       |              |         |              |        |             |        |                 |        |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Precinct OS            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Precinct VBM           | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Provisional            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Total                  | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| 400013.08-CP 400013.08 |                             |       |              |         |              |        |             |        |                 |        |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Precinct OS            | 0                           | 0.00% | 1            | 100.00% | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 0            | 0.00%   | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Provisional            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Total                  | 0                           | 0.00% | 1            | 50.00%  | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| 400014.08-CP 400014.08 |                             |       |              |         |              |        |             |        |                 |        |
| Precinct TSx           | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Precinct OS            | 0                           | 0.00% | 2            | 100.00% | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 2            | 33.33%  | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |
| MB Precinct            | 0                           | -     | 0            | -       | 0            | -      | 0           | -      | 0               | -      |
| Provisional            | 0                           | 0.00% | 2            | 100.00% | 0            | 0.00%  | 0           | 0.00%  | 0               | 0.00%  |

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|                        | CECIL INCE |       | GARY JOHNSON |         | STEVE KERBEL |        | JOHN MCAFFEE |        | DARRYL W. PERRY |        |
|------------------------|------------|-------|--------------|---------|--------------|--------|--------------|--------|-----------------|--------|
| Total                  | 0          | 0.00% | 6            | 60.00%  | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| 400017.08-CP 400017.08 |            |       |              |         |              |        |              |        |                 |        |
| Precinct TSx           | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Precinct OS            | 0          | 0.00% | 2            | 100.00% | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0          | 0.00% | 1            | 33.33%  | 0            | 0.00%  | 1            | 33.33% | 0               | 0.00%  |
| MB Precinct            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Provisional            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Total                  | 0          | 0.00% | 3            | 60.00%  | 0            | 0.00%  | 1            | 20.00% | 0               | 0.00%  |
| 400018.08-CP 400018.08 |            |       |              |         |              |        |              |        |                 |        |
| Precinct TSx           | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Precinct OS            | 0          | 0.00% | 2            | 100.00% | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0          | 0.00% | 1            | 50.00%  | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| MB Precinct            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Provisional            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Total                  | 0          | 0.00% | 3            | 75.00%  | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| 400020.08-CP 400020.08 |            |       |              |         |              |        |              |        |                 |        |
| Precinct TSx           | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Precinct OS            | 0          | 0.00% | 0            | 0.00%   | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0          | 0.00% | 1            | 50.00%  | 0            | 0.00%  | 1            | 50.00% | 0               | 0.00%  |
| MB Precinct            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Provisional            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Total                  | 0          | 0.00% | 1            | 33.33%  | 0            | 0.00%  | 1            | 33.33% | 0               | 0.00%  |
| 400021.07-CP 400021.07 |            |       |              |         |              |        |              |        |                 |        |
| Precinct TSx           | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Precinct OS            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Precinct VBM           | 0          | 0.00% | 8            | 100.00% | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| MB Precinct            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Provisional            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Total                  | 0          | 0.00% | 8            | 100.00% | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| 500001.10-CP 500001.10 |            |       |              |         |              |        |              |        |                 |        |
| Precinct TSx           | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Precinct OS            | 0          | 0.00% | 2            | 50.00%  | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0          | 0.00% | 2            | 50.00%  | 0            | 0.00%  | 0            | 0.00%  | 1               | 25.00% |
| MB Precinct            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Provisional            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Total                  | 0          | 0.00% | 4            | 50.00%  | 0            | 0.00%  | 0            | 0.00%  | 1               | 12.50% |
| 500002.10-CP 500002.10 |            |       |              |         |              |        |              |        |                 |        |
| Precinct TSx           | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Precinct OS            | 0          | 0.00% | 0            | 0.00%   | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0          | 0.00% | 1            | 33.33%  | 1            | 33.33% | 0            | 0.00%  | 0               | 0.00%  |
| MB Precinct            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Provisional            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Total                  | 0          | 0.00% | 1            | 25.00%  | 1            | 25.00% | 0            | 0.00%  | 0               | 0.00%  |
| 500003.10-CP 500003.10 |            |       |              |         |              |        |              |        |                 |        |
| Precinct TSx           | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Precinct OS            | 0          | 0.00% | 1            | 100.00% | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0          | 0.00% | 1            | 100.00% | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| MB Precinct            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Provisional            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Total                  | 0          | 0.00% | 2            | 100.00% | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| 500004.10-CP 500004.10 |            |       |              |         |              |        |              |        |                 |        |
| Precinct TSx           | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Precinct OS            | 0          | 0.00% | 0            | 0.00%   | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0          | 0.00% | 1            | 50.00%  | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| MB Precinct            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Provisional            | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Total                  | 0          | 0.00% | 1            | 33.33%  | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| 500005.10-CP 500005.10 |            |       |              |         |              |        |              |        |                 |        |
| Precinct TSx           | 0          | -     | 0            | -       | 0            | -      | 0            | -      | 0               | -      |
| Precinct OS            | 0          | 0.00% | 1            | 50.00%  | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 0          | 0.00% | 3            | 100.00% | 0            | 0.00%  | 0            | 0.00%  | 0               | 0.00%  |

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|                        | CECIL INCE | GARY JOHNSON | STEVE KERBEL | JOHN MCAFEE | DARRYL W. PERRY |
|------------------------|------------|--------------|--------------|-------------|-----------------|
| MB Precinct            | 0          | 0            | 0            | 0           | 0               |
| Provisional            | 0          | 0            | 0            | 0           | 0               |
| Total                  | 0 0.00%    | 4 80.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 500006.10-CP 500006.10 |            |              |              |             |                 |
| Precinct TSx           | 0          | 0            | 0            | 0           | 0               |
| Precinct OS            | 0 0.00%    | 2 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 1 25.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0          | 0            | 0            | 0           | 0               |
| Provisional            | 0          | 0            | 0            | 0           | 0               |
| Total                  | 0 0.00%    | 3 37.50%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 500007.10-CP 500007.10 |            |              |              |             |                 |
| Precinct TSx           | 0          | 0            | 0            | 0           | 0               |
| Precinct OS            | 0 0.00%    | 2 50.00%     | 1 25.00%     | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 1 33.33%     | 0 0.00%      | 1 33.33%    | 0 0.00%         |
| MB Precinct            | 0          | 0            | 0            | 0           | 0               |
| Provisional            | 0          | 0            | 0            | 0           | 0               |
| Total                  | 0 0.00%    | 3 42.86%     | 1 14.29%     | 1 14.29%    | 0 0.00%         |
| 500009.10-CP 500009.10 |            |              |              |             |                 |
| Precinct TSx           | 0          | 0            | 0            | 0           | 0               |
| Precinct OS            | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 1 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0          | 0            | 0            | 0           | 0               |
| Provisional            | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Total                  | 0 0.00%    | 3 75.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 500011.10-CP 500011.10 |            |              |              |             |                 |
| Precinct TSx           | 0          | 0            | 0            | 0           | 0               |
| Precinct OS            | 0 0.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0          | 0            | 0            | 0           | 0               |
| Provisional            | 0          | 0            | 0            | 0           | 0               |
| Total                  | 0 0.00%    | 1 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 500012.10-CP 500012.10 |            |              |              |             |                 |
| Precinct TSx           | 0          | 0            | 0            | 0           | 0               |
| Precinct OS            | 0          | 0            | 0            | 0           | 0               |
| Precinct VBM           | 0 0.00%    | 1 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0          | 0            | 0            | 0           | 0               |
| Provisional            | 0          | 0            | 0            | 0           | 0               |
| Total                  | 0 0.00%    | 1 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 500015.10-CP 500015.10 |            |              |              |             |                 |
| Precinct TSx           | 0          | 0            | 0            | 0           | 0               |
| Precinct OS            | 0          | 0            | 0            | 0           | 0               |
| Precinct VBM           | 0          | 0            | 0            | 0           | 0               |
| MB Precinct            | 0          | 0            | 0            | 0           | 0               |
| Provisional            | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Total                  | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 500020.10-CP 500020.10 |            |              |              |             |                 |
| Precinct TSx           | 0          | 0            | 0            | 0           | 0               |
| Precinct OS            | 0 0.00%    | 3 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 1 20.00%     | 0 0.00%      | 1 20.00%    | 0 0.00%         |
| MB Precinct            | 0          | 0            | 0            | 0           | 0               |
| Provisional            | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Total                  | 0 0.00%    | 5 55.56%     | 0 0.00%      | 1 11.11%    | 0 0.00%         |
| 500021.10-CP 500021.10 |            |              |              |             |                 |
| Precinct TSx           | 0          | 0            | 0            | 0           | 0               |
| Precinct OS            | 0 0.00%    | 4 40.00%     | 0 0.00%      | 3 30.00%    | 0 0.00%         |
| Precinct VBM           | 0 0.00%    | 3 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| MB Precinct            | 0          | 0            | 0            | 0           | 0               |
| Provisional            | 0 0.00%    | 1 50.00%     | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Total                  | 0 0.00%    | 8 53.33%     | 0 0.00%      | 3 20.00%    | 0 0.00%         |
| 95U201.01-MB 95U201.01 |            |              |              |             |                 |
| Precinct TSx           | 0          | 0            | 0            | 0           | 0               |

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|------------------------|-----------------------------|-------|--------------|--------|--------------|-------|-------------|---------|-----------------|-------|
|                        | CECIL INCE                  |       | GARY JOHNSON |        | STEVE KERBEL |       | JOHN MCAFEE |         | DARRYL W. PERRY |       |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| MB Precinct            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Total                  | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| 95U201.02-MB 95U201.02 |                             |       |              |        |              |       |             |         |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| MB Precinct            | 0                           | 0.00% | 0            | 0.00%  | 0            | 0.00% | 1           | 100.00% | 0               | 0.00% |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Total                  | 0                           | 0.00% | 0            | 0.00%  | 0            | 0.00% | 1           | 100.00% | 0               | 0.00% |
| 95U202.02-MB 95U202.02 |                             |       |              |        |              |       |             |         |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| MB Precinct            | 0                           | 0.00% | 2            | 50.00% | 0            | 0.00% | 0           | 0.00%   | 0               | 0.00% |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Total                  | 0                           | 0.00% | 2            | 50.00% | 0            | 0.00% | 0           | 0.00%   | 0               | 0.00% |
| 95U203.02-MB 95U203.02 |                             |       |              |        |              |       |             |         |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| MB Precinct            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Total                  | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| 95U204.01-MB 95U204.01 |                             |       |              |        |              |       |             |         |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| MB Precinct            | 0                           | 0.00% | 2            | 50.00% | 0            | 0.00% | 0           | 0.00%   | 0               | 0.00% |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Total                  | 0                           | 0.00% | 2            | 50.00% | 0            | 0.00% | 0           | 0.00%   | 0               | 0.00% |
| 95U301.05-MB 95U301.05 |                             |       |              |        |              |       |             |         |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| MB Precinct            | 0                           | 0.00% | 0            | 0.00%  | 0            | 0.00% | 0           | 0.00%   | 0               | 0.00% |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Total                  | 0                           | 0.00% | 0            | 0.00%  | 0            | 0.00% | 0           | 0.00%   | 0               | 0.00% |
| 95U302.04-MB 95U302.04 |                             |       |              |        |              |       |             |         |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| MB Precinct            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Total                  | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| 95U302.05-MB 95U302.05 |                             |       |              |        |              |       |             |         |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| MB Precinct            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Total                  | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| 95U303.03-MB 95U303.03 |                             |       |              |        |              |       |             |         |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| MB Precinct            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |
| Total                  | 0                           | -     | 0            | -      | 0            | -     | 0           | -       | 0               | -     |

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|------------------------|-----------------------------|-------|--------------|--------|--------------|-------|-------------|-------|-----------------|-------|
|                        | CECIL INCE                  |       | GARY JOHNSON |        | STEVE KERBEL |       | JOHN MCAFEE |       | DARRYL W. PERRY |       |
| 95U303.05-MB 95U303.05 |                             |       |              |        |              |       |             |       |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| MB Precinct            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Total                  | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| 95U304.05-MB 95U304.05 |                             |       |              |        |              |       |             |       |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| MB Precinct            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Total                  | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| 95U305.05-MB 95U305.05 |                             |       |              |        |              |       |             |       |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| MB Precinct            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Total                  | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| 95U401.08-MB 95U401.08 |                             |       |              |        |              |       |             |       |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| MB Precinct            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Total                  | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| 95U402.07-MB 95U402.07 |                             |       |              |        |              |       |             |       |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| MB Precinct            | 0                           | 0.00% | 1            | 50.00% | 0            | 0.00% | 0           | 0.00% | 0               | 0.00% |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Total                  | 0                           | 0.00% | 1            | 50.00% | 0            | 0.00% | 0           | 0.00% | 0               | 0.00% |
| 95U402.08-MB 95U402.08 |                             |       |              |        |              |       |             |       |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| MB Precinct            | 0                           | 0.00% | 2            | 66.67% | 0            | 0.00% | 0           | 0.00% | 0               | 0.00% |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Total                  | 0                           | 0.00% | 2            | 66.67% | 0            | 0.00% | 0           | 0.00% | 0               | 0.00% |
| 95U403.08-MB 95U403.08 |                             |       |              |        |              |       |             |       |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| MB Precinct            | 0                           | 0.00% | 4            | 44.44% | 0            | 0.00% | 0           | 0.00% | 0               | 0.00% |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Total                  | 0                           | 0.00% | 4            | 44.44% | 0            | 0.00% | 0           | 0.00% | 0               | 0.00% |
| 95U404.06-MB 95U404.06 |                             |       |              |        |              |       |             |       |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| MB Precinct            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Provisional            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Total                  | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| 95U404.08-MB 95U404.08 |                             |       |              |        |              |       |             |       |                 |       |
| Precinct TSx           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct OS            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| Precinct VBM           | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |
| MB Precinct            | 0                           | -     | 0            | -      | 0            | -     | 0           | -     | 0               | -     |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

Date:07/05/16  
 Time:08:08:12  
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President Libertarian Party

|                               | CECIL INCE | GARY JOHNSON | STEVE KERBEL | JOHN MCAFEE | DARRYL W. PERRY |
|-------------------------------|------------|--------------|--------------|-------------|-----------------|
| Provisional                   | 0          | 0            | 0            | 0           | 0               |
| Total                         | 0          | 0            | 0            | 0           | 0               |
| 95U501.09-MB 95U501.09        |            |              |              |             |                 |
| Precinct TSx                  | 0          | 0            | 0            | 0           | 0               |
| Precinct OS                   | 0          | 0            | 0            | 0           | 0               |
| Precinct VBM                  | 0          | 0            | 0            | 0           | 0               |
| MB Precinct                   | 0          | 0            | 0            | 0           | 0               |
| Provisional                   | 0          | 0            | 0            | 0           | 0               |
| Total                         | 0          | 0            | 0            | 0           | 0               |
| 95U501.10-MB 95U501.10        |            |              |              |             |                 |
| Precinct TSx                  | 0          | 0            | 0            | 0           | 0               |
| Precinct OS                   | 0          | 0            | 0            | 0           | 0               |
| Precinct VBM                  | 0          | 0            | 0            | 0           | 0               |
| MB Precinct                   | 0          | 0            | 0            | 0           | 0               |
| Provisional                   | 0          | 0            | 0            | 0           | 0               |
| Total                         | 0          | 0            | 0            | 0           | 0               |
| 95U598.10-MB 95U598.10        |            |              |              |             |                 |
| Precinct TSx                  | 0          | 0            | 0            | 0           | 0               |
| Precinct OS                   | 0          | 0            | 0            | 0           | 0               |
| Precinct VBM                  | 0          | 0            | 0            | 0           | 0               |
| MB Precinct                   | 1 25.00%   | 0 0.00%      | 0 0.00%      | 1 25.00%    | 0 0.00%         |
| Provisional                   | 0          | 0            | 0            | 0           | 0               |
| Total                         | 1 25.00%   | 0 0.00%      | 0 0.00%      | 1 25.00%    | 0 0.00%         |
| 95U599.10-MB 95U599.10        |            |              |              |             |                 |
| Precinct TSx                  | 0          | 0            | 0            | 0           | 0               |
| Precinct OS                   | 0          | 0            | 0            | 0           | 0               |
| Precinct VBM                  | 0          | 0            | 0            | 0           | 0               |
| MB Precinct                   | 0          | 0            | 0            | 0           | 0               |
| Provisional                   | 0          | 0            | 0            | 0           | 0               |
| Total                         | 0          | 0            | 0            | 0           | 0               |
| 96U101.11-MB 96U101.11        |            |              |              |             |                 |
| Precinct TSx                  | 0          | 0            | 0            | 0           | 0               |
| Precinct OS                   | 0          | 0            | 0            | 0           | 0               |
| Precinct VBM                  | 0          | 0            | 0            | 0           | 0               |
| MB Precinct                   | 0 0.00%    | 2 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Provisional                   | 0          | 0            | 0            | 0           | 0               |
| Total                         | 0 0.00%    | 2 100.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| 96U401.14-MB 96U401.14        |            |              |              |             |                 |
| Precinct TSx                  | 0          | 0            | 0            | 0           | 0               |
| Precinct OS                   | 0          | 0            | 0            | 0           | 0               |
| Precinct VBM                  | 0          | 0            | 0            | 0           | 0               |
| MB Precinct                   | 0          | 0            | 0            | 0           | 0               |
| Provisional                   | 0          | 0            | 0            | 0           | 0               |
| Total                         | 0          | 0            | 0            | 0           | 0               |
| Total                         |            |              |              |             |                 |
| Precinct TSx                  | 0          | 0            | 0            | 0           | 0               |
| Precinct OS                   | 2 1.52%    | 71 53.79%    | 3 2.27%      | 12 9.09%    | 0 0.00%         |
| Precinct VBM                  | 1 0.34%    | 142 48.97%   | 1 0.34%      | 19 6.55%    | 2 0.69%         |
| MB Precinct                   | 1 3.23%    | 13 41.94%    | 0 0.00%      | 2 6.45%     | 0 0.00%         |
| Provisional                   | 0 0.00%    | 13 50.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Total                         | 4 0.84%    | 239 49.90%   | 4 0.84%      | 33 6.89%    | 2 0.42%         |
| STATE BOARD OF EQUALIZATION   |            |              |              |             |                 |
| STATE BOARD OF EQUALIZATION 1 |            |              |              |             |                 |
| Precinct TSx                  | 0          | 0            | 0            | 0           | 0               |
| Precinct OS                   | 2 1.52%    | 71 53.79%    | 3 2.27%      | 12 9.09%    | 0 0.00%         |
| Precinct VBM                  | 1 0.34%    | 142 48.97%   | 1 0.34%      | 19 6.55%    | 2 0.69%         |
| MB Precinct                   | 1 3.23%    | 13 41.94%    | 0 0.00%      | 2 6.45%     | 0 0.00%         |
| Provisional                   | 0 0.00%    | 13 50.00%    | 0 0.00%      | 0 0.00%     | 0 0.00%         |
| Total                         | 4 0.84%    | 239 49.90%   | 4 0.84%      | 33 6.89%    | 2 0.42%         |
| Total                         |            |              |              |             |                 |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

Date:07/05/16  
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President Libertarian Party

|                                     | CECIL INCE |              | GARY JOHNSON |               | STEVE KERBEL |              | JOHN MCAFFEE |              | DARRYL W. PERRY |              |
|-------------------------------------|------------|--------------|--------------|---------------|--------------|--------------|--------------|--------------|-----------------|--------------|
| Precinct TSx                        | 0          | -            | 0            | -             | 0            | -            | 0            | -            | 0               | -            |
| Precinct OS                         | 2          | 1.52%        | 71           | 53.79%        | 3            | 2.27%        | 12           | 9.09%        | 0               | 0.00%        |
| Precinct VBM                        | 1          | 0.34%        | 142          | 48.97%        | 1            | 0.34%        | 19           | 6.55%        | 2               | 0.69%        |
| MB Precinct                         | 1          | 3.23%        | 13           | 41.94%        | 0            | 0.00%        | 2            | 6.45%        | 0               | 0.00%        |
| Provisional                         | 0          | 0.00%        | 13           | 50.00%        | 0            | 0.00%        | 0            | 0.00%        | 0               | 0.00%        |
| <b>Total</b>                        | <b>4</b>   | <b>0.84%</b> | <b>239</b>   | <b>49.90%</b> | <b>4</b>     | <b>0.84%</b> | <b>33</b>    | <b>6.89%</b> | <b>2</b>        | <b>0.42%</b> |
| <b>UNITED STATES SENATE</b>         |            |              |              |               |              |              |              |              |                 |              |
| <b>UNITED STATES SENATE</b>         |            |              |              |               |              |              |              |              |                 |              |
| Precinct TSx                        | 0          | -            | 0            | -             | 0            | -            | 0            | -            | 0               | -            |
| Precinct OS                         | 2          | 1.52%        | 71           | 53.79%        | 3            | 2.27%        | 12           | 9.09%        | 0               | 0.00%        |
| Precinct VBM                        | 1          | 0.34%        | 142          | 48.97%        | 1            | 0.34%        | 19           | 6.55%        | 2               | 0.69%        |
| MB Precinct                         | 1          | 3.23%        | 13           | 41.94%        | 0            | 0.00%        | 2            | 6.45%        | 0               | 0.00%        |
| Provisional                         | 0          | 0.00%        | 13           | 50.00%        | 0            | 0.00%        | 0            | 0.00%        | 0               | 0.00%        |
| <b>Total</b>                        | <b>4</b>   | <b>0.84%</b> | <b>239</b>   | <b>49.90%</b> | <b>4</b>     | <b>0.84%</b> | <b>33</b>    | <b>6.89%</b> | <b>2</b>        | <b>0.42%</b> |
| <b>Total</b>                        | <b>4</b>   | <b>0.84%</b> | <b>239</b>   | <b>49.90%</b> | <b>4</b>     | <b>0.84%</b> | <b>33</b>    | <b>6.89%</b> | <b>2</b>        | <b>0.42%</b> |
| Precinct TSx                        | 0          | -            | 0            | -             | 0            | -            | 0            | -            | 0               | -            |
| Precinct OS                         | 2          | 1.52%        | 71           | 53.79%        | 3            | 2.27%        | 12           | 9.09%        | 0               | 0.00%        |
| Precinct VBM                        | 1          | 0.34%        | 142          | 48.97%        | 1            | 0.34%        | 19           | 6.55%        | 2               | 0.69%        |
| MB Precinct                         | 1          | 3.23%        | 13           | 41.94%        | 0            | 0.00%        | 2            | 6.45%        | 0               | 0.00%        |
| Provisional                         | 0          | 0.00%        | 13           | 50.00%        | 0            | 0.00%        | 0            | 0.00%        | 0               | 0.00%        |
| <b>Total</b>                        | <b>4</b>   | <b>0.84%</b> | <b>239</b>   | <b>49.90%</b> | <b>4</b>     | <b>0.84%</b> | <b>33</b>    | <b>6.89%</b> | <b>2</b>        | <b>0.42%</b> |
| <b>Total</b>                        | <b>4</b>   | <b>0.84%</b> | <b>239</b>   | <b>49.90%</b> | <b>4</b>     | <b>0.84%</b> | <b>33</b>    | <b>6.89%</b> | <b>2</b>        | <b>0.42%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |            |              |              |               |              |              |              |              |                 |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |            |              |              |               |              |              |              |              |                 |              |
| Precinct TSx                        | 0          | -            | 0            | -             | 0            | -            | 0            | -            | 0               | -            |
| Precinct OS                         | 2          | 1.52%        | 71           | 53.79%        | 3            | 2.27%        | 12           | 9.09%        | 0               | 0.00%        |
| Precinct VBM                        | 1          | 0.34%        | 142          | 48.97%        | 1            | 0.34%        | 19           | 6.55%        | 2               | 0.69%        |
| MB Precinct                         | 1          | 3.23%        | 13           | 41.94%        | 0            | 0.00%        | 2            | 6.45%        | 0               | 0.00%        |
| Provisional                         | 0          | 0.00%        | 13           | 50.00%        | 0            | 0.00%        | 0            | 0.00%        | 0               | 0.00%        |
| <b>Total</b>                        | <b>4</b>   | <b>0.84%</b> | <b>239</b>   | <b>49.90%</b> | <b>4</b>     | <b>0.84%</b> | <b>33</b>    | <b>6.89%</b> | <b>2</b>        | <b>0.42%</b> |
| <b>Total</b>                        | <b>4</b>   | <b>0.84%</b> | <b>239</b>   | <b>49.90%</b> | <b>4</b>     | <b>0.84%</b> | <b>33</b>    | <b>6.89%</b> | <b>2</b>        | <b>0.42%</b> |
| Precinct TSx                        | 0          | -            | 0            | -             | 0            | -            | 0            | -            | 0               | -            |
| Precinct OS                         | 2          | 1.52%        | 71           | 53.79%        | 3            | 2.27%        | 12           | 9.09%        | 0               | 0.00%        |
| Precinct VBM                        | 1          | 0.34%        | 142          | 48.97%        | 1            | 0.34%        | 19           | 6.55%        | 2               | 0.69%        |
| MB Precinct                         | 1          | 3.23%        | 13           | 41.94%        | 0            | 0.00%        | 2            | 6.45%        | 0               | 0.00%        |
| Provisional                         | 0          | 0.00%        | 13           | 50.00%        | 0            | 0.00%        | 0            | 0.00%        | 0               | 0.00%        |
| <b>Total</b>                        | <b>4</b>   | <b>0.84%</b> | <b>239</b>   | <b>49.90%</b> | <b>4</b>     | <b>0.84%</b> | <b>33</b>    | <b>6.89%</b> | <b>2</b>        | <b>0.42%</b> |
| <b>Total</b>                        | <b>4</b>   | <b>0.84%</b> | <b>239</b>   | <b>49.90%</b> | <b>4</b>     | <b>0.84%</b> | <b>33</b>    | <b>6.89%</b> | <b>2</b>        | <b>0.42%</b> |
| <b>STATE SENATE</b>                 |            |              |              |               |              |              |              |              |                 |              |
| <b>1ST STATE SENATE DISTRICT</b>    |            |              |              |               |              |              |              |              |                 |              |
| Precinct TSx                        | 0          | -            | 0            | -             | 0            | -            | 0            | -            | 0               | -            |
| Precinct OS                         | 2          | 1.52%        | 71           | 53.79%        | 3            | 2.27%        | 12           | 9.09%        | 0               | 0.00%        |
| Precinct VBM                        | 1          | 0.34%        | 142          | 48.97%        | 1            | 0.34%        | 19           | 6.55%        | 2               | 0.69%        |
| MB Precinct                         | 1          | 3.23%        | 13           | 41.94%        | 0            | 0.00%        | 2            | 6.45%        | 0               | 0.00%        |
| Provisional                         | 0          | 0.00%        | 13           | 50.00%        | 0            | 0.00%        | 0            | 0.00%        | 0               | 0.00%        |
| <b>Total</b>                        | <b>4</b>   | <b>0.84%</b> | <b>239</b>   | <b>49.90%</b> | <b>4</b>     | <b>0.84%</b> | <b>33</b>    | <b>6.89%</b> | <b>2</b>        | <b>0.42%</b> |
| <b>Total</b>                        | <b>4</b>   | <b>0.84%</b> | <b>239</b>   | <b>49.90%</b> | <b>4</b>     | <b>0.84%</b> | <b>33</b>    | <b>6.89%</b> | <b>2</b>        | <b>0.42%</b> |
| Precinct TSx                        | 0          | -            | 0            | -             | 0            | -            | 0            | -            | 0               | -            |
| Precinct OS                         | 2          | 1.52%        | 71           | 53.79%        | 3            | 2.27%        | 12           | 9.09%        | 0               | 0.00%        |
| Precinct VBM                        | 1          | 0.34%        | 142          | 48.97%        | 1            | 0.34%        | 19           | 6.55%        | 2               | 0.69%        |
| MB Precinct                         | 1          | 3.23%        | 13           | 41.94%        | 0            | 0.00%        | 2            | 6.45%        | 0               | 0.00%        |
| Provisional                         | 0          | 0.00%        | 13           | 50.00%        | 0            | 0.00%        | 0            | 0.00%        | 0               | 0.00%        |
| <b>Total</b>                        | <b>4</b>   | <b>0.84%</b> | <b>239</b>   | <b>49.90%</b> | <b>4</b>     | <b>0.84%</b> | <b>33</b>    | <b>6.89%</b> | <b>2</b>        | <b>0.42%</b> |
| <b>Total</b>                        | <b>4</b>   | <b>0.84%</b> | <b>239</b>   | <b>49.90%</b> | <b>4</b>     | <b>0.84%</b> | <b>33</b>    | <b>6.89%</b> | <b>2</b>        | <b>0.42%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |            |              |              |               |              |              |              |              |                 |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |            |              |              |               |              |              |              |              |                 |              |
| Precinct TSx                        | 0          | -            | 0            | -             | 0            | -            | 0            | -            | 0               | -            |
| Precinct OS                         | 2          | 2.38%        | 43           | 51.19%        | 3            | 3.57%        | 6            | 7.14%        | 0               | 0.00%        |
| Precinct VBM                        | 0          | 0.00%        | 91           | 48.66%        | 1            | 0.53%        | 15           | 8.02%        | 2               | 1.07%        |
| MB Precinct                         | 1          | 3.45%        | 11           | 37.93%        | 0            | 0.00%        | 2            | 6.90%        | 0               | 0.00%        |

**COUNTY OF EL DORADO**  
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President Libertarian Party

|                                   | CECIL INCE |        | GARY JOHNSON |         | STEVE KERBEL |       | JOHN MCAFFEE |        | DARRYL W. PERRY |       |
|-----------------------------------|------------|--------|--------------|---------|--------------|-------|--------------|--------|-----------------|-------|
| Provisional                       | 0          | 0.00%  | 10           | 71.43%  | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Total                             | 3          | 0.96%  | 155          | 49.36%  | 4            | 1.27% | 23           | 7.32%  | 2               | 0.64% |
| <b>6TH STATE ASSEMBLY DIST</b>    |            |        |              |         |              |       |              |        |                 |       |
| Precinct TSx                      | 0          | -      | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS                       | 0          | 0.00%  | 28           | 58.33%  | 0            | 0.00% | 6            | 12.50% | 0               | 0.00% |
| Precinct VBM                      | 1          | 0.97%  | 51           | 49.51%  | 0            | 0.00% | 4            | 3.88%  | 0               | 0.00% |
| MB Precinct                       | 0          | 0.00%  | 2            | 100.00% | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Provisional                       | 0          | 0.00%  | 3            | 25.00%  | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Total                             | 1          | 0.61%  | 84           | 50.91%  | 0            | 0.00% | 10           | 6.06%  | 0               | 0.00% |
| <b>Total</b>                      |            |        |              |         |              |       |              |        |                 |       |
| Precinct TSx                      | 0          | -      | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS                       | 2          | 1.52%  | 71           | 53.79%  | 3            | 2.27% | 12           | 9.09%  | 0               | 0.00% |
| Precinct VBM                      | 1          | 0.34%  | 142          | 48.97%  | 1            | 0.34% | 19           | 6.55%  | 2               | 0.69% |
| MB Precinct                       | 1          | 3.23%  | 13           | 41.94%  | 0            | 0.00% | 2            | 6.45%  | 0               | 0.00% |
| Provisional                       | 0          | 0.00%  | 13           | 50.00%  | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Total                             | 4          | 0.84%  | 239          | 49.90%  | 4            | 0.84% | 33           | 6.89%  | 2               | 0.42% |
| <b>SUPERVISORIAL DISTRICTS</b>    |            |        |              |         |              |       |              |        |                 |       |
| <b>1ST SUPERVISORIAL DISTRICT</b> |            |        |              |         |              |       |              |        |                 |       |
| Precinct TSx                      | 0          | -      | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS                       | 0          | 0.00%  | 12           | 52.17%  | 0            | 0.00% | 4            | 17.39% | 0               | 0.00% |
| Precinct VBM                      | 0          | 0.00%  | 35           | 56.45%  | 0            | 0.00% | 3            | 4.84%  | 0               | 0.00% |
| MB Precinct                       | 0          | 0.00%  | 2            | 100.00% | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Provisional                       | 0          | 0.00%  | 1            | 33.33%  | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Total                             | 0          | 0.00%  | 50           | 55.56%  | 0            | 0.00% | 7            | 7.78%  | 0               | 0.00% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |            |        |              |         |              |       |              |        |                 |       |
| Precinct TSx                      | 0          | -      | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS                       | 1          | 2.78%  | 17           | 47.22%  | 1            | 2.78% | 3            | 8.33%  | 0               | 0.00% |
| Precinct VBM                      | 1          | 1.30%  | 34           | 44.16%  | 0            | 0.00% | 3            | 3.90%  | 0               | 0.00% |
| MB Precinct                       | 0          | 0.00%  | 4            | 44.44%  | 0            | 0.00% | 1            | 11.11% | 0               | 0.00% |
| Provisional                       | 0          | 0.00%  | 3            | 30.00%  | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Total                             | 2          | 1.52%  | 58           | 43.94%  | 1            | 0.76% | 7            | 5.30%  | 0               | 0.00% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |            |        |              |         |              |       |              |        |                 |       |
| Precinct TSx                      | 0          | -      | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS                       | 1          | 5.56%  | 10           | 55.56%  | 0            | 0.00% | 1            | 5.56%  | 0               | 0.00% |
| Precinct VBM                      | 0          | 0.00%  | 20           | 37.74%  | 0            | 0.00% | 3            | 5.66%  | 0               | 0.00% |
| MB Precinct                       | 0          | 0.00%  | 0            | 0.00%   | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Provisional                       | 0          | 0.00%  | 0            | 0.00%   | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Total                             | 1          | 1.35%  | 30           | 40.54%  | 0            | 0.00% | 4            | 5.41%  | 0               | 0.00% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |            |        |              |         |              |       |              |        |                 |       |
| Precinct TSx                      | 0          | -      | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS                       | 0          | 0.00%  | 16           | 69.57%  | 1            | 4.35% | 1            | 4.35%  | 0               | 0.00% |
| Precinct VBM                      | 0          | 0.00%  | 36           | 55.38%  | 0            | 0.00% | 8            | 12.31% | 1               | 1.54% |
| MB Precinct                       | 0          | 0.00%  | 7            | 50.00%  | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Provisional                       | 0          | 0.00%  | 5            | 71.43%  | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Total                             | 0          | 0.00%  | 64           | 58.72%  | 1            | 0.92% | 9            | 8.26%  | 1               | 0.92% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |            |        |              |         |              |       |              |        |                 |       |
| Precinct TSx                      | 0          | -      | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS                       | 0          | 0.00%  | 16           | 50.00%  | 1            | 3.13% | 3            | 9.38%  | 0               | 0.00% |
| Precinct VBM                      | 0          | 0.00%  | 17           | 51.52%  | 1            | 3.03% | 2            | 6.06%  | 1               | 3.03% |
| MB Precinct                       | 1          | 25.00% | 0            | 0.00%   | 0            | 0.00% | 1            | 25.00% | 0               | 0.00% |
| Provisional                       | 0          | 0.00%  | 4            | 80.00%  | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Total                             | 1          | 1.35%  | 37           | 50.00%  | 2            | 2.70% | 6            | 8.11%  | 1               | 1.35% |
| <b>Total</b>                      |            |        |              |         |              |       |              |        |                 |       |
| Precinct TSx                      | 0          | -      | 0            | -       | 0            | -     | 0            | -      | 0               | -     |
| Precinct OS                       | 2          | 1.52%  | 71           | 53.79%  | 3            | 2.27% | 12           | 9.09%  | 0               | 0.00% |
| Precinct VBM                      | 1          | 0.34%  | 142          | 48.97%  | 1            | 0.34% | 19           | 6.55%  | 2               | 0.69% |
| MB Precinct                       | 1          | 3.23%  | 13           | 41.94%  | 0            | 0.00% | 2            | 6.45%  | 0               | 0.00% |
| Provisional                       | 0          | 0.00%  | 13           | 50.00%  | 0            | 0.00% | 0            | 0.00%  | 0               | 0.00% |
| Total                             | 4          | 0.84%  | 239          | 49.90%  | 4            | 0.84% | 33           | 6.89%  | 2               | 0.42% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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President Libertarian Party

|                                 | CECIL INCE | GARY JOHNSON | STEVE KERBEL | JOHN MCAFFEE | DARRYL W. PERRY |
|---------------------------------|------------|--------------|--------------|--------------|-----------------|
| <b>CITIES</b>                   |            |              |              |              |                 |
| <b>CITY OF PLACERVILLE</b>      |            |              |              |              |                 |
| Precinct TSx                    | 0 -        | 0 -          | 0 -          | 0 -          | 0 -             |
| Precinct OS                     | 0 0.00%    | 2 66.67%     | 0 0.00%      | 0 0.00%      | 0 0.00%         |
| Precinct VBM                    | 0 0.00%    | 6 42.86%     | 0 0.00%      | 2 14.29%     | 0 0.00%         |
| MB Precinct                     | 0 -        | 0 -          | 0 -          | 0 -          | 0 -             |
| Provisional                     | 0 -        | 0 -          | 0 -          | 0 -          | 0 -             |
| Total                           | 0 0.00%    | 8 47.06%     | 0 0.00%      | 2 11.76%     | 0 0.00%         |
| <b>CITY OF SOUTH LAKE TAHOE</b> |            |              |              |              |                 |
| Precinct TSx                    | 0 -        | 0 -          | 0 -          | 0 -          | 0 -             |
| Precinct OS                     | 0 0.00%    | 6 42.86%     | 1 7.14%      | 0 0.00%      | 0 0.00%         |
| Precinct VBM                    | 0 0.00%    | 9 50.00%     | 1 5.56%      | 1 5.56%      | 0 0.00%         |
| MB Precinct                     | 0 -        | 0 -          | 0 -          | 0 -          | 0 -             |
| Provisional                     | 0 0.00%    | 1 100.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%         |
| Total                           | 0 0.00%    | 16 48.48%    | 2 6.06%      | 1 3.03%      | 0 0.00%         |
| <b>UNINCORPORATED AREA</b>      |            |              |              |              |                 |
| Precinct TSx                    | 0 -        | 0 -          | 0 -          | 0 -          | 0 -             |
| Precinct OS                     | 2 1.74%    | 63 54.78%    | 2 1.74%      | 12 10.43%    | 0 0.00%         |
| Precinct VBM                    | 1 0.39%    | 127 49.22%   | 0 0.00%      | 16 6.20%     | 2 0.78%         |
| MB Precinct                     | 1 3.23%    | 13 41.94%    | 0 0.00%      | 2 6.45%      | 0 0.00%         |
| Provisional                     | 0 0.00%    | 12 48.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%         |
| Total                           | 4 0.93%    | 215 50.12%   | 2 0.47%      | 30 6.99%     | 2 0.47%         |
| <b>Total</b>                    |            |              |              |              |                 |
| Precinct TSx                    | 0 -        | 0 -          | 0 -          | 0 -          | 0 -             |
| Precinct OS                     | 2 1.52%    | 71 53.79%    | 3 2.27%      | 12 9.09%     | 0 0.00%         |
| Precinct VBM                    | 1 0.34%    | 142 48.97%   | 1 0.34%      | 19 6.55%     | 2 0.69%         |
| MB Precinct                     | 1 3.23%    | 13 41.94%    | 0 0.00%      | 2 6.45%      | 0 0.00%         |
| Provisional                     | 0 0.00%    | 13 50.00%    | 0 0.00%      | 0 0.00%      | 0 0.00%         |
| Total                           | 4 0.84%    | 239 49.90%   | 4 0.84%      | 33 6.89%     | 2 0.42%         |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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|                        | President Libertarian Party |        |                    |       |                       |       |                         |       |             |       |
|------------------------|-----------------------------|--------|--------------------|-------|-----------------------|-------|-------------------------|-------|-------------|-------|
|                        | AUSTIN<br>PETERSEN          |        | DERRICK M.<br>REID |       | JACK<br>ROBINSON, JR. |       | R.WHITE<br>FEATHER SMIT |       | JOY WAYMIRE |       |
| Jurisdiction Wide      |                             |        |                    |       |                       |       |                         |       |             |       |
| 100001.11-CP 100001.11 |                             |        |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 100002.11-CP 100002.11 |                             |        |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 100003.11-CP 100003.11 |                             |        |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 1                           | 50.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Total                  | 1                           | 10.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 100004.11-CP 100004.11 |                             |        |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 100005.11-CP 100005.11 |                             |        |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 100006.11-CP 100006.11 |                             |        |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 100008.11-CP 100008.11 |                             |        |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 100009.11-CP 100009.11 |                             |        |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 100012.11-CP 100012.11 |                             |        |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | President Libertarian Party |        |                    |        |                       |       |                         |       |             |        |
|------------------------|-----------------------------|--------|--------------------|--------|-----------------------|-------|-------------------------|-------|-------------|--------|
|                        | AUSTIN<br>PETERSEN          |        | DERRICK M.<br>REID |        | JACK<br>ROBINSON, JR. |       | R.WHITE<br>FEATHER SMIT |       | JOY WAYMIRE |        |
| MB Precinct            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 100013.11-CP 100013.11 |                             |        |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 1           | 25.00% |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 1           | 6.67%  |
| 100014.11-CP 100014.11 |                             |        |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 100022.11-CP 100022.11 |                             |        |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| MB Precinct            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 100023.11-CP 100023.11 |                             |        |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00%  | 1                  | 50.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00%  | 1                  | 20.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 200003.12-CP 200003.12 |                             |        |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 1                           | 50.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Total                  | 1                           | 25.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 200004.12-CP 200004.12 |                             |        |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 2                           | 40.00% | 1                  | 20.00% | 0                     | 0.00% | 0                       | 0.00% | 1           | 20.00% |
| MB Precinct            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Total                  | 2                           | 18.18% | 1                  | 9.09%  | 0                     | 0.00% | 0                       | 0.00% | 1           | 9.09%  |
| 200005.02-CP 200005.02 |                             |        |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 200006.12-CP 200006.12 |                             |        |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 200008.12-CP 200008.12 |                             |        |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |

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|------------------------|-----------------------------|---------|--------------------|-------|-----------------------|-------|-------------------------|--------|-------------|--------|
|                        | AUSTIN<br>PETERSEN          |         | DERRICK M.<br>REID |       | JACK<br>ROBINSON, JR. |       | R.WHITE<br>FEATHER SMIT |        | JOY WAYMIRE |        |
| Precinct OS            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Precinct VBM           | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Provisional            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Total                  | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| 200010.12-CP 200010.12 |                             |         |                    |       |                       |       |                         |        |             |        |
| Precinct TSx           | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Precinct OS            | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Provisional            | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| Total                  | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| 200012.12-CP 200012.12 |                             |         |                    |       |                       |       |                         |        |             |        |
| Precinct TSx           | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Precinct OS            | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Provisional            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Total                  | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| 200013.12-CP 200013.12 |                             |         |                    |       |                       |       |                         |        |             |        |
| Precinct TSx           | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Precinct OS            | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 1           | 16.67% |
| Precinct VBM           | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 1                       | 33.33% | 0           | 0.00%  |
| MB Precinct            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Provisional            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Total                  | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 1                       | 11.11% | 1           | 11.11% |
| 200014.12-CP 200014.12 |                             |         |                    |       |                       |       |                         |        |             |        |
| Precinct TSx           | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Precinct OS            | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 2                           | 40.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Provisional            | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| Total                  | 2                           | 18.18%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| 200015.02-CP 200015.02 |                             |         |                    |       |                       |       |                         |        |             |        |
| Precinct TSx           | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Precinct OS            | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Provisional            | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| Total                  | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| 200016.12-CP 200016.12 |                             |         |                    |       |                       |       |                         |        |             |        |
| Precinct TSx           | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Precinct OS            | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Provisional            | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| Total                  | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| 200016.13-CP 200016.13 |                             |         |                    |       |                       |       |                         |        |             |        |
| Precinct TSx           | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Precinct OS            | 1                           | 100.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| MB Precinct            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Provisional            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Total                  | 1                           | 33.33%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| 200017.02-CP 200017.02 |                             |         |                    |       |                       |       |                         |        |             |        |
| Precinct TSx           | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Precinct OS            | 0                           | 0.00%   | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00%  |
| Precinct VBM           | 1                           | 9.09%   | 0                  | 0.00% | 0                     | 0.00% | 3                       | 27.27% | 0           | 0.00%  |
| MB Precinct            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Provisional            | 0                           | -       | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -      |
| Total                  | 1                           | 7.69%   | 0                  | 0.00% | 0                     | 0.00% | 3                       | 23.08% | 0           | 0.00%  |

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|------------------------|-----------------------------|--------|--------------------|-------|-----------------------|-------|-------------------------|--------|-------------|-------|
|                        | AUSTIN<br>PETERSEN          |        | DERRICK M.<br>REID |       | JACK<br>ROBINSON, JR. |       | R.WHITE<br>FEATHER SMIT |        | JOY WAYMIRE |       |
| 200018.02-CP 200018.02 |                             |        |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 200020.02-CP 200020.02 |                             |        |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 200021.01-CP 200021.01 |                             |        |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 200021.02-CP 200021.02 |                             |        |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct VBM           | 2                           | 25.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 2                           | 25.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 200024.02-CP 200024.02 |                             |        |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 300001.04-CP 300001.04 |                             |        |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 300001.05-CP 300001.05 |                             |        |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 300003.05-CP 300003.05 |                             |        |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 300004.05-CP 300004.05 |                             |        |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct VBM           | 1                           | 25.00% | 0                  | 0.00% | 0                     | 0.00% | 1                       | 25.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |



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|                        | AUSTIN<br>PETERSEN          |        | DERRICK M.<br>REID |       | JACK<br>ROBINSON, JR. |        | R.WHITE<br>FEATHER SMIT |        | JOY WAYMIRE |       |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Total                  | 1                           | 25.00% | 0                  | 0.00% | 0                     | 0.00%  | 1                       | 25.00% | 0           | 0.00% |
| 300005.05-CP 300005.05 |                             |        |                    |       |                       |        |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 1                           | 33.33% | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Total                  | 1                           | 25.00% | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| 300008.05-CP 300008.05 |                             |        |                    |       |                       |        |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 1                     | 25.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 1                     | 20.00% | 0                       | 0.00%  | 0           | 0.00% |
| 300009.04-CP 300009.04 |                             |        |                    |       |                       |        |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| 300010.04-CP 300010.04 |                             |        |                    |       |                       |        |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| 300011.04-CP 300011.04 |                             |        |                    |       |                       |        |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Provisional            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| 300013.05-CP 300013.05 |                             |        |                    |       |                       |        |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| 300015.04-CP 300015.04 |                             |        |                    |       |                       |        |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| 300015.05-CP 300015.05 |                             |        |                    |       |                       |        |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |
| 300017.05-CP 300017.05 |                             |        |                    |       |                       |        |                         |        |             |       |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -      | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%  | 0                       | 0.00%  | 0           | 0.00% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | President Libertarian Party |        |                    |       |                       |         |                         |         |             |        |
|------------------------|-----------------------------|--------|--------------------|-------|-----------------------|---------|-------------------------|---------|-------------|--------|
|                        | AUSTIN<br>PETERSEN          |        | DERRICK M.<br>REID |       | JACK<br>ROBINSON, JR. |         | R.WHITE<br>FEATHER SMIT |         | JOY WAYMIRE |        |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 1                     | 100.00% | 0                       | 0.00%   | 0           | 0.00%  |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 1                     | 25.00%  | 0                       | 0.00%   | 0           | 0.00%  |
| 300018.04-CP 300018.04 |                             |        |                    |       |                       |         |                         |         |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| Precinct VBM           | 1                           | 50.00% | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Total                  | 1                           | 25.00% | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| 300020.04-CP 300020.04 |                             |        |                    |       |                       |         |                         |         |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| Precinct VBM           | 2                           | 33.33% | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 1           | 16.67% |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Total                  | 2                           | 28.57% | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 1           | 14.29% |
| 300021.03-CP 300021.03 |                             |        |                    |       |                       |         |                         |         |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| 300022.05-CP 300022.05 |                             |        |                    |       |                       |         |                         |         |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| 300023.03-CP 300023.03 |                             |        |                    |       |                       |         |                         |         |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| 300023.05-CP 300023.05 |                             |        |                    |       |                       |         |                         |         |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Precinct OS            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 1                       | 50.00%  | 0           | 0.00%  |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 1                       | 50.00%  | 0           | 0.00%  |
| 400001.14-CP 400001.14 |                             |        |                    |       |                       |         |                         |         |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Precinct OS            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Provisional            | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 1                       | 100.00% | 0           | 0.00%  |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 1                       | 14.29%  | 0           | 0.00%  |
| 400002.08-CP 400002.08 |                             |        |                    |       |                       |         |                         |         |             |        |
| Precinct TSx           | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Precinct OS            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Precinct VBM           | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| MB Precinct            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Provisional            | 0                           | -      | 0                  | -     | 0                     | -       | 0                       | -       | 0           | -      |
| Total                  | 0                           | 0.00%  | 0                  | 0.00% | 0                     | 0.00%   | 0                       | 0.00%   | 0           | 0.00%  |
| 400003.08-CP 400003.08 |                             |        |                    |       |                       |         |                         |         |             |        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | President Libertarian Party |       |                    |       |                       |       |                         |       |             |       |
|------------------------|-----------------------------|-------|--------------------|-------|-----------------------|-------|-------------------------|-------|-------------|-------|
|                        | AUSTIN<br>PETERSEN          |       | DERRICK M.<br>REID |       | JACK<br>ROBINSON, JR. |       | R.WHITE<br>FEATHER SMIT |       | JOY WAYMIRE |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 400004.14-CP 400004.14 |                             |       |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Total                  | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 400005.08-CP 400005.08 |                             |       |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 400006.08-CP 400006.08 |                             |       |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 400007.08-CP 400007.08 |                             |       |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Total                  | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 400010.08-CP 400010.08 |                             |       |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Total                  | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 400012.08-CP 400012.08 |                             |       |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| 400013.08-CP 400013.08 |                             |       |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 400014.08-CP 400014.08 |                             |       |                    |       |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |

**COUNTY OF EL DORADO**  
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|------------------------|-----------------------------|-------|--------------------|--------|-----------------------|-------|-------------------------|-------|-------------|--------|
|                        | AUSTIN<br>PETERSEN          |       | DERRICK M.<br>REID |        | JACK<br>ROBINSON, JR. |       | R.WHITE<br>FEATHER SMIT |       | JOY WAYMIRE |        |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 400017.08-CP 400017.08 |                             |       |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 400018.08-CP 400018.08 |                             |       |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 1           | 50.00% |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 1           | 25.00% |
| 400020.08-CP 400020.08 |                             |       |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 400021.07-CP 400021.07 |                             |       |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 500001.10-CP 500001.10 |                             |       |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 500002.10-CP 500002.10 |                             |       |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 500003.10-CP 500003.10 |                             |       |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 500004.10-CP 500004.10 |                             |       |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| 500005.10-CP 500005.10 |                             |       |                    |        |                       |       |                         |       |             |        |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -      |
| Precinct OS            | 0                           | 0.00% | 1                  | 50.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00%  |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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|                        | President Libertarian Party |       |                    |        |                       |       |                         |        |             |       |
|------------------------|-----------------------------|-------|--------------------|--------|-----------------------|-------|-------------------------|--------|-------------|-------|
|                        | AUSTIN<br>PETERSEN          |       | DERRICK M.<br>REID |        | JACK<br>ROBINSON, JR. |       | R.WHITE<br>FEATHER SMIT |        | JOY WAYMIRE |       |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00% | 1                  | 20.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 500006.10-CP 500006.10 |                             |       |                    |        |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 500007.10-CP 500007.10 |                             |       |                    |        |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 500009.10-CP 500009.10 |                             |       |                    |        |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 500011.10-CP 500011.10 |                             |       |                    |        |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 500012.10-CP 500012.10 |                             |       |                    |        |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 1                       | 50.00% | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 1                       | 50.00% | 0           | 0.00% |
| 500015.10-CP 500015.10 |                             |       |                    |        |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 500020.10-CP 500020.10 |                             |       |                    |        |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 500021.10-CP 500021.10 |                             |       |                    |        |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM           | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 95U201.01-MB 95U201.01 |                             |       |                    |        |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |

**COUNTY OF EL DORADO**  
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|                        | President Libertarian Party |       |                    |        |                       |       |                         |       |             |       |
|------------------------|-----------------------------|-------|--------------------|--------|-----------------------|-------|-------------------------|-------|-------------|-------|
|                        | AUSTIN<br>PETERSEN          |       | DERRICK M.<br>REID |        | JACK<br>ROBINSON, JR. |       | R.WHITE<br>FEATHER SMIT |       | JOY WAYMIRE |       |
| Precinct OS            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| 95U201.02-MB 95U201.02 |                             |       |                    |        |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| MB Precinct            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 95U202.02-MB 95U202.02 |                             |       |                    |        |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| MB Precinct            | 0                           | 0.00% | 1                  | 25.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | 0.00% | 1                  | 25.00% | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 95U203.02-MB 95U203.02 |                             |       |                    |        |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| 95U204.01-MB 95U204.01 |                             |       |                    |        |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| MB Precinct            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 95U301.05-MB 95U301.05 |                             |       |                    |        |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| MB Precinct            | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | 0.00% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00% | 0           | 0.00% |
| 95U302.04-MB 95U302.04 |                             |       |                    |        |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| 95U302.05-MB 95U302.05 |                             |       |                    |        |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| 95U303.03-MB 95U303.03 |                             |       |                    |        |                       |       |                         |       |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| MB Precinct            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |
| Total                  | 0                           | -     | 0                  | -      | 0                     | -     | 0                       | -     | 0           | -     |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                        | President Libertarian Party |       |                    |       |                       |       |                         |        |             |       |
|------------------------|-----------------------------|-------|--------------------|-------|-----------------------|-------|-------------------------|--------|-------------|-------|
|                        | AUSTIN<br>PETERSEN          |       | DERRICK M.<br>REID |       | JACK<br>ROBINSON, JR. |       | R.WHITE<br>FEATHER SMIT |        | JOY WAYMIRE |       |
| 95U303.05-MB 95U303.05 |                             |       |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| 95U304.05-MB 95U304.05 |                             |       |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| 95U305.05-MB 95U305.05 |                             |       |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| 95U401.08-MB 95U401.08 |                             |       |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| 95U402.07-MB 95U402.07 |                             |       |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| MB Precinct            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Provisional            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 95U402.08-MB 95U402.08 |                             |       |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| MB Precinct            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Provisional            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| 95U403.08-MB 95U403.08 |                             |       |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| MB Precinct            | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 1                       | 11.11% | 0           | 0.00% |
| Provisional            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | 0.00% | 0                  | 0.00% | 0                     | 0.00% | 1                       | 11.11% | 0           | 0.00% |
| 95U404.06-MB 95U404.06 |                             |       |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Provisional            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Total                  | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| 95U404.08-MB 95U404.08 |                             |       |                    |       |                       |       |                         |        |             |       |
| Precinct TSx           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct VBM           | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |
| MB Precinct            | 0                           | -     | 0                  | -     | 0                     | -     | 0                       | -      | 0           | -     |





**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                                     | President Libertarian Party |              |                    |              |                       |              |                         |              |             |              |
|-------------------------------------|-----------------------------|--------------|--------------------|--------------|-----------------------|--------------|-------------------------|--------------|-------------|--------------|
|                                     | AUSTIN<br>PETERSEN          |              | DERRICK M.<br>REID |              | JACK<br>ROBINSON, JR. |              | R.WHITE<br>FEATHER SMIT |              | JOY WAYMIRE |              |
| Precinct TSx                        | 0                           | -            | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Precinct OS                         | 1                           | 0.76%        | 1                  | 0.76%        | 0                     | 0.00%        | 0                       | 0.00%        | 2           | 1.52%        |
| Precinct VBM                        | 13                          | 4.48%        | 2                  | 0.69%        | 2                     | 0.69%        | 7                       | 2.41%        | 3           | 1.03%        |
| MB Precinct                         | 0                           | 0.00%        | 1                  | 3.23%        | 0                     | 0.00%        | 1                       | 3.23%        | 0           | 0.00%        |
| Provisional                         | 1                           | 3.85%        | 0                  | 0.00%        | 0                     | 0.00%        | 1                       | 3.85%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>15</b>                   | <b>3.13%</b> | <b>4</b>           | <b>0.84%</b> | <b>2</b>              | <b>0.42%</b> | <b>9</b>                | <b>1.88%</b> | <b>5</b>    | <b>1.04%</b> |
| <b>UNITED STATES SENATE</b>         |                             |              |                    |              |                       |              |                         |              |             |              |
| <b>UNITED STATES SENATE</b>         |                             |              |                    |              |                       |              |                         |              |             |              |
| Precinct TSx                        | 0                           | -            | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Precinct OS                         | 1                           | 0.76%        | 1                  | 0.76%        | 0                     | 0.00%        | 0                       | 0.00%        | 2           | 1.52%        |
| Precinct VBM                        | 13                          | 4.48%        | 2                  | 0.69%        | 2                     | 0.69%        | 7                       | 2.41%        | 3           | 1.03%        |
| MB Precinct                         | 0                           | 0.00%        | 1                  | 3.23%        | 0                     | 0.00%        | 1                       | 3.23%        | 0           | 0.00%        |
| Provisional                         | 1                           | 3.85%        | 0                  | 0.00%        | 0                     | 0.00%        | 1                       | 3.85%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>15</b>                   | <b>3.13%</b> | <b>4</b>           | <b>0.84%</b> | <b>2</b>              | <b>0.42%</b> | <b>9</b>                | <b>1.88%</b> | <b>5</b>    | <b>1.04%</b> |
| <b>Total</b>                        |                             |              |                    |              |                       |              |                         |              |             |              |
| Precinct TSx                        | 0                           | -            | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Precinct OS                         | 1                           | 0.76%        | 1                  | 0.76%        | 0                     | 0.00%        | 0                       | 0.00%        | 2           | 1.52%        |
| Precinct VBM                        | 13                          | 4.48%        | 2                  | 0.69%        | 2                     | 0.69%        | 7                       | 2.41%        | 3           | 1.03%        |
| MB Precinct                         | 0                           | 0.00%        | 1                  | 3.23%        | 0                     | 0.00%        | 1                       | 3.23%        | 0           | 0.00%        |
| Provisional                         | 1                           | 3.85%        | 0                  | 0.00%        | 0                     | 0.00%        | 1                       | 3.85%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>15</b>                   | <b>3.13%</b> | <b>4</b>           | <b>0.84%</b> | <b>2</b>              | <b>0.42%</b> | <b>9</b>                | <b>1.88%</b> | <b>5</b>    | <b>1.04%</b> |
| <b>Total</b>                        |                             |              |                    |              |                       |              |                         |              |             |              |
| Precinct TSx                        | 0                           | -            | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Precinct OS                         | 1                           | 0.76%        | 1                  | 0.76%        | 0                     | 0.00%        | 0                       | 0.00%        | 2           | 1.52%        |
| Precinct VBM                        | 13                          | 4.48%        | 2                  | 0.69%        | 2                     | 0.69%        | 7                       | 2.41%        | 3           | 1.03%        |
| MB Precinct                         | 0                           | 0.00%        | 1                  | 3.23%        | 0                     | 0.00%        | 1                       | 3.23%        | 0           | 0.00%        |
| Provisional                         | 1                           | 3.85%        | 0                  | 0.00%        | 0                     | 0.00%        | 1                       | 3.85%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>15</b>                   | <b>3.13%</b> | <b>4</b>           | <b>0.84%</b> | <b>2</b>              | <b>0.42%</b> | <b>9</b>                | <b>1.88%</b> | <b>5</b>    | <b>1.04%</b> |
| <b>Total</b>                        |                             |              |                    |              |                       |              |                         |              |             |              |
| <b>UNITED STATES REPRESENTATIVE</b> |                             |              |                    |              |                       |              |                         |              |             |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                             |              |                    |              |                       |              |                         |              |             |              |
| Precinct TSx                        | 0                           | -            | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Precinct OS                         | 1                           | 0.76%        | 1                  | 0.76%        | 0                     | 0.00%        | 0                       | 0.00%        | 2           | 1.52%        |
| Precinct VBM                        | 13                          | 4.48%        | 2                  | 0.69%        | 2                     | 0.69%        | 7                       | 2.41%        | 3           | 1.03%        |
| MB Precinct                         | 0                           | 0.00%        | 1                  | 3.23%        | 0                     | 0.00%        | 1                       | 3.23%        | 0           | 0.00%        |
| Provisional                         | 1                           | 3.85%        | 0                  | 0.00%        | 0                     | 0.00%        | 1                       | 3.85%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>15</b>                   | <b>3.13%</b> | <b>4</b>           | <b>0.84%</b> | <b>2</b>              | <b>0.42%</b> | <b>9</b>                | <b>1.88%</b> | <b>5</b>    | <b>1.04%</b> |
| <b>Total</b>                        |                             |              |                    |              |                       |              |                         |              |             |              |
| Precinct TSx                        | 0                           | -            | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Precinct OS                         | 1                           | 0.76%        | 1                  | 0.76%        | 0                     | 0.00%        | 0                       | 0.00%        | 2           | 1.52%        |
| Precinct VBM                        | 13                          | 4.48%        | 2                  | 0.69%        | 2                     | 0.69%        | 7                       | 2.41%        | 3           | 1.03%        |
| MB Precinct                         | 0                           | 0.00%        | 1                  | 3.23%        | 0                     | 0.00%        | 1                       | 3.23%        | 0           | 0.00%        |
| Provisional                         | 1                           | 3.85%        | 0                  | 0.00%        | 0                     | 0.00%        | 1                       | 3.85%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>15</b>                   | <b>3.13%</b> | <b>4</b>           | <b>0.84%</b> | <b>2</b>              | <b>0.42%</b> | <b>9</b>                | <b>1.88%</b> | <b>5</b>    | <b>1.04%</b> |
| <b>Total</b>                        |                             |              |                    |              |                       |              |                         |              |             |              |
| <b>STATE SENATE</b>                 |                             |              |                    |              |                       |              |                         |              |             |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                             |              |                    |              |                       |              |                         |              |             |              |
| Precinct TSx                        | 0                           | -            | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Precinct OS                         | 1                           | 0.76%        | 1                  | 0.76%        | 0                     | 0.00%        | 0                       | 0.00%        | 2           | 1.52%        |
| Precinct VBM                        | 13                          | 4.48%        | 2                  | 0.69%        | 2                     | 0.69%        | 7                       | 2.41%        | 3           | 1.03%        |
| MB Precinct                         | 0                           | 0.00%        | 1                  | 3.23%        | 0                     | 0.00%        | 1                       | 3.23%        | 0           | 0.00%        |
| Provisional                         | 1                           | 3.85%        | 0                  | 0.00%        | 0                     | 0.00%        | 1                       | 3.85%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>15</b>                   | <b>3.13%</b> | <b>4</b>           | <b>0.84%</b> | <b>2</b>              | <b>0.42%</b> | <b>9</b>                | <b>1.88%</b> | <b>5</b>    | <b>1.04%</b> |
| <b>Total</b>                        |                             |              |                    |              |                       |              |                         |              |             |              |
| Precinct TSx                        | 0                           | -            | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Precinct OS                         | 1                           | 0.76%        | 1                  | 0.76%        | 0                     | 0.00%        | 0                       | 0.00%        | 2           | 1.52%        |
| Precinct VBM                        | 13                          | 4.48%        | 2                  | 0.69%        | 2                     | 0.69%        | 7                       | 2.41%        | 3           | 1.03%        |
| MB Precinct                         | 0                           | 0.00%        | 1                  | 3.23%        | 0                     | 0.00%        | 1                       | 3.23%        | 0           | 0.00%        |
| Provisional                         | 1                           | 3.85%        | 0                  | 0.00%        | 0                     | 0.00%        | 1                       | 3.85%        | 0           | 0.00%        |
| <b>Total</b>                        | <b>15</b>                   | <b>3.13%</b> | <b>4</b>           | <b>0.84%</b> | <b>2</b>              | <b>0.42%</b> | <b>9</b>                | <b>1.88%</b> | <b>5</b>    | <b>1.04%</b> |
| <b>Total</b>                        |                             |              |                    |              |                       |              |                         |              |             |              |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                             |              |                    |              |                       |              |                         |              |             |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                             |              |                    |              |                       |              |                         |              |             |              |
| Precinct TSx                        | 0                           | -            | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Precinct OS                         | 0                           | 0.00%        | 1                  | 1.19%        | 0                     | 0.00%        | 0                       | 0.00%        | 0           | 0.00%        |
| Precinct VBM                        | 8                           | 4.28%        | 0                  | 0.00%        | 2                     | 1.07%        | 6                       | 3.21%        | 2           | 1.07%        |
| MB Precinct                         | 0                           | 0.00%        | 1                  | 3.45%        | 0                     | 0.00%        | 1                       | 3.45%        | 0           | 0.00%        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                                   | President Libertarian Party |        |                    |        |                       |       |                         |        |             |       |
|-----------------------------------|-----------------------------|--------|--------------------|--------|-----------------------|-------|-------------------------|--------|-------------|-------|
|                                   | AUSTIN<br>PETERSEN          |        | DERRICK M.<br>REID |        | JACK<br>ROBINSON, JR. |       | R.WHITE<br>FEATHER SMIT |        | JOY WAYMIRE |       |
| Provisional                       | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Total                             | 8                           | 2.55%  | 2                  | 0.64%  | 2                     | 0.64% | 7                       | 2.23%  | 2           | 0.64% |
| <b>6TH STATE ASSEMBLY DIST</b>    |                             |        |                    |        |                       |       |                         |        |             |       |
| Precinct TSx                      | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS                       | 1                           | 2.08%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 2           | 4.17% |
| Precinct VBM                      | 5                           | 4.85%  | 2                  | 1.94%  | 0                     | 0.00% | 1                       | 0.97%  | 1           | 0.97% |
| MB Precinct                       | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Provisional                       | 1                           | 8.33%  | 0                  | 0.00%  | 0                     | 0.00% | 1                       | 8.33%  | 0           | 0.00% |
| Total                             | 7                           | 4.24%  | 2                  | 1.21%  | 0                     | 0.00% | 2                       | 1.21%  | 3           | 1.82% |
| <b>Total</b>                      |                             |        |                    |        |                       |       |                         |        |             |       |
| Precinct TSx                      | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS                       | 1                           | 0.76%  | 1                  | 0.76%  | 0                     | 0.00% | 0                       | 0.00%  | 2           | 1.52% |
| Precinct VBM                      | 13                          | 4.48%  | 2                  | 0.69%  | 2                     | 0.69% | 7                       | 2.41%  | 3           | 1.03% |
| MB Precinct                       | 0                           | 0.00%  | 1                  | 3.23%  | 0                     | 0.00% | 1                       | 3.23%  | 0           | 0.00% |
| Provisional                       | 1                           | 3.85%  | 0                  | 0.00%  | 0                     | 0.00% | 1                       | 3.85%  | 0           | 0.00% |
| Total                             | 15                          | 3.13%  | 4                  | 0.84%  | 2                     | 0.42% | 9                       | 1.88%  | 5           | 1.04% |
| <b>SUPERVISORIAL DISTRICTS</b>    |                             |        |                    |        |                       |       |                         |        |             |       |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                             |        |                    |        |                       |       |                         |        |             |       |
| Precinct TSx                      | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS                       | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 1           | 4.35% |
| Precinct VBM                      | 0                           | 0.00%  | 1                  | 1.61%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| MB Precinct                       | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Provisional                       | 1                           | 33.33% | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Total                             | 1                           | 1.11%  | 1                  | 1.11%  | 0                     | 0.00% | 0                       | 0.00%  | 1           | 1.11% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                             |        |                    |        |                       |       |                         |        |             |       |
| Precinct TSx                      | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS                       | 1                           | 2.78%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 1           | 2.78% |
| Precinct VBM                      | 8                           | 10.39% | 1                  | 1.30%  | 0                     | 0.00% | 4                       | 5.19%  | 1           | 1.30% |
| MB Precinct                       | 0                           | 0.00%  | 1                  | 11.11% | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Provisional                       | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Total                             | 9                           | 6.82%  | 2                  | 1.52%  | 0                     | 0.00% | 4                       | 3.03%  | 2           | 1.52% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                             |        |                    |        |                       |       |                         |        |             |       |
| Precinct TSx                      | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS                       | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM                      | 5                           | 9.43%  | 0                  | 0.00%  | 2                     | 3.77% | 2                       | 3.77%  | 1           | 1.89% |
| MB Precinct                       | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Provisional                       | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Total                             | 5                           | 6.76%  | 0                  | 0.00%  | 2                     | 2.70% | 2                       | 2.70%  | 1           | 1.35% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                             |        |                    |        |                       |       |                         |        |             |       |
| Precinct TSx                      | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS                       | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM                      | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 1           | 1.54% |
| MB Precinct                       | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 1                       | 7.14%  | 0           | 0.00% |
| Provisional                       | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 1                       | 14.29% | 0           | 0.00% |
| Total                             | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 2                       | 1.83%  | 1           | 0.92% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                             |        |                    |        |                       |       |                         |        |             |       |
| Precinct TSx                      | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS                       | 0                           | 0.00%  | 1                  | 3.13%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Precinct VBM                      | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 1                       | 3.03%  | 0           | 0.00% |
| MB Precinct                       | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Provisional                       | 0                           | 0.00%  | 0                  | 0.00%  | 0                     | 0.00% | 0                       | 0.00%  | 0           | 0.00% |
| Total                             | 0                           | 0.00%  | 1                  | 1.35%  | 0                     | 0.00% | 1                       | 1.35%  | 0           | 0.00% |
| <b>Total</b>                      |                             |        |                    |        |                       |       |                         |        |             |       |
| Precinct TSx                      | 0                           | -      | 0                  | -      | 0                     | -     | 0                       | -      | 0           | -     |
| Precinct OS                       | 1                           | 0.76%  | 1                  | 0.76%  | 0                     | 0.00% | 0                       | 0.00%  | 2           | 1.52% |
| Precinct VBM                      | 13                          | 4.48%  | 2                  | 0.69%  | 2                     | 0.69% | 7                       | 2.41%  | 3           | 1.03% |
| MB Precinct                       | 0                           | 0.00%  | 1                  | 3.23%  | 0                     | 0.00% | 1                       | 3.23%  | 0           | 0.00% |
| Provisional                       | 1                           | 3.85%  | 0                  | 0.00%  | 0                     | 0.00% | 1                       | 3.85%  | 0           | 0.00% |
| Total                             | 15                          | 3.13%  | 4                  | 0.84%  | 2                     | 0.42% | 9                       | 1.88%  | 5           | 1.04% |

**COUNTY OF EL DORADO**  
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|                                 | President Libertarian Party |               |                    |              |                       |              |                         |              |             |              |
|---------------------------------|-----------------------------|---------------|--------------------|--------------|-----------------------|--------------|-------------------------|--------------|-------------|--------------|
|                                 | AUSTIN<br>PETERSEN          |               | DERRICK M.<br>REID |              | JACK<br>ROBINSON, JR. |              | R.WHITE<br>FEATHER SMIT |              | JOY WAYMIRE |              |
| <b>CITIES</b>                   |                             |               |                    |              |                       |              |                         |              |             |              |
| <b>CITY OF PLACERVILLE</b>      |                             |               |                    |              |                       |              |                         |              |             |              |
| Precinct TSx                    | 0                           | -             | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Precinct OS                     | 0                           | 0.00%         | 0                  | 0.00%        | 0                     | 0.00%        | 0                       | 0.00%        | 0           | 0.00%        |
| Precinct VBM                    | 2                           | 14.29%        | 0                  | 0.00%        | 0                     | 0.00%        | 1                       | 7.14%        | 0           | 0.00%        |
| MB Precinct                     | 0                           | -             | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Provisional                     | 0                           | -             | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| <b>Total</b>                    | <b>2</b>                    | <b>11.76%</b> | <b>0</b>           | <b>0.00%</b> | <b>0</b>              | <b>0.00%</b> | <b>1</b>                | <b>5.88%</b> | <b>0</b>    | <b>0.00%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                             |               |                    |              |                       |              |                         |              |             |              |
| Precinct TSx                    | 0                           | -             | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Precinct OS                     | 0                           | 0.00%         | 1                  | 7.14%        | 0                     | 0.00%        | 0                       | 0.00%        | 0           | 0.00%        |
| Precinct VBM                    | 0                           | 0.00%         | 0                  | 0.00%        | 0                     | 0.00%        | 0                       | 0.00%        | 0           | 0.00%        |
| MB Precinct                     | 0                           | -             | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Provisional                     | 0                           | 0.00%         | 0                  | 0.00%        | 0                     | 0.00%        | 0                       | 0.00%        | 0           | 0.00%        |
| <b>Total</b>                    | <b>0</b>                    | <b>0.00%</b>  | <b>1</b>           | <b>3.03%</b> | <b>0</b>              | <b>0.00%</b> | <b>0</b>                | <b>0.00%</b> | <b>0</b>    | <b>0.00%</b> |
| <b>UNINCORPORATED AREA</b>      |                             |               |                    |              |                       |              |                         |              |             |              |
| Precinct TSx                    | 0                           | -             | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Precinct OS                     | 1                           | 0.87%         | 0                  | 0.00%        | 0                     | 0.00%        | 0                       | 0.00%        | 2           | 1.74%        |
| Precinct VBM                    | 11                          | 4.26%         | 2                  | 0.78%        | 2                     | 0.78%        | 6                       | 2.33%        | 3           | 1.16%        |
| MB Precinct                     | 0                           | 0.00%         | 1                  | 3.23%        | 0                     | 0.00%        | 1                       | 3.23%        | 0           | 0.00%        |
| Provisional                     | 1                           | 4.00%         | 0                  | 0.00%        | 0                     | 0.00%        | 1                       | 4.00%        | 0           | 0.00%        |
| <b>Total</b>                    | <b>13</b>                   | <b>3.03%</b>  | <b>3</b>           | <b>0.70%</b> | <b>2</b>              | <b>0.47%</b> | <b>8</b>                | <b>1.86%</b> | <b>5</b>    | <b>1.17%</b> |
| <b>Total</b>                    |                             |               |                    |              |                       |              |                         |              |             |              |
| Precinct TSx                    | 0                           | -             | 0                  | -            | 0                     | -            | 0                       | -            | 0           | -            |
| Precinct OS                     | 1                           | 0.76%         | 1                  | 0.76%        | 0                     | 0.00%        | 0                       | 0.00%        | 2           | 1.52%        |
| Precinct VBM                    | 13                          | 4.48%         | 2                  | 0.69%        | 2                     | 0.69%        | 7                       | 2.41%        | 3           | 1.03%        |
| MB Precinct                     | 0                           | 0.00%         | 1                  | 3.23%        | 0                     | 0.00%        | 1                       | 3.23%        | 0           | 0.00%        |
| Provisional                     | 1                           | 3.85%         | 0                  | 0.00%        | 0                     | 0.00%        | 1                       | 3.85%        | 0           | 0.00%        |
| <b>Total</b>                    | <b>15</b>                   | <b>3.13%</b>  | <b>4</b>           | <b>0.84%</b> | <b>2</b>              | <b>0.42%</b> | <b>9</b>                | <b>1.88%</b> | <b>5</b>    | <b>1.04%</b> |

**COUNTY OF EL DORADO**  
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|                        | President Peace and Freedom Party |          |               |             |              |                   |                  |         |   |       |
|------------------------|-----------------------------------|----------|---------------|-------------|--------------|-------------------|------------------|---------|---|-------|
|                        | Reg. Voters                       | Vote For | Times Counted | Total Votes | LYNN S. KAHN | GLORIA E. LA RIVA | MONICA MOOREHEAD |         |   |       |
| Jurisdiction Wide      |                                   |          |               |             |              |                   |                  |         |   |       |
| 100001.11-CP 100001.11 |                                   |          |               |             |              |                   |                  |         |   |       |
| Precinct TSx           | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct OS            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct VBM           | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| MB Precinct            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Provisional            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Total                  | 2                                 | 5        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| 100002.11-CP 100002.11 |                                   |          |               |             |              |                   |                  |         |   |       |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| 100003.11-CP 100003.11 |                                   |          |               |             |              |                   |                  |         |   |       |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| 100004.11-CP 100004.11 |                                   |          |               |             |              |                   |                  |         |   |       |
| Precinct TSx           | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct OS            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct VBM           | 2                                 | 1        | 1             | 1           | 0            | 0.00%             | 1                | 100.00% | 0 | 0.00% |
| MB Precinct            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Provisional            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Total                  | 2                                 | 5        | 1             | 1           | 0            | 0.00%             | 1                | 100.00% | 0 | 0.00% |
| 100005.11-CP 100005.11 |                                   |          |               |             |              |                   |                  |         |   |       |
| Precinct TSx           | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct OS            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct VBM           | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| MB Precinct            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Provisional            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Total                  | 2                                 | 5        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| 100006.11-CP 100006.11 |                                   |          |               |             |              |                   |                  |         |   |       |
| Precinct TSx           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct OS            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct VBM           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| MB Precinct            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Provisional            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Total                  | 1                                 | 5        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| 100008.11-CP 100008.11 |                                   |          |               |             |              |                   |                  |         |   |       |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| 100009.11-CP 100009.11 |                                   |          |               |             |              |                   |                  |         |   |       |
| Precinct TSx           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct OS            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct VBM           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| MB Precinct            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Provisional            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Total                  | 1                                 | 5        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| 100012.11-CP 100012.11 |                                   |          |               |             |              |                   |                  |         |   |       |
| Precinct TSx           | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct OS            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |
| Precinct VBM           | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -       | 0 |       |

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|                        | President Peace and Freedom Party |          |               |             |              |                   |                  |       |   |
|------------------------|-----------------------------------|----------|---------------|-------------|--------------|-------------------|------------------|-------|---|
|                        | Reg. Voters                       | Vote For | Times Counted | Total Votes | LYNN S. KAHN | GLORIA E. LA RIVA | MONICA MOOREHEAD |       |   |
| MB Precinct            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 2                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 100013.11-CP 100013.11 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 1                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 100014.11-CP 100014.11 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 1                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 100022.11-CP 100022.11 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 1                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 100023.11-CP 100023.11 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 2                                 | 1        | 1             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 2                                 | 5        | 1             | 0           | 0            | -                 | 0                | -     | 0 |
| 200003.12-CP 200003.12 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 6                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 6                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 6                                 | 1        | 1             | 1           | 0            | 0.00%             | 0                | 0.00% | 0 |
| MB Precinct            | 6                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 6                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 6                                 | 5        | 1             | 1           | 0            | 0.00%             | 0                | 0.00% | 0 |
| 200004.12-CP 200004.12 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 3                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 3                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 3                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 3                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 3                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 3                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 200005.02-CP 200005.02 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 1                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 200006.12-CP 200006.12 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 200008.12-CP 200008.12 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |

**COUNTY OF EL DORADO**  
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|                        | President Peace and Freedom Party |          |               |             |              |                   |                  |       |   |
|------------------------|-----------------------------------|----------|---------------|-------------|--------------|-------------------|------------------|-------|---|
|                        | Reg. Voters                       | Vote For | Times Counted | Total Votes | LYNN S. KAHN | GLORIA E. LA RIVA | MONICA MOOREHEAD |       |   |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 200010.12-CP 200010.12 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 5                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 5                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 5                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 5                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 5                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 5                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 200012.12-CP 200012.12 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 3                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 3                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 3                                 | 1        | 2             | 1           | 0            | 0.00%             | 0                | 0.00% | 0 |
| MB Precinct            | 3                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 3                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 3                                 | 5        | 2             | 1           | 0            | 0.00%             | 0                | 0.00% | 0 |
| 200013.12-CP 200013.12 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 1                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 200014.12-CP 200014.12 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 4                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 4                                 | 1        | 1             | 1           | 0            | 0.00%             | 0                | 0.00% | 0 |
| Precinct VBM           | 4                                 | 1        | 1             | 1           | 0            | 0.00%             | 0                | 0.00% | 1 |
| MB Precinct            | 4                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 4                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 4                                 | 5        | 2             | 2           | 0            | 0.00%             | 0                | 0.00% | 1 |
| 200015.02-CP 200015.02 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 5                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 5                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 5                                 | 1        | 1             | 1           | 0            | 0.00%             | 0                | 0.00% | 0 |
| MB Precinct            | 5                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 5                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 5                                 | 5        | 1             | 1           | 0            | 0.00%             | 0                | 0.00% | 0 |
| 200016.12-CP 200016.12 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 200016.13-CP 200016.13 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 2                                 | 1        | 1             | 1           | 0            | 0.00%             | 0                | 0.00% | 0 |
| Precinct VBM           | 2                                 | 1        | 1             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 2                                 | 5        | 2             | 1           | 0            | 0.00%             | 0                | 0.00% | 0 |
| 200017.02-CP 200017.02 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 2                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |

**COUNTY OF EL DORADO**  
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|                        | Reg. Voters | Vote For | Times Counted | Total Votes | LYNN S. KAHN | GLORIA E. LA RIVA | MONICA MOOREHEAD |       |          |
|------------------------|-------------|----------|---------------|-------------|--------------|-------------------|------------------|-------|----------|
| 200018.02-CP 200018.02 |             |          |               |             |              |                   |                  |       |          |
| Precinct TSx           | 5           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct OS            | 5           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct VBM           | 5           | 1        | 2             | 2           | 0            | 0.00%             | 0                | 0.00% | 1 50.00% |
| MB Precinct            | 5           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Provisional            | 5           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Total                  | 5           | 5        | 2             | 2           | 0            | 0.00%             | 0                | 0.00% | 1 50.00% |
| 200020.02-CP 200020.02 |             |          |               |             |              |                   |                  |       |          |
| Precinct TSx           | 5           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct OS            | 5           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct VBM           | 5           | 1        | 1             | 1           | 0            | 0.00%             | 0                | 0.00% | 0 0.00%  |
| MB Precinct            | 5           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Provisional            | 5           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Total                  | 5           | 5        | 1             | 1           | 0            | 0.00%             | 0                | 0.00% | 0 0.00%  |
| 200021.01-CP 200021.01 |             |          |               |             |              |                   |                  |       |          |
| Precinct TSx           | 0           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct OS            | 0           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct VBM           | 0           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| MB Precinct            | 0           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Provisional            | 0           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Total                  | 0           | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| 200021.02-CP 200021.02 |             |          |               |             |              |                   |                  |       |          |
| Precinct TSx           | 3           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct OS            | 3           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct VBM           | 3           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| MB Precinct            | 3           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Provisional            | 3           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Total                  | 3           | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| 200024.02-CP 200024.02 |             |          |               |             |              |                   |                  |       |          |
| Precinct TSx           | 2           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct OS            | 2           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct VBM           | 2           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| MB Precinct            | 2           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Provisional            | 2           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Total                  | 2           | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| 300001.04-CP 300001.04 |             |          |               |             |              |                   |                  |       |          |
| Precinct TSx           | 2           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct OS            | 2           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct VBM           | 2           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| MB Precinct            | 2           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Provisional            | 2           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Total                  | 2           | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| 300001.05-CP 300001.05 |             |          |               |             |              |                   |                  |       |          |
| Precinct TSx           | 3           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct OS            | 3           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct VBM           | 3           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| MB Precinct            | 3           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Provisional            | 3           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Total                  | 3           | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| 300003.05-CP 300003.05 |             |          |               |             |              |                   |                  |       |          |
| Precinct TSx           | 9           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct OS            | 9           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct VBM           | 9           | 1        | 1             | 0           | 0            | -                 | 0                | -     | 0        |
| MB Precinct            | 9           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Provisional            | 9           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Total                  | 9           | 5        | 1             | 0           | 0            | -                 | 0                | -     | 0        |
| 300004.05-CP 300004.05 |             |          |               |             |              |                   |                  |       |          |
| Precinct TSx           | 8           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct OS            | 8           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |
| Precinct VBM           | 8           | 1        | 1             | 0           | 0            | -                 | 0                | -     | 0        |
| MB Precinct            | 8           | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0        |

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|                        | Reg. Voters | Vote For | Times Counted | Total Votes | LYNN S. KAHN | GLORIA E. LA RIVA | MONICA MOOREHEAD |
|------------------------|-------------|----------|---------------|-------------|--------------|-------------------|------------------|
| Provisional            | 8           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 8           | 5        | 1             | 0           | 0            | 0                 | 0                |
| 300005.05-CP 300005.05 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 6           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 6           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 6           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 6           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 6           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 6           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 300008.05-CP 300008.05 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 5           | 1        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| Precinct VBM           | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 5           | 5        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| 300009.04-CP 300009.04 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 5           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 300010.04-CP 300010.04 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 5           | 1        | 1             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 5           | 1        | 2             | 2           | 0 0.00%      | 1 50.00%          | 1 50.00%         |
| MB Precinct            | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 5           | 5        | 3             | 2           | 0 0.00%      | 1 50.00%          | 1 50.00%         |
| 300011.04-CP 300011.04 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 3           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 300013.05-CP 300013.05 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 4           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 300015.04-CP 300015.04 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 1           | 1        | 1             | 0           | 0            | 0                 | 0                |
| Total                  | 1           | 5        | 1             | 0           | 0            | 0                 | 0                |
| 300015.05-CP 300015.05 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 0           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 300017.05-CP 300017.05 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |



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|                        | Reg. Voters | Vote For | Times Counted | Total Votes | LYNN S. KAHN | GLORIA E. LA RIVA | MONICA MOOREHEAD |
|------------------------|-------------|----------|---------------|-------------|--------------|-------------------|------------------|
| Precinct VBM           | 4           | 1        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| MB Precinct            | 4           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Provisional            | 4           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Total                  | 4           | 5        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| 300018.04-CP 300018.04 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 5           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct OS            | 5           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct VBM           | 5           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| MB Precinct            | 5           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Provisional            | 5           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Total                  | 5           | 5        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| 300020.04-CP 300020.04 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 3           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct OS            | 3           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct VBM           | 3           | 1        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| MB Precinct            | 3           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Provisional            | 3           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Total                  | 3           | 5        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| 300021.03-CP 300021.03 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 4           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct OS            | 4           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct VBM           | 4           | 1        | 1             | 1           | 0 0.00%      | 0 0.00%           | 1 100.00%        |
| MB Precinct            | 4           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Provisional            | 4           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Total                  | 4           | 5        | 1             | 1           | 0 0.00%      | 0 0.00%           | 1 100.00%        |
| 300022.05-CP 300022.05 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 6           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct OS            | 6           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct VBM           | 6           | 1        | 1             | 0           | 0 -          | 0 -               | 0 -              |
| MB Precinct            | 6           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Provisional            | 6           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Total                  | 6           | 5        | 1             | 0           | 0 -          | 0 -               | 0 -              |
| 300023.03-CP 300023.03 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 5           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct OS            | 5           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct VBM           | 5           | 1        | 1             | 1           | 0 0.00%      | 1 100.00%         | 0 0.00%          |
| MB Precinct            | 5           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Provisional            | 5           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Total                  | 5           | 5        | 1             | 1           | 0 0.00%      | 1 100.00%         | 0 0.00%          |
| 300023.05-CP 300023.05 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 4           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct OS            | 4           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct VBM           | 4           | 1        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| MB Precinct            | 4           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Provisional            | 4           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Total                  | 4           | 5        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| 400001.14-CP 400001.14 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 1           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct OS            | 1           | 1        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| Precinct VBM           | 1           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| MB Precinct            | 1           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Provisional            | 1           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Total                  | 1           | 5        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| 400002.08-CP 400002.08 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 3           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct OS            | 3           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Precinct VBM           | 3           | 1        | 2             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| MB Precinct            | 3           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Provisional            | 3           | 1        | 0             | 0           | 0 -          | 0 -               | 0 -              |
| Total                  | 3           | 5        | 2             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| 400003.08-CP 400003.08 |             |          |               |             |              |                   |                  |

**COUNTY OF EL DORADO**  
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President Peace and Freedom Party

|                        | Reg. Voters | Vote For | Times Counted | Total Votes | LYNN S. KAHN | GLORIA E. LA RIVA | MONICA MOOREHEAD |
|------------------------|-------------|----------|---------------|-------------|--------------|-------------------|------------------|
| Precinct TSx           | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 0           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 400004.14-CP 400004.14 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 3           | 1        | 1             | 1           | 0            | 0.00%             | 0 0.00%          |
| Total                  | 3           | 5        | 1             | 1           | 0            | 0.00%             | 0 0.00%          |
| 400005.08-CP 400005.08 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 4           | 1        | 1             | 1           | 0            | 0.00%             | 0 0.00%          |
| MB Precinct            | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 4           | 5        | 1             | 1           | 0            | 0.00%             | 0 0.00%          |
| 400006.08-CP 400006.08 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 3           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 400007.08-CP 400007.08 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 5           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 400010.08-CP 400010.08 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 3           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 400012.08-CP 400012.08 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 2           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 400013.08-CP 400013.08 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 1           | 1        | 1             | 1           | 0            | 0.00%             | 0 0.00%          |
| Precinct VBM           | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 1           | 5        | 1             | 1           | 0            | 0.00%             | 0 0.00%          |
| 400014.08-CP 400014.08 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |

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|                        | Reg. Voters | Vote For | Times Counted | Total Votes | LYNN S. KAHN | GLORIA E. LA RIVA | MONICA MOOREHEAD |
|------------------------|-------------|----------|---------------|-------------|--------------|-------------------|------------------|
| Total                  | 2           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 400017.08-CP 400017.08 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 2           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 400018.08-CP 400018.08 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 0           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 400020.08-CP 400020.08 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 1           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 400021.07-CP 400021.07 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 1           | 1        | 1             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 1           | 5        | 1             | 0           | 0            | 0                 | 0                |
| 500001.10-CP 500001.10 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 5           | 1        | 1             | 1           | 0.00%        | 1 100.00%         | 0 0.00%          |
| MB Precinct            | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 5           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 5           | 5        | 1             | 1           | 0 0.00%      | 1 100.00%         | 0 0.00%          |
| 500002.10-CP 500002.10 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 9           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 9           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 9           | 1        | 1             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 9           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 9           | 1        | 2             | 2           | 0 0.00%      | 2 100.00%         | 0 0.00%          |
| Total                  | 9           | 5        | 3             | 2           | 0 0.00%      | 2 100.00%         | 0 0.00%          |
| 500003.10-CP 500003.10 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 3           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 500004.10-CP 500004.10 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 7           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 7           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 7           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 7           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 7           | 1        | 1             | 0           | 0            | 0                 | 0                |
| Total                  | 7           | 5        | 1             | 0           | 0            | 0                 | 0                |
| 500005.10-CP 500005.10 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 7           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 7           | 1        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| Precinct VBM           | 7           | 1        | 0             | 0           | 0            | 0                 | 0                |

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|                        | Reg. Voters | Vote For | Times Counted | Total Votes | LYNN S. KAHN | GLORIA E. LA RIVA | MONICA MOOREHEAD |
|------------------------|-------------|----------|---------------|-------------|--------------|-------------------|------------------|
| MB Precinct            | 7           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 7           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 7           | 5        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| 500006.10-CP 500006.10 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 7           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 7           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 7           | 1        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| MB Precinct            | 7           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 7           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 7           | 5        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| 500007.10-CP 500007.10 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 11          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 11          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 11          | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 11          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 11          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 11          | 5        | 0             | 0           | 0            | 0                 | 0                |
| 500009.10-CP 500009.10 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 11          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 11          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 11          | 1        | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| MB Precinct            | 11          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 11          | 1        | 1             | 0           | 0            | 0                 | 0                |
| Total                  | 11          | 5        | 2             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| 500011.10-CP 500011.10 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 12          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 12          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 12          | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 12          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 12          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 12          | 5        | 0             | 0           | 0            | 0                 | 0                |
| 500012.10-CP 500012.10 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 10          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 10          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 10          | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 10          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 10          | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 10          | 5        | 0             | 0           | 0            | 0                 | 0                |
| 500015.10-CP 500015.10 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 1           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 500020.10-CP 500020.10 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 2           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 500021.10-CP 500021.10 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS            | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM           | 4           | 1        | 1             | 0           | 0            | 0                 | 0                |
| MB Precinct            | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional            | 4           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                  | 4           | 5        | 1             | 0           | 0            | 0                 | 0                |
| 95U201.01-MB 95U201.01 |             |          |               |             |              |                   |                  |
| Precinct TSx           | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |

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|                        | President Peace and Freedom Party |          |               |             |              |                   |                  |       |   |
|------------------------|-----------------------------------|----------|---------------|-------------|--------------|-------------------|------------------|-------|---|
|                        | Reg. Voters                       | Vote For | Times Counted | Total Votes | LYNN S. KAHN | GLORIA E. LA RIVA | MONICA MOOREHEAD |       |   |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 95U201.02-MB 95U201.02 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 1                                 | 1        | 1             | 1           | 0            | 0.00%             | 0                | 0.00% | 0 |
| Provisional            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 1                                 | 5        | 1             | 1           | 0            | 0.00%             | 0                | 0.00% | 0 |
| 95U202.02-MB 95U202.02 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 1                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 95U203.02-MB 95U203.02 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 95U204.01-MB 95U204.01 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 1                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 95U301.05-MB 95U301.05 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 95U302.04-MB 95U302.04 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 95U302.05-MB 95U302.05 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 95U303.03-MB 95U303.03 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 1                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 1                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                        | President Peace and Freedom Party |          |               |             |              |                   |                  |       |   |
|------------------------|-----------------------------------|----------|---------------|-------------|--------------|-------------------|------------------|-------|---|
|                        | Reg. Voters                       | Vote For | Times Counted | Total Votes | LYNN S. KAHN | GLORIA E. LA RIVA | MONICA MOOREHEAD |       |   |
| 95U303.05-MB 95U303.05 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 95U304.05-MB 95U304.05 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 95U305.05-MB 95U305.05 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 95U401.08-MB 95U401.08 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 95U402.07-MB 95U402.07 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 2                                 | 1        | 1             | 1           | 1            | 100.00%           | 0                | 0.00% | 0 |
| Provisional            | 2                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 2                                 | 5        | 1             | 1           | 1            | 100.00%           | 0                | 0.00% | 0 |
| 95U402.08-MB 95U402.08 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 95U403.08-MB 95U403.08 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 4                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 4                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 4                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 4                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 4                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 4                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 95U404.06-MB 95U404.06 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Provisional            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Total                  | 0                                 | 5        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| 95U404.08-MB 95U404.08 |                                   |          |               |             |              |                   |                  |       |   |
| Precinct TSx           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct OS            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| Precinct VBM           | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |
| MB Precinct            | 0                                 | 1        | 0             | 0           | 0            | -                 | 0                | -     | 0 |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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President Peace and Freedom Party

|                               | Reg. Voters | Vote For | Times Counted | Total Votes | LYNN S. KAHN | GLORIA E. LA RIVA | MONICA MOOREHEAD |
|-------------------------------|-------------|----------|---------------|-------------|--------------|-------------------|------------------|
| Provisional                   | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                         | 0           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 95U501.09-MB 95U501.09        |             |          |               |             |              |                   |                  |
| Precinct TSx                  | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS                   | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM                  | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct                   | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional                   | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                         | 0           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 95U501.10-MB 95U501.10        |             |          |               |             |              |                   |                  |
| Precinct TSx                  | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS                   | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM                  | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct                   | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional                   | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                         | 0           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 95U598.10-MB 95U598.10        |             |          |               |             |              |                   |                  |
| Precinct TSx                  | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS                   | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM                  | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct                   | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional                   | 3           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                         | 3           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 95U599.10-MB 95U599.10        |             |          |               |             |              |                   |                  |
| Precinct TSx                  | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS                   | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM                  | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct                   | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional                   | 0           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                         | 0           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 96U101.11-MB 96U101.11        |             |          |               |             |              |                   |                  |
| Precinct TSx                  | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS                   | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM                  | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct                   | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Provisional                   | 1           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                         | 1           | 5        | 0             | 0           | 0            | 0                 | 0                |
| 96U401.14-MB 96U401.14        |             |          |               |             |              |                   |                  |
| Precinct TSx                  | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS                   | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Precinct VBM                  | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| MB Precinct                   | 2           | 1        | 1             | 1           | 0 0.00%      | 1 100.00%         | 0 0.00%          |
| Provisional                   | 2           | 1        | 0             | 0           | 0            | 0                 | 0                |
| Total                         | 2           | 5        | 1             | 1           | 0 0.00%      | 1 100.00%         | 0 0.00%          |
| Total                         | 280         | 102      | 0             | 0           | 0            | 0                 | 0                |
| Precinct TSx                  | 280         | 102      | 7             | 6           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| Precinct OS                   | 280         | 102      | 30            | 20          | 0 0.00%      | 4 20.00%          | 4 20.00%         |
| Precinct VBM                  | 280         | 102      | 3             | 3           | 1 33.33%     | 1 33.33%          | 0 0.00%          |
| MB Precinct                   | 280         | 102      | 6             | 3           | 0 0.00%      | 2 66.67%          | 0 0.00%          |
| Provisional                   | 280         | 102      | 46            | 32          | 1 3.13%      | 7 21.88%          | 4 12.50%         |
| Total                         | 280         | 510      | 46            | 32          | 1 3.13%      | 7 21.88%          | 4 12.50%         |
| STATE BOARD OF EQUALIZATION   |             |          |               |             |              |                   |                  |
| STATE BOARD OF EQUALIZATION 1 |             |          |               |             |              |                   |                  |
| Precinct TSx                  | 280         | 102      | 0             | 0           | 0            | 0                 | 0                |
| Precinct OS                   | 280         | 102      | 7             | 6           | 0 0.00%      | 0 0.00%           | 0 0.00%          |
| Precinct VBM                  | 280         | 102      | 30            | 20          | 0 0.00%      | 4 20.00%          | 4 20.00%         |
| MB Precinct                   | 280         | 102      | 3             | 3           | 1 33.33%     | 1 33.33%          | 0 0.00%          |
| Provisional                   | 280         | 102      | 6             | 3           | 0 0.00%      | 2 66.67%          | 0 0.00%          |
| Total                         | 280         | 510      | 46            | 32          | 1 3.13%      | 7 21.88%          | 4 12.50%         |
| Total                         |             |          |               |             |              |                   |                  |

**COUNTY OF EL DORADO**  
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President Peace and Freedom Party

|                                     | Reg. Voters | Vote For   | Times Counted | Total Votes | LYNN S. KAHN   | GLORIA E. LA RIVA | MONICA MOOREHEAD |
|-------------------------------------|-------------|------------|---------------|-------------|----------------|-------------------|------------------|
| Precinct TSx                        | 280         | 102        | 0             | 0           | 0              | 0                 | 0                |
| Precinct OS                         | 280         | 102        | 7             | 6           | 0 0.00%        | 0 0.00%           | 0 0.00%          |
| Precinct VBM                        | 280         | 102        | 30            | 20          | 0 0.00%        | 4 20.00%          | 4 20.00%         |
| MB Precinct                         | 280         | 102        | 3             | 3           | 1 33.33%       | 1 33.33%          | 0 0.00%          |
| Provisional                         | 280         | 102        | 6             | 3           | 0 0.00%        | 2 66.67%          | 0 0.00%          |
| <b>Total</b>                        | <b>280</b>  | <b>510</b> | <b>46</b>     | <b>32</b>   | <b>1 3.13%</b> | <b>7 21.88%</b>   | <b>4 12.50%</b>  |
| <b>UNITED STATES SENATE</b>         |             |            |               |             |                |                   |                  |
| <b>UNITED STATES SENATE</b>         |             |            |               |             |                |                   |                  |
| Precinct TSx                        | 280         | 102        | 0             | 0           | 0              | 0                 | 0                |
| Precinct OS                         | 280         | 102        | 7             | 6           | 0 0.00%        | 0 0.00%           | 0 0.00%          |
| Precinct VBM                        | 280         | 102        | 30            | 20          | 0 0.00%        | 4 20.00%          | 4 20.00%         |
| MB Precinct                         | 280         | 102        | 3             | 3           | 1 33.33%       | 1 33.33%          | 0 0.00%          |
| Provisional                         | 280         | 102        | 6             | 3           | 0 0.00%        | 2 66.67%          | 0 0.00%          |
| <b>Total</b>                        | <b>280</b>  | <b>510</b> | <b>46</b>     | <b>32</b>   | <b>1 3.13%</b> | <b>7 21.88%</b>   | <b>4 12.50%</b>  |
| <b>Total</b>                        |             |            |               |             |                |                   |                  |
| Precinct TSx                        | 280         | 102        | 0             | 0           | 0              | 0                 | 0                |
| Precinct OS                         | 280         | 102        | 7             | 6           | 0 0.00%        | 0 0.00%           | 0 0.00%          |
| Precinct VBM                        | 280         | 102        | 30            | 20          | 0 0.00%        | 4 20.00%          | 4 20.00%         |
| MB Precinct                         | 280         | 102        | 3             | 3           | 1 33.33%       | 1 33.33%          | 0 0.00%          |
| Provisional                         | 280         | 102        | 6             | 3           | 0 0.00%        | 2 66.67%          | 0 0.00%          |
| <b>Total</b>                        | <b>280</b>  | <b>510</b> | <b>46</b>     | <b>32</b>   | <b>1 3.13%</b> | <b>7 21.88%</b>   | <b>4 12.50%</b>  |
| <b>UNITED STATES REPRESENTATIVE</b> |             |            |               |             |                |                   |                  |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |             |            |               |             |                |                   |                  |
| Precinct TSx                        | 280         | 102        | 0             | 0           | 0              | 0                 | 0                |
| Precinct OS                         | 280         | 102        | 7             | 6           | 0 0.00%        | 0 0.00%           | 0 0.00%          |
| Precinct VBM                        | 280         | 102        | 30            | 20          | 0 0.00%        | 4 20.00%          | 4 20.00%         |
| MB Precinct                         | 280         | 102        | 3             | 3           | 1 33.33%       | 1 33.33%          | 0 0.00%          |
| Provisional                         | 280         | 102        | 6             | 3           | 0 0.00%        | 2 66.67%          | 0 0.00%          |
| <b>Total</b>                        | <b>280</b>  | <b>510</b> | <b>46</b>     | <b>32</b>   | <b>1 3.13%</b> | <b>7 21.88%</b>   | <b>4 12.50%</b>  |
| <b>Total</b>                        |             |            |               |             |                |                   |                  |
| Precinct TSx                        | 280         | 102        | 0             | 0           | 0              | 0                 | 0                |
| Precinct OS                         | 280         | 102        | 7             | 6           | 0 0.00%        | 0 0.00%           | 0 0.00%          |
| Precinct VBM                        | 280         | 102        | 30            | 20          | 0 0.00%        | 4 20.00%          | 4 20.00%         |
| MB Precinct                         | 280         | 102        | 3             | 3           | 1 33.33%       | 1 33.33%          | 0 0.00%          |
| Provisional                         | 280         | 102        | 6             | 3           | 0 0.00%        | 2 66.67%          | 0 0.00%          |
| <b>Total</b>                        | <b>280</b>  | <b>510</b> | <b>46</b>     | <b>32</b>   | <b>1 3.13%</b> | <b>7 21.88%</b>   | <b>4 12.50%</b>  |
| <b>STATE SENATE</b>                 |             |            |               |             |                |                   |                  |
| <b>1ST STATE SENATE DISTRICT</b>    |             |            |               |             |                |                   |                  |
| Precinct TSx                        | 280         | 102        | 0             | 0           | 0              | 0                 | 0                |
| Precinct OS                         | 280         | 102        | 7             | 6           | 0 0.00%        | 0 0.00%           | 0 0.00%          |
| Precinct VBM                        | 280         | 102        | 30            | 20          | 0 0.00%        | 4 20.00%          | 4 20.00%         |
| MB Precinct                         | 280         | 102        | 3             | 3           | 1 33.33%       | 1 33.33%          | 0 0.00%          |
| Provisional                         | 280         | 102        | 6             | 3           | 0 0.00%        | 2 66.67%          | 0 0.00%          |
| <b>Total</b>                        | <b>280</b>  | <b>510</b> | <b>46</b>     | <b>32</b>   | <b>1 3.13%</b> | <b>7 21.88%</b>   | <b>4 12.50%</b>  |
| <b>Total</b>                        |             |            |               |             |                |                   |                  |
| Precinct TSx                        | 280         | 102        | 0             | 0           | 0              | 0                 | 0                |
| Precinct OS                         | 280         | 102        | 7             | 6           | 0 0.00%        | 0 0.00%           | 0 0.00%          |
| Precinct VBM                        | 280         | 102        | 30            | 20          | 0 0.00%        | 4 20.00%          | 4 20.00%         |
| MB Precinct                         | 280         | 102        | 3             | 3           | 1 33.33%       | 1 33.33%          | 0 0.00%          |
| Provisional                         | 280         | 102        | 6             | 3           | 0 0.00%        | 2 66.67%          | 0 0.00%          |
| <b>Total</b>                        | <b>280</b>  | <b>510</b> | <b>46</b>     | <b>32</b>   | <b>1 3.13%</b> | <b>7 21.88%</b>   | <b>4 12.50%</b>  |
| <b>STATE ASSEMBLY DISTRICTS</b>     |             |            |               |             |                |                   |                  |
| <b>5TH STATE ASSEMBLY DIST</b>      |             |            |               |             |                |                   |                  |
| Precinct TSx                        | 234         | 75         | 0             | 0           | 0              | 0                 | 0                |
| Precinct OS                         | 234         | 75         | 4             | 3           | 0 0.00%        | 0 0.00%           | 0 0.00%          |
| Precinct VBM                        | 234         | 75         | 23            | 16          | 0 0.00%        | 3 18.75%          | 3 18.75%         |
| MB Precinct                         | 234         | 75         | 2             | 2           | 1 50.00%       | 0 0.00%           | 0 0.00%          |



**COUNTY OF EL DORADO**  
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President Peace and Freedom Party

|                                   | Reg. Voters | Vote For | Times Counted | Total Votes | LYNN S. KAHN | GLORIA E. LA RIVA | MONICA MOOREHEAD |  |
|-----------------------------------|-------------|----------|---------------|-------------|--------------|-------------------|------------------|--|
| Provisional                       | 234         | 75       | 5             | 2           | 0 0.00%      | 2 100.00%         | 0 0.00%          |  |
| Total                             | 234         | 375      | 34            | 23          | 1 4.35%      | 5 21.74%          | 3 13.04%         |  |
| <b>6TH STATE ASSEMBLY DIST</b>    |             |          |               |             |              |                   |                  |  |
| Precinct TSx                      | 46          | 27       | 0             | 0           | 0 -          | 0 -               | 0 -              |  |
| Precinct OS                       | 46          | 27       | 3             | 3           | 0 0.00%      | 0 0.00%           | 0 0.00%          |  |
| Precinct VBM                      | 46          | 27       | 7             | 4           | 0 0.00%      | 1 25.00%          | 1 25.00%         |  |
| MB Precinct                       | 46          | 27       | 1             | 1           | 0 0.00%      | 1 100.00%         | 0 0.00%          |  |
| Provisional                       | 46          | 27       | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |  |
| Total                             | 46          | 135      | 12            | 9           | 0 0.00%      | 2 22.22%          | 1 11.11%         |  |
| <b>Total</b>                      |             |          |               |             |              |                   |                  |  |
| Precinct TSx                      | 280         | 102      | 0             | 0           | 0 -          | 0 -               | 0 -              |  |
| Precinct OS                       | 280         | 102      | 7             | 6           | 0 0.00%      | 0 0.00%           | 0 0.00%          |  |
| Precinct VBM                      | 280         | 102      | 30            | 20          | 0 0.00%      | 4 20.00%          | 4 20.00%         |  |
| MB Precinct                       | 280         | 102      | 3             | 3           | 1 33.33%     | 1 33.33%          | 0 0.00%          |  |
| Provisional                       | 280         | 102      | 6             | 3           | 0 0.00%      | 2 66.67%          | 0 0.00%          |  |
| Total                             | 280         | 510      | 46            | 32          | 1 3.13%      | 7 21.88%          | 4 12.50%         |  |
| <b>SUPERVISORIAL DISTRICTS</b>    |             |          |               |             |              |                   |                  |  |
| <b>1ST SUPERVISORIAL DISTRICT</b> |             |          |               |             |              |                   |                  |  |
| Precinct TSx                      | 16          | 14       | 0             | 0           | 0 -          | 0 -               | 0 -              |  |
| Precinct OS                       | 16          | 14       | 0             | 0           | 0 -          | 0 -               | 0 -              |  |
| Precinct VBM                      | 16          | 14       | 2             | 1           | 0 0.00%      | 1 100.00%         | 0 0.00%          |  |
| MB Precinct                       | 16          | 14       | 0             | 0           | 0 -          | 0 -               | 0 -              |  |
| Provisional                       | 16          | 14       | 0             | 0           | 0 -          | 0 -               | 0 -              |  |
| Total                             | 16          | 70       | 2             | 1           | 0 0.00%      | 1 100.00%         | 0 0.00%          |  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |             |          |               |             |              |                   |                  |  |
| Precinct TSx                      | 50          | 23       | 0             | 0           | 0 -          | 0 -               | 0 -              |  |
| Precinct OS                       | 50          | 23       | 2             | 2           | 0 0.00%      | 0 0.00%           | 0 0.00%          |  |
| Precinct VBM                      | 50          | 23       | 9             | 7           | 0 0.00%      | 0 0.00%           | 2 28.57%         |  |
| MB Precinct                       | 50          | 23       | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |  |
| Provisional                       | 50          | 23       | 0             | 0           | 0 -          | 0 -               | 0 -              |  |
| Total                             | 50          | 115      | 12            | 10          | 0 0.00%      | 0 0.00%           | 2 20.00%         |  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |             |          |               |             |              |                   |                  |  |
| Precinct TSx                      | 83          | 26       | 0             | 0           | 0 -          | 0 -               | 0 -              |  |
| Precinct OS                       | 83          | 26       | 2             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |  |
| Precinct VBM                      | 83          | 26       | 10            | 7           | 0 0.00%      | 2 28.57%          | 2 28.57%         |  |
| MB Precinct                       | 83          | 26       | 0             | 0           | 0 -          | 0 -               | 0 -              |  |
| Provisional                       | 83          | 26       | 1             | 0           | 0 -          | 0 -               | 0 -              |  |
| Total                             | 83          | 130      | 13            | 8           | 0 0.00%      | 2 25.00%          | 2 25.00%         |  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |              |                   |                  |  |
| Precinct TSx                      | 39          | 22       | 0             | 0           | 0 -          | 0 -               | 0 -              |  |
| Precinct OS                       | 39          | 22       | 2             | 2           | 0 0.00%      | 0 0.00%           | 0 0.00%          |  |
| Precinct VBM                      | 39          | 22       | 4             | 2           | 0 0.00%      | 0 0.00%           | 0 0.00%          |  |
| MB Precinct                       | 39          | 22       | 2             | 2           | 1 50.00%     | 1 50.00%          | 0 0.00%          |  |
| Provisional                       | 39          | 22       | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |  |
| Total                             | 39          | 110      | 9             | 7           | 1 14.29%     | 1 14.29%          | 0 0.00%          |  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |              |                   |                  |  |
| Precinct TSx                      | 92          | 17       | 0             | 0           | 0 -          | 0 -               | 0 -              |  |
| Precinct OS                       | 92          | 17       | 1             | 1           | 0 0.00%      | 0 0.00%           | 0 0.00%          |  |
| Precinct VBM                      | 92          | 17       | 5             | 3           | 0 0.00%      | 1 33.33%          | 0 0.00%          |  |
| MB Precinct                       | 92          | 17       | 0             | 0           | 0 -          | 0 -               | 0 -              |  |
| Provisional                       | 92          | 17       | 4             | 2           | 0 0.00%      | 2 100.00%         | 0 0.00%          |  |
| Total                             | 92          | 85       | 10            | 6           | 0 0.00%      | 3 50.00%          | 0 0.00%          |  |
| <b>Total</b>                      |             |          |               |             |              |                   |                  |  |
| Precinct TSx                      | 280         | 102      | 0             | 0           | 0 -          | 0 -               | 0 -              |  |
| Precinct OS                       | 280         | 102      | 7             | 6           | 0 0.00%      | 0 0.00%           | 0 0.00%          |  |
| Precinct VBM                      | 280         | 102      | 30            | 20          | 0 0.00%      | 4 20.00%          | 4 20.00%         |  |
| MB Precinct                       | 280         | 102      | 3             | 3           | 1 33.33%     | 1 33.33%          | 0 0.00%          |  |
| Provisional                       | 280         | 102      | 6             | 3           | 0 0.00%      | 2 66.67%          | 0 0.00%          |  |
| Total                             | 280         | 510      | 46            | 32          | 1 3.13%      | 7 21.88%          | 4 12.50%         |  |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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President Peace and Freedom Party

|                                 | Reg. Voters | Vote For   | Times Counted | Total Votes | LYNN S. KAHN   | GLORIA E. LA RIVA | MONICA MOOREHEAD |
|---------------------------------|-------------|------------|---------------|-------------|----------------|-------------------|------------------|
| <b>CITIES</b>                   |             |            |               |             |                |                   |                  |
| <b>CITY OF PLACERVILLE</b>      |             |            |               |             |                |                   |                  |
| Precinct TSx                    | 28          | 5          | 0             | 0           | 0              | -                 | 0                |
| Precinct OS                     | 28          | 5          | 0             | 0           | 0              | -                 | 0                |
| Precinct VBM                    | 28          | 5          | 2             | 0           | 0              | -                 | 0                |
| MB Precinct                     | 28          | 5          | 0             | 0           | 0              | -                 | 0                |
| Provisional                     | 28          | 5          | 0             | 0           | 0              | -                 | 0                |
| <b>Total</b>                    | <b>28</b>   | <b>25</b>  | <b>2</b>      | <b>0</b>    | <b>0</b>       | <b>-</b>          | <b>0</b>         |
| <b>CITY OF SOUTH LAKE TAHOE</b> |             |            |               |             |                |                   |                  |
| Precinct TSx                    | 64          | 7          | 0             | 0           | 0              | -                 | 0                |
| Precinct OS                     | 64          | 7          | 1             | 1           | 0 0.00%        | 0 0.00%           | 0 0.00%          |
| Precinct VBM                    | 64          | 7          | 3             | 2           | 0 0.00%        | 0 0.00%           | 0 0.00%          |
| MB Precinct                     | 64          | 7          | 0             | 0           | 0              | -                 | 0                |
| Provisional                     | 64          | 7          | 4             | 2           | 0 0.00%        | 2 100.00%         | 0 0.00%          |
| <b>Total</b>                    | <b>64</b>   | <b>35</b>  | <b>8</b>      | <b>5</b>    | <b>0 0.00%</b> | <b>2 40.00%</b>   | <b>0 0.00%</b>   |
| <b>UNINCORPORATED AREA</b>      |             |            |               |             |                |                   |                  |
| Precinct TSx                    | 188         | 90         | 0             | 0           | 0              | -                 | 0                |
| Precinct OS                     | 188         | 90         | 6             | 5           | 0 0.00%        | 0 0.00%           | 0 0.00%          |
| Precinct VBM                    | 188         | 90         | 25            | 18          | 0 0.00%        | 4 22.22%          | 4 22.22%         |
| MB Precinct                     | 188         | 90         | 3             | 3           | 1 33.33%       | 1 33.33%          | 0 0.00%          |
| Provisional                     | 188         | 90         | 2             | 1           | 0 0.00%        | 0 0.00%           | 0 0.00%          |
| <b>Total</b>                    | <b>188</b>  | <b>450</b> | <b>36</b>     | <b>27</b>   | <b>1 3.70%</b> | <b>5 18.52%</b>   | <b>4 14.81%</b>  |
| <b>Total</b>                    |             |            |               |             |                |                   |                  |
| Precinct TSx                    | 280         | 102        | 0             | 0           | 0              | -                 | 0                |
| Precinct OS                     | 280         | 102        | 7             | 6           | 0 0.00%        | 0 0.00%           | 0 0.00%          |
| Precinct VBM                    | 280         | 102        | 30            | 20          | 0 0.00%        | 4 20.00%          | 4 20.00%         |
| MB Precinct                     | 280         | 102        | 3             | 3           | 1 33.33%       | 1 33.33%          | 0 0.00%          |
| Provisional                     | 280         | 102        | 6             | 3           | 0 0.00%        | 2 66.67%          | 0 0.00%          |
| <b>Total</b>                    | <b>280</b>  | <b>510</b> | <b>46</b>     | <b>32</b>   | <b>1 3.13%</b> | <b>7 21.88%</b>   | <b>4 12.50%</b>  |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                          | United States Senator |          |               |             |                      |                              |
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|                          | Reg. Voters           | Vote For | Times Counted | Total Votes | JASON KRAUS<br>(NPP) | PRESIDENT C.<br>GRAPPO (DEM) |
| <b>Jurisdiction Wide</b> |                       |          |               |             |                      |                              |
| 100001.11-CP 100001.11   |                       |          |               |             |                      |                              |
| Precinct TSx             | 1615                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS              | 1615                  | 1        | 190           | 157         | 3                    | 1.91%                        |
| Precinct VBM             | 1615                  | 1        | 781           | 703         | 4                    | 0.57%                        |
| MB Precinct              | 1615                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional              | 1615                  | 1        | 17            | 16          | 0                    | 0.00%                        |
| Total                    | 1615                  | 5        | 988           | 876         | 7                    | 0.80%                        |
| 100002.11-CP 100002.11   |                       |          |               |             |                      |                              |
| Precinct TSx             | 1000                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS              | 1000                  | 1        | 89            | 76          | 0                    | 0.00%                        |
| Precinct VBM             | 1000                  | 1        | 476           | 403         | 4                    | 0.99%                        |
| MB Precinct              | 1000                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional              | 1000                  | 1        | 20            | 16          | 0                    | 0.00%                        |
| Total                    | 1000                  | 5        | 585           | 495         | 4                    | 0.81%                        |
| 100003.11-CP 100003.11   |                       |          |               |             |                      |                              |
| Precinct TSx             | 1269                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS              | 1269                  | 1        | 123           | 111         | 0                    | 0.00%                        |
| Precinct VBM             | 1269                  | 1        | 773           | 708         | 9                    | 1.27%                        |
| MB Precinct              | 1269                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional              | 1269                  | 1        | 24            | 22          | 0                    | 0.00%                        |
| Total                    | 1269                  | 5        | 920           | 841         | 9                    | 1.07%                        |
| 100004.11-CP 100004.11   |                       |          |               |             |                      |                              |
| Precinct TSx             | 1633                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS              | 1633                  | 1        | 254           | 234         | 6                    | 2.56%                        |
| Precinct VBM             | 1633                  | 1        | 738           | 666         | 4                    | 0.60%                        |
| MB Precinct              | 1633                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional              | 1633                  | 1        | 4             | 2           | 0                    | 0.00%                        |
| Total                    | 1633                  | 5        | 996           | 902         | 10                   | 1.11%                        |
| 100005.11-CP 100005.11   |                       |          |               |             |                      |                              |
| Precinct TSx             | 1586                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS              | 1586                  | 1        | 146           | 134         | 0                    | 0.00%                        |
| Precinct VBM             | 1586                  | 1        | 895           | 807         | 0                    | 0.00%                        |
| MB Precinct              | 1586                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional              | 1586                  | 1        | 23            | 19          | 0                    | 0.00%                        |
| Total                    | 1586                  | 5        | 1064          | 960         | 0                    | 0.00%                        |
| 100006.11-CP 100006.11   |                       |          |               |             |                      |                              |
| Precinct TSx             | 1928                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS              | 1928                  | 1        | 252           | 220         | 3                    | 1.36%                        |
| Precinct VBM             | 1928                  | 1        | 944           | 846         | 22                   | 2.60%                        |
| MB Precinct              | 1928                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional              | 1928                  | 1        | 31            | 21          | 2                    | 9.52%                        |
| Total                    | 1928                  | 5        | 1227          | 1087        | 27                   | 2.48%                        |
| 100008.11-CP 100008.11   |                       |          |               |             |                      |                              |
| Precinct TSx             | 1671                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS              | 1671                  | 1        | 197           | 174         | 3                    | 1.72%                        |
| Precinct VBM             | 1671                  | 1        | 740           | 646         | 5                    | 0.77%                        |
| MB Precinct              | 1671                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional              | 1671                  | 1        | 17            | 11          | 1                    | 9.09%                        |
| Total                    | 1671                  | 5        | 954           | 831         | 9                    | 1.08%                        |
| 100009.11-CP 100009.11   |                       |          |               |             |                      |                              |
| Precinct TSx             | 1838                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS              | 1838                  | 1        | 169           | 146         | 2                    | 1.37%                        |
| Precinct VBM             | 1838                  | 1        | 751           | 676         | 5                    | 0.74%                        |
| MB Precinct              | 1838                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional              | 1838                  | 1        | 26            | 18          | 0                    | 0.00%                        |
| Total                    | 1838                  | 5        | 946           | 840         | 7                    | 0.83%                        |
| 100012.11-CP 100012.11   |                       |          |               |             |                      |                              |
| Precinct TSx             | 2108                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS              | 2108                  | 1        | 255           | 229         | 2                    | 0.87%                        |
| Precinct VBM             | 2108                  | 1        | 968           | 857         | 11                   | 1.28%                        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | United States Senator |          |               |             |                      |                              |
|------------------------|-----------------------|----------|---------------|-------------|----------------------|------------------------------|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | JASON KRAUS<br>(NPP) | PRESIDENT C.<br>GRAPPO (DEM) |
| MB Precinct            | 2108                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 2108                  | 1        | 29            | 28          | 0                    | 0.00%                        |
| Total                  | 2108                  | 5        | 1252          | 1114        | 13                   | 1.17%                        |
| 100013.11-CP 100013.11 |                       |          |               |             |                      |                              |
| Precinct TSx           | 2087                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 2087                  | 1        | 250           | 221         | 1                    | 0.45%                        |
| Precinct VBM           | 2087                  | 1        | 908           | 809         | 4                    | 0.49%                        |
| MB Precinct            | 2087                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 2087                  | 1        | 28            | 24          | 0                    | 0.00%                        |
| Total                  | 2087                  | 5        | 1186          | 1054        | 5                    | 0.47%                        |
| 100014.11-CP 100014.11 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1659                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 1659                  | 1        | 211           | 173         | 2                    | 1.16%                        |
| Precinct VBM           | 1659                  | 1        | 706           | 625         | 6                    | 0.96%                        |
| MB Precinct            | 1659                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 1659                  | 1        | 30            | 22          | 0                    | 0.00%                        |
| Total                  | 1659                  | 5        | 947           | 820         | 8                    | 0.98%                        |
| 100022.11-CP 100022.11 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1432                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 1432                  | 1        | 177           | 161         | 0                    | 0.00%                        |
| Precinct VBM           | 1432                  | 1        | 678           | 612         | 3                    | 0.49%                        |
| MB Precinct            | 1432                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 1432                  | 1        | 13            | 10          | 0                    | 0.00%                        |
| Total                  | 1432                  | 5        | 868           | 783         | 3                    | 0.38%                        |
| 100023.11-CP 100023.11 |                       |          |               |             |                      |                              |
| Precinct TSx           | 2135                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 2135                  | 1        | 217           | 193         | 1                    | 0.52%                        |
| Precinct VBM           | 2135                  | 1        | 1053          | 932         | 5                    | 0.54%                        |
| MB Precinct            | 2135                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 2135                  | 1        | 33            | 28          | 0                    | 0.00%                        |
| Total                  | 2135                  | 5        | 1303          | 1153        | 6                    | 0.52%                        |
| 200003.12-CP 200003.12 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1362                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 1362                  | 1        | 166           | 148         | 2                    | 1.35%                        |
| Precinct VBM           | 1362                  | 1        | 558           | 503         | 4                    | 0.80%                        |
| MB Precinct            | 1362                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 1362                  | 1        | 31            | 26          | 1                    | 3.85%                        |
| Total                  | 1362                  | 5        | 755           | 677         | 7                    | 1.03%                        |
| 200004.12-CP 200004.12 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1507                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 1507                  | 1        | 182           | 165         | 0                    | 0.00%                        |
| Precinct VBM           | 1507                  | 1        | 662           | 603         | 1                    | 0.17%                        |
| MB Precinct            | 1507                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 1507                  | 1        | 14            | 14          | 0                    | 0.00%                        |
| Total                  | 1507                  | 5        | 858           | 782         | 1                    | 0.13%                        |
| 200005.02-CP 200005.02 |                       |          |               |             |                      |                              |
| Precinct TSx           | 867                   | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 867                   | 1        | 94            | 87          | 0                    | 0.00%                        |
| Precinct VBM           | 867                   | 1        | 452           | 405         | 0                    | 0.00%                        |
| MB Precinct            | 867                   | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 867                   | 1        | 21            | 19          | 0                    | 0.00%                        |
| Total                  | 867                   | 5        | 567           | 511         | 0                    | 0.00%                        |
| 200006.12-CP 200006.12 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1086                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 1086                  | 1        | 117           | 108         | 0                    | 0.00%                        |
| Precinct VBM           | 1086                  | 1        | 532           | 479         | 3                    | 0.63%                        |
| MB Precinct            | 1086                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 1086                  | 1        | 29            | 27          | 0                    | 0.00%                        |
| Total                  | 1086                  | 5        | 678           | 614         | 3                    | 0.49%                        |
| 200008.12-CP 200008.12 |                       |          |               |             |                      |                              |
| Precinct TSx           | 999                   | 1        | 0             | 0           | 0                    | -                            |

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|                        | United States Senator |          |               |             |                      |       |                              |       |  |  |
|------------------------|-----------------------|----------|---------------|-------------|----------------------|-------|------------------------------|-------|--|--|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | JASON KRAUS<br>(NPP) |       | PRESIDENT C.<br>GRAPPO (DEM) |       |  |  |
| Precinct OS            | 999                   | 1        | 155           | 133         | 0                    | 0.00% | 0                            | 0.00% |  |  |
| Precinct VBM           | 999                   | 1        | 379           | 327         | 2                    | 0.61% | 1                            | 0.31% |  |  |
| MB Precinct            | 999                   | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Provisional            | 999                   | 1        | 10            | 8           | 0                    | 0.00% | 0                            | 0.00% |  |  |
| Total                  | 999                   | 5        | 544           | 468         | 2                    | 0.43% | 1                            | 0.21% |  |  |
| 200010.12-CP 200010.12 |                       |          |               |             |                      |       |                              |       |  |  |
| Precinct TSx           | 1976                  | 1        | 4             | 4           | 0                    | 0.00% | 0                            | 0.00% |  |  |
| Precinct OS            | 1976                  | 1        | 197           | 179         | 6                    | 3.35% | 1                            | 0.56% |  |  |
| Precinct VBM           | 1976                  | 1        | 859           | 753         | 5                    | 0.66% | 0                            | 0.00% |  |  |
| MB Precinct            | 1976                  | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Provisional            | 1976                  | 1        | 34            | 26          | 1                    | 3.85% | 0                            | 0.00% |  |  |
| Total                  | 1976                  | 5        | 1094          | 962         | 12                   | 1.25% | 1                            | 0.10% |  |  |
| 200012.12-CP 200012.12 |                       |          |               |             |                      |       |                              |       |  |  |
| Precinct TSx           | 1064                  | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Precinct OS            | 1064                  | 1        | 145           | 126         | 1                    | 0.79% | 0                            | 0.00% |  |  |
| Precinct VBM           | 1064                  | 1        | 464           | 404         | 1                    | 0.25% | 4                            | 0.99% |  |  |
| MB Precinct            | 1064                  | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Provisional            | 1064                  | 1        | 14            | 12          | 0                    | 0.00% | 0                            | 0.00% |  |  |
| Total                  | 1064                  | 5        | 623           | 542         | 2                    | 0.37% | 4                            | 0.74% |  |  |
| 200013.12-CP 200013.12 |                       |          |               |             |                      |       |                              |       |  |  |
| Precinct TSx           | 1574                  | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Precinct OS            | 1574                  | 1        | 211           | 191         | 1                    | 0.52% | 0                            | 0.00% |  |  |
| Precinct VBM           | 1574                  | 1        | 701           | 639         | 6                    | 0.94% | 1                            | 0.16% |  |  |
| MB Precinct            | 1574                  | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Provisional            | 1574                  | 1        | 23            | 20          | 0                    | 0.00% | 0                            | 0.00% |  |  |
| Total                  | 1574                  | 5        | 935           | 850         | 7                    | 0.82% | 1                            | 0.12% |  |  |
| 200014.12-CP 200014.12 |                       |          |               |             |                      |       |                              |       |  |  |
| Precinct TSx           | 1438                  | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Precinct OS            | 1438                  | 1        | 201           | 187         | 0                    | 0.00% | 0                            | 0.00% |  |  |
| Precinct VBM           | 1438                  | 1        | 613           | 549         | 4                    | 0.73% | 2                            | 0.36% |  |  |
| MB Precinct            | 1438                  | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Provisional            | 1438                  | 1        | 30            | 25          | 0                    | 0.00% | 0                            | 0.00% |  |  |
| Total                  | 1438                  | 5        | 844           | 761         | 4                    | 0.53% | 2                            | 0.26% |  |  |
| 200015.02-CP 200015.02 |                       |          |               |             |                      |       |                              |       |  |  |
| Precinct TSx           | 1832                  | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Precinct OS            | 1832                  | 1        | 218           | 195         | 1                    | 0.51% | 0                            | 0.00% |  |  |
| Precinct VBM           | 1832                  | 1        | 886           | 790         | 7                    | 0.89% | 1                            | 0.13% |  |  |
| MB Precinct            | 1832                  | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Provisional            | 1832                  | 1        | 22            | 20          | 1                    | 5.00% | 0                            | 0.00% |  |  |
| Total                  | 1832                  | 5        | 1126          | 1005        | 9                    | 0.90% | 1                            | 0.10% |  |  |
| 200016.12-CP 200016.12 |                       |          |               |             |                      |       |                              |       |  |  |
| Precinct TSx           | 301                   | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Precinct OS            | 301                   | 1        | 26            | 20          | 0                    | 0.00% | 0                            | 0.00% |  |  |
| Precinct VBM           | 301                   | 1        | 139           | 126         | 1                    | 0.79% | 0                            | 0.00% |  |  |
| MB Precinct            | 301                   | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Provisional            | 301                   | 1        | 6             | 3           | 0                    | 0.00% | 0                            | 0.00% |  |  |
| Total                  | 301                   | 5        | 171           | 149         | 1                    | 0.67% | 0                            | 0.00% |  |  |
| 200016.13-CP 200016.13 |                       |          |               |             |                      |       |                              |       |  |  |
| Precinct TSx           | 565                   | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Precinct OS            | 565                   | 1        | 80            | 72          | 0                    | 0.00% | 0                            | 0.00% |  |  |
| Precinct VBM           | 565                   | 1        | 338           | 294         | 2                    | 0.68% | 1                            | 0.34% |  |  |
| MB Precinct            | 565                   | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Provisional            | 565                   | 1        | 6             | 6           | 0                    | 0.00% | 0                            | 0.00% |  |  |
| Total                  | 565                   | 5        | 424           | 372         | 2                    | 0.54% | 1                            | 0.27% |  |  |
| 200017.02-CP 200017.02 |                       |          |               |             |                      |       |                              |       |  |  |
| Precinct TSx           | 1286                  | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Precinct OS            | 1286                  | 1        | 146           | 128         | 1                    | 0.78% | 1                            | 0.78% |  |  |
| Precinct VBM           | 1286                  | 1        | 594           | 547         | 7                    | 1.28% | 2                            | 0.37% |  |  |
| MB Precinct            | 1286                  | 1        | 0             | 0           | 0                    | -     | 0                            | -     |  |  |
| Provisional            | 1286                  | 1        | 12            | 11          | 0                    | 0.00% | 0                            | 0.00% |  |  |
| Total                  | 1286                  | 5        | 752           | 686         | 8                    | 1.17% | 3                            | 0.44% |  |  |

**COUNTY OF EL DORADO**  
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|                        | United States Senator |          |               |             |                      |                              |   |
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|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | JASON KRAUS<br>(NPP) | PRESIDENT C.<br>GRAPPO (DEM) |   |
| 200018.02-CP 200018.02 |                       |          |               |             |                      |                              |   |
| Precinct TSx           | 1454                  | 1        | 0             | 0           | 0                    | -                            | 0 |
| Precinct OS            | 1454                  | 1        | 169           | 152         | 0                    | 0.00%                        | 0 |
| Precinct VBM           | 1454                  | 1        | 704           | 632         | 3                    | 0.47%                        | 2 |
| MB Precinct            | 1454                  | 1        | 0             | 0           | 0                    | -                            | 0 |
| Provisional            | 1454                  | 1        | 13            | 12          | 0                    | 0.00%                        | 0 |
| Total                  | 1454                  | 5        | 886           | 796         | 3                    | 0.38%                        | 2 |
| 200020.02-CP 200020.02 |                       |          |               |             |                      |                              |   |
| Precinct TSx           | 1213                  | 1        | 0             | 0           | 0                    | -                            | 0 |
| Precinct OS            | 1213                  | 1        | 139           | 124         | 0                    | 0.00%                        | 0 |
| Precinct VBM           | 1213                  | 1        | 526           | 476         | 3                    | 0.63%                        | 1 |
| MB Precinct            | 1213                  | 1        | 0             | 0           | 0                    | -                            | 0 |
| Provisional            | 1213                  | 1        | 19            | 17          | 0                    | 0.00%                        | 0 |
| Total                  | 1213                  | 5        | 684           | 617         | 3                    | 0.49%                        | 1 |
| 200021.01-CP 200021.01 |                       |          |               |             |                      |                              |   |
| Precinct TSx           | 436                   | 1        | 0             | 0           | 0                    | -                            | 0 |
| Precinct OS            | 436                   | 1        | 38            | 30          | 0                    | 0.00%                        | 0 |
| Precinct VBM           | 436                   | 1        | 206           | 180         | 1                    | 0.56%                        | 0 |
| MB Precinct            | 436                   | 1        | 0             | 0           | 0                    | -                            | 0 |
| Provisional            | 436                   | 1        | 7             | 5           | 0                    | 0.00%                        | 0 |
| Total                  | 436                   | 5        | 251           | 215         | 1                    | 0.47%                        | 0 |
| 200021.02-CP 200021.02 |                       |          |               |             |                      |                              |   |
| Precinct TSx           | 1296                  | 1        | 0             | 0           | 0                    | -                            | 0 |
| Precinct OS            | 1296                  | 1        | 170           | 154         | 2                    | 1.30%                        | 0 |
| Precinct VBM           | 1296                  | 1        | 752           | 675         | 1                    | 0.15%                        | 0 |
| MB Precinct            | 1296                  | 1        | 0             | 0           | 0                    | -                            | 0 |
| Provisional            | 1296                  | 1        | 20            | 15          | 0                    | 0.00%                        | 0 |
| Total                  | 1296                  | 5        | 942           | 844         | 3                    | 0.36%                        | 0 |
| 200024.02-CP 200024.02 |                       |          |               |             |                      |                              |   |
| Precinct TSx           | 555                   | 1        | 0             | 0           | 0                    | -                            | 0 |
| Precinct OS            | 555                   | 1        | 55            | 53          | 2                    | 3.77%                        | 0 |
| Precinct VBM           | 555                   | 1        | 282           | 260         | 0                    | 0.00%                        | 0 |
| MB Precinct            | 555                   | 1        | 0             | 0           | 0                    | -                            | 0 |
| Provisional            | 555                   | 1        | 9             | 5           | 0                    | 0.00%                        | 0 |
| Total                  | 555                   | 5        | 346           | 318         | 2                    | 0.63%                        | 0 |
| 300001.04-CP 300001.04 |                       |          |               |             |                      |                              |   |
| Precinct TSx           | 259                   | 1        | 0             | 0           | 0                    | -                            | 0 |
| Precinct OS            | 259                   | 1        | 12            | 11          | 0                    | 0.00%                        | 0 |
| Precinct VBM           | 259                   | 1        | 100           | 94          | 0                    | 0.00%                        | 0 |
| MB Precinct            | 259                   | 1        | 0             | 0           | 0                    | -                            | 0 |
| Provisional            | 259                   | 1        | 0             | 0           | 0                    | -                            | 0 |
| Total                  | 259                   | 5        | 112           | 105         | 0                    | 0.00%                        | 0 |
| 300001.05-CP 300001.05 |                       |          |               |             |                      |                              |   |
| Precinct TSx           | 731                   | 1        | 0             | 0           | 0                    | -                            | 0 |
| Precinct OS            | 731                   | 1        | 76            | 68          | 0                    | 0.00%                        | 0 |
| Precinct VBM           | 731                   | 1        | 330           | 295         | 0                    | 0.00%                        | 0 |
| MB Precinct            | 731                   | 1        | 0             | 0           | 0                    | -                            | 0 |
| Provisional            | 731                   | 1        | 10            | 9           | 0                    | 0.00%                        | 0 |
| Total                  | 731                   | 5        | 416           | 372         | 0                    | 0.00%                        | 0 |
| 300003.05-CP 300003.05 |                       |          |               |             |                      |                              |   |
| Precinct TSx           | 1932                  | 1        | 0             | 0           | 0                    | -                            | 0 |
| Precinct OS            | 1932                  | 1        | 205           | 180         | 4                    | 2.22%                        | 0 |
| Precinct VBM           | 1932                  | 1        | 924           | 841         | 7                    | 0.83%                        | 0 |
| MB Precinct            | 1932                  | 1        | 0             | 0           | 0                    | -                            | 0 |
| Provisional            | 1932                  | 1        | 27            | 23          | 1                    | 4.35%                        | 0 |
| Total                  | 1932                  | 5        | 1156          | 1044        | 12                   | 1.15%                        | 0 |
| 300004.05-CP 300004.05 |                       |          |               |             |                      |                              |   |
| Precinct TSx           | 1273                  | 1        | 0             | 0           | 0                    | -                            | 0 |
| Precinct OS            | 1273                  | 1        | 175           | 155         | 0                    | 0.00%                        | 0 |
| Precinct VBM           | 1273                  | 1        | 576           | 526         | 4                    | 0.76%                        | 0 |
| MB Precinct            | 1273                  | 1        | 0             | 0           | 0                    | -                            | 0 |

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|                        | United States Senator |          |               |             |                      |        |                              |       |
|------------------------|-----------------------|----------|---------------|-------------|----------------------|--------|------------------------------|-------|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | JASON KRAUS<br>(NPP) |        | PRESIDENT C.<br>GRAPPO (DEM) |       |
| Provisional            | 1273                  | 1        | 17            | 16          | 0                    | 0.00%  | 0                            | 0.00% |
| Total                  | 1273                  | 5        | 768           | 697         | 4                    | 0.57%  | 0                            | 0.00% |
| 300005.05-CP 300005.05 |                       |          |               |             |                      |        |                              |       |
| Precinct TSx           | 1374                  | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Precinct OS            | 1374                  | 1        | 163           | 144         | 1                    | 0.69%  | 0                            | 0.00% |
| Precinct VBM           | 1374                  | 1        | 660           | 590         | 7                    | 1.19%  | 2                            | 0.34% |
| MB Precinct            | 1374                  | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Provisional            | 1374                  | 1        | 26            | 22          | 0                    | 0.00%  | 0                            | 0.00% |
| Total                  | 1374                  | 5        | 849           | 756         | 8                    | 1.06%  | 2                            | 0.26% |
| 300008.05-CP 300008.05 |                       |          |               |             |                      |        |                              |       |
| Precinct TSx           | 1366                  | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Precinct OS            | 1366                  | 1        | 139           | 128         | 0                    | 0.00%  | 0                            | 0.00% |
| Precinct VBM           | 1366                  | 1        | 740           | 682         | 3                    | 0.44%  | 0                            | 0.00% |
| MB Precinct            | 1366                  | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Provisional            | 1366                  | 1        | 14            | 11          | 0                    | 0.00%  | 0                            | 0.00% |
| Total                  | 1366                  | 5        | 893           | 821         | 3                    | 0.37%  | 0                            | 0.00% |
| 300009.04-CP 300009.04 |                       |          |               |             |                      |        |                              |       |
| Precinct TSx           | 873                   | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Precinct OS            | 873                   | 1        | 121           | 110         | 0                    | 0.00%  | 0                            | 0.00% |
| Precinct VBM           | 873                   | 1        | 395           | 344         | 8                    | 2.33%  | 0                            | 0.00% |
| MB Precinct            | 873                   | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Provisional            | 873                   | 1        | 13            | 12          | 0                    | 0.00%  | 0                            | 0.00% |
| Total                  | 873                   | 5        | 529           | 466         | 8                    | 1.72%  | 0                            | 0.00% |
| 300010.04-CP 300010.04 |                       |          |               |             |                      |        |                              |       |
| Precinct TSx           | 1340                  | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Precinct OS            | 1340                  | 1        | 171           | 155         | 2                    | 1.29%  | 0                            | 0.00% |
| Precinct VBM           | 1340                  | 1        | 656           | 594         | 8                    | 1.35%  | 0                            | 0.00% |
| MB Precinct            | 1340                  | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Provisional            | 1340                  | 1        | 11            | 11          | 0                    | 0.00%  | 0                            | 0.00% |
| Total                  | 1340                  | 5        | 838           | 760         | 10                   | 1.32%  | 0                            | 0.00% |
| 300011.04-CP 300011.04 |                       |          |               |             |                      |        |                              |       |
| Precinct TSx           | 1491                  | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Precinct OS            | 1491                  | 1        | 142           | 130         | 1                    | 0.77%  | 0                            | 0.00% |
| Precinct VBM           | 1491                  | 1        | 711           | 640         | 3                    | 0.47%  | 0                            | 0.00% |
| MB Precinct            | 1491                  | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Provisional            | 1491                  | 1        | 22            | 20          | 0                    | 0.00%  | 0                            | 0.00% |
| Total                  | 1491                  | 5        | 875           | 790         | 4                    | 0.51%  | 0                            | 0.00% |
| 300013.05-CP 300013.05 |                       |          |               |             |                      |        |                              |       |
| Precinct TSx           | 1490                  | 1        | 3             | 3           | 0                    | 0.00%  | 0                            | 0.00% |
| Precinct OS            | 1490                  | 1        | 128           | 117         | 1                    | 0.85%  | 0                            | 0.00% |
| Precinct VBM           | 1490                  | 1        | 721           | 657         | 4                    | 0.61%  | 0                            | 0.00% |
| MB Precinct            | 1490                  | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Provisional            | 1490                  | 1        | 15            | 11          | 0                    | 0.00%  | 0                            | 0.00% |
| Total                  | 1490                  | 5        | 867           | 788         | 5                    | 0.63%  | 0                            | 0.00% |
| 300015.04-CP 300015.04 |                       |          |               |             |                      |        |                              |       |
| Precinct TSx           | 119                   | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Precinct OS            | 119                   | 1        | 10            | 8           | 0                    | 0.00%  | 0                            | 0.00% |
| Precinct VBM           | 119                   | 1        | 63            | 51          | 0                    | 0.00%  | 0                            | 0.00% |
| MB Precinct            | 119                   | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Provisional            | 119                   | 1        | 4             | 4           | 1                    | 25.00% | 0                            | 0.00% |
| Total                  | 119                   | 5        | 77            | 63          | 1                    | 1.59%  | 0                            | 0.00% |
| 300015.05-CP 300015.05 |                       |          |               |             |                      |        |                              |       |
| Precinct TSx           | 503                   | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Precinct OS            | 503                   | 1        | 66            | 61          | 0                    | 0.00%  | 0                            | 0.00% |
| Precinct VBM           | 503                   | 1        | 266           | 228         | 0                    | 0.00%  | 0                            | 0.00% |
| MB Precinct            | 503                   | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Provisional            | 503                   | 1        | 2             | 1           | 0                    | 0.00%  | 0                            | 0.00% |
| Total                  | 503                   | 5        | 334           | 290         | 0                    | 0.00%  | 0                            | 0.00% |
| 300017.05-CP 300017.05 |                       |          |               |             |                      |        |                              |       |
| Precinct TSx           | 1240                  | 1        | 0             | 0           | 0                    | -      | 0                            | -     |
| Precinct OS            | 1240                  | 1        | 117           | 109         | 3                    | 2.75%  | 0                            | 0.00% |

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|                        | United States Senator |          |               |             |                      |                              |
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|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | JASON KRAUS<br>(NPP) | PRESIDENT C.<br>GRAPPO (DEM) |
| Precinct VBM           | 1240                  | 1        | 655           | 595         | 1 0.17%              | 0 0.00%                      |
| MB Precinct            | 1240                  | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Provisional            | 1240                  | 1        | 9             | 6           | 0 0.00%              | 0 0.00%                      |
| Total                  | 1240                  | 5        | 781           | 710         | 4 0.56%              | 0 0.00%                      |
| 300018.04-CP 300018.04 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1306                  | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Precinct OS            | 1306                  | 1        | 148           | 126         | 0 0.00%              | 0 0.00%                      |
| Precinct VBM           | 1306                  | 1        | 505           | 453         | 5 1.10%              | 2 0.44%                      |
| MB Precinct            | 1306                  | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Provisional            | 1306                  | 1        | 31            | 25          | 0 0.00%              | 0 0.00%                      |
| Total                  | 1306                  | 5        | 684           | 604         | 5 0.83%              | 2 0.33%                      |
| 300020.04-CP 300020.04 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1401                  | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Precinct OS            | 1401                  | 1        | 168           | 145         | 0 0.00%              | 2 1.38%                      |
| Precinct VBM           | 1401                  | 1        | 632           | 571         | 1 0.18%              | 1 0.18%                      |
| MB Precinct            | 1401                  | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Provisional            | 1401                  | 1        | 14            | 12          | 0 0.00%              | 0 0.00%                      |
| Total                  | 1401                  | 5        | 814           | 728         | 1 0.14%              | 3 0.41%                      |
| 300021.03-CP 300021.03 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1234                  | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Precinct OS            | 1234                  | 1        | 153           | 133         | 4 3.01%              | 0 0.00%                      |
| Precinct VBM           | 1234                  | 1        | 614           | 564         | 2 0.35%              | 0 0.00%                      |
| MB Precinct            | 1234                  | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Provisional            | 1234                  | 1        | 13            | 12          | 1 8.33%              | 0 0.00%                      |
| Total                  | 1234                  | 5        | 780           | 709         | 7 0.99%              | 0 0.00%                      |
| 300022.05-CP 300022.05 |                       |          |               |             |                      |                              |
| Precinct TSx           | 872                   | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Precinct OS            | 872                   | 1        | 101           | 88          | 0 0.00%              | 0 0.00%                      |
| Precinct VBM           | 872                   | 1        | 356           | 325         | 5 1.54%              | 0 0.00%                      |
| MB Precinct            | 872                   | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Provisional            | 872                   | 1        | 12            | 12          | 0 0.00%              | 0 0.00%                      |
| Total                  | 872                   | 5        | 469           | 425         | 5 1.18%              | 0 0.00%                      |
| 300023.03-CP 300023.03 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1819                  | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Precinct OS            | 1819                  | 1        | 228           | 209         | 3 1.44%              | 2 0.96%                      |
| Precinct VBM           | 1819                  | 1        | 924           | 832         | 6 0.72%              | 0 0.00%                      |
| MB Precinct            | 1819                  | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Provisional            | 1819                  | 1        | 26            | 19          | 0 0.00%              | 1 5.26%                      |
| Total                  | 1819                  | 5        | 1178          | 1060        | 9 0.85%              | 3 0.28%                      |
| 300023.05-CP 300023.05 |                       |          |               |             |                      |                              |
| Precinct TSx           | 386                   | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Precinct OS            | 386                   | 1        | 45            | 42          | 0 0.00%              | 0 0.00%                      |
| Precinct VBM           | 386                   | 1        | 193           | 173         | 3 1.73%              | 0 0.00%                      |
| MB Precinct            | 386                   | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Provisional            | 386                   | 1        | 8             | 8           | 0 0.00%              | 0 0.00%                      |
| Total                  | 386                   | 5        | 246           | 223         | 3 1.35%              | 0 0.00%                      |
| 400001.14-CP 400001.14 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1957                  | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Precinct OS            | 1957                  | 1        | 178           | 160         | 0 0.00%              | 0 0.00%                      |
| Precinct VBM           | 1957                  | 1        | 992           | 905         | 4 0.44%              | 3 0.33%                      |
| MB Precinct            | 1957                  | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Provisional            | 1957                  | 1        | 23            | 20          | 0 0.00%              | 0 0.00%                      |
| Total                  | 1957                  | 5        | 1193          | 1085        | 4 0.37%              | 3 0.28%                      |
| 400002.08-CP 400002.08 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1508                  | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Precinct OS            | 1508                  | 1        | 200           | 174         | 0 0.00%              | 0 0.00%                      |
| Precinct VBM           | 1508                  | 1        | 730           | 638         | 4 0.63%              | 0 0.00%                      |
| MB Precinct            | 1508                  | 1        | 0             | 0           | 0 -                  | 0 -                          |
| Provisional            | 1508                  | 1        | 15            | 13          | 0 0.00%              | 0 0.00%                      |
| Total                  | 1508                  | 5        | 945           | 825         | 4 0.48%              | 0 0.00%                      |
| 400003.08-CP 400003.08 |                       |          |               |             |                      |                              |



**COUNTY OF EL DORADO**  
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|                               | United States Senator |          |               |             |                      |              |                              |              |
|-------------------------------|-----------------------|----------|---------------|-------------|----------------------|--------------|------------------------------|--------------|
|                               | Reg. Voters           | Vote For | Times Counted | Total Votes | JASON KRAUS<br>(NPP) |              | PRESIDENT C.<br>GRAPPO (DEM) |              |
| Precinct TSx                  | 1800                  | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Precinct OS                   | 1800                  | 1        | 245           | 221         | 3                    | 1.36%        | 1                            | 0.45%        |
| Precinct VBM                  | 1800                  | 1        | 773           | 695         | 5                    | 0.72%        | 0                            | 0.00%        |
| MB Precinct                   | 1800                  | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Provisional                   | 1800                  | 1        | 35            | 30          | 0                    | 0.00%        | 0                            | 0.00%        |
| <b>Total</b>                  | <b>1800</b>           | <b>5</b> | <b>1053</b>   | <b>946</b>  | <b>8</b>             | <b>0.85%</b> | <b>1</b>                     | <b>0.11%</b> |
| <b>400004.14-CP 400004.14</b> |                       |          |               |             |                      |              |                              |              |
| Precinct TSx                  | 1536                  | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Precinct OS                   | 1536                  | 1        | 194           | 170         | 0                    | 0.00%        | 0                            | 0.00%        |
| Precinct VBM                  | 1536                  | 1        | 662           | 610         | 6                    | 0.98%        | 1                            | 0.16%        |
| MB Precinct                   | 1536                  | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Provisional                   | 1536                  | 1        | 19            | 18          | 0                    | 0.00%        | 0                            | 0.00%        |
| <b>Total</b>                  | <b>1536</b>           | <b>5</b> | <b>875</b>    | <b>798</b>  | <b>6</b>             | <b>0.75%</b> | <b>1</b>                     | <b>0.13%</b> |
| <b>400005.08-CP 400005.08</b> |                       |          |               |             |                      |              |                              |              |
| Precinct TSx                  | 655                   | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Precinct OS                   | 655                   | 1        | 79            | 66          | 2                    | 3.03%        | 0                            | 0.00%        |
| Precinct VBM                  | 655                   | 1        | 262           | 233         | 0                    | 0.00%        | 0                            | 0.00%        |
| MB Precinct                   | 655                   | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Provisional                   | 655                   | 1        | 11            | 7           | 0                    | 0.00%        | 0                            | 0.00%        |
| <b>Total</b>                  | <b>655</b>            | <b>5</b> | <b>352</b>    | <b>306</b>  | <b>2</b>             | <b>0.65%</b> | <b>0</b>                     | <b>0.00%</b> |
| <b>400006.08-CP 400006.08</b> |                       |          |               |             |                      |              |                              |              |
| Precinct TSx                  | 1537                  | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Precinct OS                   | 1537                  | 1        | 228           | 210         | 2                    | 0.95%        | 0                            | 0.00%        |
| Precinct VBM                  | 1537                  | 1        | 722           | 644         | 2                    | 0.31%        | 0                            | 0.00%        |
| MB Precinct                   | 1537                  | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Provisional                   | 1537                  | 1        | 19            | 15          | 1                    | 6.67%        | 0                            | 0.00%        |
| <b>Total</b>                  | <b>1537</b>           | <b>5</b> | <b>969</b>    | <b>869</b>  | <b>5</b>             | <b>0.58%</b> | <b>0</b>                     | <b>0.00%</b> |
| <b>400007.08-CP 400007.08</b> |                       |          |               |             |                      |              |                              |              |
| Precinct TSx                  | 1766                  | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Precinct OS                   | 1766                  | 1        | 228           | 211         | 2                    | 0.95%        | 1                            | 0.47%        |
| Precinct VBM                  | 1766                  | 1        | 765           | 707         | 4                    | 0.57%        | 1                            | 0.14%        |
| MB Precinct                   | 1766                  | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Provisional                   | 1766                  | 1        | 22            | 20          | 0                    | 0.00%        | 0                            | 0.00%        |
| <b>Total</b>                  | <b>1766</b>           | <b>5</b> | <b>1015</b>   | <b>938</b>  | <b>6</b>             | <b>0.64%</b> | <b>2</b>                     | <b>0.21%</b> |
| <b>400010.08-CP 400010.08</b> |                       |          |               |             |                      |              |                              |              |
| Precinct TSx                  | 1875                  | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Precinct OS                   | 1875                  | 1        | 244           | 221         | 0                    | 0.00%        | 1                            | 0.45%        |
| Precinct VBM                  | 1875                  | 1        | 869           | 812         | 1                    | 0.12%        | 0                            | 0.00%        |
| MB Precinct                   | 1875                  | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Provisional                   | 1875                  | 1        | 31            | 21          | 0                    | 0.00%        | 0                            | 0.00%        |
| <b>Total</b>                  | <b>1875</b>           | <b>5</b> | <b>1144</b>   | <b>1054</b> | <b>1</b>             | <b>0.09%</b> | <b>1</b>                     | <b>0.09%</b> |
| <b>400012.08-CP 400012.08</b> |                       |          |               |             |                      |              |                              |              |
| Precinct TSx                  | 877                   | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Precinct OS                   | 877                   | 1        | 113           | 105         | 1                    | 0.95%        | 0                            | 0.00%        |
| Precinct VBM                  | 877                   | 1        | 434           | 392         | 1                    | 0.26%        | 0                            | 0.00%        |
| MB Precinct                   | 877                   | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Provisional                   | 877                   | 1        | 22            | 15          | 0                    | 0.00%        | 0                            | 0.00%        |
| <b>Total</b>                  | <b>877</b>            | <b>5</b> | <b>569</b>    | <b>512</b>  | <b>2</b>             | <b>0.39%</b> | <b>0</b>                     | <b>0.00%</b> |
| <b>400013.08-CP 400013.08</b> |                       |          |               |             |                      |              |                              |              |
| Precinct TSx                  | 758                   | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Precinct OS                   | 758                   | 1        | 127           | 107         | 1                    | 0.93%        | 0                            | 0.00%        |
| Precinct VBM                  | 758                   | 1        | 315           | 292         | 0                    | 0.00%        | 0                            | 0.00%        |
| MB Precinct                   | 758                   | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Provisional                   | 758                   | 1        | 4             | 3           | 0                    | 0.00%        | 0                            | 0.00%        |
| <b>Total</b>                  | <b>758</b>            | <b>5</b> | <b>446</b>    | <b>402</b>  | <b>1</b>             | <b>0.25%</b> | <b>0</b>                     | <b>0.00%</b> |
| <b>400014.08-CP 400014.08</b> |                       |          |               |             |                      |              |                              |              |
| Precinct TSx                  | 1526                  | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Precinct OS                   | 1526                  | 1        | 230           | 196         | 1                    | 0.51%        | 1                            | 0.51%        |
| Precinct VBM                  | 1526                  | 1        | 736           | 664         | 3                    | 0.45%        | 0                            | 0.00%        |
| MB Precinct                   | 1526                  | 1        | 0             | 0           | 0                    | -            | 0                            | -            |
| Provisional                   | 1526                  | 1        | 36            | 31          | 0                    | 0.00%        | 0                            | 0.00%        |

**COUNTY OF EL DORADO**  
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|                        | United States Senator |          |               |             |                      |       |   |                              |  |  |
|------------------------|-----------------------|----------|---------------|-------------|----------------------|-------|---|------------------------------|--|--|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | JASON KRAUS<br>(NPP) |       |   | PRESIDENT C.<br>GRAPPO (DEM) |  |  |
| Total                  | 1526                  | 5        | 1002          | 891         | 4                    | 0.45% | 1 | 0.11%                        |  |  |
| 400017.08-CP 400017.08 |                       |          |               |             |                      |       |   |                              |  |  |
| Precinct TSx           | 1495                  | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Precinct OS            | 1495                  | 1        | 175           | 153         | 0                    | 0.00% | 0 | 0.00%                        |  |  |
| Precinct VBM           | 1495                  | 1        | 752           | 668         | 4                    | 0.60% | 0 | 0.00%                        |  |  |
| MB Precinct            | 1495                  | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Provisional            | 1495                  | 1        | 28            | 23          | 0                    | 0.00% | 0 | 0.00%                        |  |  |
| Total                  | 1495                  | 5        | 955           | 844         | 4                    | 0.47% | 0 | 0.00%                        |  |  |
| 400018.08-CP 400018.08 |                       |          |               |             |                      |       |   |                              |  |  |
| Precinct TSx           | 597                   | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Precinct OS            | 597                   | 1        | 86            | 77          | 0                    | 0.00% | 0 | 0.00%                        |  |  |
| Precinct VBM           | 597                   | 1        | 276           | 240         | 1                    | 0.42% | 0 | 0.00%                        |  |  |
| MB Precinct            | 597                   | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Provisional            | 597                   | 1        | 7             | 4           | 0                    | 0.00% | 0 | 0.00%                        |  |  |
| Total                  | 597                   | 5        | 369           | 321         | 1                    | 0.31% | 0 | 0.00%                        |  |  |
| 400020.08-CP 400020.08 |                       |          |               |             |                      |       |   |                              |  |  |
| Precinct TSx           | 724                   | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Precinct OS            | 724                   | 1        | 114           | 105         | 1                    | 0.95% | 1 | 0.95%                        |  |  |
| Precinct VBM           | 724                   | 1        | 343           | 309         | 1                    | 0.32% | 1 | 0.32%                        |  |  |
| MB Precinct            | 724                   | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Provisional            | 724                   | 1        | 2             | 2           | 0                    | 0.00% | 0 | 0.00%                        |  |  |
| Total                  | 724                   | 5        | 459           | 416         | 2                    | 0.48% | 2 | 0.48%                        |  |  |
| 400021.07-CP 400021.07 |                       |          |               |             |                      |       |   |                              |  |  |
| Precinct TSx           | 1332                  | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Precinct OS            | 1332                  | 1        | 127           | 118         | 1                    | 0.85% | 0 | 0.00%                        |  |  |
| Precinct VBM           | 1332                  | 1        | 767           | 686         | 4                    | 0.58% | 0 | 0.00%                        |  |  |
| MB Precinct            | 1332                  | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Provisional            | 1332                  | 1        | 18            | 14          | 0                    | 0.00% | 0 | 0.00%                        |  |  |
| Total                  | 1332                  | 5        | 912           | 818         | 5                    | 0.61% | 0 | 0.00%                        |  |  |
| 500001.10-CP 500001.10 |                       |          |               |             |                      |       |   |                              |  |  |
| Precinct TSx           | 1717                  | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Precinct OS            | 1717                  | 1        | 210           | 185         | 2                    | 1.08% | 1 | 0.54%                        |  |  |
| Precinct VBM           | 1717                  | 1        | 617           | 537         | 3                    | 0.56% | 3 | 0.56%                        |  |  |
| MB Precinct            | 1717                  | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Provisional            | 1717                  | 1        | 19            | 14          | 0                    | 0.00% | 0 | 0.00%                        |  |  |
| Total                  | 1717                  | 5        | 846           | 736         | 5                    | 0.68% | 4 | 0.54%                        |  |  |
| 500002.10-CP 500002.10 |                       |          |               |             |                      |       |   |                              |  |  |
| Precinct TSx           | 1381                  | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Precinct OS            | 1381                  | 1        | 137           | 120         | 0                    | 0.00% | 2 | 1.67%                        |  |  |
| Precinct VBM           | 1381                  | 1        | 412           | 371         | 0                    | 0.00% | 0 | 0.00%                        |  |  |
| MB Precinct            | 1381                  | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Provisional            | 1381                  | 1        | 31            | 25          | 0                    | 0.00% | 0 | 0.00%                        |  |  |
| Total                  | 1381                  | 5        | 580           | 516         | 0                    | 0.00% | 2 | 0.39%                        |  |  |
| 500003.10-CP 500003.10 |                       |          |               |             |                      |       |   |                              |  |  |
| Precinct TSx           | 1065                  | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Precinct OS            | 1065                  | 1        | 131           | 119         | 0                    | 0.00% | 2 | 1.68%                        |  |  |
| Precinct VBM           | 1065                  | 1        | 346           | 302         | 5                    | 1.66% | 1 | 0.33%                        |  |  |
| MB Precinct            | 1065                  | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Provisional            | 1065                  | 1        | 11            | 7           | 0                    | 0.00% | 0 | 0.00%                        |  |  |
| Total                  | 1065                  | 5        | 488           | 428         | 5                    | 1.17% | 3 | 0.70%                        |  |  |
| 500004.10-CP 500004.10 |                       |          |               |             |                      |       |   |                              |  |  |
| Precinct TSx           | 981                   | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Precinct OS            | 981                   | 1        | 122           | 107         | 0                    | 0.00% | 2 | 1.87%                        |  |  |
| Precinct VBM           | 981                   | 1        | 264           | 230         | 1                    | 0.43% | 0 | 0.00%                        |  |  |
| MB Precinct            | 981                   | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Provisional            | 981                   | 1        | 21            | 18          | 0                    | 0.00% | 0 | 0.00%                        |  |  |
| Total                  | 981                   | 5        | 407           | 355         | 1                    | 0.28% | 2 | 0.56%                        |  |  |
| 500005.10-CP 500005.10 |                       |          |               |             |                      |       |   |                              |  |  |
| Precinct TSx           | 946                   | 1        | 0             | 0           | 0                    | -     | 0 | -                            |  |  |
| Precinct OS            | 946                   | 1        | 107           | 97          | 0                    | 0.00% | 2 | 2.06%                        |  |  |
| Precinct VBM           | 946                   | 1        | 250           | 219         | 2                    | 0.91% | 0 | 0.00%                        |  |  |

**COUNTY OF EL DORADO**  
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|                        | United States Senator |          |               |             |                      |                              |
|------------------------|-----------------------|----------|---------------|-------------|----------------------|------------------------------|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | JASON KRAUS<br>(NPP) | PRESIDENT C.<br>GRAPPO (DEM) |
| MB Precinct            | 946                   | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 946                   | 1        | 16            | 12          | 0                    | 0.00%                        |
| Total                  | 946                   | 5        | 373           | 328         | 2                    | 0.61%                        |
| 500006.10-CP 500006.10 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1661                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 1661                  | 1        | 190           | 164         | 0                    | 0.00%                        |
| Precinct VBM           | 1661                  | 1        | 498           | 439         | 2                    | 0.46%                        |
| MB Precinct            | 1661                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 1661                  | 1        | 58            | 49          | 1                    | 2.04%                        |
| Total                  | 1661                  | 5        | 746           | 652         | 3                    | 0.46%                        |
| 500007.10-CP 500007.10 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1788                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 1788                  | 1        | 208           | 188         | 0                    | 0.00%                        |
| Precinct VBM           | 1788                  | 1        | 688           | 602         | 0                    | 0.00%                        |
| MB Precinct            | 1788                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 1788                  | 1        | 20            | 18          | 1                    | 5.56%                        |
| Total                  | 1788                  | 5        | 916           | 808         | 1                    | 0.12%                        |
| 500009.10-CP 500009.10 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1073                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 1073                  | 1        | 121           | 108         | 1                    | 0.93%                        |
| Precinct VBM           | 1073                  | 1        | 304           | 267         | 1                    | 0.37%                        |
| MB Precinct            | 1073                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 1073                  | 1        | 38            | 31          | 0                    | 0.00%                        |
| Total                  | 1073                  | 5        | 463           | 406         | 2                    | 0.49%                        |
| 500011.10-CP 500011.10 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1635                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 1635                  | 1        | 178           | 163         | 2                    | 1.23%                        |
| Precinct VBM           | 1635                  | 1        | 380           | 338         | 1                    | 0.30%                        |
| MB Precinct            | 1635                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 1635                  | 1        | 40            | 33          | 0                    | 0.00%                        |
| Total                  | 1635                  | 5        | 598           | 534         | 3                    | 0.56%                        |
| 500012.10-CP 500012.10 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1574                  | 1        | 1             | 1           | 0                    | 0.00%                        |
| Precinct OS            | 1574                  | 1        | 195           | 170         | 1                    | 0.59%                        |
| Precinct VBM           | 1574                  | 1        | 601           | 542         | 3                    | 0.55%                        |
| MB Precinct            | 1574                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 1574                  | 1        | 37            | 30          | 0                    | 0.00%                        |
| Total                  | 1574                  | 5        | 834           | 743         | 4                    | 0.54%                        |
| 500015.10-CP 500015.10 |                       |          |               |             |                      |                              |
| Precinct TSx           | 978                   | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 978                   | 1        | 105           | 96          | 1                    | 1.04%                        |
| Precinct VBM           | 978                   | 1        | 407           | 367         | 0                    | 0.00%                        |
| MB Precinct            | 978                   | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 978                   | 1        | 13            | 10          | 0                    | 0.00%                        |
| Total                  | 978                   | 5        | 525           | 473         | 1                    | 0.21%                        |
| 500020.10-CP 500020.10 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1530                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 1530                  | 1        | 161           | 141         | 0                    | 0.00%                        |
| Precinct VBM           | 1530                  | 1        | 659           | 607         | 7                    | 1.15%                        |
| MB Precinct            | 1530                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 1530                  | 1        | 27            | 26          | 1                    | 3.85%                        |
| Total                  | 1530                  | 5        | 847           | 774         | 8                    | 1.03%                        |
| 500021.10-CP 500021.10 |                       |          |               |             |                      |                              |
| Precinct TSx           | 1404                  | 1        | 0             | 0           | 0                    | -                            |
| Precinct OS            | 1404                  | 1        | 169           | 149         | 0                    | 0.00%                        |
| Precinct VBM           | 1404                  | 1        | 632           | 572         | 5                    | 0.87%                        |
| MB Precinct            | 1404                  | 1        | 0             | 0           | 0                    | -                            |
| Provisional            | 1404                  | 1        | 28            | 25          | 0                    | 0.00%                        |
| Total                  | 1404                  | 5        | 829           | 746         | 5                    | 0.67%                        |
| 95U201.01-MB 95U201.01 |                       |          |               |             |                      |                              |
| Precinct TSx           | 8                     | 1        | 0             | 0           | 0                    | -                            |

**COUNTY OF EL DORADO**  
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|                        | United States Senator |          |               |             |                      |                              |
|------------------------|-----------------------|----------|---------------|-------------|----------------------|------------------------------|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | JASON KRAUS<br>(NPP) | PRESIDENT C.<br>GRAPPO (DEM) |
| Precinct OS            | 8                     | 1        | 0             | 0           | 0                    | 0                            |
| Precinct VBM           | 8                     | 1        | 0             | 0           | 0                    | 0                            |
| MB Precinct            | 8                     | 1        | 7             | 7           | 0                    | 0.00%                        |
| Provisional            | 8                     | 1        | 0             | 0           | 0                    | 0                            |
| Total                  | 8                     | 5        | 7             | 7           | 0                    | 0.00%                        |
| 95U201.02-MB 95U201.02 |                       |          |               |             |                      |                              |
| Precinct TSx           | 541                   | 1        | 0             | 0           | 0                    | 0                            |
| Precinct OS            | 541                   | 1        | 0             | 0           | 0                    | 0                            |
| Precinct VBM           | 541                   | 1        | 0             | 0           | 0                    | 0                            |
| MB Precinct            | 541                   | 1        | 358           | 304         | 1                    | 0.33%                        |
| Provisional            | 541                   | 1        | 1             | 1           | 0                    | 0.00%                        |
| Total                  | 541                   | 5        | 359           | 305         | 1                    | 0.33%                        |
| 95U202.02-MB 95U202.02 |                       |          |               |             |                      |                              |
| Precinct TSx           | 859                   | 1        | 0             | 0           | 0                    | 0                            |
| Precinct OS            | 859                   | 1        | 0             | 0           | 0                    | 0                            |
| Precinct VBM           | 859                   | 1        | 0             | 0           | 0                    | 0                            |
| MB Precinct            | 859                   | 1        | 574           | 505         | 2                    | 0.40%                        |
| Provisional            | 859                   | 1        | 4             | 3           | 1                    | 33.33%                       |
| Total                  | 859                   | 5        | 578           | 508         | 3                    | 0.59%                        |
| 95U203.02-MB 95U203.02 |                       |          |               |             |                      |                              |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0                    | 0                            |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0                    | 0                            |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0                    | 0                            |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0                    | 0                            |
| Provisional            | 0                     | 1        | 0             | 0           | 0                    | 0                            |
| Total                  | 0                     | 5        | 0             | 0           | 0                    | 0                            |
| 95U204.01-MB 95U204.01 |                       |          |               |             |                      |                              |
| Precinct TSx           | 895                   | 1        | 0             | 0           | 0                    | 0                            |
| Precinct OS            | 895                   | 1        | 0             | 0           | 0                    | 0                            |
| Precinct VBM           | 895                   | 1        | 0             | 0           | 0                    | 0                            |
| MB Precinct            | 895                   | 1        | 651           | 597         | 8                    | 1.34%                        |
| Provisional            | 895                   | 1        | 2             | 2           | 0                    | 0.00%                        |
| Total                  | 895                   | 5        | 653           | 599         | 8                    | 1.34%                        |
| 95U301.05-MB 95U301.05 |                       |          |               |             |                      |                              |
| Precinct TSx           | 244                   | 1        | 0             | 0           | 0                    | 0                            |
| Precinct OS            | 244                   | 1        | 0             | 0           | 0                    | 0                            |
| Precinct VBM           | 244                   | 1        | 0             | 0           | 0                    | 0                            |
| MB Precinct            | 244                   | 1        | 151           | 136         | 0                    | 0.00%                        |
| Provisional            | 244                   | 1        | 2             | 2           | 0                    | 0.00%                        |
| Total                  | 244                   | 5        | 153           | 138         | 0                    | 0.00%                        |
| 95U302.04-MB 95U302.04 |                       |          |               |             |                      |                              |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0                    | 0                            |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0                    | 0                            |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0                    | 0                            |
| MB Precinct            | 0                     | 1        | 1             | 1           | 0                    | 0.00%                        |
| Provisional            | 0                     | 1        | 0             | 0           | 0                    | 0                            |
| Total                  | 0                     | 5        | 1             | 1           | 0                    | 0.00%                        |
| 95U302.05-MB 95U302.05 |                       |          |               |             |                      |                              |
| Precinct TSx           | 128                   | 1        | 0             | 0           | 0                    | 0                            |
| Precinct OS            | 128                   | 1        | 0             | 0           | 0                    | 0                            |
| Precinct VBM           | 128                   | 1        | 0             | 0           | 0                    | 0                            |
| MB Precinct            | 128                   | 1        | 86            | 81          | 1                    | 1.23%                        |
| Provisional            | 128                   | 1        | 0             | 0           | 0                    | 0                            |
| Total                  | 128                   | 5        | 86            | 81          | 1                    | 1.23%                        |
| 95U303.03-MB 95U303.03 |                       |          |               |             |                      |                              |
| Precinct TSx           | 144                   | 1        | 0             | 0           | 0                    | 0                            |
| Precinct OS            | 144                   | 1        | 0             | 0           | 0                    | 0                            |
| Precinct VBM           | 144                   | 1        | 0             | 0           | 0                    | 0                            |
| MB Precinct            | 144                   | 1        | 84            | 78          | 1                    | 1.28%                        |
| Provisional            | 144                   | 1        | 0             | 0           | 0                    | 0                            |
| Total                  | 144                   | 5        | 84            | 78          | 1                    | 1.28%                        |

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|                        | United States Senator |          |               |             |                      |       |                              |       |
|------------------------|-----------------------|----------|---------------|-------------|----------------------|-------|------------------------------|-------|
|                        | Reg. Voters           | Vote For | Times Counted | Total Votes | JASON KRAUS<br>(NPP) |       | PRESIDENT C.<br>GRAPPO (DEM) |       |
| 95U303.05-MB 95U303.05 |                       |          |               |             |                      |       |                              |       |
| Precinct TSx           | 159                   | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct OS            | 159                   | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct VBM           | 159                   | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| MB Precinct            | 159                   | 1        | 111           | 95          | 1                    | 1.05% | 0                            | 0.00% |
| Provisional            | 159                   | 1        | 1             | 1           | 0                    | 0.00% | 0                            | 0.00% |
| Total                  | 159                   | 5        | 112           | 96          | 1                    | 1.04% | 0                            | 0.00% |
| 95U304.05-MB 95U304.05 |                       |          |               |             |                      |       |                              |       |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Provisional            | 0                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Total                  | 0                     | 5        | 0             | 0           | 0                    | -     | 0                            | -     |
| 95U305.05-MB 95U305.05 |                       |          |               |             |                      |       |                              |       |
| Precinct TSx           | 7                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct OS            | 7                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct VBM           | 7                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| MB Precinct            | 7                     | 1        | 7             | 7           | 0                    | 0.00% | 0                            | 0.00% |
| Provisional            | 7                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Total                  | 7                     | 5        | 7             | 7           | 0                    | 0.00% | 0                            | 0.00% |
| 95U401.08-MB 95U401.08 |                       |          |               |             |                      |       |                              |       |
| Precinct TSx           | 39                    | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct OS            | 39                    | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct VBM           | 39                    | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| MB Precinct            | 39                    | 1        | 16            | 15          | 0                    | 0.00% | 0                            | 0.00% |
| Provisional            | 39                    | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Total                  | 39                    | 5        | 16            | 15          | 0                    | 0.00% | 0                            | 0.00% |
| 95U402.07-MB 95U402.07 |                       |          |               |             |                      |       |                              |       |
| Precinct TSx           | 413                   | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct OS            | 413                   | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct VBM           | 413                   | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| MB Precinct            | 413                   | 1        | 256           | 234         | 0                    | 0.00% | 0                            | 0.00% |
| Provisional            | 413                   | 1        | 1             | 0           | 0                    | -     | 0                            | -     |
| Total                  | 413                   | 5        | 257           | 234         | 0                    | 0.00% | 0                            | 0.00% |
| 95U402.08-MB 95U402.08 |                       |          |               |             |                      |       |                              |       |
| Precinct TSx           | 873                   | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct OS            | 873                   | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct VBM           | 873                   | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| MB Precinct            | 873                   | 1        | 566           | 504         | 8                    | 1.59% | 0                            | 0.00% |
| Provisional            | 873                   | 1        | 3             | 3           | 0                    | 0.00% | 0                            | 0.00% |
| Total                  | 873                   | 5        | 569           | 507         | 8                    | 1.58% | 0                            | 0.00% |
| 95U403.08-MB 95U403.08 |                       |          |               |             |                      |       |                              |       |
| Precinct TSx           | 1591                  | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct OS            | 1591                  | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct VBM           | 1591                  | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| MB Precinct            | 1591                  | 1        | 1042          | 946         | 2                    | 0.21% | 0                            | 0.00% |
| Provisional            | 1591                  | 1        | 13            | 10          | 0                    | 0.00% | 0                            | 0.00% |
| Total                  | 1591                  | 5        | 1055          | 956         | 2                    | 0.21% | 0                            | 0.00% |
| 95U404.06-MB 95U404.06 |                       |          |               |             |                      |       |                              |       |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Provisional            | 0                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Total                  | 0                     | 5        | 0             | 0           | 0                    | -     | 0                            | -     |
| 95U404.08-MB 95U404.08 |                       |          |               |             |                      |       |                              |       |
| Precinct TSx           | 0                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct OS            | 0                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct VBM           | 0                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |
| MB Precinct            | 0                     | 1        | 0             | 0           | 0                    | -     | 0                            | -     |

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|                               | United States Senator |          |               |             |                      |                              |    |
|-------------------------------|-----------------------|----------|---------------|-------------|----------------------|------------------------------|----|
|                               | Reg. Voters           | Vote For | Times Counted | Total Votes | JASON KRAUS<br>(NPP) | PRESIDENT C.<br>GRAPPO (DEM) |    |
| Provisional                   | 0                     | 1        | 0             | 0           | 0                    | -                            | 0  |
| Total                         | 0                     | 5        | 0             | 0           | 0                    | -                            | 0  |
| 95U501.09-MB 95U501.09        |                       |          |               |             |                      |                              |    |
| Precinct TSx                  | 0                     | 1        | 0             | 0           | 0                    | -                            | 0  |
| Precinct OS                   | 0                     | 1        | 0             | 0           | 0                    | -                            | 0  |
| Precinct VBM                  | 0                     | 1        | 0             | 0           | 0                    | -                            | 0  |
| MB Precinct                   | 0                     | 1        | 2             | 2           | 0                    | 0.00%                        | 0  |
| Provisional                   | 0                     | 1        | 0             | 0           | 0                    | -                            | 0  |
| Total                         | 0                     | 5        | 2             | 2           | 0                    | 0.00%                        | 0  |
| 95U501.10-MB 95U501.10        |                       |          |               |             |                      |                              |    |
| Precinct TSx                  | 286                   | 1        | 0             | 0           | 0                    | -                            | 0  |
| Precinct OS                   | 286                   | 1        | 0             | 0           | 0                    | -                            | 0  |
| Precinct VBM                  | 286                   | 1        | 0             | 0           | 0                    | -                            | 0  |
| MB Precinct                   | 286                   | 1        | 172           | 160         | 0                    | 0.00%                        | 0  |
| Provisional                   | 286                   | 1        | 2             | 2           | 0                    | 0.00%                        | 0  |
| Total                         | 286                   | 5        | 174           | 162         | 0                    | 0.00%                        | 0  |
| 95U598.10-MB 95U598.10        |                       |          |               |             |                      |                              |    |
| Precinct TSx                  | 568                   | 1        | 0             | 0           | 0                    | -                            | 0  |
| Precinct OS                   | 568                   | 1        | 0             | 0           | 0                    | -                            | 0  |
| Precinct VBM                  | 568                   | 1        | 0             | 0           | 0                    | -                            | 0  |
| MB Precinct                   | 568                   | 1        | 239           | 214         | 1                    | 0.47%                        | 1  |
| Provisional                   | 568                   | 1        | 3             | 2           | 0                    | 0.00%                        | 0  |
| Total                         | 568                   | 5        | 242           | 216         | 1                    | 0.46%                        | 1  |
| 95U599.10-MB 95U599.10        |                       |          |               |             |                      |                              |    |
| Precinct TSx                  | 75                    | 1        | 0             | 0           | 0                    | -                            | 0  |
| Precinct OS                   | 75                    | 1        | 0             | 0           | 0                    | -                            | 0  |
| Precinct VBM                  | 75                    | 1        | 0             | 0           | 0                    | -                            | 0  |
| MB Precinct                   | 75                    | 1        | 38            | 34          | 0                    | 0.00%                        | 0  |
| Provisional                   | 75                    | 1        | 0             | 0           | 0                    | -                            | 0  |
| Total                         | 75                    | 5        | 38            | 34          | 0                    | 0.00%                        | 0  |
| 96U101.11-MB 96U101.11        |                       |          |               |             |                      |                              |    |
| Precinct TSx                  | 626                   | 1        | 0             | 0           | 0                    | -                            | 0  |
| Precinct OS                   | 626                   | 1        | 0             | 0           | 0                    | -                            | 0  |
| Precinct VBM                  | 626                   | 1        | 0             | 0           | 0                    | -                            | 0  |
| MB Precinct                   | 626                   | 1        | 423           | 367         | 0                    | 0.00%                        | 0  |
| Provisional                   | 626                   | 1        | 6             | 6           | 0                    | 0.00%                        | 0  |
| Total                         | 626                   | 5        | 429           | 373         | 0                    | 0.00%                        | 0  |
| 96U401.14-MB 96U401.14        |                       |          |               |             |                      |                              |    |
| Precinct TSx                  | 566                   | 1        | 0             | 0           | 0                    | -                            | 0  |
| Precinct OS                   | 566                   | 1        | 0             | 0           | 0                    | -                            | 0  |
| Precinct VBM                  | 566                   | 1        | 0             | 0           | 0                    | -                            | 0  |
| MB Precinct                   | 566                   | 1        | 359           | 322         | 1                    | 0.31%                        | 2  |
| Provisional                   | 566                   | 1        | 6             | 6           | 0                    | 0.00%                        | 0  |
| Total                         | 566                   | 5        | 365           | 328         | 1                    | 0.30%                        | 2  |
| Total                         |                       |          |               |             |                      |                              |    |
| Precinct TSx                  | 109479                | 102      | 8             | 8           | 0                    | 0.00%                        | 0  |
| Precinct OS                   | 109479                | 102      | 12009         | 10701       | 79                   | 0.74%                        | 25 |
| Precinct VBM                  | 109479                | 102      | 45535         | 40875       | 270                  | 0.66%                        | 44 |
| MB Precinct                   | 109479                | 102      | 5143          | 4609        | 26                   | 0.56%                        | 6  |
| Provisional                   | 109479                | 102      | 1584          | 1314        | 14                   | 1.07%                        | 3  |
| Total                         | 109479                | 510      | 64279         | 57507       | 389                  | 0.68%                        | 78 |
| STATE BOARD OF EQUALIZATION   |                       |          |               |             |                      |                              |    |
| STATE BOARD OF EQUALIZATION 1 |                       |          |               |             |                      |                              |    |
| Precinct TSx                  | 109479                | 102      | 8             | 8           | 0                    | 0.00%                        | 0  |
| Precinct OS                   | 109479                | 102      | 12009         | 10701       | 79                   | 0.74%                        | 25 |
| Precinct VBM                  | 109479                | 102      | 45535         | 40875       | 270                  | 0.66%                        | 44 |
| MB Precinct                   | 109479                | 102      | 5143          | 4609        | 26                   | 0.56%                        | 6  |
| Provisional                   | 109479                | 102      | 1584          | 1314        | 14                   | 1.07%                        | 3  |
| Total                         | 109479                | 510      | 64279         | 57507       | 389                  | 0.68%                        | 78 |
| Total                         |                       |          |               |             |                      |                              |    |

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|                                     | United States Senator |            |               |              |                      |              |                              |              |
|-------------------------------------|-----------------------|------------|---------------|--------------|----------------------|--------------|------------------------------|--------------|
|                                     | Reg. Voters           | Vote For   | Times Counted | Total Votes  | JASON KRAUS<br>(NPP) |              | PRESIDENT C.<br>GRAPPO (DEM) |              |
| Precinct TSx                        | 109479                | 102        | 8             | 8            | 0                    | 0.00%        | 0                            | 0.00%        |
| Precinct OS                         | 109479                | 102        | 12009         | 10701        | 79                   | 0.74%        | 25                           | 0.23%        |
| Precinct VBM                        | 109479                | 102        | 45535         | 40875        | 270                  | 0.66%        | 44                           | 0.11%        |
| MB Precinct                         | 109479                | 102        | 5143          | 4609         | 26                   | 0.56%        | 6                            | 0.13%        |
| Provisional                         | 109479                | 102        | 1584          | 1314         | 14                   | 1.07%        | 3                            | 0.23%        |
| <b>Total</b>                        | <b>109479</b>         | <b>510</b> | <b>64279</b>  | <b>57507</b> | <b>389</b>           | <b>0.68%</b> | <b>78</b>                    | <b>0.14%</b> |
| <b>UNITED STATES SENATE</b>         |                       |            |               |              |                      |              |                              |              |
| <b>UNITED STATES SENATE</b>         |                       |            |               |              |                      |              |                              |              |
| Precinct TSx                        | 109479                | 102        | 8             | 8            | 0                    | 0.00%        | 0                            | 0.00%        |
| Precinct OS                         | 109479                | 102        | 12009         | 10701        | 79                   | 0.74%        | 25                           | 0.23%        |
| Precinct VBM                        | 109479                | 102        | 45535         | 40875        | 270                  | 0.66%        | 44                           | 0.11%        |
| MB Precinct                         | 109479                | 102        | 5143          | 4609         | 26                   | 0.56%        | 6                            | 0.13%        |
| Provisional                         | 109479                | 102        | 1584          | 1314         | 14                   | 1.07%        | 3                            | 0.23%        |
| <b>Total</b>                        | <b>109479</b>         | <b>510</b> | <b>64279</b>  | <b>57507</b> | <b>389</b>           | <b>0.68%</b> | <b>78</b>                    | <b>0.14%</b> |
| <b>Total</b>                        |                       |            |               |              |                      |              |                              |              |
| Precinct TSx                        | 109479                | 102        | 8             | 8            | 0                    | 0.00%        | 0                            | 0.00%        |
| Precinct OS                         | 109479                | 102        | 12009         | 10701        | 79                   | 0.74%        | 25                           | 0.23%        |
| Precinct VBM                        | 109479                | 102        | 45535         | 40875        | 270                  | 0.66%        | 44                           | 0.11%        |
| MB Precinct                         | 109479                | 102        | 5143          | 4609         | 26                   | 0.56%        | 6                            | 0.13%        |
| Provisional                         | 109479                | 102        | 1584          | 1314         | 14                   | 1.07%        | 3                            | 0.23%        |
| <b>Total</b>                        | <b>109479</b>         | <b>510</b> | <b>64279</b>  | <b>57507</b> | <b>389</b>           | <b>0.68%</b> | <b>78</b>                    | <b>0.14%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |                       |            |               |              |                      |              |                              |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                       |            |               |              |                      |              |                              |              |
| Precinct TSx                        | 109479                | 102        | 8             | 8            | 0                    | 0.00%        | 0                            | 0.00%        |
| Precinct OS                         | 109479                | 102        | 12009         | 10701        | 79                   | 0.74%        | 25                           | 0.23%        |
| Precinct VBM                        | 109479                | 102        | 45535         | 40875        | 270                  | 0.66%        | 44                           | 0.11%        |
| MB Precinct                         | 109479                | 102        | 5143          | 4609         | 26                   | 0.56%        | 6                            | 0.13%        |
| Provisional                         | 109479                | 102        | 1584          | 1314         | 14                   | 1.07%        | 3                            | 0.23%        |
| <b>Total</b>                        | <b>109479</b>         | <b>510</b> | <b>64279</b>  | <b>57507</b> | <b>389</b>           | <b>0.68%</b> | <b>78</b>                    | <b>0.14%</b> |
| <b>Total</b>                        |                       |            |               |              |                      |              |                              |              |
| Precinct TSx                        | 109479                | 102        | 8             | 8            | 0                    | 0.00%        | 0                            | 0.00%        |
| Precinct OS                         | 109479                | 102        | 12009         | 10701        | 79                   | 0.74%        | 25                           | 0.23%        |
| Precinct VBM                        | 109479                | 102        | 45535         | 40875        | 270                  | 0.66%        | 44                           | 0.11%        |
| MB Precinct                         | 109479                | 102        | 5143          | 4609         | 26                   | 0.56%        | 6                            | 0.13%        |
| Provisional                         | 109479                | 102        | 1584          | 1314         | 14                   | 1.07%        | 3                            | 0.23%        |
| <b>Total</b>                        | <b>109479</b>         | <b>510</b> | <b>64279</b>  | <b>57507</b> | <b>389</b>           | <b>0.68%</b> | <b>78</b>                    | <b>0.14%</b> |
| <b>STATE SENATE</b>                 |                       |            |               |              |                      |              |                              |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                       |            |               |              |                      |              |                              |              |
| Precinct TSx                        | 109479                | 102        | 8             | 8            | 0                    | 0.00%        | 0                            | 0.00%        |
| Precinct OS                         | 109479                | 102        | 12009         | 10701        | 79                   | 0.74%        | 25                           | 0.23%        |
| Precinct VBM                        | 109479                | 102        | 45535         | 40875        | 270                  | 0.66%        | 44                           | 0.11%        |
| MB Precinct                         | 109479                | 102        | 5143          | 4609         | 26                   | 0.56%        | 6                            | 0.13%        |
| Provisional                         | 109479                | 102        | 1584          | 1314         | 14                   | 1.07%        | 3                            | 0.23%        |
| <b>Total</b>                        | <b>109479</b>         | <b>510</b> | <b>64279</b>  | <b>57507</b> | <b>389</b>           | <b>0.68%</b> | <b>78</b>                    | <b>0.14%</b> |
| <b>Total</b>                        |                       |            |               |              |                      |              |                              |              |
| Precinct TSx                        | 109479                | 102        | 8             | 8            | 0                    | 0.00%        | 0                            | 0.00%        |
| Precinct OS                         | 109479                | 102        | 12009         | 10701        | 79                   | 0.74%        | 25                           | 0.23%        |
| Precinct VBM                        | 109479                | 102        | 45535         | 40875        | 270                  | 0.66%        | 44                           | 0.11%        |
| MB Precinct                         | 109479                | 102        | 5143          | 4609         | 26                   | 0.56%        | 6                            | 0.13%        |
| Provisional                         | 109479                | 102        | 1584          | 1314         | 14                   | 1.07%        | 3                            | 0.23%        |
| <b>Total</b>                        | <b>109479</b>         | <b>510</b> | <b>64279</b>  | <b>57507</b> | <b>389</b>           | <b>0.68%</b> | <b>78</b>                    | <b>0.14%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                       |            |               |              |                      |              |                              |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                       |            |               |              |                      |              |                              |              |
| Precinct TSx                        | 70961                 | 75         | 4             | 4            | 0                    | 0.00%        | 0                            | 0.00%        |
| Precinct OS                         | 70961                 | 75         | 7627          | 6813         | 46                   | 0.68%        | 21                           | 0.31%        |
| Precinct VBM                        | 70961                 | 75         | 28225         | 25393        | 149                  | 0.59%        | 24                           | 0.09%        |
| MB Precinct                         | 70961                 | 75         | 4361          | 3920         | 25                   | 0.64%        | 4                            | 0.10%        |

**COUNTY OF EL DORADO**  
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|                                   | United States Senator |          |               |             |                      |       |                              |       |
|-----------------------------------|-----------------------|----------|---------------|-------------|----------------------|-------|------------------------------|-------|
|                                   | Reg. Voters           | Vote For | Times Counted | Total Votes | JASON KRAUS<br>(NPP) |       | PRESIDENT C.<br>GRAPPO (DEM) |       |
| Provisional                       | 70961                 | 75       | 1038          | 860         | 9                    | 1.05% | 3                            | 0.35% |
| Total                             | 70961                 | 375      | 41255         | 36990       | 229                  | 0.62% | 52                           | 0.14% |
| <b>6TH STATE ASSEMBLY DIST</b>    |                       |          |               |             |                      |       |                              |       |
| Precinct TSx                      | 38518                 | 27       | 4             | 4           | 0                    | 0.00% | 0                            | 0.00% |
| Precinct OS                       | 38518                 | 27       | 4382          | 3888        | 33                   | 0.85% | 4                            | 0.10% |
| Precinct VBM                      | 38518                 | 27       | 17310         | 15482       | 121                  | 0.78% | 20                           | 0.13% |
| MB Precinct                       | 38518                 | 27       | 782           | 689         | 1                    | 0.15% | 2                            | 0.29% |
| Provisional                       | 38518                 | 27       | 546           | 454         | 5                    | 1.10% | 0                            | 0.00% |
| Total                             | 38518                 | 135      | 23024         | 20517       | 160                  | 0.78% | 26                           | 0.13% |
| <b>Total</b>                      |                       |          |               |             |                      |       |                              |       |
| Precinct TSx                      | 109479                | 102      | 8             | 8           | 0                    | 0.00% | 0                            | 0.00% |
| Precinct OS                       | 109479                | 102      | 12009         | 10701       | 79                   | 0.74% | 25                           | 0.23% |
| Precinct VBM                      | 109479                | 102      | 45535         | 40875       | 270                  | 0.66% | 44                           | 0.11% |
| MB Precinct                       | 109479                | 102      | 5143          | 4609        | 26                   | 0.56% | 6                            | 0.13% |
| Provisional                       | 109479                | 102      | 1584          | 1314        | 14                   | 1.07% | 3                            | 0.23% |
| Total                             | 109479                | 510      | 64279         | 57507       | 389                  | 0.68% | 78                           | 0.14% |
| <b>SUPERVISORIAL DISTRICTS</b>    |                       |          |               |             |                      |       |                              |       |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                       |          |               |             |                      |       |                              |       |
| Precinct TSx                      | 22587                 | 14       | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct OS                       | 22587                 | 14       | 2530          | 2229        | 23                   | 1.03% | 2                            | 0.09% |
| Precinct VBM                      | 22587                 | 14       | 10411         | 9290        | 82                   | 0.88% | 4                            | 0.04% |
| MB Precinct                       | 22587                 | 14       | 423           | 367         | 0                    | 0.00% | 0                            | 0.00% |
| Provisional                       | 22587                 | 14       | 301           | 243         | 3                    | 1.23% | 0                            | 0.00% |
| Total                             | 22587                 | 70       | 13665         | 12129       | 108                  | 0.89% | 6                            | 0.05% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                       |          |               |             |                      |       |                              |       |
| Precinct TSx                      | 23114                 | 23       | 4             | 4           | 0                    | 0.00% | 0                            | 0.00% |
| Precinct OS                       | 23114                 | 23       | 2509          | 2252        | 16                   | 0.71% | 3                            | 0.13% |
| Precinct VBM                      | 23114                 | 23       | 9647          | 8642        | 51                   | 0.59% | 18                           | 0.21% |
| MB Precinct                       | 23114                 | 23       | 1590          | 1413        | 11                   | 0.78% | 0                            | 0.00% |
| Provisional                       | 23114                 | 23       | 327           | 277         | 4                    | 1.44% | 0                            | 0.00% |
| Total                             | 23114                 | 115      | 14077         | 12588       | 82                   | 0.65% | 21                           | 0.17% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                       |          |               |             |                      |       |                              |       |
| Precinct TSx                      | 21691                 | 26       | 3             | 3           | 0                    | 0.00% | 0                            | 0.00% |
| Precinct OS                       | 21691                 | 26       | 2368          | 2119        | 19                   | 0.90% | 4                            | 0.19% |
| Precinct VBM                      | 21691                 | 26       | 10021         | 9055        | 67                   | 0.74% | 5                            | 0.06% |
| MB Precinct                       | 21691                 | 26       | 440           | 398         | 3                    | 0.75% | 3                            | 0.75% |
| Provisional                       | 21691                 | 26       | 277           | 237         | 3                    | 1.27% | 1                            | 0.42% |
| Total                             | 21691                 | 130      | 13109         | 11812       | 92                   | 0.78% | 13                           | 0.11% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                       |          |               |             |                      |       |                              |       |
| Precinct TSx                      | 23425                 | 22       | 0             | 0           | 0                    | -     | 0                            | -     |
| Precinct OS                       | 23425                 | 22       | 2568          | 2294        | 14                   | 0.61% | 5                            | 0.22% |
| Precinct VBM                      | 23425                 | 22       | 9398          | 8495        | 40                   | 0.47% | 6                            | 0.07% |
| MB Precinct                       | 23425                 | 22       | 2239          | 2021        | 11                   | 0.54% | 2                            | 0.10% |
| Provisional                       | 23425                 | 22       | 315           | 255         | 1                    | 0.39% | 0                            | 0.00% |
| Total                             | 23425                 | 110      | 14520         | 13065       | 66                   | 0.51% | 13                           | 0.10% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                       |          |               |             |                      |       |                              |       |
| Precinct TSx                      | 18662                 | 17       | 1             | 1           | 0                    | 0.00% | 0                            | 0.00% |
| Precinct OS                       | 18662                 | 17       | 2034          | 1807        | 7                    | 0.39% | 11                           | 0.61% |
| Precinct VBM                      | 18662                 | 17       | 6058          | 5393        | 30                   | 0.56% | 11                           | 0.20% |
| MB Precinct                       | 18662                 | 17       | 451           | 410         | 1                    | 0.24% | 1                            | 0.24% |
| Provisional                       | 18662                 | 17       | 364           | 302         | 3                    | 0.99% | 2                            | 0.66% |
| Total                             | 18662                 | 85       | 8908          | 7913        | 41                   | 0.52% | 25                           | 0.32% |
| <b>Total</b>                      |                       |          |               |             |                      |       |                              |       |
| Precinct TSx                      | 109479                | 102      | 8             | 8           | 0                    | 0.00% | 0                            | 0.00% |
| Precinct OS                       | 109479                | 102      | 12009         | 10701       | 79                   | 0.74% | 25                           | 0.23% |
| Precinct VBM                      | 109479                | 102      | 45535         | 40875       | 270                  | 0.66% | 44                           | 0.11% |
| MB Precinct                       | 109479                | 102      | 5143          | 4609        | 26                   | 0.56% | 6                            | 0.13% |
| Provisional                       | 109479                | 102      | 1584          | 1314        | 14                   | 1.07% | 3                            | 0.23% |
| Total                             | 109479                | 510      | 64279         | 57507       | 389                  | 0.68% | 78                           | 0.14% |



**COUNTY OF EL DORADO**  
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|                                 | United States Senator |            |               |              |                      |                              |
|---------------------------------|-----------------------|------------|---------------|--------------|----------------------|------------------------------|
|                                 | Reg. Voters           | Vote For   | Times Counted | Total Votes  | JASON KRAUS<br>(NPP) | PRESIDENT C.<br>GRAPPO (DEM) |
| <b>CITIES</b>                   |                       |            |               |              |                      |                              |
| <b>CITY OF PLACERVILLE</b>      |                       |            |               |              |                      |                              |
| Precinct TSx                    | 5569                  | 5          | 0             | 0            | 0                    | -                            |
| Precinct OS                     | 5569                  | 5          | 631           | 558          | 5                    | 0.90%                        |
| Precinct VBM                    | 5569                  | 5          | 2590          | 2346         | 18                   | 0.77%                        |
| MB Precinct                     | 5569                  | 5          | 0             | 0            | 0                    | -                            |
| Provisional                     | 5569                  | 5          | 80            | 70           | 1                    | 1.43%                        |
| <b>Total</b>                    | <b>5569</b>           | <b>25</b>  | <b>3301</b>   | <b>2974</b>  | <b>24</b>            | <b>0.81%</b>                 |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                       |            |               |              |                      |                              |
| Precinct TSx                    | 9465                  | 7          | 0             | 0            | 0                    | -                            |
| Precinct OS                     | 9465                  | 7          | 1063          | 947          | 3                    | 0.32%                        |
| Precinct VBM                    | 9465                  | 7          | 2796          | 2466         | 7                    | 0.28%                        |
| MB Precinct                     | 9465                  | 7          | 0             | 0            | 0                    | -                            |
| Provisional                     | 9465                  | 7          | 224           | 186          | 2                    | 1.08%                        |
| <b>Total</b>                    | <b>9465</b>           | <b>35</b>  | <b>4083</b>   | <b>3599</b>  | <b>12</b>            | <b>0.33%</b>                 |
| <b>UNINCORPORATED AREA</b>      |                       |            |               |              |                      |                              |
| Precinct TSx                    | 94445                 | 90         | 8             | 8            | 0                    | 0.00%                        |
| Precinct OS                     | 94445                 | 90         | 10315         | 9196         | 71                   | 0.77%                        |
| Precinct VBM                    | 94445                 | 90         | 40149         | 36063        | 245                  | 0.68%                        |
| MB Precinct                     | 94445                 | 90         | 5143          | 4609         | 26                   | 0.56%                        |
| Provisional                     | 94445                 | 90         | 1280          | 1058         | 11                   | 1.04%                        |
| <b>Total</b>                    | <b>94445</b>          | <b>450</b> | <b>56895</b>  | <b>50934</b> | <b>353</b>           | <b>0.69%</b>                 |
| <b>Total</b>                    |                       |            |               |              |                      |                              |
| Precinct TSx                    | 109479                | 102        | 8             | 8            | 0                    | 0.00%                        |
| Precinct OS                     | 109479                | 102        | 12009         | 10701        | 79                   | 0.74%                        |
| Precinct VBM                    | 109479                | 102        | 45535         | 40875        | 270                  | 0.66%                        |
| MB Precinct                     | 109479                | 102        | 5143          | 4609         | 26                   | 0.56%                        |
| Provisional                     | 109479                | 102        | 1584          | 1314         | 14                   | 1.07%                        |
| <b>Total</b>                    | <b>109479</b>         | <b>510</b> | <b>64279</b>  | <b>57507</b> | <b>389</b>           | <b>0.68%</b>                 |

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|                        | United States Senator  |       |                            |       |                        |       |                             |        |
|------------------------|------------------------|-------|----------------------------|-------|------------------------|-------|-----------------------------|--------|
|                        | MASSIE MUNROE<br>(DEM) |       | HERBERT G.<br>PETERS (DEM) |       | EMORY RODGERS<br>(DEM) |       | LORETTA L.<br>SANCHEZ (DEM) |        |
| Jurisdiction Wide      |                        |       |                            |       |                        |       |                             |        |
| 100001.11-CP 100001.11 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.64% | 0                          | 0.00% | 0                      | 0.00% | 19                          | 12.10% |
| Precinct VBM           | 3                      | 0.43% | 1                          | 0.14% | 0                      | 0.00% | 55                          | 7.82%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 1                           | 6.25%  |
| Total                  | 4                      | 0.46% | 1                          | 0.11% | 0                      | 0.00% | 75                          | 8.56%  |
| 100002.11-CP 100002.11 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 3                           | 3.95%  |
| Precinct VBM           | 4                      | 0.99% | 0                          | 0.00% | 0                      | 0.00% | 32                          | 7.94%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 6.25% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 5                      | 1.01% | 0                          | 0.00% | 0                      | 0.00% | 35                          | 7.07%  |
| 100003.11-CP 100003.11 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 1                      | 0.90% | 12                          | 10.81% |
| Precinct VBM           | 1                      | 0.14% | 2                          | 0.28% | 0                      | 0.00% | 62                          | 8.76%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 3                           | 13.64% |
| Total                  | 1                      | 0.12% | 2                          | 0.24% | 1                      | 0.12% | 77                          | 9.16%  |
| 100004.11-CP 100004.11 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 3                      | 1.28% | 0                          | 0.00% | 1                      | 0.43% | 22                          | 9.40%  |
| Precinct VBM           | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 56                          | 8.41%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 1                           | 50.00% |
| Total                  | 3                      | 0.33% | 0                          | 0.00% | 1                      | 0.11% | 79                          | 8.76%  |
| 100005.11-CP 100005.11 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 6                           | 4.48%  |
| Precinct VBM           | 1                      | 0.12% | 0                          | 0.00% | 0                      | 0.00% | 44                          | 5.45%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 5                           | 26.32% |
| Total                  | 1                      | 0.10% | 0                          | 0.00% | 0                      | 0.00% | 55                          | 5.73%  |
| 100006.11-CP 100006.11 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 3                      | 1.36% | 0                          | 0.00% | 2                      | 0.91% | 24                          | 10.91% |
| Precinct VBM           | 4                      | 0.47% | 1                          | 0.12% | 0                      | 0.00% | 70                          | 8.27%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 7                      | 0.64% | 1                          | 0.09% | 2                      | 0.18% | 94                          | 8.65%  |
| 100008.11-CP 100008.11 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.57% | 0                          | 0.00% | 0                      | 0.00% | 21                          | 12.07% |
| Precinct VBM           | 3                      | 0.46% | 0                          | 0.00% | 0                      | 0.00% | 42                          | 6.50%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 4                      | 0.48% | 0                          | 0.00% | 0                      | 0.00% | 63                          | 7.58%  |
| 100009.11-CP 100009.11 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 3                      | 2.05% | 0                          | 0.00% | 0                      | 0.00% | 10                          | 6.85%  |
| Precinct VBM           | 3                      | 0.44% | 2                          | 0.30% | 3                      | 0.44% | 77                          | 11.39% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 5.56% | 0                          | 0.00% | 0                      | 0.00% | 4                           | 22.22% |
| Total                  | 7                      | 0.83% | 2                          | 0.24% | 3                      | 0.36% | 91                          | 10.83% |
| 100012.11-CP 100012.11 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 4                      | 1.75% | 0                          | 0.00% | 0                      | 0.00% | 14                          | 6.11%  |
| Precinct VBM           | 5                      | 0.58% | 0                          | 0.00% | 0                      | 0.00% | 58                          | 6.77%  |

**COUNTY OF EL DORADO**  
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|                        | United States Senator  |       |                            |       |                        |       |                             |        |
|------------------------|------------------------|-------|----------------------------|-------|------------------------|-------|-----------------------------|--------|
|                        | MASSIE MUNROE<br>(DEM) |       | HERBERT G.<br>PETERS (DEM) |       | EMORY RODGERS<br>(DEM) |       | LORETTA L.<br>SANCHEZ (DEM) |        |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 3.57% | 0                          | 0.00% | 0                      | 0.00% | 1                           | 3.57%  |
| Total                  | 10                     | 0.90% | 0                          | 0.00% | 0                      | 0.00% | 73                          | 6.55%  |
| 100013.11-CP 100013.11 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.45% | 1                          | 0.45% | 0                      | 0.00% | 27                          | 12.22% |
| Precinct VBM           | 1                      | 0.12% | 0                          | 0.00% | 0                      | 0.00% | 69                          | 8.53%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 4.17% | 0                          | 0.00% | 0                      | 0.00% | 2                           | 8.33%  |
| Total                  | 3                      | 0.28% | 1                          | 0.09% | 0                      | 0.00% | 98                          | 9.30%  |
| 100014.11-CP 100014.11 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.58% | 0                          | 0.00% | 0                      | 0.00% | 19                          | 10.98% |
| Precinct VBM           | 3                      | 0.48% | 0                          | 0.00% | 0                      | 0.00% | 31                          | 4.96%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 7                           | 31.82% |
| Total                  | 4                      | 0.49% | 0                          | 0.00% | 0                      | 0.00% | 57                          | 6.95%  |
| 100022.11-CP 100022.11 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.62% | 0                          | 0.00% | 0                      | 0.00% | 15                          | 9.32%  |
| Precinct VBM           | 2                      | 0.33% | 0                          | 0.00% | 0                      | 0.00% | 61                          | 9.97%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 1                           | 10.00% |
| Total                  | 3                      | 0.38% | 0                          | 0.00% | 0                      | 0.00% | 77                          | 9.83%  |
| 100023.11-CP 100023.11 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 2                      | 1.04% | 0                          | 0.00% | 0                      | 0.00% | 21                          | 10.88% |
| Precinct VBM           | 4                      | 0.43% | 0                          | 0.00% | 0                      | 0.00% | 70                          | 7.51%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 4                           | 14.29% |
| Total                  | 6                      | 0.52% | 0                          | 0.00% | 0                      | 0.00% | 95                          | 8.24%  |
| 200003.12-CP 200003.12 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 1                      | 0.68% | 13                          | 8.78%  |
| Precinct VBM           | 3                      | 0.60% | 1                          | 0.20% | 0                      | 0.00% | 48                          | 9.54%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 3.85% | 0                          | 0.00% | 0                      | 0.00% | 3                           | 11.54% |
| Total                  | 4                      | 0.59% | 1                          | 0.15% | 1                      | 0.15% | 64                          | 9.45%  |
| 200004.12-CP 200004.12 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 3                      | 1.82% | 0                          | 0.00% | 1                      | 0.61% | 14                          | 8.48%  |
| Precinct VBM           | 0                      | 0.00% | 1                          | 0.17% | 0                      | 0.00% | 64                          | 10.61% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 3                      | 0.38% | 1                          | 0.13% | 1                      | 0.13% | 78                          | 9.97%  |
| 200005.02-CP 200005.02 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 2                      | 2.30% | 1                          | 1.15% | 0                      | 0.00% | 8                           | 9.20%  |
| Precinct VBM           | 1                      | 0.25% | 0                          | 0.00% | 1                      | 0.25% | 38                          | 9.38%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 2                           | 10.53% |
| Total                  | 3                      | 0.59% | 1                          | 0.20% | 1                      | 0.20% | 48                          | 9.39%  |
| 200006.12-CP 200006.12 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.93% | 0                          | 0.00% | 0                      | 0.00% | 5                           | 4.63%  |
| Precinct VBM           | 5                      | 1.04% | 0                          | 0.00% | 0                      | 0.00% | 24                          | 5.01%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 3.70% | 0                          | 0.00% | 0                      | 0.00% | 2                           | 7.41%  |
| Total                  | 7                      | 1.14% | 0                          | 0.00% | 0                      | 0.00% | 31                          | 5.05%  |
| 200008.12-CP 200008.12 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |

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|                        | MASSIE MUNROE<br>(DEM) |        | HERBERT G.<br>PETERS (DEM) |       | EMORY RODGERS<br>(DEM) |       | LORETTA L.<br>SANCHEZ (DEM) |        |
| Precinct OS            | 3                      | 2.26%  | 0                          | 0.00% | 0                      | 0.00% | 8                           | 6.02%  |
| Precinct VBM           | 2                      | 0.61%  | 2                          | 0.61% | 2                      | 0.61% | 17                          | 5.20%  |
| MB Precinct            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 1                           | 12.50% |
| Total                  | 5                      | 1.07%  | 2                          | 0.43% | 2                      | 0.43% | 26                          | 5.56%  |
| 200010.12-CP 200010.12 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 1                           | 25.00% |
| Precinct OS            | 2                      | 1.12%  | 0                          | 0.00% | 0                      | 0.00% | 23                          | 12.85% |
| Precinct VBM           | 3                      | 0.40%  | 0                          | 0.00% | 0                      | 0.00% | 65                          | 8.63%  |
| MB Precinct            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 5                      | 0.52%  | 0                          | 0.00% | 0                      | 0.00% | 89                          | 9.25%  |
| 200012.12-CP 200012.12 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 3                      | 2.38%  | 0                          | 0.00% | 0                      | 0.00% | 12                          | 9.52%  |
| Precinct VBM           | 2                      | 0.50%  | 0                          | 0.00% | 0                      | 0.00% | 24                          | 5.94%  |
| MB Precinct            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 2                      | 16.67% | 0                          | 0.00% | 0                      | 0.00% | 4                           | 33.33% |
| Total                  | 7                      | 1.29%  | 0                          | 0.00% | 0                      | 0.00% | 40                          | 7.38%  |
| 200013.12-CP 200013.12 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 3                      | 1.57%  | 0                          | 0.00% | 0                      | 0.00% | 14                          | 7.33%  |
| Precinct VBM           | 4                      | 0.63%  | 0                          | 0.00% | 0                      | 0.00% | 40                          | 6.26%  |
| MB Precinct            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 1                           | 5.00%  |
| Total                  | 7                      | 0.82%  | 0                          | 0.00% | 0                      | 0.00% | 55                          | 6.47%  |
| 200014.12-CP 200014.12 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.53%  | 0                          | 0.00% | 0                      | 0.00% | 22                          | 11.76% |
| Precinct VBM           | 3                      | 0.55%  | 1                          | 0.18% | 0                      | 0.00% | 39                          | 7.10%  |
| MB Precinct            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 4.00%  | 0                          | 0.00% | 0                      | 0.00% | 2                           | 8.00%  |
| Total                  | 5                      | 0.66%  | 1                          | 0.13% | 0                      | 0.00% | 63                          | 8.28%  |
| 200015.02-CP 200015.02 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 2                      | 1.03%  | 0                          | 0.00% | 0                      | 0.00% | 18                          | 9.23%  |
| Precinct VBM           | 7                      | 0.89%  | 2                          | 0.25% | 0                      | 0.00% | 70                          | 8.86%  |
| MB Precinct            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 9                      | 0.90%  | 2                          | 0.20% | 0                      | 0.00% | 88                          | 8.76%  |
| 200016.12-CP 200016.12 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Precinct VBM           | 2                      | 1.59%  | 0                          | 0.00% | 0                      | 0.00% | 11                          | 8.73%  |
| MB Precinct            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 2                      | 1.34%  | 0                          | 0.00% | 0                      | 0.00% | 11                          | 7.38%  |
| 200016.13-CP 200016.13 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 3                           | 4.17%  |
| Precinct VBM           | 1                      | 0.34%  | 0                          | 0.00% | 1                      | 0.34% | 26                          | 8.84%  |
| MB Precinct            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 1                      | 0.27%  | 0                          | 0.00% | 1                      | 0.27% | 29                          | 7.80%  |
| 200017.02-CP 200017.02 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 13                          | 10.16% |
| Precinct VBM           | 3                      | 0.55%  | 0                          | 0.00% | 0                      | 0.00% | 41                          | 7.50%  |
| MB Precinct            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 2                           | 18.18% |
| Total                  | 3                      | 0.44%  | 0                          | 0.00% | 0                      | 0.00% | 56                          | 8.16%  |

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|                        | MASSIE MUNROE<br>(DEM) |       | HERBERT G.<br>PETERS (DEM) |       | EMORY RODGERS<br>(DEM) |       | LORETTA L.<br>SANCHEZ (DEM) |        |
| 200018.02-CP 200018.02 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 7                           | 4.61%  |
| Precinct VBM           | 3                      | 0.47% | 0                          | 0.00% | 1                      | 0.16% | 53                          | 8.39%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 8.33% | 0                          | 0.00% | 0                      | 0.00% | 1                           | 8.33%  |
| Total                  | 4                      | 0.50% | 0                          | 0.00% | 1                      | 0.13% | 61                          | 7.66%  |
| 200020.02-CP 200020.02 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.81% | 0                          | 0.00% | 0                      | 0.00% | 12                          | 9.68%  |
| Precinct VBM           | 1                      | 0.21% | 1                          | 0.21% | 0                      | 0.00% | 37                          | 7.77%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 2                      | 0.32% | 1                          | 0.16% | 0                      | 0.00% | 49                          | 7.94%  |
| 200021.01-CP 200021.01 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 1                           | 3.33%  |
| Precinct VBM           | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 12                          | 6.67%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 13                          | 6.05%  |
| 200021.02-CP 200021.02 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 14                          | 9.09%  |
| Precinct VBM           | 7                      | 1.04% | 1                          | 0.15% | 1                      | 0.15% | 54                          | 8.00%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 5                           | 33.33% |
| Total                  | 7                      | 0.83% | 1                          | 0.12% | 1                      | 0.12% | 73                          | 8.65%  |
| 200024.02-CP 200024.02 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 3                           | 5.66%  |
| Precinct VBM           | 1                      | 0.38% | 0                          | 0.00% | 0                      | 0.00% | 8                           | 3.08%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 2                           | 40.00% |
| Total                  | 1                      | 0.31% | 0                          | 0.00% | 0                      | 0.00% | 13                          | 4.09%  |
| 300001.04-CP 300001.04 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 2                           | 18.18% |
| Precinct VBM           | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 19                          | 20.21% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Total                  | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 21                          | 20.00% |
| 300001.05-CP 300001.05 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 2                      | 2.94% | 0                          | 0.00% | 2                      | 2.94% | 4                           | 5.88%  |
| Precinct VBM           | 6                      | 2.03% | 2                          | 0.68% | 2                      | 0.68% | 41                          | 13.90% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 2                           | 22.22% |
| Total                  | 8                      | 2.15% | 2                          | 0.54% | 4                      | 1.08% | 47                          | 12.63% |
| 300003.05-CP 300003.05 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 2                      | 1.11% | 0                          | 0.00% | 0                      | 0.00% | 25                          | 13.89% |
| Precinct VBM           | 4                      | 0.48% | 0                          | 0.00% | 1                      | 0.12% | 72                          | 8.56%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 6                           | 26.09% |
| Total                  | 6                      | 0.57% | 0                          | 0.00% | 1                      | 0.10% | 103                         | 9.87%  |
| 300004.05-CP 300004.05 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 3                      | 1.94% | 0                          | 0.00% | 0                      | 0.00% | 16                          | 10.32% |
| Precinct VBM           | 5                      | 0.95% | 0                          | 0.00% | 1                      | 0.19% | 51                          | 9.70%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |

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|                        | MASSIE MUNROE<br>(DEM) |       | HERBERT G.<br>PETERS (DEM) |       | EMORY RODGERS<br>(DEM) |       | LORETTA L.<br>SANCHEZ (DEM) |        |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 5                           | 31.25% |
| Total                  | 8                      | 1.15% | 0                          | 0.00% | 1                      | 0.14% | 72                          | 10.33% |
| 300005.05-CP 300005.05 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 17                          | 11.81% |
| Precinct VBM           | 3                      | 0.51% | 2                          | 0.34% | 0                      | 0.00% | 64                          | 10.85% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 4                           | 18.18% |
| Total                  | 3                      | 0.40% | 2                          | 0.26% | 0                      | 0.00% | 85                          | 11.24% |
| 300008.05-CP 300008.05 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 2                      | 1.56% | 0                          | 0.00% | 0                      | 0.00% | 10                          | 7.81%  |
| Precinct VBM           | 7                      | 1.03% | 0                          | 0.00% | 0                      | 0.00% | 48                          | 7.04%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 9                      | 1.10% | 0                          | 0.00% | 0                      | 0.00% | 58                          | 7.06%  |
| 300009.04-CP 300009.04 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 2                      | 1.82% | 0                          | 0.00% | 0                      | 0.00% | 10                          | 9.09%  |
| Precinct VBM           | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 24                          | 6.98%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 8.33% | 0                          | 0.00% | 0                      | 0.00% | 2                           | 16.67% |
| Total                  | 3                      | 0.64% | 0                          | 0.00% | 0                      | 0.00% | 36                          | 7.73%  |
| 300010.04-CP 300010.04 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.65% | 3                          | 1.94% | 0                      | 0.00% | 13                          | 8.39%  |
| Precinct VBM           | 8                      | 1.35% | 1                          | 0.17% | 0                      | 0.00% | 57                          | 9.60%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 9                      | 1.18% | 4                          | 0.53% | 0                      | 0.00% | 70                          | 9.21%  |
| 300011.04-CP 300011.04 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.77% | 0                          | 0.00% | 1                      | 0.77% | 10                          | 7.69%  |
| Precinct VBM           | 6                      | 0.94% | 0                          | 0.00% | 1                      | 0.16% | 45                          | 7.03%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 5.00% | 0                          | 0.00% | 0                      | 0.00% | 4                           | 20.00% |
| Total                  | 8                      | 1.01% | 0                          | 0.00% | 2                      | 0.25% | 59                          | 7.47%  |
| 300013.05-CP 300013.05 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 1                           | 33.33% |
| Precinct OS            | 1                      | 0.85% | 0                          | 0.00% | 0                      | 0.00% | 12                          | 10.26% |
| Precinct VBM           | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 59                          | 8.98%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 1                      | 0.13% | 0                          | 0.00% | 0                      | 0.00% | 72                          | 9.14%  |
| 300015.04-CP 300015.04 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Precinct VBM           | 1                      | 1.96% | 0                          | 0.00% | 0                      | 0.00% | 1                           | 1.96%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 1                      | 1.59% | 0                          | 0.00% | 0                      | 0.00% | 1                           | 1.59%  |
| 300015.05-CP 300015.05 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 4                           | 6.56%  |
| Precinct VBM           | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 20                          | 8.77%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 24                          | 8.28%  |
| 300017.05-CP 300017.05 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 2                      | 1.83% | 0                          | 0.00% | 0                      | 0.00% | 8                           | 7.34%  |



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|                        | MASSIE MUNROE<br>(DEM) |       | HERBERT G.<br>PETERS (DEM) |        | EMORY RODGERS<br>(DEM) |       | LORETTA L.<br>SANCHEZ (DEM) |        |
| Precinct TSx           | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS            | 4                      | 1.81% | 0                          | 0.00%  | 0                      | 0.00% | 17                          | 7.69%  |
| Precinct VBM           | 4                      | 0.58% | 1                          | 0.14%  | 3                      | 0.43% | 73                          | 10.50% |
| MB Precinct            | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 3.33% | 0                          | 0.00%  | 0                      | 0.00% | 2                           | 6.67%  |
| Total                  | 9                      | 0.95% | 1                          | 0.11%  | 3                      | 0.32% | 92                          | 9.73%  |
| 400004.14-CP 400004.14 |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 1                          | 0.59%  | 0                      | 0.00% | 17                          | 10.00% |
| Precinct VBM           | 6                      | 0.98% | 0                          | 0.00%  | 0                      | 0.00% | 61                          | 10.00% |
| MB Precinct            | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00%  | 0                      | 0.00% | 1                           | 5.56%  |
| Total                  | 6                      | 0.75% | 1                          | 0.13%  | 0                      | 0.00% | 79                          | 9.90%  |
| 400005.08-CP 400005.08 |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 1.52% | 0                          | 0.00%  | 0                      | 0.00% | 6                           | 9.09%  |
| Precinct VBM           | 4                      | 1.72% | 0                          | 0.00%  | 0                      | 0.00% | 20                          | 8.58%  |
| MB Precinct            | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00%  | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 5                      | 1.63% | 0                          | 0.00%  | 0                      | 0.00% | 26                          | 8.50%  |
| 400006.08-CP 400006.08 |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.48% | 0                          | 0.00%  | 0                      | 0.00% | 13                          | 6.19%  |
| Precinct VBM           | 6                      | 0.93% | 0                          | 0.00%  | 0                      | 0.00% | 40                          | 6.21%  |
| MB Precinct            | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00%  | 0                      | 0.00% | 1                           | 6.67%  |
| Total                  | 7                      | 0.81% | 0                          | 0.00%  | 0                      | 0.00% | 54                          | 6.21%  |
| 400007.08-CP 400007.08 |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS            | 5                      | 2.37% | 0                          | 0.00%  | 0                      | 0.00% | 11                          | 5.21%  |
| Precinct VBM           | 6                      | 0.85% | 0                          | 0.00%  | 0                      | 0.00% | 41                          | 5.80%  |
| MB Precinct            | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 2                          | 10.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 11                     | 1.17% | 2                          | 0.21%  | 0                      | 0.00% | 52                          | 5.54%  |
| 400010.08-CP 400010.08 |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS            | 2                      | 0.90% | 0                          | 0.00%  | 0                      | 0.00% | 17                          | 7.69%  |
| Precinct VBM           | 6                      | 0.74% | 0                          | 0.00%  | 0                      | 0.00% | 74                          | 9.11%  |
| MB Precinct            | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 4.76% | 0                          | 0.00%  | 0                      | 0.00% | 2                           | 9.52%  |
| Total                  | 9                      | 0.85% | 0                          | 0.00%  | 0                      | 0.00% | 93                          | 8.82%  |
| 400012.08-CP 400012.08 |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.95% | 0                          | 0.00%  | 0                      | 0.00% | 12                          | 11.43% |
| Precinct VBM           | 4                      | 1.02% | 0                          | 0.00%  | 0                      | 0.00% | 31                          | 7.91%  |
| MB Precinct            | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00%  | 0                      | 0.00% | 2                           | 13.33% |
| Total                  | 5                      | 0.98% | 0                          | 0.00%  | 0                      | 0.00% | 45                          | 8.79%  |
| 400013.08-CP 400013.08 |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.93% | 0                          | 0.00%  | 0                      | 0.00% | 7                           | 6.54%  |
| Precinct VBM           | 1                      | 0.34% | 0                          | 0.00%  | 0                      | 0.00% | 21                          | 7.19%  |
| MB Precinct            | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00%  | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 2                      | 0.50% | 0                          | 0.00%  | 0                      | 0.00% | 28                          | 6.97%  |
| 400014.08-CP 400014.08 |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS            | 2                      | 1.02% | 0                          | 0.00%  | 0                      | 0.00% | 10                          | 5.10%  |
| Precinct VBM           | 5                      | 0.75% | 1                          | 0.15%  | 0                      | 0.00% | 49                          | 7.38%  |
| MB Precinct            | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 3.23% | 0                          | 0.00%  | 0                      | 0.00% | 1                           | 3.23%  |



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|                        | MASSIE MUNROE<br>(DEM) |       | HERBERT G.<br>PETERS (DEM) |       | EMORY RODGERS<br>(DEM) |       | LORETTA L.<br>SANCHEZ (DEM) |        |
| Total                  | 8                      | 0.90% | 1                          | 0.11% | 0                      | 0.00% | 60                          | 6.73%  |
| 400017.08-CP 400017.08 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 2                      | 1.31% | 0                          | 0.00% | 1                      | 0.65% | 12                          | 7.84%  |
| Precinct VBM           | 6                      | 0.90% | 0                          | 0.00% | 0                      | 0.00% | 52                          | 7.78%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 4.35% | 0                          | 0.00% | 0                      | 0.00% | 2                           | 8.70%  |
| Total                  | 9                      | 1.07% | 0                          | 0.00% | 1                      | 0.12% | 66                          | 7.82%  |
| 400018.08-CP 400018.08 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 4                      | 5.19% | 0                          | 0.00% | 0                      | 0.00% | 2                           | 2.60%  |
| Precinct VBM           | 3                      | 1.25% | 0                          | 0.00% | 0                      | 0.00% | 11                          | 4.58%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 7                      | 2.18% | 0                          | 0.00% | 0                      | 0.00% | 13                          | 4.05%  |
| 400020.08-CP 400020.08 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 8                           | 7.62%  |
| Precinct VBM           | 4                      | 1.29% | 0                          | 0.00% | 0                      | 0.00% | 27                          | 8.74%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 4                      | 0.96% | 0                          | 0.00% | 0                      | 0.00% | 35                          | 8.41%  |
| 400021.07-CP 400021.07 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 10                          | 8.47%  |
| Precinct VBM           | 1                      | 0.15% | 0                          | 0.00% | 0                      | 0.00% | 47                          | 6.85%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 6                           | 42.86% |
| Total                  | 1                      | 0.12% | 0                          | 0.00% | 0                      | 0.00% | 63                          | 7.70%  |
| 500001.10-CP 500001.10 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 3                      | 1.62% | 0                          | 0.00% | 2                      | 1.08% | 29                          | 15.68% |
| Precinct VBM           | 3                      | 0.56% | 0                          | 0.00% | 1                      | 0.19% | 83                          | 15.46% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 7.14% | 0                          | 0.00% | 0                      | 0.00% | 3                           | 21.43% |
| Total                  | 7                      | 0.95% | 0                          | 0.00% | 3                      | 0.41% | 115                         | 15.63% |
| 500002.10-CP 500002.10 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.83% | 0                          | 0.00% | 1                      | 0.83% | 24                          | 20.00% |
| Precinct VBM           | 9                      | 2.43% | 1                          | 0.27% | 2                      | 0.54% | 64                          | 17.25% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 2                      | 8.00% | 1                          | 4.00% | 0                      | 0.00% | 6                           | 24.00% |
| Total                  | 12                     | 2.33% | 2                          | 0.39% | 3                      | 0.58% | 94                          | 18.22% |
| 500003.10-CP 500003.10 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 2                      | 1.68% | 0                          | 0.00% | 0                      | 0.00% | 27                          | 22.69% |
| Precinct VBM           | 5                      | 1.66% | 0                          | 0.00% | 0                      | 0.00% | 43                          | 14.24% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 7                      | 1.64% | 0                          | 0.00% | 0                      | 0.00% | 70                          | 16.36% |
| 500004.10-CP 500004.10 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 2                      | 1.87% | 1                          | 0.93% | 2                      | 1.87% | 22                          | 20.56% |
| Precinct VBM           | 2                      | 0.87% | 0                          | 0.00% | 1                      | 0.43% | 29                          | 12.61% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 5.56% | 0                          | 0.00% | 0                      | 0.00% | 5                           | 27.78% |
| Total                  | 5                      | 1.41% | 1                          | 0.28% | 3                      | 0.85% | 56                          | 15.77% |
| 500005.10-CP 500005.10 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 5                      | 5.15% | 0                          | 0.00% | 1                      | 1.03% | 16                          | 16.49% |
| Precinct VBM           | 7                      | 3.20% | 1                          | 0.46% | 0                      | 0.00% | 45                          | 20.55% |

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|                        | MASSIE MUNROE<br>(DEM) |       | HERBERT G.<br>PETERS (DEM) |       | EMORY RODGERS<br>(DEM) |       | LORETTA L.<br>SANCHEZ (DEM) |        |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 3                           | 25.00% |
| Total                  | 12                     | 3.66% | 1                          | 0.30% | 1                      | 0.30% | 64                          | 19.51% |
| 500006.10-CP 500006.10 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 5                      | 3.05% | 1                          | 0.61% | 1                      | 0.61% | 35                          | 21.34% |
| Precinct VBM           | 8                      | 1.82% | 1                          | 0.23% | 1                      | 0.23% | 80                          | 18.22% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 4                      | 8.16% | 0                          | 0.00% | 0                      | 0.00% | 7                           | 14.29% |
| Total                  | 17                     | 2.61% | 2                          | 0.31% | 2                      | 0.31% | 122                         | 18.71% |
| 500007.10-CP 500007.10 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 7                      | 3.72% | 0                          | 0.00% | 2                      | 1.06% | 29                          | 15.43% |
| Precinct VBM           | 7                      | 1.16% | 0                          | 0.00% | 0                      | 0.00% | 77                          | 12.79% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 6                           | 33.33% |
| Total                  | 14                     | 1.73% | 0                          | 0.00% | 2                      | 0.25% | 112                         | 13.86% |
| 500009.10-CP 500009.10 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 7                      | 6.48% | 1                          | 0.93% | 0                      | 0.00% | 25                          | 23.15% |
| Precinct VBM           | 2                      | 0.75% | 0                          | 0.00% | 0                      | 0.00% | 47                          | 17.60% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 3.23% | 0                          | 0.00% | 0                      | 0.00% | 8                           | 25.81% |
| Total                  | 10                     | 2.46% | 1                          | 0.25% | 0                      | 0.00% | 80                          | 19.70% |
| 500011.10-CP 500011.10 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 9                      | 5.52% | 0                          | 0.00% | 0                      | 0.00% | 40                          | 24.54% |
| Precinct VBM           | 6                      | 1.78% | 0                          | 0.00% | 2                      | 0.59% | 77                          | 22.78% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 2                      | 6.06% | 0                          | 0.00% | 0                      | 0.00% | 13                          | 39.39% |
| Total                  | 17                     | 3.18% | 0                          | 0.00% | 2                      | 0.37% | 130                         | 24.34% |
| 500012.10-CP 500012.10 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 22                          | 12.94% |
| Precinct VBM           | 5                      | 0.92% | 0                          | 0.00% | 0                      | 0.00% | 74                          | 13.65% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 1                      | 3.33% | 1                          | 3.33% | 0                      | 0.00% | 7                           | 23.33% |
| Total                  | 6                      | 0.81% | 1                          | 0.13% | 0                      | 0.00% | 103                         | 13.86% |
| 500015.10-CP 500015.10 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 23                          | 23.96% |
| Precinct VBM           | 2                      | 0.54% | 1                          | 0.27% | 0                      | 0.00% | 56                          | 15.26% |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 1                           | 10.00% |
| Total                  | 2                      | 0.42% | 1                          | 0.21% | 0                      | 0.00% | 80                          | 16.91% |
| 500020.10-CP 500020.10 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 9                           | 6.38%  |
| Precinct VBM           | 7                      | 1.15% | 1                          | 0.16% | 0                      | 0.00% | 49                          | 8.07%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 2                           | 7.69%  |
| Total                  | 7                      | 0.90% | 1                          | 0.13% | 0                      | 0.00% | 60                          | 7.75%  |
| 500021.10-CP 500021.10 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 1                      | 0.67% | 0                          | 0.00% | 0                      | 0.00% | 12                          | 8.05%  |
| Precinct VBM           | 6                      | 1.05% | 0                          | 0.00% | 0                      | 0.00% | 36                          | 6.29%  |
| MB Precinct            | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 3                           | 12.00% |
| Total                  | 7                      | 0.94% | 0                          | 0.00% | 0                      | 0.00% | 51                          | 6.84%  |
| 95U201.01-MB 95U201.01 |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |

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|------------------------|------------------------|--------|----------------------------|-------|------------------------|-------|-----------------------------|--------|
|                        | MASSIE MUNROE<br>(DEM) |        | HERBERT G.<br>PETERS (DEM) |       | EMORY RODGERS<br>(DEM) |       | LORETTA L.<br>SANCHEZ (DEM) |        |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| MB Precinct            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Provisional            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Total                  | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| 95U201.02-MB 95U201.02 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| MB Precinct            | 0                      | 0.00%  | 1                          | 0.33% | 0                      | 0.00% | 31                          | 10.20% |
| Provisional            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 0                      | 0.00%  | 1                          | 0.33% | 0                      | 0.00% | 31                          | 10.16% |
| 95U202.02-MB 95U202.02 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| MB Precinct            | 4                      | 0.79%  | 0                          | 0.00% | 0                      | 0.00% | 38                          | 7.52%  |
| Provisional            | 1                      | 33.33% | 0                          | 0.00% | 0                      | 0.00% | 1                           | 33.33% |
| Total                  | 5                      | 0.98%  | 0                          | 0.00% | 0                      | 0.00% | 39                          | 7.68%  |
| 95U203.02-MB 95U203.02 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| MB Precinct            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Provisional            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Total                  | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| 95U204.01-MB 95U204.01 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| MB Precinct            | 6                      | 1.01%  | 0                          | 0.00% | 0                      | 0.00% | 51                          | 8.54%  |
| Provisional            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 6                      | 1.00%  | 0                          | 0.00% | 0                      | 0.00% | 51                          | 8.51%  |
| 95U301.05-MB 95U301.05 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| MB Precinct            | 2                      | 1.47%  | 0                          | 0.00% | 0                      | 0.00% | 8                           | 5.88%  |
| Provisional            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Total                  | 2                      | 1.45%  | 0                          | 0.00% | 0                      | 0.00% | 8                           | 5.80%  |
| 95U302.04-MB 95U302.04 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| MB Precinct            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Provisional            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Total                  | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| 95U302.05-MB 95U302.05 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| MB Precinct            | 0                      | 0.00%  | 1                          | 1.23% | 2                      | 2.47% | 1                           | 1.23%  |
| Provisional            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Total                  | 0                      | 0.00%  | 1                          | 1.23% | 2                      | 2.47% | 1                           | 1.23%  |
| 95U303.03-MB 95U303.03 |                        |        |                            |       |                        |       |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| MB Precinct            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 16                          | 20.51% |
| Provisional            | 0                      | -      | 0                          | -     | 0                      | -     | 0                           | -      |
| Total                  | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00% | 16                          | 20.51% |

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|                        | MASSIE MUNROE<br>(DEM) |        | HERBERT G.<br>PETERS (DEM) |       | EMORY RODGERS<br>(DEM) |        | LORETTA L.<br>SANCHEZ (DEM) |        |
| 95U303.05-MB 95U303.05 |                        |        |                            |       |                        |        |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| MB Precinct            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00%  | 7                           | 7.37%  |
| Provisional            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00%  | 0                           | 0.00%  |
| Total                  | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00%  | 7                           | 7.29%  |
| 95U304.05-MB 95U304.05 |                        |        |                            |       |                        |        |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| MB Precinct            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Provisional            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Total                  | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| 95U305.05-MB 95U305.05 |                        |        |                            |       |                        |        |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| MB Precinct            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00%  | 0                           | 0.00%  |
| Provisional            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Total                  | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00%  | 0                           | 0.00%  |
| 95U401.08-MB 95U401.08 |                        |        |                            |       |                        |        |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| MB Precinct            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00%  | 3                           | 20.00% |
| Provisional            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Total                  | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00%  | 3                           | 20.00% |
| 95U402.07-MB 95U402.07 |                        |        |                            |       |                        |        |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| MB Precinct            | 0                      | 0.00%  | 1                          | 0.43% | 0                      | 0.00%  | 18                          | 7.69%  |
| Provisional            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Total                  | 0                      | 0.00%  | 1                          | 0.43% | 0                      | 0.00%  | 18                          | 7.69%  |
| 95U402.08-MB 95U402.08 |                        |        |                            |       |                        |        |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| MB Precinct            | 4                      | 0.79%  | 0                          | 0.00% | 0                      | 0.00%  | 31                          | 6.15%  |
| Provisional            | 0                      | 0.00%  | 0                          | 0.00% | 0                      | 0.00%  | 0                           | 0.00%  |
| Total                  | 4                      | 0.79%  | 0                          | 0.00% | 0                      | 0.00%  | 31                          | 6.11%  |
| 95U403.08-MB 95U403.08 |                        |        |                            |       |                        |        |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| MB Precinct            | 8                      | 0.85%  | 0                          | 0.00% | 0                      | 0.00%  | 90                          | 9.51%  |
| Provisional            | 1                      | 10.00% | 0                          | 0.00% | 1                      | 10.00% | 0                           | 0.00%  |
| Total                  | 9                      | 0.94%  | 0                          | 0.00% | 1                      | 0.10%  | 90                          | 9.41%  |
| 95U404.06-MB 95U404.06 |                        |        |                            |       |                        |        |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| MB Precinct            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Provisional            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Total                  | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| 95U404.08-MB 95U404.08 |                        |        |                            |       |                        |        |                             |        |
| Precinct TSx           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct OS            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| Precinct VBM           | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |
| MB Precinct            | 0                      | -      | 0                          | -     | 0                      | -      | 0                           | -      |

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|                               | MASSIE MUNROE<br>(DEM) |       | HERBERT G.<br>PETERS (DEM) |        | EMORY RODGERS<br>(DEM) |       | LORETTA L.<br>SANCHEZ (DEM) |        |
| Provisional                   | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Total                         | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| 95U501.09-MB 95U501.09        |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx                  | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS                   | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct VBM                  | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| MB Precinct                   | 0                      | 0.00% | 0                          | 0.00%  | 0                      | 0.00% | 0                           | 0.00%  |
| Provisional                   | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Total                         | 0                      | 0.00% | 0                          | 0.00%  | 0                      | 0.00% | 0                           | 0.00%  |
| 95U501.10-MB 95U501.10        |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx                  | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS                   | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct VBM                  | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| MB Precinct                   | 1                      | 0.63% | 0                          | 0.00%  | 0                      | 0.00% | 11                          | 6.88%  |
| Provisional                   | 0                      | 0.00% | 0                          | 0.00%  | 0                      | 0.00% | 1                           | 50.00% |
| Total                         | 1                      | 0.62% | 0                          | 0.00%  | 0                      | 0.00% | 12                          | 7.41%  |
| 95U598.10-MB 95U598.10        |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx                  | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS                   | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct VBM                  | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| MB Precinct                   | 6                      | 2.80% | 0                          | 0.00%  | 1                      | 0.47% | 27                          | 12.62% |
| Provisional                   | 0                      | 0.00% | 0                          | 0.00%  | 0                      | 0.00% | 0                           | 0.00%  |
| Total                         | 6                      | 2.78% | 0                          | 0.00%  | 1                      | 0.46% | 27                          | 12.50% |
| 95U599.10-MB 95U599.10        |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx                  | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS                   | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct VBM                  | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| MB Precinct                   | 1                      | 2.94% | 0                          | 0.00%  | 0                      | 0.00% | 5                           | 14.71% |
| Provisional                   | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Total                         | 1                      | 2.94% | 0                          | 0.00%  | 0                      | 0.00% | 5                           | 14.71% |
| 96U101.11-MB 96U101.11        |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx                  | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS                   | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct VBM                  | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| MB Precinct                   | 2                      | 0.54% | 0                          | 0.00%  | 0                      | 0.00% | 29                          | 7.90%  |
| Provisional                   | 0                      | 0.00% | 0                          | 0.00%  | 0                      | 0.00% | 2                           | 33.33% |
| Total                         | 2                      | 0.54% | 0                          | 0.00%  | 0                      | 0.00% | 31                          | 8.31%  |
| 96U401.14-MB 96U401.14        |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx                  | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct OS                   | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| Precinct VBM                  | 0                      | -     | 0                          | -      | 0                      | -     | 0                           | -      |
| MB Precinct                   | 3                      | 0.93% | 0                          | 0.00%  | 2                      | 0.62% | 22                          | 6.83%  |
| Provisional                   | 0                      | 0.00% | 1                          | 16.67% | 0                      | 0.00% | 1                           | 16.67% |
| Total                         | 3                      | 0.91% | 1                          | 0.30%  | 2                      | 0.61% | 23                          | 7.01%  |
| Total                         |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx                  | 0                      | 0.00% | 0                          | 0.00%  | 0                      | 0.00% | 2                           | 25.00% |
| Precinct OS                   | 133                    | 1.24% | 9                          | 0.08%  | 19                     | 0.18% | 1086                        | 10.15% |
| Precinct VBM                  | 278                    | 0.68% | 31                         | 0.08%  | 31                     | 0.08% | 3615                        | 8.84%  |
| MB Precinct                   | 37                     | 0.80% | 3                          | 0.07%  | 5                      | 0.11% | 388                         | 8.42%  |
| Provisional                   | 31                     | 2.36% | 5                          | 0.38%  | 1                      | 0.08% | 176                         | 13.39% |
| Total                         | 479                    | 0.83% | 48                         | 0.08%  | 56                     | 0.10% | 5267                        | 9.16%  |
| STATE BOARD OF EQUALIZATION   |                        |       |                            |        |                        |       |                             |        |
| STATE BOARD OF EQUALIZATION 1 |                        |       |                            |        |                        |       |                             |        |
| Precinct TSx                  | 0                      | 0.00% | 0                          | 0.00%  | 0                      | 0.00% | 2                           | 25.00% |
| Precinct OS                   | 133                    | 1.24% | 9                          | 0.08%  | 19                     | 0.18% | 1086                        | 10.15% |
| Precinct VBM                  | 278                    | 0.68% | 31                         | 0.08%  | 31                     | 0.08% | 3615                        | 8.84%  |
| MB Precinct                   | 37                     | 0.80% | 3                          | 0.07%  | 5                      | 0.11% | 388                         | 8.42%  |
| Provisional                   | 31                     | 2.36% | 5                          | 0.38%  | 1                      | 0.08% | 176                         | 13.39% |
| Total                         | 479                    | 0.83% | 48                         | 0.08%  | 56                     | 0.10% | 5267                        | 9.16%  |
| Total                         |                        |       |                            |        |                        |       |                             |        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                                     | United States Senator  |              |                            |              |                        |              |                             |              |
|-------------------------------------|------------------------|--------------|----------------------------|--------------|------------------------|--------------|-----------------------------|--------------|
|                                     | MASSIE MUNROE<br>(DEM) |              | HERBERT G.<br>PETERS (DEM) |              | EMORY RODGERS<br>(DEM) |              | LORETTA L.<br>SANCHEZ (DEM) |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                          | 0.00%        | 0                      | 0.00%        | 2                           | 25.00%       |
| Precinct OS                         | 133                    | 1.24%        | 9                          | 0.08%        | 19                     | 0.18%        | 1086                        | 10.15%       |
| Precinct VBM                        | 278                    | 0.68%        | 31                         | 0.08%        | 31                     | 0.08%        | 3615                        | 8.84%        |
| MB Precinct                         | 37                     | 0.80%        | 3                          | 0.07%        | 5                      | 0.11%        | 388                         | 8.42%        |
| Provisional                         | 31                     | 2.36%        | 5                          | 0.38%        | 1                      | 0.08%        | 176                         | 13.39%       |
| <b>Total</b>                        | <b>479</b>             | <b>0.83%</b> | <b>48</b>                  | <b>0.08%</b> | <b>56</b>              | <b>0.10%</b> | <b>5267</b>                 | <b>9.16%</b> |
| <b>UNITED STATES SENATE</b>         |                        |              |                            |              |                        |              |                             |              |
| <b>UNITED STATES SENATE</b>         |                        |              |                            |              |                        |              |                             |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                          | 0.00%        | 0                      | 0.00%        | 2                           | 25.00%       |
| Precinct OS                         | 133                    | 1.24%        | 9                          | 0.08%        | 19                     | 0.18%        | 1086                        | 10.15%       |
| Precinct VBM                        | 278                    | 0.68%        | 31                         | 0.08%        | 31                     | 0.08%        | 3615                        | 8.84%        |
| MB Precinct                         | 37                     | 0.80%        | 3                          | 0.07%        | 5                      | 0.11%        | 388                         | 8.42%        |
| Provisional                         | 31                     | 2.36%        | 5                          | 0.38%        | 1                      | 0.08%        | 176                         | 13.39%       |
| <b>Total</b>                        | <b>479</b>             | <b>0.83%</b> | <b>48</b>                  | <b>0.08%</b> | <b>56</b>              | <b>0.10%</b> | <b>5267</b>                 | <b>9.16%</b> |
| <b>Total</b>                        |                        |              |                            |              |                        |              |                             |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                          | 0.00%        | 0                      | 0.00%        | 2                           | 25.00%       |
| Precinct OS                         | 133                    | 1.24%        | 9                          | 0.08%        | 19                     | 0.18%        | 1086                        | 10.15%       |
| Precinct VBM                        | 278                    | 0.68%        | 31                         | 0.08%        | 31                     | 0.08%        | 3615                        | 8.84%        |
| MB Precinct                         | 37                     | 0.80%        | 3                          | 0.07%        | 5                      | 0.11%        | 388                         | 8.42%        |
| Provisional                         | 31                     | 2.36%        | 5                          | 0.38%        | 1                      | 0.08%        | 176                         | 13.39%       |
| <b>Total</b>                        | <b>479</b>             | <b>0.83%</b> | <b>48</b>                  | <b>0.08%</b> | <b>56</b>              | <b>0.10%</b> | <b>5267</b>                 | <b>9.16%</b> |
| <b>Total</b>                        |                        |              |                            |              |                        |              |                             |              |
| <b>UNITED STATES REPRESENTATIVE</b> |                        |              |                            |              |                        |              |                             |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                        |              |                            |              |                        |              |                             |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                          | 0.00%        | 0                      | 0.00%        | 2                           | 25.00%       |
| Precinct OS                         | 133                    | 1.24%        | 9                          | 0.08%        | 19                     | 0.18%        | 1086                        | 10.15%       |
| Precinct VBM                        | 278                    | 0.68%        | 31                         | 0.08%        | 31                     | 0.08%        | 3615                        | 8.84%        |
| MB Precinct                         | 37                     | 0.80%        | 3                          | 0.07%        | 5                      | 0.11%        | 388                         | 8.42%        |
| Provisional                         | 31                     | 2.36%        | 5                          | 0.38%        | 1                      | 0.08%        | 176                         | 13.39%       |
| <b>Total</b>                        | <b>479</b>             | <b>0.83%</b> | <b>48</b>                  | <b>0.08%</b> | <b>56</b>              | <b>0.10%</b> | <b>5267</b>                 | <b>9.16%</b> |
| <b>Total</b>                        |                        |              |                            |              |                        |              |                             |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                          | 0.00%        | 0                      | 0.00%        | 2                           | 25.00%       |
| Precinct OS                         | 133                    | 1.24%        | 9                          | 0.08%        | 19                     | 0.18%        | 1086                        | 10.15%       |
| Precinct VBM                        | 278                    | 0.68%        | 31                         | 0.08%        | 31                     | 0.08%        | 3615                        | 8.84%        |
| MB Precinct                         | 37                     | 0.80%        | 3                          | 0.07%        | 5                      | 0.11%        | 388                         | 8.42%        |
| Provisional                         | 31                     | 2.36%        | 5                          | 0.38%        | 1                      | 0.08%        | 176                         | 13.39%       |
| <b>Total</b>                        | <b>479</b>             | <b>0.83%</b> | <b>48</b>                  | <b>0.08%</b> | <b>56</b>              | <b>0.10%</b> | <b>5267</b>                 | <b>9.16%</b> |
| <b>Total</b>                        |                        |              |                            |              |                        |              |                             |              |
| <b>STATE SENATE</b>                 |                        |              |                            |              |                        |              |                             |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                        |              |                            |              |                        |              |                             |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                          | 0.00%        | 0                      | 0.00%        | 2                           | 25.00%       |
| Precinct OS                         | 133                    | 1.24%        | 9                          | 0.08%        | 19                     | 0.18%        | 1086                        | 10.15%       |
| Precinct VBM                        | 278                    | 0.68%        | 31                         | 0.08%        | 31                     | 0.08%        | 3615                        | 8.84%        |
| MB Precinct                         | 37                     | 0.80%        | 3                          | 0.07%        | 5                      | 0.11%        | 388                         | 8.42%        |
| Provisional                         | 31                     | 2.36%        | 5                          | 0.38%        | 1                      | 0.08%        | 176                         | 13.39%       |
| <b>Total</b>                        | <b>479</b>             | <b>0.83%</b> | <b>48</b>                  | <b>0.08%</b> | <b>56</b>              | <b>0.10%</b> | <b>5267</b>                 | <b>9.16%</b> |
| <b>Total</b>                        |                        |              |                            |              |                        |              |                             |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                          | 0.00%        | 0                      | 0.00%        | 2                           | 25.00%       |
| Precinct OS                         | 133                    | 1.24%        | 9                          | 0.08%        | 19                     | 0.18%        | 1086                        | 10.15%       |
| Precinct VBM                        | 278                    | 0.68%        | 31                         | 0.08%        | 31                     | 0.08%        | 3615                        | 8.84%        |
| MB Precinct                         | 37                     | 0.80%        | 3                          | 0.07%        | 5                      | 0.11%        | 388                         | 8.42%        |
| Provisional                         | 31                     | 2.36%        | 5                          | 0.38%        | 1                      | 0.08%        | 176                         | 13.39%       |
| <b>Total</b>                        | <b>479</b>             | <b>0.83%</b> | <b>48</b>                  | <b>0.08%</b> | <b>56</b>              | <b>0.10%</b> | <b>5267</b>                 | <b>9.16%</b> |
| <b>Total</b>                        |                        |              |                            |              |                        |              |                             |              |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                        |              |                            |              |                        |              |                             |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                        |              |                            |              |                        |              |                             |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                          | 0.00%        | 0                      | 0.00%        | 1                           | 25.00%       |
| Precinct OS                         | 96                     | 1.41%        | 7                          | 0.10%        | 13                     | 0.19%        | 719                         | 10.55%       |
| Precinct VBM                        | 210                    | 0.83%        | 19                         | 0.07%        | 23                     | 0.09%        | 2386                        | 9.40%        |
| MB Precinct                         | 32                     | 0.82%        | 3                          | 0.08%        | 3                      | 0.08%        | 337                         | 8.60%        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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|                                   | United States Senator  |       |                            |       |                        |       |                             |        |
|-----------------------------------|------------------------|-------|----------------------------|-------|------------------------|-------|-----------------------------|--------|
|                                   | MASSIE MUNROE<br>(DEM) |       | HERBERT G.<br>PETERS (DEM) |       | EMORY RODGERS<br>(DEM) |       | LORETTA L.<br>SANCHEZ (DEM) |        |
| Provisional                       | 22                     | 2.56% | 4                          | 0.47% | 1                      | 0.12% | 127                         | 14.77% |
| Total                             | 360                    | 0.97% | 33                         | 0.09% | 40                     | 0.11% | 3570                        | 9.65%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx                      | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 1                           | 25.00% |
| Precinct OS                       | 37                     | 0.95% | 2                          | 0.05% | 6                      | 0.15% | 367                         | 9.44%  |
| Precinct VBM                      | 68                     | 0.44% | 12                         | 0.08% | 8                      | 0.05% | 1229                        | 7.94%  |
| MB Precinct                       | 5                      | 0.73% | 0                          | 0.00% | 2                      | 0.29% | 51                          | 7.40%  |
| Provisional                       | 9                      | 1.98% | 1                          | 0.22% | 0                      | 0.00% | 49                          | 10.79% |
| Total                             | 119                    | 0.58% | 15                         | 0.07% | 16                     | 0.08% | 1697                        | 8.27%  |
| <b>Total</b>                      |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx                      | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 2                           | 25.00% |
| Precinct OS                       | 133                    | 1.24% | 9                          | 0.08% | 19                     | 0.18% | 1086                        | 10.15% |
| Precinct VBM                      | 278                    | 0.68% | 31                         | 0.08% | 31                     | 0.08% | 3615                        | 8.84%  |
| MB Precinct                       | 37                     | 0.80% | 3                          | 0.07% | 5                      | 0.11% | 388                         | 8.42%  |
| Provisional                       | 31                     | 2.36% | 5                          | 0.38% | 1                      | 0.08% | 176                         | 13.39% |
| Total                             | 479                    | 0.83% | 48                         | 0.08% | 56                     | 0.10% | 5267                        | 9.16%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |                        |       |                            |       |                        |       |                             |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx                      | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS                       | 20                     | 0.90% | 1                          | 0.04% | 4                      | 0.18% | 213                         | 9.56%  |
| Precinct VBM                      | 34                     | 0.37% | 6                          | 0.06% | 3                      | 0.03% | 727                         | 7.83%  |
| MB Precinct                       | 2                      | 0.54% | 0                          | 0.00% | 0                      | 0.00% | 29                          | 7.90%  |
| Provisional                       | 4                      | 1.65% | 0                          | 0.00% | 0                      | 0.00% | 31                          | 12.76% |
| Total                             | 60                     | 0.49% | 7                          | 0.06% | 7                      | 0.06% | 1000                        | 8.24%  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx                      | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 1                           | 25.00% |
| Precinct OS                       | 21                     | 0.93% | 1                          | 0.04% | 2                      | 0.09% | 190                         | 8.44%  |
| Precinct VBM                      | 48                     | 0.56% | 9                          | 0.10% | 6                      | 0.07% | 671                         | 7.76%  |
| MB Precinct                       | 10                     | 0.71% | 1                          | 0.07% | 0                      | 0.00% | 120                         | 8.49%  |
| Provisional                       | 7                      | 2.53% | 0                          | 0.00% | 0                      | 0.00% | 26                          | 9.39%  |
| Total                             | 86                     | 0.68% | 11                         | 0.09% | 8                      | 0.06% | 1008                        | 8.01%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx                      | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 1                           | 33.33% |
| Precinct OS                       | 25                     | 1.18% | 3                          | 0.14% | 3                      | 0.14% | 199                         | 9.39%  |
| Precinct VBM                      | 67                     | 0.74% | 7                          | 0.08% | 9                      | 0.10% | 774                         | 8.55%  |
| MB Precinct                       | 2                      | 0.50% | 1                          | 0.25% | 2                      | 0.50% | 32                          | 8.04%  |
| Provisional                       | 2                      | 0.84% | 0                          | 0.00% | 0                      | 0.00% | 31                          | 13.08% |
| Total                             | 96                     | 0.81% | 11                         | 0.09% | 14                     | 0.12% | 1037                        | 8.78%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx                      | 0                      | -     | 0                          | -     | 0                      | -     | 0                           | -      |
| Precinct OS                       | 25                     | 1.09% | 1                          | 0.04% | 1                      | 0.04% | 171                         | 7.45%  |
| Precinct VBM                      | 60                     | 0.71% | 4                          | 0.05% | 6                      | 0.07% | 683                         | 8.04%  |
| MB Precinct                       | 15                     | 0.74% | 1                          | 0.05% | 2                      | 0.10% | 164                         | 8.11%  |
| Provisional                       | 6                      | 2.35% | 3                          | 1.18% | 1                      | 0.39% | 23                          | 9.02%  |
| Total                             | 106                    | 0.81% | 9                          | 0.07% | 10                     | 0.08% | 1041                        | 7.97%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx                      | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 0                           | 0.00%  |
| Precinct OS                       | 42                     | 2.32% | 3                          | 0.17% | 9                      | 0.50% | 313                         | 17.32% |
| Precinct VBM                      | 69                     | 1.28% | 5                          | 0.09% | 7                      | 0.13% | 760                         | 14.09% |
| MB Precinct                       | 8                      | 1.95% | 0                          | 0.00% | 1                      | 0.24% | 43                          | 10.49% |
| Provisional                       | 12                     | 3.97% | 2                          | 0.66% | 0                      | 0.00% | 65                          | 21.52% |
| Total                             | 131                    | 1.66% | 10                         | 0.13% | 17                     | 0.21% | 1181                        | 14.92% |
| <b>Total</b>                      |                        |       |                            |       |                        |       |                             |        |
| Precinct TSx                      | 0                      | 0.00% | 0                          | 0.00% | 0                      | 0.00% | 2                           | 25.00% |
| Precinct OS                       | 133                    | 1.24% | 9                          | 0.08% | 19                     | 0.18% | 1086                        | 10.15% |
| Precinct VBM                      | 278                    | 0.68% | 31                         | 0.08% | 31                     | 0.08% | 3615                        | 8.84%  |
| MB Precinct                       | 37                     | 0.80% | 3                          | 0.07% | 5                      | 0.11% | 388                         | 8.42%  |
| Provisional                       | 31                     | 2.36% | 5                          | 0.38% | 1                      | 0.08% | 176                         | 13.39% |
| Total                             | 479                    | 0.83% | 48                         | 0.08% | 56                     | 0.10% | 5267                        | 9.16%  |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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|                                 | United States Senator  |              |                            |              |                        |              |                             |               |
|---------------------------------|------------------------|--------------|----------------------------|--------------|------------------------|--------------|-----------------------------|---------------|
|                                 | MASSIE MUNROE<br>(DEM) |              | HERBERT G.<br>PETERS (DEM) |              | EMORY RODGERS<br>(DEM) |              | LORETTA L.<br>SANCHEZ (DEM) |               |
| <b>CITIES</b>                   |                        |              |                            |              |                        |              |                             |               |
| <b>CITY OF PLACERVILLE</b>      |                        |              |                            |              |                        |              |                             |               |
| Precinct TSx                    | 0                      | -            | 0                          | -            | 0                      | -            | 0                           | -             |
| Precinct OS                     | 7                      | 1.25%        | 0                          | 0.00%        | 2                      | 0.36%        | 64                          | 11.47%        |
| Precinct VBM                    | 18                     | 0.77%        | 4                          | 0.17%        | 4                      | 0.17%        | 247                         | 10.53%        |
| MB Precinct                     | 0                      | -            | 0                          | -            | 0                      | -            | 0                           | -             |
| Provisional                     | 0                      | 0.00%        | 0                          | 0.00%        | 0                      | 0.00%        | 17                          | 24.29%        |
| <b>Total</b>                    | <b>25</b>              | <b>0.84%</b> | <b>4</b>                   | <b>0.13%</b> | <b>6</b>               | <b>0.20%</b> | <b>328</b>                  | <b>11.03%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                        |              |                            |              |                        |              |                             |               |
| Precinct TSx                    | 0                      | -            | 0                          | -            | 0                      | -            | 0                           | -             |
| Precinct OS                     | 36                     | 3.80%        | 3                          | 0.32%        | 7                      | 0.74%        | 191                         | 20.17%        |
| Precinct VBM                    | 41                     | 1.66%        | 3                          | 0.12%        | 6                      | 0.24%        | 419                         | 16.99%        |
| MB Precinct                     | 0                      | -            | 0                          | -            | 0                      | -            | 0                           | -             |
| Provisional                     | 10                     | 5.38%        | 1                          | 0.54%        | 0                      | 0.00%        | 48                          | 25.81%        |
| <b>Total</b>                    | <b>87</b>              | <b>2.42%</b> | <b>7</b>                   | <b>0.19%</b> | <b>13</b>              | <b>0.36%</b> | <b>658</b>                  | <b>18.28%</b> |
| <b>UNINCORPORATED AREA</b>      |                        |              |                            |              |                        |              |                             |               |
| Precinct TSx                    | 0                      | 0.00%        | 0                          | 0.00%        | 0                      | 0.00%        | 2                           | 25.00%        |
| Precinct OS                     | 90                     | 0.98%        | 6                          | 0.07%        | 10                     | 0.11%        | 831                         | 9.04%         |
| Precinct VBM                    | 219                    | 0.61%        | 24                         | 0.07%        | 21                     | 0.06%        | 2949                        | 8.18%         |
| MB Precinct                     | 37                     | 0.80%        | 3                          | 0.07%        | 5                      | 0.11%        | 388                         | 8.42%         |
| Provisional                     | 21                     | 1.98%        | 4                          | 0.38%        | 1                      | 0.09%        | 111                         | 10.49%        |
| <b>Total</b>                    | <b>367</b>             | <b>0.72%</b> | <b>37</b>                  | <b>0.07%</b> | <b>37</b>              | <b>0.07%</b> | <b>4281</b>                 | <b>8.40%</b>  |
| <b>Total</b>                    |                        |              |                            |              |                        |              |                             |               |
| Precinct TSx                    | 0                      | 0.00%        | 0                          | 0.00%        | 0                      | 0.00%        | 2                           | 25.00%        |
| Precinct OS                     | 133                    | 1.24%        | 9                          | 0.08%        | 19                     | 0.18%        | 1086                        | 10.15%        |
| Precinct VBM                    | 278                    | 0.68%        | 31                         | 0.08%        | 31                     | 0.08%        | 3615                        | 8.84%         |
| MB Precinct                     | 37                     | 0.80%        | 3                          | 0.07%        | 5                      | 0.11%        | 388                         | 8.42%         |
| Provisional                     | 31                     | 2.36%        | 5                          | 0.38%        | 1                      | 0.08%        | 176                         | 13.39%        |
| <b>Total</b>                    | <b>479</b>             | <b>0.83%</b> | <b>48</b>                  | <b>0.08%</b> | <b>56</b>              | <b>0.10%</b> | <b>5267</b>                 | <b>9.16%</b>  |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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United States Senator

|                        | STEVE STOKES<br>(DEM) |        | GREG CONLON (REP) |        | T.G.DEL BECCARO<br>(REP) |        | VON HOUGO (REP) |        |
|------------------------|-----------------------|--------|-------------------|--------|--------------------------|--------|-----------------|--------|
|                        |                       |        |                   |        |                          |        |                 |        |
| Jurisdiction Wide      |                       |        |                   |        |                          |        |                 |        |
| 100001.11-CP 100001.11 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 3                     | 1.91%  | 7                 | 4.46%  | 12                       | 7.64%  | 1               | 0.64%  |
| Precinct VBM           | 13                    | 1.85%  | 51                | 7.25%  | 74                       | 10.53% | 8               | 1.14%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 1                     | 6.25%  | 2                 | 12.50% | 0                        | 0.00%  | 0               | 0.00%  |
| Total                  | 17                    | 1.94%  | 60                | 6.85%  | 86                       | 9.82%  | 9               | 1.03%  |
| 100002.11-CP 100002.11 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 1                     | 1.32%  | 6                 | 7.89%  | 10                       | 13.16% | 3               | 3.95%  |
| Precinct VBM           | 4                     | 0.99%  | 20                | 4.96%  | 45                       | 11.17% | 6               | 1.49%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 0                     | 0.00%  | 0                 | 0.00%  | 2                        | 12.50% | 2               | 12.50% |
| Total                  | 5                     | 1.01%  | 26                | 5.25%  | 57                       | 11.52% | 11              | 2.22%  |
| 100003.11-CP 100003.11 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 0                     | 0.00%  | 6                 | 5.41%  | 13                       | 11.71% | 0               | 0.00%  |
| Precinct VBM           | 9                     | 1.27%  | 36                | 5.08%  | 80                       | 11.30% | 3               | 0.42%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 2                     | 9.09%  | 1                 | 4.55%  | 2                        | 9.09%  | 0               | 0.00%  |
| Total                  | 11                    | 1.31%  | 43                | 5.11%  | 95                       | 11.30% | 3               | 0.36%  |
| 100004.11-CP 100004.11 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 4                     | 1.71%  | 6                 | 2.56%  | 15                       | 6.41%  | 4               | 1.71%  |
| Precinct VBM           | 7                     | 1.05%  | 33                | 4.95%  | 69                       | 10.36% | 7               | 1.05%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 0                     | 0.00%  | 1                 | 50.00% | 0                        | 0.00%  | 0               | 0.00%  |
| Total                  | 11                    | 1.22%  | 40                | 4.43%  | 84                       | 9.31%  | 11              | 1.22%  |
| 100005.11-CP 100005.11 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 2                     | 1.49%  | 12                | 8.96%  | 19                       | 14.18% | 0               | 0.00%  |
| Precinct VBM           | 7                     | 0.87%  | 63                | 7.81%  | 119                      | 14.75% | 11              | 1.36%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 1                     | 5.26%  | 0                 | 0.00%  | 1                        | 5.26%  | 0               | 0.00%  |
| Total                  | 10                    | 1.04%  | 75                | 7.81%  | 139                      | 14.48% | 11              | 1.15%  |
| 100006.11-CP 100006.11 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 3                     | 1.36%  | 10                | 4.55%  | 20                       | 9.09%  | 2               | 0.91%  |
| Precinct VBM           | 12                    | 1.42%  | 47                | 5.56%  | 100                      | 11.82% | 7               | 0.83%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 1                     | 4.76%  | 1                 | 4.76%  | 1                        | 4.76%  | 0               | 0.00%  |
| Total                  | 16                    | 1.47%  | 58                | 5.34%  | 121                      | 11.13% | 9               | 0.83%  |
| 100008.11-CP 100008.11 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 5                     | 2.87%  | 5                 | 2.87%  | 19                       | 10.92% | 0               | 0.00%  |
| Precinct VBM           | 13                    | 2.01%  | 51                | 7.89%  | 112                      | 17.34% | 1               | 0.15%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 0                     | 0.00%  | 0                 | 0.00%  | 0                        | 0.00%  | 0               | 0.00%  |
| Total                  | 18                    | 2.17%  | 56                | 6.74%  | 131                      | 15.76% | 1               | 0.12%  |
| 100009.11-CP 100009.11 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 4                     | 2.74%  | 10                | 6.85%  | 11                       | 7.53%  | 2               | 1.37%  |
| Precinct VBM           | 7                     | 1.04%  | 34                | 5.03%  | 57                       | 8.43%  | 6               | 0.89%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 2                     | 11.11% | 1                 | 5.56%  | 1                        | 5.56%  | 0               | 0.00%  |
| Total                  | 13                    | 1.55%  | 45                | 5.36%  | 69                       | 8.21%  | 8               | 0.95%  |
| 100012.11-CP 100012.11 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 7                     | 3.06%  | 10                | 4.37%  | 24                       | 10.48% | 1               | 0.44%  |
| Precinct VBM           | 13                    | 1.52%  | 34                | 3.97%  | 101                      | 11.79% | 13              | 1.52%  |

**COUNTY OF EL DORADO**  
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United States Senator

|                        | STEVE STOKES<br>(DEM) |        | GREG CONLON (REP) |        | T.G.DEL BECCARO<br>(REP) |        | VON HOUGO (REP) |       |
|------------------------|-----------------------|--------|-------------------|--------|--------------------------|--------|-----------------|-------|
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 1                     | 3.57%  | 0                 | 0.00%  | 1                        | 3.57%  | 0               | 0.00% |
| Total                  | 21                    | 1.89%  | 44                | 3.95%  | 126                      | 11.31% | 14              | 1.26% |
| 100013.11-CP 100013.11 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 6                     | 2.71%  | 9                 | 4.07%  | 28                       | 12.67% | 2               | 0.90% |
| Precinct VBM           | 13                    | 1.61%  | 47                | 5.81%  | 115                      | 14.22% | 8               | 0.99% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 1                     | 4.17%  | 2                 | 8.33%  | 1                        | 4.17%  | 0               | 0.00% |
| Total                  | 20                    | 1.90%  | 58                | 5.50%  | 144                      | 13.66% | 10              | 0.95% |
| 100014.11-CP 100014.11 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 7                     | 4.05%  | 8                 | 4.62%  | 25                       | 14.45% | 3               | 1.73% |
| Precinct VBM           | 5                     | 0.80%  | 33                | 5.28%  | 89                       | 14.24% | 3               | 0.48% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 1                     | 4.55%  | 1                 | 4.55%  | 3                        | 13.64% | 0               | 0.00% |
| Total                  | 13                    | 1.59%  | 42                | 5.12%  | 117                      | 14.27% | 6               | 0.73% |
| 100022.11-CP 100022.11 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 2                     | 1.24%  | 10                | 6.21%  | 19                       | 11.80% | 0               | 0.00% |
| Precinct VBM           | 6                     | 0.98%  | 42                | 6.86%  | 67                       | 10.95% | 4               | 0.65% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 1                     | 10.00% | 0                 | 0.00%  | 1                        | 10.00% | 0               | 0.00% |
| Total                  | 9                     | 1.15%  | 52                | 6.64%  | 87                       | 11.11% | 4               | 0.51% |
| 100023.11-CP 100023.11 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 7                     | 3.63%  | 20                | 10.36% | 26                       | 13.47% | 2               | 1.04% |
| Precinct VBM           | 12                    | 1.29%  | 68                | 7.30%  | 125                      | 13.41% | 9               | 0.97% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 2                     | 7.14%  | 0                 | 0.00%  | 2                        | 7.14%  | 0               | 0.00% |
| Total                  | 21                    | 1.82%  | 88                | 7.63%  | 153                      | 13.27% | 11              | 0.95% |
| 200003.12-CP 200003.12 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 7                     | 4.73%  | 10                | 6.76%  | 9                        | 6.08%  | 3               | 2.03% |
| Precinct VBM           | 19                    | 3.78%  | 23                | 4.57%  | 40                       | 7.95%  | 12              | 2.39% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 1                     | 3.85%  | 2                 | 7.69%  | 1                        | 3.85%  | 1               | 3.85% |
| Total                  | 27                    | 3.99%  | 35                | 5.17%  | 50                       | 7.39%  | 16              | 2.36% |
| 200004.12-CP 200004.12 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 4                     | 2.42%  | 10                | 6.06%  | 20                       | 12.12% | 3               | 1.82% |
| Precinct VBM           | 5                     | 0.83%  | 23                | 3.81%  | 58                       | 9.62%  | 5               | 0.83% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 1                     | 7.14%  | 1                 | 7.14%  | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 10                    | 1.28%  | 34                | 4.35%  | 78                       | 9.97%  | 8               | 1.02% |
| 200005.02-CP 200005.02 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 1                     | 1.15%  | 8                 | 9.20%  | 5                        | 5.75%  | 1               | 1.15% |
| Precinct VBM           | 6                     | 1.48%  | 19                | 4.69%  | 20                       | 4.94%  | 2               | 0.49% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 1                     | 5.26%  | 0                 | 0.00%  | 1                        | 5.26%  | 1               | 5.26% |
| Total                  | 8                     | 1.57%  | 27                | 5.28%  | 26                       | 5.09%  | 4               | 0.78% |
| 200006.12-CP 200006.12 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 3                     | 2.78%  | 5                 | 4.63%  | 26                       | 24.07% | 2               | 1.85% |
| Precinct VBM           | 9                     | 1.88%  | 26                | 5.43%  | 64                       | 13.36% | 4               | 0.84% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00%  | 3                 | 11.11% | 2                        | 7.41%  | 0               | 0.00% |
| Total                  | 12                    | 1.95%  | 34                | 5.54%  | 92                       | 14.98% | 6               | 0.98% |
| 200008.12-CP 200008.12 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |

**COUNTY OF EL DORADO**  
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|                        | STEVE STOKES<br>(DEM) |        | GREG CONLON (REP) |        | T.G.DEL BECCARO<br>(REP) |        | VON HOUGO (REP) |        |
|------------------------|-----------------------|--------|-------------------|--------|--------------------------|--------|-----------------|--------|
| Precinct OS            | 5                     | 3.76%  | 9                 | 6.77%  | 11                       | 8.27%  | 1               | 0.75%  |
| Precinct VBM           | 7                     | 2.14%  | 11                | 3.36%  | 28                       | 8.56%  | 8               | 2.45%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 1                     | 12.50% | 0                 | 0.00%  | 1                        | 12.50% | 0               | 0.00%  |
| Total                  | 13                    | 2.78%  | 20                | 4.27%  | 40                       | 8.55%  | 9               | 1.92%  |
| 200010.12-CP 200010.12 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | 0.00%  | 0                 | 0.00%  | 1                        | 25.00% | 0               | 0.00%  |
| Precinct OS            | 5                     | 2.79%  | 7                 | 3.91%  | 15                       | 8.38%  | 0               | 0.00%  |
| Precinct VBM           | 15                    | 1.99%  | 51                | 6.77%  | 89                       | 11.82% | 6               | 0.80%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 2                     | 7.69%  | 2                 | 7.69%  | 0                        | 0.00%  | 0               | 0.00%  |
| Total                  | 22                    | 2.29%  | 60                | 6.24%  | 105                      | 10.91% | 6               | 0.62%  |
| 200012.12-CP 200012.12 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 6                     | 4.76%  | 6                 | 4.76%  | 12                       | 9.52%  | 2               | 1.59%  |
| Precinct VBM           | 6                     | 1.49%  | 29                | 7.18%  | 54                       | 13.37% | 10              | 2.48%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 0                     | 0.00%  | 0                 | 0.00%  | 1                        | 8.33%  | 0               | 0.00%  |
| Total                  | 12                    | 2.21%  | 35                | 6.46%  | 67                       | 12.36% | 12              | 2.21%  |
| 200013.12-CP 200013.12 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 9                     | 4.71%  | 7                 | 3.66%  | 23                       | 12.04% | 0               | 0.00%  |
| Precinct VBM           | 12                    | 1.88%  | 35                | 5.48%  | 86                       | 13.46% | 6               | 0.94%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 0                     | 0.00%  | 0                 | 0.00%  | 1                        | 5.00%  | 1               | 5.00%  |
| Total                  | 21                    | 2.47%  | 42                | 4.94%  | 110                      | 12.94% | 7               | 0.82%  |
| 200014.12-CP 200014.12 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 4                     | 2.14%  | 9                 | 4.81%  | 31                       | 16.58% | 4               | 2.14%  |
| Precinct VBM           | 12                    | 2.19%  | 20                | 3.64%  | 60                       | 10.93% | 9               | 1.64%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 1                     | 4.00%  | 1                 | 4.00%  | 0                        | 0.00%  | 3               | 12.00% |
| Total                  | 17                    | 2.23%  | 30                | 3.94%  | 91                       | 11.96% | 16              | 2.10%  |
| 200015.02-CP 200015.02 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 5                     | 2.56%  | 8                 | 4.10%  | 11                       | 5.64%  | 5               | 2.56%  |
| Precinct VBM           | 17                    | 2.15%  | 27                | 3.42%  | 36                       | 4.56%  | 12              | 1.52%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 1                     | 5.00%  | 2                 | 10.00% | 0                        | 0.00%  | 0               | 0.00%  |
| Total                  | 23                    | 2.29%  | 37                | 3.68%  | 47                       | 4.68%  | 17              | 1.69%  |
| 200016.12-CP 200016.12 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 0                     | 0.00%  | 1                 | 5.00%  | 0                        | 0.00%  | 0               | 0.00%  |
| Precinct VBM           | 2                     | 1.59%  | 6                 | 4.76%  | 18                       | 14.29% | 1               | 0.79%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 0                     | 0.00%  | 0                 | 0.00%  | 0                        | 0.00%  | 0               | 0.00%  |
| Total                  | 2                     | 1.34%  | 7                 | 4.70%  | 18                       | 12.08% | 1               | 0.67%  |
| 200016.13-CP 200016.13 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 0                     | 0.00%  | 7                 | 9.72%  | 10                       | 13.89% | 0               | 0.00%  |
| Precinct VBM           | 3                     | 1.02%  | 20                | 6.80%  | 60                       | 20.41% | 1               | 0.34%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 0                     | 0.00%  | 2                 | 33.33% | 0                        | 0.00%  | 0               | 0.00%  |
| Total                  | 3                     | 0.81%  | 29                | 7.80%  | 70                       | 18.82% | 1               | 0.27%  |
| 200017.02-CP 200017.02 |                       |        |                   |        |                          |        |                 |        |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Precinct OS            | 1                     | 0.78%  | 6                 | 4.69%  | 4                        | 3.13%  | 1               | 0.78%  |
| Precinct VBM           | 17                    | 3.11%  | 16                | 2.93%  | 25                       | 4.57%  | 9               | 1.65%  |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -      |
| Provisional            | 0                     | 0.00%  | 0                 | 0.00%  | 0                        | 0.00%  | 0               | 0.00%  |
| Total                  | 18                    | 2.62%  | 22                | 3.21%  | 29                       | 4.23%  | 10              | 1.46%  |

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|                        | STEVE STOKES<br>(DEM) |       | GREG CONLON (REP) |       | T.G.DEL BECCARO<br>(REP) |        | VON HOUGO (REP) |       |
|------------------------|-----------------------|-------|-------------------|-------|--------------------------|--------|-----------------|-------|
| 200018.02-CP 200018.02 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | 0.00% | 13                | 8.55% | 5                        | 3.29%  | 2               | 1.32% |
| Precinct VBM           | 7                     | 1.11% | 14                | 2.22% | 35                       | 5.54%  | 8               | 1.27% |
| MB Precinct            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00% | 0                 | 0.00% | 1                        | 8.33%  | 0               | 0.00% |
| Total                  | 7                     | 0.88% | 27                | 3.39% | 41                       | 5.15%  | 10              | 1.26% |
| 200020.02-CP 200020.02 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 2                     | 1.61% | 8                 | 6.45% | 7                        | 5.65%  | 0               | 0.00% |
| Precinct VBM           | 13                    | 2.73% | 12                | 2.52% | 50                       | 10.50% | 4               | 0.84% |
| MB Precinct            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00% | 0                 | 0.00% | 2                        | 11.76% | 1               | 5.88% |
| Total                  | 15                    | 2.43% | 20                | 3.24% | 59                       | 9.56%  | 5               | 0.81% |
| 200021.01-CP 200021.01 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | 0.00% | 2                 | 6.67% | 7                        | 23.33% | 0               | 0.00% |
| Precinct VBM           | 1                     | 0.56% | 9                 | 5.00% | 9                        | 5.00%  | 3               | 1.67% |
| MB Precinct            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 1                     | 0.47% | 11                | 5.12% | 16                       | 7.44%  | 3               | 1.40% |
| 200021.02-CP 200021.02 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 2                     | 1.30% | 10                | 6.49% | 8                        | 5.19%  | 3               | 1.95% |
| Precinct VBM           | 8                     | 1.19% | 28                | 4.15% | 47                       | 6.96%  | 13              | 1.93% |
| MB Precinct            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 10                    | 1.18% | 38                | 4.50% | 55                       | 6.52%  | 16              | 1.90% |
| 200024.02-CP 200024.02 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 1                     | 1.89% | 2                 | 3.77% | 5                        | 9.43%  | 4               | 7.55% |
| Precinct VBM           | 3                     | 1.15% | 17                | 6.54% | 18                       | 6.92%  | 2               | 0.77% |
| MB Precinct            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 4                     | 1.26% | 19                | 5.97% | 23                       | 7.23%  | 6               | 1.89% |
| 300001.04-CP 300001.04 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | 0.00% | 1                 | 9.09% | 1                        | 9.09%  | 1               | 9.09% |
| Precinct VBM           | 4                     | 4.26% | 1                 | 1.06% | 5                        | 5.32%  | 0               | 0.00% |
| MB Precinct            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Provisional            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Total                  | 4                     | 3.81% | 2                 | 1.90% | 6                        | 5.71%  | 1               | 0.95% |
| 300001.05-CP 300001.05 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 3                     | 4.41% | 3                 | 4.41% | 4                        | 5.88%  | 1               | 1.47% |
| Precinct VBM           | 12                    | 4.07% | 11                | 3.73% | 7                        | 2.37%  | 6               | 2.03% |
| MB Precinct            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 15                    | 4.03% | 14                | 3.76% | 11                       | 2.96%  | 7               | 1.88% |
| 300003.05-CP 300003.05 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 2                     | 1.11% | 5                 | 2.78% | 9                        | 5.00%  | 1               | 0.56% |
| Precinct VBM           | 31                    | 3.69% | 35                | 4.16% | 33                       | 3.92%  | 12              | 1.43% |
| MB Precinct            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00% | 1                 | 4.35% | 1                        | 4.35%  | 0               | 0.00% |
| Total                  | 33                    | 3.16% | 41                | 3.93% | 43                       | 4.12%  | 13              | 1.25% |
| 300004.05-CP 300004.05 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 4                     | 2.58% | 3                 | 1.94% | 17                       | 10.97% | 1               | 0.65% |
| Precinct VBM           | 13                    | 2.47% | 16                | 3.04% | 26                       | 4.94%  | 10              | 1.90% |
| MB Precinct            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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United States Senator

|                        | STEVE STOKES<br>(DEM) |        | GREG CONLON (REP) |        | T.G.DEL BECCARO<br>(REP) |        | VON HOUGO (REP) |       |
|------------------------|-----------------------|--------|-------------------|--------|--------------------------|--------|-----------------|-------|
| Provisional            | 1                     | 6.25%  | 0                 | 0.00%  | 3                        | 18.75% | 0               | 0.00% |
| Total                  | 18                    | 2.58%  | 19                | 2.73%  | 46                       | 6.60%  | 11              | 1.58% |
| 300005.05-CP 300005.05 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 8                     | 5.56%  | 8                 | 5.56%  | 14                       | 9.72%  | 0               | 0.00% |
| Precinct VBM           | 24                    | 4.07%  | 28                | 4.75%  | 30                       | 5.08%  | 7               | 1.19% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 3                     | 13.64% | 1                 | 4.55%  | 1                        | 4.55%  | 0               | 0.00% |
| Total                  | 35                    | 4.63%  | 37                | 4.89%  | 45                       | 5.95%  | 7               | 0.93% |
| 300008.05-CP 300008.05 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 1                     | 0.78%  | 11                | 8.59%  | 16                       | 12.50% | 1               | 0.78% |
| Precinct VBM           | 8                     | 1.17%  | 22                | 3.23%  | 49                       | 7.18%  | 8               | 1.17% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 1                     | 9.09%  | 0                 | 0.00%  | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 10                    | 1.22%  | 33                | 4.02%  | 65                       | 7.92%  | 9               | 1.10% |
| 300009.04-CP 300009.04 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 4                     | 3.64%  | 5                 | 4.55%  | 5                        | 4.55%  | 1               | 0.91% |
| Precinct VBM           | 6                     | 1.74%  | 12                | 3.49%  | 25                       | 7.27%  | 1               | 0.29% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00%  | 2                 | 16.67% | 1                        | 8.33%  | 0               | 0.00% |
| Total                  | 10                    | 2.15%  | 19                | 4.08%  | 31                       | 6.65%  | 2               | 0.43% |
| 300010.04-CP 300010.04 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 4                     | 2.58%  | 6                 | 3.87%  | 12                       | 7.74%  | 4               | 2.58% |
| Precinct VBM           | 20                    | 3.37%  | 16                | 2.69%  | 40                       | 6.73%  | 7               | 1.18% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00%  | 0                 | 0.00%  | 1                        | 9.09%  | 0               | 0.00% |
| Total                  | 24                    | 3.16%  | 22                | 2.89%  | 53                       | 6.97%  | 11              | 1.45% |
| 300011.04-CP 300011.04 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 3                     | 2.31%  | 5                 | 3.85%  | 14                       | 10.77% | 1               | 0.77% |
| Precinct VBM           | 13                    | 2.03%  | 22                | 3.44%  | 41                       | 6.41%  | 6               | 0.94% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 2                     | 10.00% | 1                 | 5.00%  | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 18                    | 2.28%  | 28                | 3.54%  | 55                       | 6.96%  | 7               | 0.89% |
| 300013.05-CP 300013.05 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | 0.00%  | 0                 | 0.00%  | 0                        | 0.00%  | 0               | 0.00% |
| Precinct OS            | 3                     | 2.56%  | 2                 | 1.71%  | 10                       | 8.55%  | 2               | 1.71% |
| Precinct VBM           | 6                     | 0.91%  | 29                | 4.41%  | 61                       | 9.28%  | 5               | 0.76% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00%  | 0                 | 0.00%  | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 9                     | 1.14%  | 31                | 3.93%  | 71                       | 9.01%  | 7               | 0.89% |
| 300015.04-CP 300015.04 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | 0.00%  | 1                 | 12.50% | 3                        | 37.50% | 0               | 0.00% |
| Precinct VBM           | 4                     | 7.84%  | 3                 | 5.88%  | 5                        | 9.80%  | 0               | 0.00% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00%  | 0                 | 0.00%  | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 4                     | 6.35%  | 4                 | 6.35%  | 8                        | 12.70% | 0               | 0.00% |
| 300015.05-CP 300015.05 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 2                     | 3.28%  | 3                 | 4.92%  | 6                        | 9.84%  | 2               | 3.28% |
| Precinct VBM           | 10                    | 4.39%  | 2                 | 0.88%  | 11                       | 4.82%  | 0               | 0.00% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00%  | 0                 | 0.00%  | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 12                    | 4.14%  | 5                 | 1.72%  | 17                       | 5.86%  | 2               | 0.69% |
| 300017.05-CP 300017.05 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 2                     | 1.83%  | 7                 | 6.42%  | 7                        | 6.42%  | 1               | 0.92% |



**COUNTY OF EL DORADO**  
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United States Senator

|                        | STEVE STOKES<br>(DEM) | GREG CONLON (REP) | T.G.DEL BECCARO<br>(REP) | VON HOUGO (REP) |
|------------------------|-----------------------|-------------------|--------------------------|-----------------|
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 5 2.26%               | 8 3.62%           | 25 11.31%                | 4 1.81%         |
| Precinct VBM           | 6 0.86%               | 29 4.17%          | 46 6.62%                 | 3 0.43%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 3 10.00%              | 2 6.67%           | 1 3.33%                  | 0 0.00%         |
| Total                  | 14 1.48%              | 39 4.12%          | 72 7.61%                 | 7 0.74%         |
| 400004.14-CP 400004.14 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 7 4.12%               | 8 4.71%           | 16 9.41%                 | 3 1.76%         |
| Precinct VBM           | 6 0.98%               | 20 3.28%          | 80 13.11%                | 7 1.15%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 0 0.00%               | 1 5.56%           | 3 16.67%                 | 0 0.00%         |
| Total                  | 13 1.63%              | 29 3.63%          | 99 12.41%                | 10 1.25%        |
| 400005.08-CP 400005.08 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 3 4.55%               | 3 4.55%           | 2 3.03%                  | 0 0.00%         |
| Precinct VBM           | 3 1.29%               | 11 4.72%          | 8 3.43%                  | 1 0.43%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 0 0.00%               | 1 14.29%          | 0 0.00%                  | 0 0.00%         |
| Total                  | 6 1.96%               | 15 4.90%          | 10 3.27%                 | 1 0.33%         |
| 400006.08-CP 400006.08 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 7 3.33%               | 7 3.33%           | 11 5.24%                 | 2 0.95%         |
| Precinct VBM           | 6 0.93%               | 13 2.02%          | 30 4.66%                 | 4 0.62%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 1 6.67%               | 0 0.00%           | 0 0.00%                  | 1 6.67%         |
| Total                  | 14 1.61%              | 20 2.30%          | 41 4.72%                 | 7 0.81%         |
| 400007.08-CP 400007.08 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 8 3.79%               | 15 7.11%          | 10 4.74%                 | 5 2.37%         |
| Precinct VBM           | 8 1.13%               | 27 3.82%          | 49 6.93%                 | 7 0.99%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 1 5.00%               | 3 15.00%          | 0 0.00%                  | 0 0.00%         |
| Total                  | 17 1.81%              | 45 4.80%          | 59 6.29%                 | 12 1.28%        |
| 400010.08-CP 400010.08 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 5 2.26%               | 12 5.43%          | 13 5.88%                 | 4 1.81%         |
| Precinct VBM           | 14 1.72%              | 26 3.20%          | 44 5.42%                 | 9 1.11%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 0 0.00%               | 1 4.76%           | 1 4.76%                  | 0 0.00%         |
| Total                  | 19 1.80%              | 39 3.70%          | 58 5.50%                 | 13 1.23%        |
| 400012.08-CP 400012.08 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 1 0.95%               | 4 3.81%           | 6 5.71%                  | 2 1.90%         |
| Precinct VBM           | 5 1.28%               | 11 2.81%          | 17 4.34%                 | 4 1.02%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 1 6.67%               | 1 6.67%           | 0 0.00%                  | 0 0.00%         |
| Total                  | 7 1.37%               | 16 3.13%          | 23 4.49%                 | 6 1.17%         |
| 400013.08-CP 400013.08 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 4 3.74%               | 7 6.54%           | 7 6.54%                  | 0 0.00%         |
| Precinct VBM           | 3 1.03%               | 11 3.77%          | 19 6.51%                 | 0 0.00%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 0 0.00%               | 0 0.00%           | 0 0.00%                  | 0 0.00%         |
| Total                  | 7 1.74%               | 18 4.48%          | 26 6.47%                 | 0 0.00%         |
| 400014.08-CP 400014.08 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 3 1.53%               | 7 3.57%           | 17 8.67%                 | 1 0.51%         |
| Precinct VBM           | 5 0.75%               | 44 6.63%          | 37 5.57%                 | 9 1.36%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 1 3.23%               | 1 3.23%           | 3 9.68%                  | 0 0.00%         |

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United States Senator

|                        | STEVE STOKES<br>(DEM) |        | GREG CONLON (REP) |        | T.G.DEL BECCARO<br>(REP) |        | VON HOUGO (REP) |       |
|------------------------|-----------------------|--------|-------------------|--------|--------------------------|--------|-----------------|-------|
|                        |                       |        |                   |        |                          |        |                 |       |
| Total                  | 9                     | 1.01%  | 52                | 5.84%  | 57                       | 6.40%  | 10              | 1.12% |
| 400017.08-CP 400017.08 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 1                     | 0.65%  | 14                | 9.15%  | 18                       | 11.76% | 2               | 1.31% |
| Precinct VBM           | 10                    | 1.50%  | 48                | 7.19%  | 48                       | 7.19%  | 6               | 0.90% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 2                     | 8.70%  | 3                 | 13.04% | 0                        | 0.00%  | 1               | 4.35% |
| Total                  | 13                    | 1.54%  | 65                | 7.70%  | 66                       | 7.82%  | 9               | 1.07% |
| 400018.08-CP 400018.08 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 2                     | 2.60%  | 2                 | 2.60%  | 3                        | 3.90%  | 1               | 1.30% |
| Precinct VBM           | 2                     | 0.83%  | 11                | 4.58%  | 15                       | 6.25%  | 2               | 0.83% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 1                     | 25.00% | 0                 | 0.00%  | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 5                     | 1.56%  | 13                | 4.05%  | 18                       | 5.61%  | 3               | 0.93% |
| 400020.08-CP 400020.08 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 2                     | 1.90%  | 9                 | 8.57%  | 6                        | 5.71%  | 3               | 2.86% |
| Precinct VBM           | 1                     | 0.32%  | 18                | 5.83%  | 23                       | 7.44%  | 7               | 2.27% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00%  | 0                 | 0.00%  | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 3                     | 0.72%  | 27                | 6.49%  | 29                       | 6.97%  | 10              | 2.40% |
| 400021.07-CP 400021.07 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | 0.00%  | 7                 | 5.93%  | 6                        | 5.08%  | 5               | 4.24% |
| Precinct VBM           | 13                    | 1.90%  | 30                | 4.37%  | 66                       | 9.62%  | 1               | 0.15% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 0                     | 0.00%  | 0                 | 0.00%  | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 13                    | 1.59%  | 37                | 4.52%  | 72                       | 8.80%  | 6               | 0.73% |
| 500001.10-CP 500001.10 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 8                     | 4.32%  | 3                 | 1.62%  | 3                        | 1.62%  | 3               | 1.62% |
| Precinct VBM           | 15                    | 2.79%  | 16                | 2.98%  | 18                       | 3.35%  | 13              | 2.42% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 4                     | 28.57% | 0                 | 0.00%  | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 27                    | 3.67%  | 19                | 2.58%  | 21                       | 2.85%  | 16              | 2.17% |
| 500002.10-CP 500002.10 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 5                     | 4.17%  | 7                 | 5.83%  | 2                        | 1.67%  | 1               | 0.83% |
| Precinct VBM           | 16                    | 4.31%  | 11                | 2.96%  | 11                       | 2.96%  | 6               | 1.62% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 3                     | 12.00% | 1                 | 4.00%  | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 24                    | 4.65%  | 19                | 3.68%  | 13                       | 2.52%  | 7               | 1.36% |
| 500003.10-CP 500003.10 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 6                     | 5.04%  | 2                 | 1.68%  | 2                        | 1.68%  | 0               | 0.00% |
| Precinct VBM           | 9                     | 2.98%  | 7                 | 2.32%  | 6                        | 1.99%  | 3               | 0.99% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 2                     | 28.57% | 0                 | 0.00%  | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 17                    | 3.97%  | 9                 | 2.10%  | 8                        | 1.87%  | 3               | 0.70% |
| 500004.10-CP 500004.10 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 7                     | 6.54%  | 4                 | 3.74%  | 5                        | 4.67%  | 2               | 1.87% |
| Precinct VBM           | 11                    | 4.78%  | 9                 | 3.91%  | 6                        | 2.61%  | 0               | 0.00% |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 2                     | 11.11% | 1                 | 5.56%  | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 20                    | 5.63%  | 14                | 3.94%  | 11                       | 3.10%  | 2               | 0.56% |
| 500005.10-CP 500005.10 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 4                     | 4.12%  | 4                 | 4.12%  | 1                        | 1.03%  | 2               | 2.06% |
| Precinct VBM           | 8                     | 3.65%  | 3                 | 1.37%  | 6                        | 2.74%  | 0               | 0.00% |



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|                        | STEVE STOKES<br>(DEM) | GREG CONLON (REP) | T.G.DEL BECCARO<br>(REP) | VON HOUGO (REP) |
|------------------------|-----------------------|-------------------|--------------------------|-----------------|
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 3 25.00%              | 0 0.00%           | 0 0.00%                  | 1 8.33%         |
| Total                  | 15 4.57%              | 7 2.13%           | 7 2.13%                  | 3 0.91%         |
| 500006.10-CP 500006.10 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 14 8.54%              | 4 2.44%           | 1 0.61%                  | 1 0.61%         |
| Precinct VBM           | 22 5.01%              | 11 2.51%          | 6 1.37%                  | 3 0.68%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 7 14.29%              | 0 0.00%           | 0 0.00%                  | 1 2.04%         |
| Total                  | 43 6.60%              | 15 2.30%          | 7 1.07%                  | 5 0.77%         |
| 500007.10-CP 500007.10 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 7 3.72%               | 12 6.38%          | 8 4.26%                  | 1 0.53%         |
| Precinct VBM           | 18 2.99%              | 21 3.49%          | 18 2.99%                 | 6 1.00%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 0 0.00%               | 1 5.56%           | 0 0.00%                  | 0 0.00%         |
| Total                  | 25 3.09%              | 34 4.21%          | 26 3.22%                 | 7 0.87%         |
| 500009.10-CP 500009.10 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 13 12.04%             | 5 4.63%           | 4 3.70%                  | 0 0.00%         |
| Precinct VBM           | 16 5.99%              | 7 2.62%           | 12 4.49%                 | 0 0.00%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 4 12.90%              | 1 3.23%           | 0 0.00%                  | 0 0.00%         |
| Total                  | 33 8.13%              | 13 3.20%          | 16 3.94%                 | 0 0.00%         |
| 500011.10-CP 500011.10 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 12 7.36%              | 2 1.23%           | 4 2.45%                  | 3 1.84%         |
| Precinct VBM           | 13 3.85%              | 10 2.96%          | 8 2.37%                  | 2 0.59%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 1 3.03%               | 2 6.06%           | 0 0.00%                  | 0 0.00%         |
| Total                  | 26 4.87%              | 14 2.62%          | 12 2.25%                 | 5 0.94%         |
| 500012.10-CP 500012.10 |                       |                   |                          |                 |
| Precinct TSx           | 0 0.00%               | 0 0.00%           | 0 0.00%                  | 0 0.00%         |
| Precinct OS            | 6 3.53%               | 2 1.18%           | 3 1.76%                  | 1 0.59%         |
| Precinct VBM           | 26 4.80%              | 16 2.95%          | 24 4.43%                 | 10 1.85%        |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 1 3.33%               | 0 0.00%           | 0 0.00%                  | 1 3.33%         |
| Total                  | 33 4.44%              | 18 2.42%          | 27 3.63%                 | 12 1.62%        |
| 500015.10-CP 500015.10 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 2 2.08%               | 3 3.13%           | 4 4.17%                  | 1 1.04%         |
| Precinct VBM           | 11 3.00%              | 19 5.18%          | 10 2.72%                 | 4 1.09%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 1 10.00%              | 0 0.00%           | 0 0.00%                  | 2 20.00%        |
| Total                  | 14 2.96%              | 22 4.65%          | 14 2.96%                 | 7 1.48%         |
| 500020.10-CP 500020.10 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 2 1.42%               | 2 1.42%           | 7 4.96%                  | 0 0.00%         |
| Precinct VBM           | 10 1.65%              | 22 3.62%          | 41 6.75%                 | 9 1.48%         |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 0 0.00%               | 1 3.85%           | 0 0.00%                  | 0 0.00%         |
| Total                  | 12 1.55%              | 25 3.23%          | 48 6.20%                 | 9 1.16%         |
| 500021.10-CP 500021.10 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Precinct OS            | 5 3.36%               | 6 4.03%           | 9 6.04%                  | 2 1.34%         |
| Precinct VBM           | 11 1.92%              | 27 4.72%          | 35 6.12%                 | 10 1.75%        |
| MB Precinct            | 0 -                   | 0 -               | 0 -                      | 0 -             |
| Provisional            | 1 4.00%               | 0 0.00%           | 3 12.00%                 | 1 4.00%         |
| Total                  | 17 2.28%              | 33 4.42%          | 47 6.30%                 | 13 1.74%        |
| 95U201.01-MB 95U201.01 |                       |                   |                          |                 |
| Precinct TSx           | 0 -                   | 0 -               | 0 -                      | 0 -             |

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|                        | United States Senator |       |                   |       |                          |        |                 |       |
|------------------------|-----------------------|-------|-------------------|-------|--------------------------|--------|-----------------|-------|
|                        | STEVE STOKES<br>(DEM) |       | GREG CONLON (REP) |       | T.G.DEL BECCARO<br>(REP) |        | VON HOUGO (REP) |       |
| Precinct OS            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct            | 0                     | 0.00% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Provisional            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Total                  | 0                     | 0.00% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| 95U201.02-MB 95U201.02 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct            | 3                     | 0.99% | 19                | 6.25% | 11                       | 3.62%  | 2               | 0.66% |
| Provisional            | 0                     | 0.00% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 3                     | 0.98% | 19                | 6.23% | 11                       | 3.61%  | 2               | 0.66% |
| 95U202.02-MB 95U202.02 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct            | 10                    | 1.98% | 17                | 3.37% | 33                       | 6.53%  | 5               | 0.99% |
| Provisional            | 0                     | 0.00% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 10                    | 1.97% | 17                | 3.35% | 33                       | 6.50%  | 5               | 0.98% |
| 95U203.02-MB 95U203.02 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Provisional            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Total                  | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| 95U204.01-MB 95U204.01 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct            | 8                     | 1.34% | 24                | 4.02% | 38                       | 6.37%  | 2               | 0.34% |
| Provisional            | 0                     | 0.00% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 8                     | 1.34% | 24                | 4.01% | 38                       | 6.34%  | 2               | 0.33% |
| 95U301.05-MB 95U301.05 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct            | 2                     | 1.47% | 7                 | 5.15% | 16                       | 11.76% | 1               | 0.74% |
| Provisional            | 0                     | 0.00% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 2                     | 1.45% | 7                 | 5.07% | 16                       | 11.59% | 1               | 0.72% |
| 95U302.04-MB 95U302.04 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct            | 0                     | 0.00% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Provisional            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Total                  | 0                     | 0.00% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| 95U302.05-MB 95U302.05 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct            | 0                     | 0.00% | 2                 | 2.47% | 3                        | 3.70%  | 0               | 0.00% |
| Provisional            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Total                  | 0                     | 0.00% | 2                 | 2.47% | 3                        | 3.70%  | 0               | 0.00% |
| 95U303.03-MB 95U303.03 |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct            | 4                     | 5.13% | 1                 | 1.28% | 5                        | 6.41%  | 1               | 1.28% |
| Provisional            | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Total                  | 4                     | 5.13% | 1                 | 1.28% | 5                        | 6.41%  | 1               | 1.28% |

**COUNTY OF EL DORADO**  
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United States Senator

|                        | STEVE STOKES<br>(DEM) |        | GREG CONLON (REP) |        | T.G.DEL BECCARO<br>(REP) |        | VON HOUGO (REP) |       |
|------------------------|-----------------------|--------|-------------------|--------|--------------------------|--------|-----------------|-------|
| 95U303.05-MB 95U303.05 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| MB Precinct            | 0                     | 0.00%  | 3                 | 3.16%  | 5                        | 5.26%  | 1               | 1.05% |
| Provisional            | 0                     | 0.00%  | 0                 | 0.00%  | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 0                     | 0.00%  | 3                 | 3.13%  | 5                        | 5.21%  | 1               | 1.04% |
| 95U304.05-MB 95U304.05 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Total                  | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| 95U305.05-MB 95U305.05 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| MB Precinct            | 0                     | 0.00%  | 0                 | 0.00%  | 2                        | 28.57% | 0               | 0.00% |
| Provisional            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Total                  | 0                     | 0.00%  | 0                 | 0.00%  | 2                        | 28.57% | 0               | 0.00% |
| 95U401.08-MB 95U401.08 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| MB Precinct            | 0                     | 0.00%  | 1                 | 6.67%  | 0                        | 0.00%  | 0               | 0.00% |
| Provisional            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Total                  | 0                     | 0.00%  | 1                 | 6.67%  | 0                        | 0.00%  | 0               | 0.00% |
| 95U402.07-MB 95U402.07 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| MB Precinct            | 1                     | 0.43%  | 8                 | 3.42%  | 18                       | 7.69%  | 4               | 1.71% |
| Provisional            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Total                  | 1                     | 0.43%  | 8                 | 3.42%  | 18                       | 7.69%  | 4               | 1.71% |
| 95U402.08-MB 95U402.08 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| MB Precinct            | 9                     | 1.79%  | 21                | 4.17%  | 41                       | 8.13%  | 4               | 0.79% |
| Provisional            | 1                     | 33.33% | 0                 | 0.00%  | 1                        | 33.33% | 0               | 0.00% |
| Total                  | 10                    | 1.97%  | 21                | 4.14%  | 42                       | 8.28%  | 4               | 0.79% |
| 95U403.08-MB 95U403.08 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| MB Precinct            | 20                    | 2.11%  | 52                | 5.50%  | 49                       | 5.18%  | 12              | 1.27% |
| Provisional            | 0                     | 0.00%  | 1                 | 10.00% | 0                        | 0.00%  | 0               | 0.00% |
| Total                  | 20                    | 2.09%  | 53                | 5.54%  | 49                       | 5.13%  | 12              | 1.26% |
| 95U404.06-MB 95U404.06 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Provisional            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Total                  | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| 95U404.08-MB 95U404.08 |                       |        |                   |        |                          |        |                 |       |
| Precinct TSx           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct OS            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| Precinct VBM           | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |
| MB Precinct            | 0                     | -      | 0                 | -      | 0                        | -      | 0               | -     |

**COUNTY OF EL DORADO**  
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United States Senator

|                               | STEVE STOKES<br>(DEM) |        | GREG CONLON (REP) |       | T.G.DEL BECCARO<br>(REP) |        | VON HOUGO (REP) |       |
|-------------------------------|-----------------------|--------|-------------------|-------|--------------------------|--------|-----------------|-------|
| Provisional                   | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Total                         | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| 95U501.09-MB 95U501.09        |                       |        |                   |       |                          |        |                 |       |
| Precinct TSx                  | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS                   | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM                  | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct                   | 0                     | 0.00%  | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Provisional                   | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Total                         | 0                     | 0.00%  | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| 95U501.10-MB 95U501.10        |                       |        |                   |       |                          |        |                 |       |
| Precinct TSx                  | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS                   | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM                  | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct                   | 6                     | 3.75%  | 3                 | 1.88% | 7                        | 4.38%  | 3               | 1.88% |
| Provisional                   | 0                     | 0.00%  | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Total                         | 6                     | 3.70%  | 3                 | 1.85% | 7                        | 4.32%  | 3               | 1.85% |
| 95U598.10-MB 95U598.10        |                       |        |                   |       |                          |        |                 |       |
| Precinct TSx                  | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS                   | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM                  | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct                   | 8                     | 3.74%  | 3                 | 1.40% | 7                        | 3.27%  | 3               | 1.40% |
| Provisional                   | 0                     | 0.00%  | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Total                         | 8                     | 3.70%  | 3                 | 1.39% | 7                        | 3.24%  | 3               | 1.39% |
| 95U599.10-MB 95U599.10        |                       |        |                   |       |                          |        |                 |       |
| Precinct TSx                  | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS                   | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM                  | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct                   | 0                     | 0.00%  | 0                 | 0.00% | 2                        | 5.88%  | 0               | 0.00% |
| Provisional                   | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Total                         | 0                     | 0.00%  | 0                 | 0.00% | 2                        | 5.88%  | 0               | 0.00% |
| 96U101.11-MB 96U101.11        |                       |        |                   |       |                          |        |                 |       |
| Precinct TSx                  | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS                   | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM                  | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct                   | 7                     | 1.91%  | 21                | 5.72% | 49                       | 13.35% | 2               | 0.54% |
| Provisional                   | 1                     | 16.67% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Total                         | 8                     | 2.14%  | 21                | 5.63% | 49                       | 13.14% | 2               | 0.54% |
| 96U401.14-MB 96U401.14        |                       |        |                   |       |                          |        |                 |       |
| Precinct TSx                  | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS                   | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct VBM                  | 0                     | -      | 0                 | -     | 0                        | -      | 0               | -     |
| MB Precinct                   | 9                     | 2.80%  | 18                | 5.59% | 32                       | 9.94%  | 3               | 0.93% |
| Provisional                   | 0                     | 0.00%  | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Total                         | 9                     | 2.74%  | 18                | 5.49% | 32                       | 9.76%  | 3               | 0.91% |
| Total                         |                       |        |                   |       |                          |        |                 |       |
| Precinct TSx                  | 0                     | 0.00%  | 0                 | 0.00% | 1                        | 12.50% | 0               | 0.00% |
| Precinct OS                   | 313                   | 2.92%  | 532               | 4.97% | 826                      | 7.72%  | 135             | 1.26% |
| Precinct VBM                  | 812                   | 1.99%  | 1814              | 4.44% | 3298                     | 8.07%  | 446             | 1.09% |
| MB Precinct                   | 87                    | 1.89%  | 200               | 4.34% | 318                      | 6.90%  | 43              | 0.93% |
| Provisional                   | 77                    | 5.86%  | 52                | 3.96% | 48                       | 3.65%  | 17              | 1.29% |
| Total                         | 1289                  | 2.24%  | 2598              | 4.52% | 4491                     | 7.81%  | 641             | 1.11% |
| STATE BOARD OF EQUALIZATION   |                       |        |                   |       |                          |        |                 |       |
| STATE BOARD OF EQUALIZATION 1 |                       |        |                   |       |                          |        |                 |       |
| Precinct TSx                  | 0                     | 0.00%  | 0                 | 0.00% | 1                        | 12.50% | 0               | 0.00% |
| Precinct OS                   | 313                   | 2.92%  | 532               | 4.97% | 826                      | 7.72%  | 135             | 1.26% |
| Precinct VBM                  | 812                   | 1.99%  | 1814              | 4.44% | 3298                     | 8.07%  | 446             | 1.09% |
| MB Precinct                   | 87                    | 1.89%  | 200               | 4.34% | 318                      | 6.90%  | 43              | 0.93% |
| Provisional                   | 77                    | 5.86%  | 52                | 3.96% | 48                       | 3.65%  | 17              | 1.29% |
| Total                         | 1289                  | 2.24%  | 2598              | 4.52% | 4491                     | 7.81%  | 641             | 1.11% |
| Total                         |                       |        |                   |       |                          |        |                 |       |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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United States Senator

|                                     | STEVE STOKES<br>(DEM) |              | GREG CONLON (REP) |              | T.G.DEL BECCARO<br>(REP) |              | VON HOUGO (REP) |              |
|-------------------------------------|-----------------------|--------------|-------------------|--------------|--------------------------|--------------|-----------------|--------------|
| Precinct TSx                        | 0                     | 0.00%        | 0                 | 0.00%        | 1                        | 12.50%       | 0               | 0.00%        |
| Precinct OS                         | 313                   | 2.92%        | 532               | 4.97%        | 826                      | 7.72%        | 135             | 1.26%        |
| Precinct VBM                        | 812                   | 1.99%        | 1814              | 4.44%        | 3298                     | 8.07%        | 446             | 1.09%        |
| MB Precinct                         | 87                    | 1.89%        | 200               | 4.34%        | 318                      | 6.90%        | 43              | 0.93%        |
| Provisional                         | 77                    | 5.86%        | 52                | 3.96%        | 48                       | 3.65%        | 17              | 1.29%        |
| <b>Total</b>                        | <b>1289</b>           | <b>2.24%</b> | <b>2598</b>       | <b>4.52%</b> | <b>4491</b>              | <b>7.81%</b> | <b>641</b>      | <b>1.11%</b> |
| <b>UNITED STATES SENATE</b>         |                       |              |                   |              |                          |              |                 |              |
| <b>UNITED STATES SENATE</b>         |                       |              |                   |              |                          |              |                 |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                 | 0.00%        | 1                        | 12.50%       | 0               | 0.00%        |
| Precinct OS                         | 313                   | 2.92%        | 532               | 4.97%        | 826                      | 7.72%        | 135             | 1.26%        |
| Precinct VBM                        | 812                   | 1.99%        | 1814              | 4.44%        | 3298                     | 8.07%        | 446             | 1.09%        |
| MB Precinct                         | 87                    | 1.89%        | 200               | 4.34%        | 318                      | 6.90%        | 43              | 0.93%        |
| Provisional                         | 77                    | 5.86%        | 52                | 3.96%        | 48                       | 3.65%        | 17              | 1.29%        |
| <b>Total</b>                        | <b>1289</b>           | <b>2.24%</b> | <b>2598</b>       | <b>4.52%</b> | <b>4491</b>              | <b>7.81%</b> | <b>641</b>      | <b>1.11%</b> |
| <b>Total</b>                        |                       |              |                   |              |                          |              |                 |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                 | 0.00%        | 1                        | 12.50%       | 0               | 0.00%        |
| Precinct OS                         | 313                   | 2.92%        | 532               | 4.97%        | 826                      | 7.72%        | 135             | 1.26%        |
| Precinct VBM                        | 812                   | 1.99%        | 1814              | 4.44%        | 3298                     | 8.07%        | 446             | 1.09%        |
| MB Precinct                         | 87                    | 1.89%        | 200               | 4.34%        | 318                      | 6.90%        | 43              | 0.93%        |
| Provisional                         | 77                    | 5.86%        | 52                | 3.96%        | 48                       | 3.65%        | 17              | 1.29%        |
| <b>Total</b>                        | <b>1289</b>           | <b>2.24%</b> | <b>2598</b>       | <b>4.52%</b> | <b>4491</b>              | <b>7.81%</b> | <b>641</b>      | <b>1.11%</b> |
| <b>Total</b>                        |                       |              |                   |              |                          |              |                 |              |
| <b>UNITED STATES REPRESENTATIVE</b> |                       |              |                   |              |                          |              |                 |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                       |              |                   |              |                          |              |                 |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                 | 0.00%        | 1                        | 12.50%       | 0               | 0.00%        |
| Precinct OS                         | 313                   | 2.92%        | 532               | 4.97%        | 826                      | 7.72%        | 135             | 1.26%        |
| Precinct VBM                        | 812                   | 1.99%        | 1814              | 4.44%        | 3298                     | 8.07%        | 446             | 1.09%        |
| MB Precinct                         | 87                    | 1.89%        | 200               | 4.34%        | 318                      | 6.90%        | 43              | 0.93%        |
| Provisional                         | 77                    | 5.86%        | 52                | 3.96%        | 48                       | 3.65%        | 17              | 1.29%        |
| <b>Total</b>                        | <b>1289</b>           | <b>2.24%</b> | <b>2598</b>       | <b>4.52%</b> | <b>4491</b>              | <b>7.81%</b> | <b>641</b>      | <b>1.11%</b> |
| <b>Total</b>                        |                       |              |                   |              |                          |              |                 |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                 | 0.00%        | 1                        | 12.50%       | 0               | 0.00%        |
| Precinct OS                         | 313                   | 2.92%        | 532               | 4.97%        | 826                      | 7.72%        | 135             | 1.26%        |
| Precinct VBM                        | 812                   | 1.99%        | 1814              | 4.44%        | 3298                     | 8.07%        | 446             | 1.09%        |
| MB Precinct                         | 87                    | 1.89%        | 200               | 4.34%        | 318                      | 6.90%        | 43              | 0.93%        |
| Provisional                         | 77                    | 5.86%        | 52                | 3.96%        | 48                       | 3.65%        | 17              | 1.29%        |
| <b>Total</b>                        | <b>1289</b>           | <b>2.24%</b> | <b>2598</b>       | <b>4.52%</b> | <b>4491</b>              | <b>7.81%</b> | <b>641</b>      | <b>1.11%</b> |
| <b>Total</b>                        |                       |              |                   |              |                          |              |                 |              |
| <b>STATE SENATE</b>                 |                       |              |                   |              |                          |              |                 |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                       |              |                   |              |                          |              |                 |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                 | 0.00%        | 1                        | 12.50%       | 0               | 0.00%        |
| Precinct OS                         | 313                   | 2.92%        | 532               | 4.97%        | 826                      | 7.72%        | 135             | 1.26%        |
| Precinct VBM                        | 812                   | 1.99%        | 1814              | 4.44%        | 3298                     | 8.07%        | 446             | 1.09%        |
| MB Precinct                         | 87                    | 1.89%        | 200               | 4.34%        | 318                      | 6.90%        | 43              | 0.93%        |
| Provisional                         | 77                    | 5.86%        | 52                | 3.96%        | 48                       | 3.65%        | 17              | 1.29%        |
| <b>Total</b>                        | <b>1289</b>           | <b>2.24%</b> | <b>2598</b>       | <b>4.52%</b> | <b>4491</b>              | <b>7.81%</b> | <b>641</b>      | <b>1.11%</b> |
| <b>Total</b>                        |                       |              |                   |              |                          |              |                 |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                 | 0.00%        | 1                        | 12.50%       | 0               | 0.00%        |
| Precinct OS                         | 313                   | 2.92%        | 532               | 4.97%        | 826                      | 7.72%        | 135             | 1.26%        |
| Precinct VBM                        | 812                   | 1.99%        | 1814              | 4.44%        | 3298                     | 8.07%        | 446             | 1.09%        |
| MB Precinct                         | 87                    | 1.89%        | 200               | 4.34%        | 318                      | 6.90%        | 43              | 0.93%        |
| Provisional                         | 77                    | 5.86%        | 52                | 3.96%        | 48                       | 3.65%        | 17              | 1.29%        |
| <b>Total</b>                        | <b>1289</b>           | <b>2.24%</b> | <b>2598</b>       | <b>4.52%</b> | <b>4491</b>              | <b>7.81%</b> | <b>641</b>      | <b>1.11%</b> |
| <b>Total</b>                        |                       |              |                   |              |                          |              |                 |              |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                       |              |                   |              |                          |              |                 |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                       |              |                   |              |                          |              |                 |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                 | 0.00%        | 0                        | 0.00%        | 0               | 0.00%        |
| Precinct OS                         | 207                   | 3.04%        | 321               | 4.71%        | 395                      | 5.80%        | 94              | 1.38%        |
| Precinct VBM                        | 578                   | 2.28%        | 955               | 3.76%        | 1422                     | 5.60%        | 283             | 1.11%        |
| MB Precinct                         | 71                    | 1.81%        | 161               | 4.11%        | 237                      | 6.05%        | 38              | 0.97%        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

Date:07/05/16  
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United States Senator

|                                   | STEVE STOKES<br>(DEM) |       | GREG CONLON (REP) |       | T.G.DEL BECCARO<br>(REP) |        | VON HOUGO (REP) |       |
|-----------------------------------|-----------------------|-------|-------------------|-------|--------------------------|--------|-----------------|-------|
| Provisional                       | 55                    | 6.40% | 31                | 3.60% | 23                       | 2.67%  | 10              | 1.16% |
| Total                             | 911                   | 2.46% | 1468              | 3.97% | 2077                     | 5.62%  | 425             | 1.15% |
| <b>6TH STATE ASSEMBLY DIST</b>    |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx                      | 0                     | 0.00% | 0                 | 0.00% | 1                        | 25.00% | 0               | 0.00% |
| Precinct OS                       | 106                   | 2.73% | 211               | 5.43% | 431                      | 11.09% | 41              | 1.05% |
| Precinct VBM                      | 234                   | 1.51% | 859               | 5.55% | 1876                     | 12.12% | 163             | 1.05% |
| MB Precinct                       | 16                    | 2.32% | 39                | 5.66% | 81                       | 11.76% | 5               | 0.73% |
| Provisional                       | 22                    | 4.85% | 21                | 4.63% | 25                       | 5.51%  | 7               | 1.54% |
| Total                             | 378                   | 1.84% | 1130              | 5.51% | 2414                     | 11.77% | 216             | 1.05% |
| <b>Total</b>                      |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx                      | 0                     | 0.00% | 0                 | 0.00% | 1                        | 12.50% | 0               | 0.00% |
| Precinct OS                       | 313                   | 2.92% | 532               | 4.97% | 826                      | 7.72%  | 135             | 1.26% |
| Precinct VBM                      | 812                   | 1.99% | 1814              | 4.44% | 3298                     | 8.07%  | 446             | 1.09% |
| MB Precinct                       | 87                    | 1.89% | 200               | 4.34% | 318                      | 6.90%  | 43              | 0.93% |
| Provisional                       | 77                    | 5.86% | 52                | 3.96% | 48                       | 3.65%  | 17              | 1.29% |
| Total                             | 1289                  | 2.24% | 2598              | 4.52% | 4491                     | 7.81%  | 641             | 1.11% |
| <b>SUPERVISORIAL DISTRICTS</b>    |                       |       |                   |       |                          |        |                 |       |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx                      | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS                       | 51                    | 2.29% | 119               | 5.34% | 241                      | 10.81% | 20              | 0.90% |
| Precinct VBM                      | 121                   | 1.30% | 559               | 6.02% | 1153                     | 12.41% | 86              | 0.93% |
| MB Precinct                       | 7                     | 1.91% | 21                | 5.72% | 49                       | 13.35% | 2               | 0.54% |
| Provisional                       | 14                    | 5.76% | 9                 | 3.70% | 15                       | 6.17%  | 2               | 0.82% |
| Total                             | 193                   | 1.59% | 708               | 5.84% | 1458                     | 12.02% | 110             | 0.91% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx                      | 0                     | 0.00% | 0                 | 0.00% | 1                        | 25.00% | 0               | 0.00% |
| Precinct OS                       | 55                    | 2.44% | 128               | 5.68% | 209                      | 9.28%  | 31              | 1.38% |
| Precinct VBM                      | 162                   | 1.87% | 386               | 4.47% | 797                      | 9.22%  | 115             | 1.33% |
| MB Precinct                       | 21                    | 1.49% | 60                | 4.25% | 82                       | 5.80%  | 9               | 0.64% |
| Provisional                       | 8                     | 2.89% | 13                | 4.69% | 10                       | 3.61%  | 7               | 2.53% |
| Total                             | 246                   | 1.95% | 587               | 4.66% | 1099                     | 8.73%  | 162             | 1.29% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx                      | 0                     | 0.00% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Precinct OS                       | 57                    | 2.69% | 96                | 4.53% | 161                      | 7.60%  | 29              | 1.37% |
| Precinct VBM                      | 230                   | 2.54% | 327               | 3.61% | 519                      | 5.73%  | 101             | 1.12% |
| MB Precinct                       | 6                     | 1.51% | 13                | 3.27% | 31                       | 7.79%  | 3               | 0.75% |
| Provisional                       | 13                    | 5.49% | 6                 | 2.53% | 10                       | 4.22%  | 0               | 0.00% |
| Total                             | 306                   | 2.59% | 442               | 3.74% | 721                      | 6.10%  | 133             | 1.13% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx                      | 0                     | -     | 0                 | -     | 0                        | -      | 0               | -     |
| Precinct OS                       | 59                    | 2.57% | 133               | 5.80% | 162                      | 7.06%  | 38              | 1.66% |
| Precinct VBM                      | 113                   | 1.33% | 363               | 4.27% | 628                      | 7.39%  | 78              | 0.92% |
| MB Precinct                       | 39                    | 1.93% | 100               | 4.95% | 140                      | 6.93%  | 23              | 1.14% |
| Provisional                       | 13                    | 5.10% | 17                | 6.67% | 10                       | 3.92%  | 2               | 0.78% |
| Total                             | 224                   | 1.71% | 613               | 4.69% | 940                      | 7.19%  | 141             | 1.08% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx                      | 0                     | 0.00% | 0                 | 0.00% | 0                        | 0.00%  | 0               | 0.00% |
| Precinct OS                       | 91                    | 5.04% | 56                | 3.10% | 53                       | 2.93%  | 17              | 0.94% |
| Precinct VBM                      | 186                   | 3.45% | 179               | 3.32% | 201                      | 3.73%  | 66              | 1.22% |
| MB Precinct                       | 14                    | 3.41% | 6                 | 1.46% | 16                       | 3.90%  | 6               | 1.46% |
| Provisional                       | 29                    | 9.60% | 7                 | 2.32% | 3                        | 0.99%  | 6               | 1.99% |
| Total                             | 320                   | 4.04% | 248               | 3.13% | 273                      | 3.45%  | 95              | 1.20% |
| <b>Total</b>                      |                       |       |                   |       |                          |        |                 |       |
| Precinct TSx                      | 0                     | 0.00% | 0                 | 0.00% | 1                        | 12.50% | 0               | 0.00% |
| Precinct OS                       | 313                   | 2.92% | 532               | 4.97% | 826                      | 7.72%  | 135             | 1.26% |
| Precinct VBM                      | 812                   | 1.99% | 1814              | 4.44% | 3298                     | 8.07%  | 446             | 1.09% |
| MB Precinct                       | 87                    | 1.89% | 200               | 4.34% | 318                      | 6.90%  | 43              | 0.93% |
| Provisional                       | 77                    | 5.86% | 52                | 3.96% | 48                       | 3.65%  | 17              | 1.29% |
| Total                             | 1289                  | 2.24% | 2598              | 4.52% | 4491                     | 7.81%  | 641             | 1.11% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

Date:07/05/16  
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United States Senator

|                                 | STEVE STOKES<br>(DEM) | GREG CONLON (REP) | T.G.DEL BECCARO<br>(REP) | VON HOUGO (REP)  |
|---------------------------------|-----------------------|-------------------|--------------------------|------------------|
| <b>CITIES</b>                   |                       |                   |                          |                  |
| <b>CITY OF PLACERVILLE</b>      |                       |                   |                          |                  |
| Precinct TSx                    | 0 -                   | 0 -               | 0 -                      | 0 -              |
| Precinct OS                     | 17 3.05%              | 20 3.58%          | 45 8.06%                 | 4 0.72%          |
| Precinct VBM                    | 84 3.58%              | 91 3.88%          | 101 4.31%                | 35 1.49%         |
| MB Precinct                     | 0 -                   | 0 -               | 0 -                      | 0 -              |
| Provisional                     | 4 5.71%               | 2 2.86%           | 5 7.14%                  | 0 0.00%          |
| <b>Total</b>                    | <b>105 3.53%</b>      | <b>113 3.80%</b>  | <b>151 5.08%</b>         | <b>39 1.31%</b>  |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                       |                   |                          |                  |
| Precinct TSx                    | 0 -                   | 0 -               | 0 -                      | 0 -              |
| Precinct OS                     | 62 6.55%              | 38 4.01%          | 25 2.64%                 | 10 1.06%         |
| Precinct VBM                    | 104 4.22%             | 72 2.92%          | 67 2.72%                 | 17 0.69%         |
| MB Precinct                     | 0 -                   | 0 -               | 0 -                      | 0 -              |
| Provisional                     | 20 10.75%             | 6 3.23%           | 0 0.00%                  | 2 1.08%          |
| <b>Total</b>                    | <b>186 5.17%</b>      | <b>116 3.22%</b>  | <b>92 2.56%</b>          | <b>29 0.81%</b>  |
| <b>UNINCORPORATED AREA</b>      |                       |                   |                          |                  |
| Precinct TSx                    | 0 0.00%               | 0 0.00%           | 1 12.50%                 | 0 0.00%          |
| Precinct OS                     | 234 2.54%             | 474 5.15%         | 756 8.22%                | 121 1.32%        |
| Precinct VBM                    | 624 1.73%             | 1651 4.58%        | 3130 8.68%               | 394 1.09%        |
| MB Precinct                     | 87 1.89%              | 200 4.34%         | 318 6.90%                | 43 0.93%         |
| Provisional                     | 53 5.01%              | 44 4.16%          | 43 4.06%                 | 15 1.42%         |
| <b>Total</b>                    | <b>998 1.96%</b>      | <b>2369 4.65%</b> | <b>4248 8.34%</b>        | <b>573 1.12%</b> |
| <b>Total</b>                    |                       |                   |                          |                  |
| Precinct TSx                    | 0 0.00%               | 0 0.00%           | 1 12.50%                 | 0 0.00%          |
| Precinct OS                     | 313 2.92%             | 532 4.97%         | 826 7.72%                | 135 1.26%        |
| Precinct VBM                    | 812 1.99%             | 1814 4.44%        | 3298 8.07%               | 446 1.09%        |
| MB Precinct                     | 87 1.89%              | 200 4.34%         | 318 6.90%                | 43 0.93%         |
| Provisional                     | 77 5.86%              | 52 3.96%          | 48 3.65%                 | 17 1.29%         |
| <b>Total</b>                    | <b>1289 2.24%</b>     | <b>2598 4.52%</b> | <b>4491 7.81%</b>        | <b>641 1.11%</b> |

**COUNTY OF EL DORADO**  
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|                        | United States Senator |       |                        |       |                  |       |                          |       |
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|                        | DON KRAMPE<br>(REP)   |       | JERRY J. LAWS<br>(REP) |       | TOM PALZER (REP) |       | KAREN<br>ROSEBERRY (REP) |       |
| Jurisdiction Wide      |                       |       |                        |       |                  |       |                          |       |
| 100001.11-CP 100001.11 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | 0.00% | 0                      | 0.00% | 1                | 0.64% | 1                        | 0.64% |
| Precinct VBM           | 5                     | 0.71% | 0                      | 0.00% | 7                | 1.00% | 7                        | 1.00% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 5                     | 0.57% | 0                      | 0.00% | 8                | 0.91% | 8                        | 0.91% |
| 100002.11-CP 100002.11 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 1                     | 1.32% | 0                      | 0.00% | 1                | 1.32% | 1                        | 1.32% |
| Precinct VBM           | 6                     | 1.49% | 0                      | 0.00% | 2                | 0.50% | 4                        | 0.99% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 7                     | 1.41% | 0                      | 0.00% | 3                | 0.61% | 5                        | 1.01% |
| 100003.11-CP 100003.11 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | 0.00% | 0                      | 0.00% | 1                | 0.90% | 2                        | 1.80% |
| Precinct VBM           | 6                     | 0.85% | 2                      | 0.28% | 2                | 0.28% | 4                        | 0.56% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 6                     | 0.71% | 2                      | 0.24% | 3                | 0.36% | 6                        | 0.71% |
| 100004.11-CP 100004.11 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 2                     | 0.85% | 1                      | 0.43% | 0                | 0.00% | 4                        | 1.71% |
| Precinct VBM           | 5                     | 0.75% | 0                      | 0.00% | 5                | 0.75% | 3                        | 0.45% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 7                     | 0.78% | 1                      | 0.11% | 5                | 0.55% | 7                        | 0.78% |
| 100005.11-CP 100005.11 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 1                     | 0.75% | 0                      | 0.00% | 1                | 0.75% | 8                        | 5.97% |
| Precinct VBM           | 10                    | 1.24% | 0                      | 0.00% | 7                | 0.87% | 9                        | 1.12% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 1                        | 5.26% |
| Total                  | 11                    | 1.15% | 0                      | 0.00% | 8                | 0.83% | 18                       | 1.88% |
| 100006.11-CP 100006.11 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 3                     | 1.36% | 0                      | 0.00% | 2                | 0.91% | 4                        | 1.82% |
| Precinct VBM           | 12                    | 1.42% | 3                      | 0.35% | 9                | 1.06% | 7                        | 0.83% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 1                        | 4.76% |
| Total                  | 15                    | 1.38% | 3                      | 0.28% | 11               | 1.01% | 12                       | 1.10% |
| 100008.11-CP 100008.11 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 4                     | 2.30% | 0                      | 0.00% | 1                | 0.57% | 1                        | 0.57% |
| Precinct VBM           | 6                     | 0.93% | 0                      | 0.00% | 5                | 0.77% | 5                        | 0.77% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 10                    | 1.20% | 0                      | 0.00% | 6                | 0.72% | 6                        | 0.72% |
| 100009.11-CP 100009.11 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 1                     | 0.68% | 0                      | 0.00% | 0                | 0.00% | 1                        | 0.68% |
| Precinct VBM           | 1                     | 0.15% | 0                      | 0.00% | 4                | 0.59% | 9                        | 1.33% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 1                        | 5.56% |
| Total                  | 2                     | 0.24% | 0                      | 0.00% | 4                | 0.48% | 11                       | 1.31% |
| 100012.11-CP 100012.11 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 6                     | 2.62% | 0                      | 0.00% | 0                | 0.00% | 2                        | 0.87% |
| Precinct VBM           | 13                    | 1.52% | 1                      | 0.12% | 4                | 0.47% | 14                       | 1.63% |



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|------------------------|-----------------------|--------|------------------------|-------|------------------|-------|--------------------------|-------|
|                        | DON KRAMPE<br>(REP)   |        | JERRY J. LAWS<br>(REP) |       | TOM PALZER (REP) |       | KAREN<br>ROSEBERRY (REP) |       |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 1                        | 3.57% |
| Total                  | 19                    | 1.71%  | 1                      | 0.09% | 4                | 0.36% | 17                       | 1.53% |
| 100013.11-CP 100013.11 |                       |        |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | 0.00%  | 0                      | 0.00% | 2                | 0.90% | 4                        | 1.81% |
| Precinct VBM           | 11                    | 1.36%  | 0                      | 0.00% | 5                | 0.62% | 9                        | 1.11% |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 1                | 4.17% | 0                        | 0.00% |
| Total                  | 11                    | 1.04%  | 0                      | 0.00% | 8                | 0.76% | 13                       | 1.23% |
| 100014.11-CP 100014.11 |                       |        |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 2                        | 1.16% |
| Precinct VBM           | 8                     | 1.28%  | 0                      | 0.00% | 4                | 0.64% | 8                        | 1.28% |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 8                     | 0.98%  | 0                      | 0.00% | 4                | 0.49% | 10                       | 1.22% |
| 100022.11-CP 100022.11 |                       |        |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 1                        | 0.62% |
| Precinct VBM           | 10                    | 1.63%  | 2                      | 0.33% | 3                | 0.49% | 8                        | 1.31% |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 1                     | 10.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 11                    | 1.40%  | 2                      | 0.26% | 3                | 0.38% | 9                        | 1.15% |
| 100023.11-CP 100023.11 |                       |        |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 2                     | 1.04%  | 0                      | 0.00% | 2                | 1.04% | 4                        | 2.07% |
| Precinct VBM           | 19                    | 2.04%  | 0                      | 0.00% | 9                | 0.97% | 15                       | 1.61% |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 1                     | 3.57%  | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 22                    | 1.91%  | 0                      | 0.00% | 11               | 0.95% | 19                       | 1.65% |
| 200003.12-CP 200003.12 |                       |        |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 2                     | 1.35%  | 3                      | 2.03% | 3                | 2.03% | 3                        | 2.03% |
| Precinct VBM           | 12                    | 2.39%  | 1                      | 0.20% | 9                | 1.79% | 7                        | 1.39% |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 1                        | 3.85% |
| Total                  | 14                    | 2.07%  | 4                      | 0.59% | 12               | 1.77% | 11                       | 1.62% |
| 200004.12-CP 200004.12 |                       |        |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 1                     | 0.61%  | 0                      | 0.00% | 2                | 1.21% | 2                        | 1.21% |
| Precinct VBM           | 17                    | 2.82%  | 3                      | 0.50% | 7                | 1.16% | 7                        | 1.16% |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 18                    | 2.30%  | 3                      | 0.38% | 9                | 1.15% | 9                        | 1.15% |
| 200005.02-CP 200005.02 |                       |        |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 2                     | 2.30%  | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct VBM           | 10                    | 2.47%  | 1                      | 0.25% | 6                | 1.48% | 1                        | 0.25% |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00%  | 1                      | 5.26% | 0                | 0.00% | 1                        | 5.26% |
| Total                  | 12                    | 2.35%  | 2                      | 0.39% | 6                | 1.17% | 2                        | 0.39% |
| 200006.12-CP 200006.12 |                       |        |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 3                     | 2.78%  | 0                      | 0.00% | 0                | 0.00% | 1                        | 0.93% |
| Precinct VBM           | 12                    | 2.51%  | 0                      | 0.00% | 7                | 1.46% | 5                        | 1.04% |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 1                        | 3.70% |
| Total                  | 15                    | 2.44%  | 0                      | 0.00% | 7                | 1.14% | 7                        | 1.14% |
| 200008.12-CP 200008.12 |                       |        |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -     |

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|                        | DON KRAMPE<br>(REP)   |        | JERRY J. LAWS<br>(REP) |       | TOM PALZER (REP) |        | KAREN<br>ROSEBERRY (REP) |        |
| Precinct OS            | 1                     | 0.75%  | 0                      | 0.00% | 1                | 0.75%  | 3                        | 2.26%  |
| Precinct VBM           | 6                     | 1.83%  | 0                      | 0.00% | 12               | 3.67%  | 0                        | 0.00%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 1                | 12.50% | 0                        | 0.00%  |
| Total                  | 7                     | 1.50%  | 0                      | 0.00% | 14               | 2.99%  | 3                        | 0.64%  |
| 200010.12-CP 200010.12 |                       |        |                        |       |                  |        |                          |        |
| Precinct TSx           | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00%  | 0                        | 0.00%  |
| Precinct OS            | 3                     | 1.68%  | 1                      | 0.56% | 1                | 0.56%  | 1                        | 0.56%  |
| Precinct VBM           | 7                     | 0.93%  | 0                      | 0.00% | 13               | 1.73%  | 10                       | 1.33%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Provisional            | 1                     | 3.85%  | 0                      | 0.00% | 0                | 0.00%  | 0                        | 0.00%  |
| Total                  | 11                    | 1.14%  | 1                      | 0.10% | 14               | 1.46%  | 11                       | 1.14%  |
| 200012.12-CP 200012.12 |                       |        |                        |       |                  |        |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Precinct OS            | 0                     | 0.00%  | 0                      | 0.00% | 1                | 0.79%  | 2                        | 1.59%  |
| Precinct VBM           | 2                     | 0.50%  | 0                      | 0.00% | 6                | 1.49%  | 4                        | 0.99%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Provisional            | 0                     | 0.00%  | 1                      | 8.33% | 0                | 0.00%  | 0                        | 0.00%  |
| Total                  | 2                     | 0.37%  | 1                      | 0.18% | 7                | 1.29%  | 6                        | 1.11%  |
| 200013.12-CP 200013.12 |                       |        |                        |       |                  |        |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Precinct OS            | 3                     | 1.57%  | 1                      | 0.52% | 1                | 0.52%  | 3                        | 1.57%  |
| Precinct VBM           | 6                     | 0.94%  | 0                      | 0.00% | 6                | 0.94%  | 12                       | 1.88%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00%  | 0                        | 0.00%  |
| Total                  | 9                     | 1.06%  | 1                      | 0.12% | 7                | 0.82%  | 15                       | 1.76%  |
| 200014.12-CP 200014.12 |                       |        |                        |       |                  |        |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Precinct OS            | 2                     | 1.07%  | 0                      | 0.00% | 2                | 1.07%  | 2                        | 1.07%  |
| Precinct VBM           | 8                     | 1.46%  | 2                      | 0.36% | 7                | 1.28%  | 8                        | 1.46%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00%  | 0                        | 0.00%  |
| Total                  | 10                    | 1.31%  | 2                      | 0.26% | 9                | 1.18%  | 10                       | 1.31%  |
| 200015.02-CP 200015.02 |                       |        |                        |       |                  |        |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Precinct OS            | 3                     | 1.54%  | 2                      | 1.03% | 1                | 0.51%  | 1                        | 0.51%  |
| Precinct VBM           | 14                    | 1.77%  | 2                      | 0.25% | 8                | 1.01%  | 10                       | 1.27%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00%  | 0                        | 0.00%  |
| Total                  | 17                    | 1.69%  | 4                      | 0.40% | 9                | 0.90%  | 11                       | 1.09%  |
| 200016.12-CP 200016.12 |                       |        |                        |       |                  |        |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Precinct OS            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00%  | 0                        | 0.00%  |
| Precinct VBM           | 1                     | 0.79%  | 1                      | 0.79% | 3                | 2.38%  | 1                        | 0.79%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00%  | 1                        | 33.33% |
| Total                  | 1                     | 0.67%  | 1                      | 0.67% | 3                | 2.01%  | 2                        | 1.34%  |
| 200016.13-CP 200016.13 |                       |        |                        |       |                  |        |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Precinct OS            | 0                     | 0.00%  | 0                      | 0.00% | 4                | 5.56%  | 1                        | 1.39%  |
| Precinct VBM           | 4                     | 1.36%  | 1                      | 0.34% | 0                | 0.00%  | 5                        | 1.70%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 1                | 16.67% | 0                        | 0.00%  |
| Total                  | 4                     | 1.08%  | 1                      | 0.27% | 5                | 1.34%  | 6                        | 1.61%  |
| 200017.02-CP 200017.02 |                       |        |                        |       |                  |        |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Precinct OS            | 2                     | 1.56%  | 0                      | 0.00% | 3                | 2.34%  | 1                        | 0.78%  |
| Precinct VBM           | 12                    | 2.19%  | 3                      | 0.55% | 4                | 0.73%  | 5                        | 0.91%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -      | 0                        | -      |
| Provisional            | 2                     | 18.18% | 0                      | 0.00% | 0                | 0.00%  | 0                        | 0.00%  |
| Total                  | 16                    | 2.33%  | 3                      | 0.44% | 7                | 1.02%  | 6                        | 0.87%  |

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|                        | DON KRAMPE<br>(REP)   |       | JERRY J. LAWS<br>(REP) |       | TOM PALZER (REP) |       | KAREN<br>ROSEBERRY (REP) |       |
| 200018.02-CP 200018.02 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 3                     | 1.97% | 0                      | 0.00% | 2                | 1.32% | 1                        | 0.66% |
| Precinct VBM           | 12                    | 1.90% | 2                      | 0.32% | 11               | 1.74% | 5                        | 0.79% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 15                    | 1.88% | 2                      | 0.25% | 13               | 1.63% | 6                        | 0.75% |
| 200020.02-CP 200020.02 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 6                     | 4.84% | 1                      | 0.81% | 4                | 3.23% | 6                        | 4.84% |
| Precinct VBM           | 16                    | 3.36% | 5                      | 1.05% | 4                | 0.84% | 3                        | 0.63% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 22                    | 3.57% | 6                      | 0.97% | 8                | 1.30% | 9                        | 1.46% |
| 200021.01-CP 200021.01 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct VBM           | 1                     | 0.56% | 0                      | 0.00% | 1                | 0.56% | 3                        | 1.67% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 1                     | 0.47% | 0                      | 0.00% | 1                | 0.47% | 3                        | 1.40% |
| 200021.02-CP 200021.02 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 3                     | 1.95% | 1                      | 0.65% | 4                | 2.60% | 1                        | 0.65% |
| Precinct VBM           | 13                    | 1.93% | 0                      | 0.00% | 4                | 0.59% | 5                        | 0.74% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 16                    | 1.90% | 1                      | 0.12% | 8                | 0.95% | 6                        | 0.71% |
| 200024.02-CP 200024.02 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 2                     | 3.77% | 0                      | 0.00% | 4                | 7.55% | 1                        | 1.89% |
| Precinct VBM           | 6                     | 2.31% | 0                      | 0.00% | 10               | 3.85% | 7                        | 2.69% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 8                     | 2.52% | 0                      | 0.00% | 14               | 4.40% | 8                        | 2.52% |
| 300001.04-CP 300001.04 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 1                     | 9.09% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct VBM           | 1                     | 1.06% | 0                      | 0.00% | 3                | 3.19% | 3                        | 3.19% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Total                  | 2                     | 1.90% | 0                      | 0.00% | 3                | 2.86% | 3                        | 2.86% |
| 300001.05-CP 300001.05 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | 0.00% | 1                      | 1.47% | 0                | 0.00% | 2                        | 2.94% |
| Precinct VBM           | 3                     | 1.02% | 1                      | 0.34% | 2                | 0.68% | 4                        | 1.36% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 3                     | 0.81% | 2                      | 0.54% | 2                | 0.54% | 6                        | 1.61% |
| 300003.05-CP 300003.05 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | 0.00% | 0                      | 0.00% | 2                | 1.11% | 1                        | 0.56% |
| Precinct VBM           | 13                    | 1.55% | 3                      | 0.36% | 7                | 0.83% | 12                       | 1.43% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 1                     | 4.35% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 14                    | 1.34% | 3                      | 0.29% | 9                | 0.86% | 13                       | 1.25% |
| 300004.05-CP 300004.05 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 2                     | 1.29% | 1                      | 0.65% | 3                | 1.94% | 1                        | 0.65% |
| Precinct VBM           | 5                     | 0.95% | 0                      | 0.00% | 8                | 1.52% | 7                        | 1.33% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |

**COUNTY OF EL DORADO**  
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|------------------------|-----------------------|-------|------------------------|-------|------------------|-------|--------------------------|-------|
|                        | DON KRAMPE<br>(REP)   |       | JERRY J. LAWS<br>(REP) |       | TOM PALZER (REP) |       | KAREN<br>ROSEBERRY (REP) |       |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 7                     | 1.00% | 1                      | 0.14% | 11               | 1.58% | 8                        | 1.15% |
| 300005.05-CP 300005.05 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 1                     | 0.69% | 0                      | 0.00% | 2                | 1.39% | 2                        | 1.39% |
| Precinct VBM           | 7                     | 1.19% | 1                      | 0.17% | 7                | 1.19% | 14                       | 2.37% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 8                     | 1.06% | 1                      | 0.13% | 9                | 1.19% | 16                       | 2.12% |
| 300008.05-CP 300008.05 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 4                     | 3.13% | 0                      | 0.00% | 3                | 2.34% | 1                        | 0.78% |
| Precinct VBM           | 11                    | 1.61% | 1                      | 0.15% | 7                | 1.03% | 13                       | 1.91% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 15                    | 1.83% | 1                      | 0.12% | 10               | 1.22% | 14                       | 1.71% |
| 300009.04-CP 300009.04 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 3                     | 2.73% | 0                      | 0.00% | 4                | 3.64% | 1                        | 0.91% |
| Precinct VBM           | 4                     | 1.16% | 2                      | 0.58% | 4                | 1.16% | 9                        | 2.62% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 7                     | 1.50% | 2                      | 0.43% | 8                | 1.72% | 10                       | 2.15% |
| 300010.04-CP 300010.04 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 4                     | 2.58% | 0                      | 0.00% | 3                | 1.94% | 3                        | 1.94% |
| Precinct VBM           | 12                    | 2.02% | 0                      | 0.00% | 6                | 1.01% | 11                       | 1.85% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 16                    | 2.11% | 0                      | 0.00% | 9                | 1.18% | 14                       | 1.84% |
| 300011.04-CP 300011.04 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 4                     | 3.08% | 0                      | 0.00% | 1                | 0.77% | 1                        | 0.77% |
| Precinct VBM           | 13                    | 2.03% | 1                      | 0.16% | 2                | 0.31% | 7                        | 1.09% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 17                    | 2.15% | 1                      | 0.13% | 3                | 0.38% | 8                        | 1.01% |
| 300013.05-CP 300013.05 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct OS            | 0                     | 0.00% | 0                      | 0.00% | 1                | 0.85% | 3                        | 2.56% |
| Precinct VBM           | 24                    | 3.65% | 3                      | 0.46% | 5                | 0.76% | 13                       | 1.98% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 1                     | 9.09% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 25                    | 3.17% | 3                      | 0.38% | 6                | 0.76% | 16                       | 2.03% |
| 300015.04-CP 300015.04 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct VBM           | 2                     | 3.92% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 2                     | 3.17% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| 300015.05-CP 300015.05 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 1                     | 1.64% | 0                      | 0.00% | 2                | 3.28% | 0                        | 0.00% |
| Precinct VBM           | 0                     | 0.00% | 3                      | 1.32% | 3                | 1.32% | 5                        | 2.19% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 1                     | 0.34% | 3                      | 1.03% | 5                | 1.72% | 5                        | 1.72% |
| 300017.05-CP 300017.05 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 2                     | 1.83% | 1                      | 0.92% | 0                | 0.00% | 4                        | 3.67% |



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|                        | DON KRAMPE<br>(REP)   |        | JERRY J. LAWS<br>(REP) |       | TOM PALZER (REP) |       | KAREN<br>ROSEBERRY (REP) |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 9                     | 4.07%  | 0                      | 0.00% | 2                | 0.90% | 5                        | 2.26%  |
| Precinct VBM           | 12                    | 1.73%  | 3                      | 0.43% | 6                | 0.86% | 9                        | 1.29%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 1                | 3.33% | 0                        | 0.00%  |
| Total                  | 21                    | 2.22%  | 3                      | 0.32% | 9                | 0.95% | 14                       | 1.48%  |
| 400004.14-CP 400004.14 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 3                     | 1.76%  | 0                      | 0.00% | 3                | 1.76% | 7                        | 4.12%  |
| Precinct VBM           | 7                     | 1.15%  | 2                      | 0.33% | 6                | 0.98% | 9                        | 1.48%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Provisional            | 1                     | 5.56%  | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00%  |
| Total                  | 11                    | 1.38%  | 2                      | 0.25% | 9                | 1.13% | 16                       | 2.01%  |
| 400005.08-CP 400005.08 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 0                     | 0.00%  | 0                      | 0.00% | 1                | 1.52% | 0                        | 0.00%  |
| Precinct VBM           | 4                     | 1.72%  | 2                      | 0.86% | 6                | 2.58% | 1                        | 0.43%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Provisional            | 1                     | 14.29% | 0                      | 0.00% | 0                | 0.00% | 1                        | 14.29% |
| Total                  | 5                     | 1.63%  | 2                      | 0.65% | 7                | 2.29% | 2                        | 0.65%  |
| 400006.08-CP 400006.08 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 4                     | 1.90%  | 3                      | 1.43% | 2                | 0.95% | 5                        | 2.38%  |
| Precinct VBM           | 5                     | 0.78%  | 0                      | 0.00% | 1                | 0.16% | 7                        | 1.09%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Provisional            | 1                     | 6.67%  | 0                      | 0.00% | 0                | 0.00% | 1                        | 6.67%  |
| Total                  | 10                    | 1.15%  | 3                      | 0.35% | 3                | 0.35% | 13                       | 1.50%  |
| 400007.08-CP 400007.08 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 3                     | 1.42%  | 0                      | 0.00% | 3                | 1.42% | 4                        | 1.90%  |
| Precinct VBM           | 15                    | 2.12%  | 2                      | 0.28% | 19               | 2.69% | 7                        | 0.99%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 1                | 5.00% | 0                        | 0.00%  |
| Total                  | 18                    | 1.92%  | 2                      | 0.21% | 23               | 2.45% | 11                       | 1.17%  |
| 400010.08-CP 400010.08 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 4                     | 1.81%  | 0                      | 0.00% | 7                | 3.17% | 2                        | 0.90%  |
| Precinct VBM           | 15                    | 1.85%  | 0                      | 0.00% | 5                | 0.62% | 6                        | 0.74%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00%  |
| Total                  | 19                    | 1.80%  | 0                      | 0.00% | 12               | 1.14% | 8                        | 0.76%  |
| 400012.08-CP 400012.08 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 4                     | 3.81%  | 0                      | 0.00% | 3                | 2.86% | 1                        | 0.95%  |
| Precinct VBM           | 8                     | 2.04%  | 0                      | 0.00% | 2                | 0.51% | 5                        | 1.28%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Provisional            | 2                     | 13.33% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00%  |
| Total                  | 14                    | 2.73%  | 0                      | 0.00% | 5                | 0.98% | 6                        | 1.17%  |
| 400013.08-CP 400013.08 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 3                     | 2.80%  | 0                      | 0.00% | 3                | 2.80% | 0                        | 0.00%  |
| Precinct VBM           | 4                     | 1.37%  | 1                      | 0.34% | 1                | 0.34% | 7                        | 2.40%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00%  |
| Total                  | 7                     | 1.74%  | 1                      | 0.25% | 4                | 1.00% | 7                        | 1.74%  |
| 400014.08-CP 400014.08 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 1                     | 0.51%  | 0                      | 0.00% | 1                | 0.51% | 3                        | 1.53%  |
| Precinct VBM           | 7                     | 1.05%  | 0                      | 0.00% | 6                | 0.90% | 9                        | 1.36%  |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 2                | 6.45% | 0                        | 0.00%  |

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|                        | DON KRAMPE<br>(REP)   |       | JERRY J. LAWS<br>(REP) |       | TOM PALZER (REP) |       | KAREN<br>ROSEBERRY (REP) |       |
| Total                  | 8                     | 0.90% | 0                      | 0.00% | 9                | 1.01% | 12                       | 1.35% |
| 400017.08-CP 400017.08 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 2                     | 1.31% | 1                      | 0.65% | 1                | 0.65% | 0                        | 0.00% |
| Precinct VBM           | 12                    | 1.80% | 8                      | 1.20% | 20               | 2.99% | 6                        | 0.90% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 14                    | 1.66% | 9                      | 1.07% | 21               | 2.49% | 6                        | 0.71% |
| 400018.08-CP 400018.08 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 2                     | 2.60% | 0                      | 0.00% | 1                | 1.30% | 0                        | 0.00% |
| Precinct VBM           | 2                     | 0.83% | 2                      | 0.83% | 7                | 2.92% | 4                        | 1.67% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 4                     | 1.25% | 2                      | 0.62% | 8                | 2.49% | 4                        | 1.25% |
| 400020.08-CP 400020.08 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 6                     | 5.71% | 0                      | 0.00% | 4                | 3.81% | 0                        | 0.00% |
| Precinct VBM           | 3                     | 0.97% | 1                      | 0.32% | 4                | 1.29% | 2                        | 0.65% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 9                     | 2.16% | 1                      | 0.24% | 8                | 1.92% | 2                        | 0.48% |
| 400021.07-CP 400021.07 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 3                     | 2.54% | 0                      | 0.00% | 1                | 0.85% | 1                        | 0.85% |
| Precinct VBM           | 9                     | 1.31% | 3                      | 0.44% | 5                | 0.73% | 8                        | 1.17% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 12                    | 1.47% | 3                      | 0.37% | 6                | 0.73% | 9                        | 1.10% |
| 500001.10-CP 500001.10 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | 0.00% | 0                      | 0.00% | 3                | 1.62% | 0                        | 0.00% |
| Precinct VBM           | 5                     | 0.93% | 1                      | 0.19% | 1                | 0.19% | 8                        | 1.49% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 5                     | 0.68% | 1                      | 0.14% | 4                | 0.54% | 8                        | 1.09% |
| 500002.10-CP 500002.10 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 2                     | 1.67% | 0                      | 0.00% | 3                | 2.50% | 1                        | 0.83% |
| Precinct VBM           | 7                     | 1.89% | 0                      | 0.00% | 0                | 0.00% | 2                        | 0.54% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 9                     | 1.74% | 0                      | 0.00% | 3                | 0.58% | 3                        | 0.58% |
| 500003.10-CP 500003.10 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 1                     | 0.84% | 0                      | 0.00% | 0                | 0.00% | 1                        | 0.84% |
| Precinct VBM           | 4                     | 1.32% | 1                      | 0.33% | 0                | 0.00% | 0                        | 0.00% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 5                     | 1.17% | 1                      | 0.23% | 0                | 0.00% | 1                        | 0.23% |
| 500004.10-CP 500004.10 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 1                     | 0.93% | 0                      | 0.00% | 2                | 1.87% | 0                        | 0.00% |
| Precinct VBM           | 7                     | 3.04% | 0                      | 0.00% | 3                | 1.30% | 5                        | 2.17% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 1                        | 5.56% |
| Total                  | 8                     | 2.25% | 0                      | 0.00% | 5                | 1.41% | 6                        | 1.69% |
| 500005.10-CP 500005.10 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | 0.00% | 0                      | 0.00% | 1                | 1.03% | 1                        | 1.03% |
| Precinct VBM           | 4                     | 1.83% | 1                      | 0.46% | 2                | 0.91% | 1                        | 0.46% |

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|------------------------|-----------------------|-------|------------------------|-------|------------------|-------|--------------------------|-------|
|                        | DON KRAMPE<br>(REP)   |       | JERRY J. LAWS<br>(REP) |       | TOM PALZER (REP) |       | KAREN<br>ROSEBERRY (REP) |       |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 4                     | 1.22% | 1                      | 0.30% | 3                | 0.91% | 2                        | 0.61% |
| 500006.10-CP 500006.10 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 2                     | 1.22% | 0                      | 0.00% | 0                | 0.00% | 1                        | 0.61% |
| Precinct VBM           | 7                     | 1.59% | 0                      | 0.00% | 4                | 0.91% | 6                        | 1.37% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 1                        | 2.04% |
| Total                  | 9                     | 1.38% | 0                      | 0.00% | 4                | 0.61% | 8                        | 1.23% |
| 500007.10-CP 500007.10 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 3                     | 1.60% | 0                      | 0.00% | 7                | 3.72% | 2                        | 1.06% |
| Precinct VBM           | 5                     | 0.83% | 1                      | 0.17% | 6                | 1.00% | 11                       | 1.83% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 8                     | 0.99% | 1                      | 0.12% | 13               | 1.61% | 13                       | 1.61% |
| 500009.10-CP 500009.10 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 1                     | 0.93% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct VBM           | 4                     | 1.50% | 0                      | 0.00% | 1                | 0.37% | 4                        | 1.50% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 5                     | 1.23% | 0                      | 0.00% | 1                | 0.25% | 4                        | 0.99% |
| 500011.10-CP 500011.10 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 1                     | 0.61% | 0                      | 0.00% | 1                | 0.61% | 0                        | 0.00% |
| Precinct VBM           | 1                     | 0.30% | 1                      | 0.30% | 3                | 0.89% | 1                        | 0.30% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 2                     | 0.37% | 1                      | 0.19% | 4                | 0.75% | 1                        | 0.19% |
| 500012.10-CP 500012.10 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct OS            | 1                     | 0.59% | 0                      | 0.00% | 4                | 2.35% | 3                        | 1.76% |
| Precinct VBM           | 7                     | 1.29% | 1                      | 0.18% | 3                | 0.55% | 4                        | 0.74% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 1                     | 3.33% | 0                      | 0.00% | 0                | 0.00% | 1                        | 3.33% |
| Total                  | 9                     | 1.21% | 1                      | 0.13% | 7                | 0.94% | 8                        | 1.08% |
| 500015.10-CP 500015.10 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 1                     | 1.04% | 0                      | 0.00% | 1                | 1.04% | 0                        | 0.00% |
| Precinct VBM           | 2                     | 0.54% | 1                      | 0.27% | 1                | 0.27% | 4                        | 1.09% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 3                     | 0.63% | 1                      | 0.21% | 2                | 0.42% | 4                        | 0.85% |
| 500020.10-CP 500020.10 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 1                     | 0.71% | 1                      | 0.71% | 3                | 2.13% | 0                        | 0.00% |
| Precinct VBM           | 11                    | 1.81% | 4                      | 0.66% | 5                | 0.82% | 9                        | 1.48% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 1                     | 3.85% | 1                      | 3.85% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 13                    | 1.68% | 6                      | 0.78% | 8                | 1.03% | 9                        | 1.16% |
| 500021.10-CP 500021.10 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 2                     | 1.34% | 0                      | 0.00% | 0                | 0.00% | 4                        | 2.68% |
| Precinct VBM           | 9                     | 1.57% | 1                      | 0.17% | 5                | 0.87% | 5                        | 0.87% |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 1                        | 4.00% |
| Total                  | 11                    | 1.47% | 1                      | 0.13% | 5                | 0.67% | 10                       | 1.34% |
| 95U201.01-MB 95U201.01 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |



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|                        | DON KRAMPE<br>(REP)   |        | JERRY J. LAWS<br>(REP) |       | TOM PALZER (REP) |       | KAREN<br>ROSEBERRY (REP) |        |
| Precinct OS            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct VBM           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| MB Precinct            | 1                     | 14.29% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00%  |
| Provisional            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Total                  | 1                     | 14.29% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00%  |
| 95U201.02-MB 95U201.02 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct VBM           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| MB Precinct            | 3                     | 0.99%  | 0                      | 0.00% | 2                | 0.66% | 6                        | 1.97%  |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00%  |
| Total                  | 3                     | 0.98%  | 0                      | 0.00% | 2                | 0.66% | 6                        | 1.97%  |
| 95U202.02-MB 95U202.02 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct VBM           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| MB Precinct            | 10                    | 1.98%  | 2                      | 0.40% | 2                | 0.40% | 16                       | 3.17%  |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00%  |
| Total                  | 10                    | 1.97%  | 2                      | 0.39% | 2                | 0.39% | 16                       | 3.15%  |
| 95U203.02-MB 95U203.02 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct VBM           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| MB Precinct            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Provisional            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Total                  | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| 95U204.01-MB 95U204.01 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct VBM           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| MB Precinct            | 9                     | 1.51%  | 0                      | 0.00% | 7                | 1.17% | 13                       | 2.18%  |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00%  |
| Total                  | 9                     | 1.50%  | 0                      | 0.00% | 7                | 1.17% | 13                       | 2.17%  |
| 95U301.05-MB 95U301.05 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct VBM           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| MB Precinct            | 1                     | 0.74%  | 0                      | 0.00% | 0                | 0.00% | 3                        | 2.21%  |
| Provisional            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 1                        | 50.00% |
| Total                  | 1                     | 0.72%  | 0                      | 0.00% | 0                | 0.00% | 4                        | 2.90%  |
| 95U302.04-MB 95U302.04 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct VBM           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| MB Precinct            | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00%  |
| Provisional            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Total                  | 0                     | 0.00%  | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00%  |
| 95U302.05-MB 95U302.05 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct VBM           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| MB Precinct            | 1                     | 1.23%  | 0                      | 0.00% | 2                | 2.47% | 5                        | 6.17%  |
| Provisional            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Total                  | 1                     | 1.23%  | 0                      | 0.00% | 2                | 2.47% | 5                        | 6.17%  |
| 95U303.03-MB 95U303.03 |                       |        |                        |       |                  |       |                          |        |
| Precinct TSx           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct OS            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Precinct VBM           | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| MB Precinct            | 0                     | 0.00%  | 2                      | 2.56% | 0                | 0.00% | 0                        | 0.00%  |
| Provisional            | 0                     | -      | 0                      | -     | 0                | -     | 0                        | -      |
| Total                  | 0                     | 0.00%  | 2                      | 2.56% | 0                | 0.00% | 0                        | 0.00%  |

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|                        | DON KRAMPE<br>(REP)   |       | JERRY J. LAWS<br>(REP) |       | TOM PALZER (REP) |       | KAREN<br>ROSEBERRY (REP) |       |
| 95U303.05-MB 95U303.05 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct            | 4                     | 4.21% | 1                      | 1.05% | 4                | 4.21% | 1                        | 1.05% |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 4                     | 4.17% | 1                      | 1.04% | 4                | 4.17% | 1                        | 1.04% |
| 95U304.05-MB 95U304.05 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Total                  | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| 95U305.05-MB 95U305.05 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Provisional            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Total                  | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| 95U401.08-MB 95U401.08 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Provisional            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Total                  | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| 95U402.07-MB 95U402.07 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct            | 3                     | 1.28% | 0                      | 0.00% | 2                | 0.85% | 4                        | 1.71% |
| Provisional            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Total                  | 3                     | 1.28% | 0                      | 0.00% | 2                | 0.85% | 4                        | 1.71% |
| 95U402.08-MB 95U402.08 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct            | 16                    | 3.17% | 0                      | 0.00% | 3                | 0.60% | 5                        | 0.99% |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 16                    | 3.16% | 0                      | 0.00% | 3                | 0.59% | 5                        | 0.99% |
| 95U403.08-MB 95U403.08 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct            | 19                    | 2.01% | 6                      | 0.63% | 8                | 0.85% | 6                        | 0.63% |
| Provisional            | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                  | 19                    | 1.99% | 6                      | 0.63% | 8                | 0.84% | 6                        | 0.63% |
| 95U404.06-MB 95U404.06 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Provisional            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Total                  | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| 95U404.08-MB 95U404.08 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM           | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct            | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                               | United States Senator |       |                        |       |                  |       |                          |       |
|-------------------------------|-----------------------|-------|------------------------|-------|------------------|-------|--------------------------|-------|
|                               | DON KRAMPE<br>(REP)   |       | JERRY J. LAWS<br>(REP) |       | TOM PALZER (REP) |       | KAREN<br>ROSEBERRY (REP) |       |
| Provisional                   | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Total                         | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| 95U501.09-MB 95U501.09        |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                  | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS                   | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM                  | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct                   | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Provisional                   | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Total                         | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| 95U501.10-MB 95U501.10        |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                  | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS                   | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM                  | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct                   | 2                     | 1.25% | 1                      | 0.63% | 0                | 0.00% | 2                        | 1.25% |
| Provisional                   | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                         | 2                     | 1.23% | 1                      | 0.62% | 0                | 0.00% | 2                        | 1.23% |
| 95U598.10-MB 95U598.10        |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                  | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS                   | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM                  | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct                   | 2                     | 0.93% | 0                      | 0.00% | 2                | 0.93% | 0                        | 0.00% |
| Provisional                   | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                         | 2                     | 0.93% | 0                      | 0.00% | 2                | 0.93% | 0                        | 0.00% |
| 95U599.10-MB 95U599.10        |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                  | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS                   | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM                  | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct                   | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Provisional                   | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Total                         | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| 96U101.11-MB 96U101.11        |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                  | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS                   | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM                  | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct                   | 3                     | 0.82% | 1                      | 0.27% | 1                | 0.27% | 5                        | 1.36% |
| Provisional                   | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                         | 3                     | 0.80% | 1                      | 0.27% | 1                | 0.27% | 5                        | 1.34% |
| 96U401.14-MB 96U401.14        |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                  | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS                   | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct VBM                  | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| MB Precinct                   | 2                     | 0.62% | 0                      | 0.00% | 2                | 0.62% | 2                        | 0.62% |
| Provisional                   | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Total                         | 2                     | 0.61% | 0                      | 0.00% | 2                | 0.61% | 2                        | 0.61% |
| Total                         |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                  | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct OS                   | 170                   | 1.59% | 22                     | 0.21% | 130              | 1.21% | 143                      | 1.34% |
| Precinct VBM                  | 633                   | 1.55% | 103                    | 0.25% | 404              | 0.99% | 525                      | 1.28% |
| MB Precinct                   | 76                    | 1.65% | 13                     | 0.28% | 35               | 0.76% | 68                       | 1.48% |
| Provisional                   | 15                    | 1.14% | 3                      | 0.23% | 7                | 0.53% | 19                       | 1.45% |
| Total                         | 894                   | 1.55% | 141                    | 0.25% | 576              | 1.00% | 755                      | 1.31% |
| STATE BOARD OF EQUALIZATION   |                       |       |                        |       |                  |       |                          |       |
| STATE BOARD OF EQUALIZATION 1 |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                  | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct OS                   | 170                   | 1.59% | 22                     | 0.21% | 130              | 1.21% | 143                      | 1.34% |
| Precinct VBM                  | 633                   | 1.55% | 103                    | 0.25% | 404              | 0.99% | 525                      | 1.28% |
| MB Precinct                   | 76                    | 1.65% | 13                     | 0.28% | 35               | 0.76% | 68                       | 1.48% |
| Provisional                   | 15                    | 1.14% | 3                      | 0.23% | 7                | 0.53% | 19                       | 1.45% |
| Total                         | 894                   | 1.55% | 141                    | 0.25% | 576              | 1.00% | 755                      | 1.31% |
| Total                         |                       |       |                        |       |                  |       |                          |       |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                                     | United States Senator |              |                        |              |                  |              |                          |              |
|-------------------------------------|-----------------------|--------------|------------------------|--------------|------------------|--------------|--------------------------|--------------|
|                                     | DON KRAMPE<br>(REP)   |              | JERRY J. LAWS<br>(REP) |              | TOM PALZER (REP) |              | KAREN<br>ROSEBERRY (REP) |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                      | 0.00%        | 0                | 0.00%        | 0                        | 0.00%        |
| Precinct OS                         | 170                   | 1.59%        | 22                     | 0.21%        | 130              | 1.21%        | 143                      | 1.34%        |
| Precinct VBM                        | 633                   | 1.55%        | 103                    | 0.25%        | 404              | 0.99%        | 525                      | 1.28%        |
| MB Precinct                         | 76                    | 1.65%        | 13                     | 0.28%        | 35               | 0.76%        | 68                       | 1.48%        |
| Provisional                         | 15                    | 1.14%        | 3                      | 0.23%        | 7                | 0.53%        | 19                       | 1.45%        |
| <b>Total</b>                        | <b>894</b>            | <b>1.55%</b> | <b>141</b>             | <b>0.25%</b> | <b>576</b>       | <b>1.00%</b> | <b>755</b>               | <b>1.31%</b> |
| <b>UNITED STATES SENATE</b>         |                       |              |                        |              |                  |              |                          |              |
| <b>UNITED STATES SENATE</b>         |                       |              |                        |              |                  |              |                          |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                      | 0.00%        | 0                | 0.00%        | 0                        | 0.00%        |
| Precinct OS                         | 170                   | 1.59%        | 22                     | 0.21%        | 130              | 1.21%        | 143                      | 1.34%        |
| Precinct VBM                        | 633                   | 1.55%        | 103                    | 0.25%        | 404              | 0.99%        | 525                      | 1.28%        |
| MB Precinct                         | 76                    | 1.65%        | 13                     | 0.28%        | 35               | 0.76%        | 68                       | 1.48%        |
| Provisional                         | 15                    | 1.14%        | 3                      | 0.23%        | 7                | 0.53%        | 19                       | 1.45%        |
| <b>Total</b>                        | <b>894</b>            | <b>1.55%</b> | <b>141</b>             | <b>0.25%</b> | <b>576</b>       | <b>1.00%</b> | <b>755</b>               | <b>1.31%</b> |
| <b>Total</b>                        |                       |              |                        |              |                  |              |                          |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                      | 0.00%        | 0                | 0.00%        | 0                        | 0.00%        |
| Precinct OS                         | 170                   | 1.59%        | 22                     | 0.21%        | 130              | 1.21%        | 143                      | 1.34%        |
| Precinct VBM                        | 633                   | 1.55%        | 103                    | 0.25%        | 404              | 0.99%        | 525                      | 1.28%        |
| MB Precinct                         | 76                    | 1.65%        | 13                     | 0.28%        | 35               | 0.76%        | 68                       | 1.48%        |
| Provisional                         | 15                    | 1.14%        | 3                      | 0.23%        | 7                | 0.53%        | 19                       | 1.45%        |
| <b>Total</b>                        | <b>894</b>            | <b>1.55%</b> | <b>141</b>             | <b>0.25%</b> | <b>576</b>       | <b>1.00%</b> | <b>755</b>               | <b>1.31%</b> |
| <b>Total</b>                        |                       |              |                        |              |                  |              |                          |              |
| <b>UNITED STATES REPRESENTATIVE</b> |                       |              |                        |              |                  |              |                          |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                       |              |                        |              |                  |              |                          |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                      | 0.00%        | 0                | 0.00%        | 0                        | 0.00%        |
| Precinct OS                         | 170                   | 1.59%        | 22                     | 0.21%        | 130              | 1.21%        | 143                      | 1.34%        |
| Precinct VBM                        | 633                   | 1.55%        | 103                    | 0.25%        | 404              | 0.99%        | 525                      | 1.28%        |
| MB Precinct                         | 76                    | 1.65%        | 13                     | 0.28%        | 35               | 0.76%        | 68                       | 1.48%        |
| Provisional                         | 15                    | 1.14%        | 3                      | 0.23%        | 7                | 0.53%        | 19                       | 1.45%        |
| <b>Total</b>                        | <b>894</b>            | <b>1.55%</b> | <b>141</b>             | <b>0.25%</b> | <b>576</b>       | <b>1.00%</b> | <b>755</b>               | <b>1.31%</b> |
| <b>Total</b>                        |                       |              |                        |              |                  |              |                          |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                      | 0.00%        | 0                | 0.00%        | 0                        | 0.00%        |
| Precinct OS                         | 170                   | 1.59%        | 22                     | 0.21%        | 130              | 1.21%        | 143                      | 1.34%        |
| Precinct VBM                        | 633                   | 1.55%        | 103                    | 0.25%        | 404              | 0.99%        | 525                      | 1.28%        |
| MB Precinct                         | 76                    | 1.65%        | 13                     | 0.28%        | 35               | 0.76%        | 68                       | 1.48%        |
| Provisional                         | 15                    | 1.14%        | 3                      | 0.23%        | 7                | 0.53%        | 19                       | 1.45%        |
| <b>Total</b>                        | <b>894</b>            | <b>1.55%</b> | <b>141</b>             | <b>0.25%</b> | <b>576</b>       | <b>1.00%</b> | <b>755</b>               | <b>1.31%</b> |
| <b>Total</b>                        |                       |              |                        |              |                  |              |                          |              |
| <b>STATE SENATE</b>                 |                       |              |                        |              |                  |              |                          |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                       |              |                        |              |                  |              |                          |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                      | 0.00%        | 0                | 0.00%        | 0                        | 0.00%        |
| Precinct OS                         | 170                   | 1.59%        | 22                     | 0.21%        | 130              | 1.21%        | 143                      | 1.34%        |
| Precinct VBM                        | 633                   | 1.55%        | 103                    | 0.25%        | 404              | 0.99%        | 525                      | 1.28%        |
| MB Precinct                         | 76                    | 1.65%        | 13                     | 0.28%        | 35               | 0.76%        | 68                       | 1.48%        |
| Provisional                         | 15                    | 1.14%        | 3                      | 0.23%        | 7                | 0.53%        | 19                       | 1.45%        |
| <b>Total</b>                        | <b>894</b>            | <b>1.55%</b> | <b>141</b>             | <b>0.25%</b> | <b>576</b>       | <b>1.00%</b> | <b>755</b>               | <b>1.31%</b> |
| <b>Total</b>                        |                       |              |                        |              |                  |              |                          |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                      | 0.00%        | 0                | 0.00%        | 0                        | 0.00%        |
| Precinct OS                         | 170                   | 1.59%        | 22                     | 0.21%        | 130              | 1.21%        | 143                      | 1.34%        |
| Precinct VBM                        | 633                   | 1.55%        | 103                    | 0.25%        | 404              | 0.99%        | 525                      | 1.28%        |
| MB Precinct                         | 76                    | 1.65%        | 13                     | 0.28%        | 35               | 0.76%        | 68                       | 1.48%        |
| Provisional                         | 15                    | 1.14%        | 3                      | 0.23%        | 7                | 0.53%        | 19                       | 1.45%        |
| <b>Total</b>                        | <b>894</b>            | <b>1.55%</b> | <b>141</b>             | <b>0.25%</b> | <b>576</b>       | <b>1.00%</b> | <b>755</b>               | <b>1.31%</b> |
| <b>Total</b>                        |                       |              |                        |              |                  |              |                          |              |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                       |              |                        |              |                  |              |                          |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                       |              |                        |              |                  |              |                          |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                      | 0.00%        | 0                | 0.00%        | 0                        | 0.00%        |
| Precinct OS                         | 127                   | 1.86%        | 13                     | 0.19%        | 100              | 1.47%        | 82                       | 1.20%        |
| Precinct VBM                        | 426                   | 1.68%        | 83                     | 0.33%        | 256              | 1.01%        | 343                      | 1.35%        |
| MB Precinct                         | 71                    | 1.81%        | 12                     | 0.31%        | 32               | 0.82%        | 61                       | 1.56%        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                                   | United States Senator |       |                        |       |                  |       |                          |       |
|-----------------------------------|-----------------------|-------|------------------------|-------|------------------|-------|--------------------------|-------|
|                                   | DON KRAMPE<br>(REP)   |       | JERRY J. LAWS<br>(REP) |       | TOM PALZER (REP) |       | KAREN<br>ROSEBERRY (REP) |       |
| Provisional                       | 11                    | 1.28% | 2                      | 0.23% | 4                | 0.47% | 11                       | 1.28% |
| Total                             | 635                   | 1.72% | 110                    | 0.30% | 392              | 1.06% | 497                      | 1.34% |
| <b>6TH STATE ASSEMBLY DIST</b>    |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                      | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct OS                       | 43                    | 1.11% | 9                      | 0.23% | 30               | 0.77% | 61                       | 1.57% |
| Precinct VBM                      | 207                   | 1.34% | 20                     | 0.13% | 148              | 0.96% | 182                      | 1.18% |
| MB Precinct                       | 5                     | 0.73% | 1                      | 0.15% | 3                | 0.44% | 7                        | 1.02% |
| Provisional                       | 4                     | 0.88% | 1                      | 0.22% | 3                | 0.66% | 8                        | 1.76% |
| Total                             | 259                   | 1.26% | 31                     | 0.15% | 184              | 0.90% | 258                      | 1.26% |
| <b>Total</b>                      |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                      | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct OS                       | 170                   | 1.59% | 22                     | 0.21% | 130              | 1.21% | 143                      | 1.34% |
| Precinct VBM                      | 633                   | 1.55% | 103                    | 0.25% | 404              | 0.99% | 525                      | 1.28% |
| MB Precinct                       | 76                    | 1.65% | 13                     | 0.28% | 35               | 0.76% | 68                       | 1.48% |
| Provisional                       | 15                    | 1.14% | 3                      | 0.23% | 7                | 0.53% | 19                       | 1.45% |
| Total                             | 894                   | 1.55% | 141                    | 0.25% | 576              | 1.00% | 755                      | 1.31% |
| <b>SUPERVISORIAL DISTRICTS</b>    |                       |       |                        |       |                  |       |                          |       |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                      | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS                       | 20                    | 0.90% | 1                      | 0.04% | 11               | 0.49% | 35                       | 1.57% |
| Precinct VBM                      | 112                   | 1.21% | 8                      | 0.09% | 66               | 0.71% | 102                      | 1.10% |
| MB Precinct                       | 3                     | 0.82% | 1                      | 0.27% | 1                | 0.27% | 5                        | 1.36% |
| Provisional                       | 2                     | 0.82% | 0                      | 0.00% | 1                | 0.41% | 4                        | 1.65% |
| Total                             | 137                   | 1.13% | 10                     | 0.08% | 79               | 0.65% | 146                      | 1.20% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                      | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct OS                       | 36                    | 1.60% | 9                      | 0.40% | 33               | 1.47% | 29                       | 1.29% |
| Precinct VBM                      | 159                   | 1.84% | 21                     | 0.24% | 118              | 1.37% | 98                       | 1.13% |
| MB Precinct                       | 23                    | 1.63% | 2                      | 0.14% | 11               | 0.78% | 35                       | 2.48% |
| Provisional                       | 3                     | 1.08% | 2                      | 0.72% | 2                | 0.72% | 4                        | 1.44% |
| Total                             | 221                   | 1.76% | 34                     | 0.27% | 164              | 1.30% | 166                      | 1.32% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                      | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct OS                       | 40                    | 1.89% | 4                      | 0.19% | 27               | 1.27% | 36                       | 1.70% |
| Precinct VBM                      | 155                   | 1.71% | 35                     | 0.39% | 89               | 0.98% | 163                      | 1.80% |
| MB Precinct                       | 6                     | 1.51% | 3                      | 0.75% | 6                | 1.51% | 9                        | 2.26% |
| Provisional                       | 3                     | 1.27% | 0                      | 0.00% | 0                | 0.00% | 4                        | 1.69% |
| Total                             | 204                   | 1.73% | 42                     | 0.36% | 122              | 1.03% | 212                      | 1.79% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                      | 0                     | -     | 0                      | -     | 0                | -     | 0                        | -     |
| Precinct OS                       | 58                    | 2.53% | 7                      | 0.31% | 34               | 1.48% | 30                       | 1.31% |
| Precinct VBM                      | 134                   | 1.58% | 27                     | 0.32% | 97               | 1.14% | 102                      | 1.20% |
| MB Precinct                       | 40                    | 1.98% | 6                      | 0.30% | 15               | 0.74% | 17                       | 0.84% |
| Provisional                       | 5                     | 1.96% | 0                      | 0.00% | 4                | 1.57% | 3                        | 1.18% |
| Total                             | 237                   | 1.81% | 40                     | 0.31% | 150              | 1.15% | 152                      | 1.16% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                      | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct OS                       | 16                    | 0.89% | 1                      | 0.06% | 25               | 1.38% | 13                       | 0.72% |
| Precinct VBM                      | 73                    | 1.35% | 12                     | 0.22% | 34               | 0.63% | 60                       | 1.11% |
| MB Precinct                       | 4                     | 0.98% | 1                      | 0.24% | 2                | 0.49% | 2                        | 0.49% |
| Provisional                       | 2                     | 0.66% | 1                      | 0.33% | 0                | 0.00% | 4                        | 1.32% |
| Total                             | 95                    | 1.20% | 15                     | 0.19% | 61               | 0.77% | 79                       | 1.00% |
| <b>Total</b>                      |                       |       |                        |       |                  |       |                          |       |
| Precinct TSx                      | 0                     | 0.00% | 0                      | 0.00% | 0                | 0.00% | 0                        | 0.00% |
| Precinct OS                       | 170                   | 1.59% | 22                     | 0.21% | 130              | 1.21% | 143                      | 1.34% |
| Precinct VBM                      | 633                   | 1.55% | 103                    | 0.25% | 404              | 0.99% | 525                      | 1.28% |
| MB Precinct                       | 76                    | 1.65% | 13                     | 0.28% | 35               | 0.76% | 68                       | 1.48% |
| Provisional                       | 15                    | 1.14% | 3                      | 0.23% | 7                | 0.53% | 19                       | 1.45% |
| Total                             | 894                   | 1.55% | 141                    | 0.25% | 576              | 1.00% | 755                      | 1.31% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                                 | United States Senator |              |                        |              |                  |              |                          |              |
|---------------------------------|-----------------------|--------------|------------------------|--------------|------------------|--------------|--------------------------|--------------|
|                                 | DON KRAMPE<br>(REP)   |              | JERRY J. LAWS<br>(REP) |              | TOM PALZER (REP) |              | KAREN<br>ROSEBERRY (REP) |              |
| <b>CITIES</b>                   |                       |              |                        |              |                  |              |                          |              |
| <b>CITY OF PLACERVILLE</b>      |                       |              |                        |              |                  |              |                          |              |
| Precinct TSx                    | 0                     | -            | 0                      | -            | 0                | -            | 0                        | -            |
| Precinct OS                     | 4                     | 0.72%        | 2                      | 0.36%        | 7                | 1.25%        | 6                        | 1.08%        |
| Precinct VBM                    | 29                    | 1.24%        | 5                      | 0.21%        | 27               | 1.15%        | 40                       | 1.71%        |
| MB Precinct                     | 0                     | -            | 0                      | -            | 0                | -            | 0                        | -            |
| Provisional                     | 1                     | 1.43%        | 0                      | 0.00%        | 0                | 0.00%        | 0                        | 0.00%        |
| <b>Total</b>                    | <b>34</b>             | <b>1.14%</b> | <b>7</b>               | <b>0.24%</b> | <b>34</b>        | <b>1.14%</b> | <b>46</b>                | <b>1.55%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                       |              |                        |              |                  |              |                          |              |
| Precinct TSx                    | 0                     | -            | 0                      | -            | 0                | -            | 0                        | -            |
| Precinct OS                     | 10                    | 1.06%        | 0                      | 0.00%        | 14               | 1.48%        | 5                        | 0.53%        |
| Precinct VBM                    | 35                    | 1.42%        | 3                      | 0.12%        | 19               | 0.77%        | 30                       | 1.22%        |
| MB Precinct                     | 0                     | -            | 0                      | -            | 0                | -            | 0                        | -            |
| Provisional                     | 0                     | 0.00%        | 0                      | 0.00%        | 0                | 0.00%        | 2                        | 1.08%        |
| <b>Total</b>                    | <b>45</b>             | <b>1.25%</b> | <b>3</b>               | <b>0.08%</b> | <b>33</b>        | <b>0.92%</b> | <b>37</b>                | <b>1.03%</b> |
| <b>UNINCORPORATED AREA</b>      |                       |              |                        |              |                  |              |                          |              |
| Precinct TSx                    | 0                     | 0.00%        | 0                      | 0.00%        | 0                | 0.00%        | 0                        | 0.00%        |
| Precinct OS                     | 156                   | 1.70%        | 20                     | 0.22%        | 109              | 1.19%        | 132                      | 1.44%        |
| Precinct VBM                    | 569                   | 1.58%        | 95                     | 0.26%        | 358              | 0.99%        | 455                      | 1.26%        |
| MB Precinct                     | 76                    | 1.65%        | 13                     | 0.28%        | 35               | 0.76%        | 68                       | 1.48%        |
| Provisional                     | 14                    | 1.32%        | 3                      | 0.28%        | 7                | 0.66%        | 17                       | 1.61%        |
| <b>Total</b>                    | <b>815</b>            | <b>1.60%</b> | <b>131</b>             | <b>0.26%</b> | <b>509</b>       | <b>1.00%</b> | <b>672</b>               | <b>1.32%</b> |
| <b>Total</b>                    |                       |              |                        |              |                  |              |                          |              |
| Precinct TSx                    | 0                     | 0.00%        | 0                      | 0.00%        | 0                | 0.00%        | 0                        | 0.00%        |
| Precinct OS                     | 170                   | 1.59%        | 22                     | 0.21%        | 130              | 1.21%        | 143                      | 1.34%        |
| Precinct VBM                    | 633                   | 1.55%        | 103                    | 0.25%        | 404              | 0.99%        | 525                      | 1.28%        |
| MB Precinct                     | 76                    | 1.65%        | 13                     | 0.28%        | 35               | 0.76%        | 68                       | 1.48%        |
| Provisional                     | 15                    | 1.14%        | 3                      | 0.23%        | 7                | 0.53%        | 19                       | 1.45%        |
| <b>Total</b>                    | <b>894</b>            | <b>1.55%</b> | <b>141</b>             | <b>0.25%</b> | <b>576</b>       | <b>1.00%</b> | <b>755</b>               | <b>1.31%</b> |

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United States Senator

|                        | DUF SUNDHEIM (REP) |        | PHIL WYMAN (REP) |       | GEORGE C. YANG (REP) |       | PAMELA ELIZONDO (GRN) |        |
|------------------------|--------------------|--------|------------------|-------|----------------------|-------|-----------------------|--------|
| Jurisdiction Wide      |                    |        |                  |       |                      |       |                       |        |
| 100001.11-CP 100001.11 |                    |        |                  |       |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Precinct OS            | 27                 | 17.20% | 13               | 8.28% | 1                    | 0.64% | 1                     | 0.64%  |
| Precinct VBM           | 138                | 19.63% | 33               | 4.69% | 9                    | 1.28% | 4                     | 0.57%  |
| MB Precinct            | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Provisional            | 2                  | 12.50% | 1                | 6.25% | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 167                | 19.06% | 47               | 5.37% | 10                   | 1.14% | 5                     | 0.57%  |
| 100002.11-CP 100002.11 |                    |        |                  |       |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Precinct OS            | 8                  | 10.53% | 5                | 6.58% | 0                    | 0.00% | 1                     | 1.32%  |
| Precinct VBM           | 75                 | 18.61% | 14               | 3.47% | 6                    | 1.49% | 1                     | 0.25%  |
| MB Precinct            | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Provisional            | 1                  | 6.25%  | 0                | 0.00% | 0                    | 0.00% | 1                     | 6.25%  |
| Total                  | 84                 | 16.97% | 19               | 3.84% | 6                    | 1.21% | 3                     | 0.61%  |
| 100003.11-CP 100003.11 |                    |        |                  |       |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Precinct OS            | 23                 | 20.72% | 9                | 8.11% | 1                    | 0.90% | 0                     | 0.00%  |
| Precinct VBM           | 119                | 16.81% | 29               | 4.10% | 5                    | 0.71% | 2                     | 0.28%  |
| MB Precinct            | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Provisional            | 2                  | 9.09%  | 0                | 0.00% | 1                    | 4.55% | 1                     | 4.55%  |
| Total                  | 144                | 17.12% | 38               | 4.52% | 7                    | 0.83% | 3                     | 0.36%  |
| 100004.11-CP 100004.11 |                    |        |                  |       |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Precinct OS            | 28                 | 11.97% | 12               | 5.13% | 6                    | 2.56% | 0                     | 0.00%  |
| Precinct VBM           | 105                | 15.77% | 50               | 7.51% | 4                    | 0.60% | 3                     | 0.45%  |
| MB Precinct            | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Provisional            | 0                  | 0.00%  | 0                | 0.00% | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 133                | 14.75% | 62               | 6.87% | 10                   | 1.11% | 3                     | 0.33%  |
| 100005.11-CP 100005.11 |                    |        |                  |       |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Precinct OS            | 25                 | 18.66% | 7                | 5.22% | 2                    | 1.49% | 3                     | 2.24%  |
| Precinct VBM           | 170                | 21.07% | 39               | 4.83% | 11                   | 1.36% | 3                     | 0.37%  |
| MB Precinct            | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Provisional            | 4                  | 21.05% | 1                | 5.26% | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 199                | 20.73% | 47               | 4.90% | 13                   | 1.35% | 6                     | 0.63%  |
| 100006.11-CP 100006.11 |                    |        |                  |       |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Precinct OS            | 29                 | 13.18% | 13               | 5.91% | 4                    | 1.82% | 0                     | 0.00%  |
| Precinct VBM           | 133                | 15.72% | 46               | 5.44% | 14                   | 1.65% | 4                     | 0.47%  |
| MB Precinct            | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Provisional            | 5                  | 23.81% | 0                | 0.00% | 0                    | 0.00% | 2                     | 9.52%  |
| Total                  | 167                | 15.36% | 59               | 5.43% | 18                   | 1.66% | 6                     | 0.55%  |
| 100008.11-CP 100008.11 |                    |        |                  |       |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Precinct OS            | 12                 | 6.90%  | 8                | 4.60% | 3                    | 1.72% | 1                     | 0.57%  |
| Precinct VBM           | 86                 | 13.31% | 36               | 5.57% | 11                   | 1.70% | 3                     | 0.46%  |
| MB Precinct            | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Provisional            | 1                  | 9.09%  | 1                | 9.09% | 0                    | 0.00% | 1                     | 9.09%  |
| Total                  | 99                 | 11.91% | 45               | 5.42% | 14                   | 1.68% | 5                     | 0.60%  |
| 100009.11-CP 100009.11 |                    |        |                  |       |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Precinct OS            | 22                 | 15.07% | 6                | 4.11% | 2                    | 1.37% | 0                     | 0.00%  |
| Precinct VBM           | 122                | 18.05% | 53               | 7.84% | 9                    | 1.33% | 3                     | 0.44%  |
| MB Precinct            | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Provisional            | 2                  | 11.11% | 0                | 0.00% | 0                    | 0.00% | 2                     | 11.11% |
| Total                  | 146                | 17.38% | 59               | 7.02% | 11                   | 1.31% | 5                     | 0.60%  |
| 100012.11-CP 100012.11 |                    |        |                  |       |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -     | 0                    | -     | 0                     | -      |
| Precinct OS            | 31                 | 13.54% | 12               | 5.24% | 2                    | 0.87% | 1                     | 0.44%  |
| Precinct VBM           | 160                | 18.67% | 57               | 6.65% | 10                   | 1.17% | 10                    | 1.17%  |

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|                        | United States Senator |        |                  |        |                      |        |                       |        |
|------------------------|-----------------------|--------|------------------|--------|----------------------|--------|-----------------------|--------|
|                        | DUF SUNDHEIM (REP)    |        | PHIL WYMAN (REP) |        | GEORGE C. YANG (REP) |        | PAMELA ELIZONDO (GRN) |        |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Provisional            | 6                     | 21.43% | 5                | 17.86% | 0                    | 0.00%  | 1                     | 3.57%  |
| Total                  | 197                   | 17.68% | 74               | 6.64%  | 12                   | 1.08%  | 12                    | 1.08%  |
| 100013.11-CP 100013.11 |                       |        |                  |        |                      |        |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Precinct OS            | 29                    | 13.12% | 19               | 8.60%  | 0                    | 0.00%  | 0                     | 0.00%  |
| Precinct VBM           | 131                   | 16.19% | 50               | 6.18%  | 7                    | 0.87%  | 3                     | 0.37%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Provisional            | 1                     | 4.17%  | 0                | 0.00%  | 1                    | 4.17%  | 0                     | 0.00%  |
| Total                  | 161                   | 15.28% | 69               | 6.55%  | 8                    | 0.76%  | 3                     | 0.28%  |
| 100014.11-CP 100014.11 |                       |        |                  |        |                      |        |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Precinct OS            | 31                    | 17.92% | 7                | 4.05%  | 0                    | 0.00%  | 1                     | 0.58%  |
| Precinct VBM           | 105                   | 16.80% | 47               | 7.52%  | 7                    | 1.12%  | 0                     | 0.00%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Provisional            | 5                     | 22.73% | 0                | 0.00%  | 0                    | 0.00%  | 0                     | 0.00%  |
| Total                  | 141                   | 17.20% | 54               | 6.59%  | 7                    | 0.85%  | 1                     | 0.12%  |
| 100022.11-CP 100022.11 |                       |        |                  |        |                      |        |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Precinct OS            | 22                    | 13.66% | 13               | 8.07%  | 1                    | 0.62%  | 1                     | 0.62%  |
| Precinct VBM           | 138                   | 22.55% | 36               | 5.88%  | 5                    | 0.82%  | 5                     | 0.82%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Provisional            | 2                     | 20.00% | 0                | 0.00%  | 0                    | 0.00%  | 1                     | 10.00% |
| Total                  | 162                   | 20.69% | 49               | 6.26%  | 6                    | 0.77%  | 7                     | 0.89%  |
| 100023.11-CP 100023.11 |                       |        |                  |        |                      |        |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Precinct OS            | 30                    | 15.54% | 11               | 5.70%  | 3                    | 1.55%  | 0                     | 0.00%  |
| Precinct VBM           | 157                   | 16.85% | 61               | 6.55%  | 7                    | 0.75%  | 3                     | 0.32%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Provisional            | 2                     | 7.14%  | 3                | 10.71% | 0                    | 0.00%  | 1                     | 3.57%  |
| Total                  | 189                   | 16.39% | 75               | 6.50%  | 10                   | 0.87%  | 4                     | 0.35%  |
| 200003.12-CP 200003.12 |                       |        |                  |        |                      |        |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Precinct OS            | 21                    | 14.19% | 6                | 4.05%  | 1                    | 0.68%  | 3                     | 2.03%  |
| Precinct VBM           | 74                    | 14.71% | 29               | 5.77%  | 0                    | 0.00%  | 6                     | 1.19%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Provisional            | 0                     | 0.00%  | 3                | 11.54% | 0                    | 0.00%  | 2                     | 7.69%  |
| Total                  | 95                    | 14.03% | 38               | 5.61%  | 1                    | 0.15%  | 11                    | 1.62%  |
| 200004.12-CP 200004.12 |                       |        |                  |        |                      |        |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Precinct OS            | 25                    | 15.15% | 11               | 6.67%  | 3                    | 1.82%  | 1                     | 0.61%  |
| Precinct VBM           | 106                   | 17.58% | 44               | 7.30%  | 5                    | 0.83%  | 2                     | 0.33%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Provisional            | 0                     | 0.00%  | 4                | 28.57% | 0                    | 0.00%  | 0                     | 0.00%  |
| Total                  | 131                   | 16.75% | 59               | 7.54%  | 8                    | 1.02%  | 3                     | 0.38%  |
| 200005.02-CP 200005.02 |                       |        |                  |        |                      |        |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Precinct OS            | 11                    | 12.64% | 5                | 5.75%  | 1                    | 1.15%  | 1                     | 1.15%  |
| Precinct VBM           | 71                    | 17.53% | 37               | 9.14%  | 3                    | 0.74%  | 7                     | 1.73%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Provisional            | 1                     | 5.26%  | 0                | 0.00%  | 2                    | 10.53% | 1                     | 5.26%  |
| Total                  | 83                    | 16.24% | 42               | 8.22%  | 6                    | 1.17%  | 9                     | 1.76%  |
| 200006.12-CP 200006.12 |                       |        |                  |        |                      |        |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Precinct OS            | 18                    | 16.67% | 6                | 5.56%  | 1                    | 0.93%  | 1                     | 0.93%  |
| Precinct VBM           | 108                   | 22.55% | 39               | 8.14%  | 2                    | 0.42%  | 1                     | 0.21%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |
| Provisional            | 3                     | 11.11% | 3                | 11.11% | 0                    | 0.00%  | 1                     | 3.70%  |
| Total                  | 129                   | 21.01% | 48               | 7.82%  | 3                    | 0.49%  | 3                     | 0.49%  |
| 200008.12-CP 200008.12 |                       |        |                  |        |                      |        |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -      | 0                     | -      |



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|------------------------|-----------------------|--------|------------------|--------|----------------------|-------|-----------------------|--------|
|                        | DUF SUNDHEIM (REP)    |        | PHIL WYMAN (REP) |        | GEORGE C. YANG (REP) |       | PAMELA ELIZONDO (GRN) |        |
| Precinct OS            | 23                    | 17.29% | 13               | 9.77%  | 1                    | 0.75% | 4                     | 3.01%  |
| Precinct VBM           | 40                    | 12.23% | 16               | 4.89%  | 3                    | 0.92% | 1                     | 0.31%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 0                     | 0.00%  | 1                | 12.50% | 0                    | 0.00% | 1                     | 12.50% |
| Total                  | 63                    | 13.46% | 30               | 6.41%  | 4                    | 0.85% | 6                     | 1.28%  |
| 200010.12-CP 200010.12 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | 0.00%  | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct OS            | 21                    | 11.73% | 10               | 5.59%  | 1                    | 0.56% | 2                     | 1.12%  |
| Precinct VBM           | 92                    | 12.22% | 48               | 6.37%  | 15                   | 1.99% | 7                     | 0.93%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 2                     | 7.69%  | 3                | 11.54% | 1                    | 3.85% | 0                     | 0.00%  |
| Total                  | 115                   | 11.95% | 61               | 6.34%  | 17                   | 1.77% | 9                     | 0.94%  |
| 200012.12-CP 200012.12 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 12                    | 9.52%  | 5                | 3.97%  | 3                    | 2.38% | 1                     | 0.79%  |
| Precinct VBM           | 73                    | 18.07% | 24               | 5.94%  | 3                    | 0.74% | 3                     | 0.74%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 0                     | 0.00%  | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 85                    | 15.68% | 29               | 5.35%  | 6                    | 1.11% | 4                     | 0.74%  |
| 200013.12-CP 200013.12 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 20                    | 10.47% | 17               | 8.90%  | 5                    | 2.62% | 0                     | 0.00%  |
| Precinct VBM           | 127                   | 19.87% | 29               | 4.54%  | 4                    | 0.63% | 7                     | 1.10%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 1                     | 5.00%  | 6                | 30.00% | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 148                   | 17.41% | 52               | 6.12%  | 9                    | 1.06% | 7                     | 0.82%  |
| 200014.12-CP 200014.12 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 20                    | 10.70% | 10               | 5.35%  | 1                    | 0.53% | 1                     | 0.53%  |
| Precinct VBM           | 102                   | 18.58% | 30               | 5.46%  | 3                    | 0.55% | 5                     | 0.91%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 2                     | 8.00%  | 1                | 4.00%  | 0                    | 0.00% | 1                     | 4.00%  |
| Total                  | 124                   | 16.29% | 41               | 5.39%  | 4                    | 0.53% | 7                     | 0.92%  |
| 200015.02-CP 200015.02 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 37                    | 18.97% | 17               | 8.72%  | 3                    | 1.54% | 4                     | 2.05%  |
| Precinct VBM           | 146                   | 18.48% | 62               | 7.85%  | 3                    | 0.38% | 18                    | 2.28%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 3                     | 15.00% | 3                | 15.00% | 0                    | 0.00% | 1                     | 5.00%  |
| Total                  | 186                   | 18.51% | 82               | 8.16%  | 6                    | 0.60% | 23                    | 2.29%  |
| 200016.12-CP 200016.12 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 5                     | 25.00% | 5                | 25.00% | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct VBM           | 24                    | 19.05% | 16               | 12.70% | 0                    | 0.00% | 2                     | 1.59%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 1                     | 33.33% | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 30                    | 20.13% | 21               | 14.09% | 0                    | 0.00% | 2                     | 1.34%  |
| 200016.13-CP 200016.13 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 13                    | 18.06% | 4                | 5.56%  | 3                    | 4.17% | 1                     | 1.39%  |
| Precinct VBM           | 50                    | 17.01% | 13               | 4.42%  | 4                    | 1.36% | 1                     | 0.34%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 2                     | 33.33% | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 65                    | 17.47% | 17               | 4.57%  | 7                    | 1.88% | 2                     | 0.54%  |
| 200017.02-CP 200017.02 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 14                    | 10.94% | 15               | 11.72% | 1                    | 0.78% | 4                     | 3.13%  |
| Precinct VBM           | 105                   | 19.20% | 46               | 8.41%  | 6                    | 1.10% | 10                    | 1.83%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 2                     | 18.18% | 0                | 0.00%  | 0                    | 0.00% | 1                     | 9.09%  |
| Total                  | 121                   | 17.64% | 61               | 8.89%  | 7                    | 1.02% | 15                    | 2.19%  |

**COUNTY OF EL DORADO**  
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|                        | DUF SUNDHEIM (REP)    |        | PHIL WYMAN (REP) |        | GEORGE C. YANG (REP) |       | PAMELA ELIZONDO (GRN) |        |
| 200018.02-CP 200018.02 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 45                    | 29.61% | 12               | 7.89%  | 1                    | 0.66% | 3                     | 1.97%  |
| Precinct VBM           | 155                   | 24.53% | 66               | 10.44% | 5                    | 0.79% | 5                     | 0.79%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 4                     | 33.33% | 1                | 8.33%  | 1                    | 8.33% | 0                     | 0.00%  |
| Total                  | 204                   | 25.63% | 79               | 9.92%  | 7                    | 0.88% | 8                     | 1.01%  |
| 200020.02-CP 200020.02 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 23                    | 18.55% | 11               | 8.87%  | 0                    | 0.00% | 2                     | 1.61%  |
| Precinct VBM           | 106                   | 22.27% | 39               | 8.19%  | 1                    | 0.21% | 8                     | 1.68%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 3                     | 17.65% | 2                | 11.76% | 0                    | 0.00% | 4                     | 23.53% |
| Total                  | 132                   | 21.39% | 52               | 8.43%  | 1                    | 0.16% | 14                    | 2.27%  |
| 200021.01-CP 200021.01 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 1                     | 3.33%  | 6                | 20.00% | 0                    | 0.00% | 1                     | 3.33%  |
| Precinct VBM           | 42                    | 23.33% | 23               | 12.78% | 5                    | 2.78% | 1                     | 0.56%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 1                     | 20.00% | 2                | 40.00% | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 44                    | 20.47% | 31               | 14.42% | 5                    | 2.33% | 2                     | 0.93%  |
| 200021.02-CP 200021.02 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 30                    | 19.48% | 12               | 7.79%  | 1                    | 0.65% | 3                     | 1.95%  |
| Precinct VBM           | 158                   | 23.41% | 43               | 6.37%  | 6                    | 0.89% | 8                     | 1.19%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 4                     | 26.67% | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 192                   | 22.75% | 55               | 6.52%  | 7                    | 0.83% | 11                    | 1.30%  |
| 200024.02-CP 200024.02 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 4                     | 7.55%  | 6                | 11.32% | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct VBM           | 56                    | 21.54% | 19               | 7.31%  | 4                    | 1.54% | 5                     | 1.92%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 1                     | 20.00% | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 61                    | 19.18% | 25               | 7.86%  | 4                    | 1.26% | 5                     | 1.57%  |
| 300001.04-CP 300001.04 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 1                     | 9.09%  | 1                | 9.09%  | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct VBM           | 12                    | 12.77% | 7                | 7.45%  | 0                    | 0.00% | 3                     | 3.19%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Total                  | 13                    | 12.38% | 8                | 7.62%  | 0                    | 0.00% | 3                     | 2.86%  |
| 300001.05-CP 300001.05 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 8                     | 11.76% | 7                | 10.29% | 1                    | 1.47% | 3                     | 4.41%  |
| Precinct VBM           | 41                    | 13.90% | 21               | 7.12%  | 2                    | 0.68% | 7                     | 2.37%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 0                     | 0.00%  | 1                | 11.11% | 0                    | 0.00% | 2                     | 22.22% |
| Total                  | 49                    | 13.17% | 29               | 7.80%  | 3                    | 0.81% | 12                    | 3.23%  |
| 300003.05-CP 300003.05 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 24                    | 13.33% | 11               | 6.11%  | 1                    | 0.56% | 6                     | 3.33%  |
| Precinct VBM           | 125                   | 14.86% | 44               | 5.23%  | 4                    | 0.48% | 16                    | 1.90%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 0                     | 0.00%  | 1                | 4.35%  | 0                    | 0.00% | 3                     | 13.04% |
| Total                  | 149                   | 14.27% | 56               | 5.36%  | 5                    | 0.48% | 25                    | 2.39%  |
| 300004.05-CP 300004.05 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 11                    | 7.10%  | 8                | 5.16%  | 0                    | 0.00% | 7                     | 4.52%  |
| Precinct VBM           | 91                    | 17.30% | 32               | 6.08%  | 2                    | 0.38% | 12                    | 2.28%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |

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|                        | DUF SUNDHEIM (REP)    |        | PHIL WYMAN (REP) |         | GEORGE C. YANG (REP) |       | PAMELA ELIZONDO (GRN) |        |
| Provisional            | 0                     | 0.00%  | 1                | 6.25%   | 0                    | 0.00% | 2                     | 12.50% |
| Total                  | 102                   | 14.63% | 41               | 5.88%   | 2                    | 0.29% | 21                    | 3.01%  |
| 300005.05-CP 300005.05 |                       |        |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 15                    | 10.42% | 8                | 5.56%   | 1                    | 0.69% | 9                     | 6.25%  |
| Precinct VBM           | 90                    | 15.25% | 34               | 5.76%   | 3                    | 0.51% | 7                     | 1.19%  |
| MB Precinct            | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Provisional            | 3                     | 13.64% | 0                | 0.00%   | 0                    | 0.00% | 2                     | 9.09%  |
| Total                  | 108                   | 14.29% | 42               | 5.56%   | 4                    | 0.53% | 18                    | 2.38%  |
| 300008.05-CP 300008.05 |                       |        |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 15                    | 11.72% | 16               | 12.50%  | 1                    | 0.78% | 0                     | 0.00%  |
| Precinct VBM           | 148                   | 21.70% | 62               | 9.09%   | 8                    | 1.17% | 8                     | 1.17%  |
| MB Precinct            | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Provisional            | 0                     | 0.00%  | 4                | 36.36%  | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 163                   | 19.85% | 82               | 9.99%   | 9                    | 1.10% | 8                     | 0.97%  |
| 300009.04-CP 300009.04 |                       |        |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 29                    | 26.36% | 10               | 9.09%   | 1                    | 0.91% | 2                     | 1.82%  |
| Precinct VBM           | 75                    | 21.80% | 26               | 7.56%   | 3                    | 0.87% | 2                     | 0.58%  |
| MB Precinct            | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Provisional            | 1                     | 8.33%  | 1                | 8.33%   | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 105                   | 22.53% | 37               | 7.94%   | 4                    | 0.86% | 4                     | 0.86%  |
| 300010.04-CP 300010.04 |                       |        |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 32                    | 20.65% | 14               | 9.03%   | 2                    | 1.29% | 4                     | 2.58%  |
| Precinct VBM           | 107                   | 18.01% | 38               | 6.40%   | 7                    | 1.18% | 7                     | 1.18%  |
| MB Precinct            | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Provisional            | 1                     | 9.09%  | 4                | 36.36%  | 0                    | 0.00% | 2                     | 18.18% |
| Total                  | 140                   | 18.42% | 56               | 7.37%   | 9                    | 1.18% | 13                    | 1.71%  |
| 300011.04-CP 300011.04 |                       |        |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 22                    | 16.92% | 13               | 10.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct VBM           | 158                   | 24.69% | 48               | 7.50%   | 11                   | 1.72% | 9                     | 1.41%  |
| MB Precinct            | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Provisional            | 2                     | 10.00% | 1                | 5.00%   | 0                    | 0.00% | 1                     | 5.00%  |
| Total                  | 182                   | 23.04% | 62               | 7.85%   | 11                   | 1.39% | 10                    | 1.27%  |
| 300013.05-CP 300013.05 |                       |        |                  |         |                      |       |                       |        |
| Precinct TSx           | 1                     | 33.33% | 1                | 33.33%  | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct OS            | 23                    | 19.66% | 5                | 4.27%   | 0                    | 0.00% | 1                     | 0.85%  |
| Precinct VBM           | 128                   | 19.48% | 52               | 7.91%   | 5                    | 0.76% | 10                    | 1.52%  |
| MB Precinct            | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Provisional            | 3                     | 27.27% | 1                | 9.09%   | 0                    | 0.00% | 2                     | 18.18% |
| Total                  | 155                   | 19.67% | 59               | 7.49%   | 5                    | 0.63% | 13                    | 1.65%  |
| 300015.04-CP 300015.04 |                       |        |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 2                     | 25.00% | 0                | 0.00%   | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct VBM           | 10                    | 19.61% | 3                | 5.88%   | 1                    | 1.96% | 0                     | 0.00%  |
| MB Precinct            | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Provisional            | 2                     | 50.00% | 0                | 0.00%   | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 14                    | 22.22% | 3                | 4.76%   | 1                    | 1.59% | 0                     | 0.00%  |
| 300015.05-CP 300015.05 |                       |        |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 10                    | 16.39% | 4                | 6.56%   | 1                    | 1.64% | 1                     | 1.64%  |
| Precinct VBM           | 43                    | 18.86% | 19               | 8.33%   | 0                    | 0.00% | 5                     | 2.19%  |
| MB Precinct            | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Provisional            | 0                     | 0.00%  | 1                | 100.00% | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 53                    | 18.28% | 24               | 8.28%   | 1                    | 0.34% | 6                     | 2.07%  |
| 300017.05-CP 300017.05 |                       |        |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 14                    | 12.84% | 5                | 4.59%   | 1                    | 0.92% | 2                     | 1.83%  |



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|                        | DUF SUNDHEIM (REP)    |        | PHIL WYMAN (REP) |        | GEORGE C. YANG (REP) |       | PAMELA ELIZONDO (GRN) |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 24                    | 10.86% | 30               | 13.57% | 1                    | 0.45% | 6                     | 2.71%  |
| Precinct VBM           | 144                   | 20.72% | 61               | 8.78%  | 4                    | 0.58% | 7                     | 1.01%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 3                     | 10.00% | 3                | 10.00% | 0                    | 0.00% | 1                     | 3.33%  |
| Total                  | 171                   | 18.08% | 94               | 9.94%  | 5                    | 0.53% | 14                    | 1.48%  |
| 400004.14-CP 400004.14 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 26                    | 15.29% | 17               | 10.00% | 0                    | 0.00% | 1                     | 0.59%  |
| Precinct VBM           | 110                   | 18.03% | 46               | 7.54%  | 2                    | 0.33% | 5                     | 0.82%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 2                     | 11.11% | 2                | 11.11% | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 138                   | 17.29% | 65               | 8.15%  | 2                    | 0.25% | 6                     | 0.75%  |
| 400005.08-CP 400005.08 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 11                    | 16.67% | 10               | 15.15% | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct VBM           | 30                    | 12.88% | 31               | 13.30% | 1                    | 0.43% | 4                     | 1.72%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 1                     | 14.29% | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 42                    | 13.73% | 41               | 13.40% | 1                    | 0.33% | 4                     | 1.31%  |
| 400006.08-CP 400006.08 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 32                    | 15.24% | 17               | 8.10%  | 3                    | 1.43% | 5                     | 2.38%  |
| Precinct VBM           | 150                   | 23.29% | 47               | 7.30%  | 8                    | 1.24% | 12                    | 1.86%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 0                     | 0.00%  | 2                | 13.33% | 0                    | 0.00% | 1                     | 6.67%  |
| Total                  | 182                   | 20.94% | 66               | 7.59%  | 11                   | 1.27% | 18                    | 2.07%  |
| 400007.08-CP 400007.08 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 31                    | 14.69% | 26               | 12.32% | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct VBM           | 174                   | 24.61% | 60               | 8.49%  | 7                    | 0.99% | 6                     | 0.85%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 1                     | 5.00%  | 3                | 15.00% | 0                    | 0.00% | 2                     | 10.00% |
| Total                  | 206                   | 21.96% | 89               | 9.49%  | 7                    | 0.75% | 8                     | 0.85%  |
| 400010.08-CP 400010.08 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 30                    | 13.57% | 20               | 9.05%  | 2                    | 0.90% | 8                     | 3.62%  |
| Precinct VBM           | 155                   | 19.09% | 59               | 7.27%  | 3                    | 0.37% | 16                    | 1.97%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 4                     | 19.05% | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 189                   | 17.93% | 79               | 7.50%  | 5                    | 0.47% | 24                    | 2.28%  |
| 400012.08-CP 400012.08 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 15                    | 14.29% | 9                | 8.57%  | 0                    | 0.00% | 4                     | 3.81%  |
| Precinct VBM           | 81                    | 20.66% | 33               | 8.42%  | 3                    | 0.77% | 7                     | 1.79%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 1                     | 6.67%  | 3                | 20.00% | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 97                    | 18.95% | 45               | 8.79%  | 3                    | 0.59% | 11                    | 2.15%  |
| 400013.08-CP 400013.08 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 28                    | 26.17% | 12               | 11.21% | 1                    | 0.93% | 5                     | 4.67%  |
| Precinct VBM           | 78                    | 26.71% | 19               | 6.51%  | 0                    | 0.00% | 3                     | 1.03%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 0                     | 0.00%  | 1                | 33.33% | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 106                   | 26.37% | 32               | 7.96%  | 1                    | 0.25% | 8                     | 1.99%  |
| 400014.08-CP 400014.08 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 41                    | 20.92% | 21               | 10.71% | 1                    | 0.51% | 5                     | 2.55%  |
| Precinct VBM           | 153                   | 23.04% | 59               | 8.89%  | 6                    | 0.90% | 9                     | 1.36%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 2                     | 6.45%  | 6                | 19.35% | 0                    | 0.00% | 1                     | 3.23%  |

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|                        | DUF SUNDHEIM (REP)    |        | PHIL WYMAN (REP) |        | GEORGE C. YANG (REP) |       | PAMELA ELIZONDO (GRN) |        |
| Total                  | 196                   | 22.00% | 86               | 9.65%  | 7                    | 0.79% | 15                    | 1.68%  |
| 400017.08-CP 400017.08 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 26                    | 16.99% | 25               | 16.34% | 2                    | 1.31% | 2                     | 1.31%  |
| Precinct VBM           | 154                   | 23.05% | 44               | 6.59%  | 3                    | 0.45% | 4                     | 0.60%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 1                     | 4.35%  | 5                | 21.74% | 0                    | 0.00% | 2                     | 8.70%  |
| Total                  | 181                   | 21.45% | 74               | 8.77%  | 5                    | 0.59% | 8                     | 0.95%  |
| 400018.08-CP 400018.08 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 24                    | 31.17% | 7                | 9.09%  | 0                    | 0.00% | 1                     | 1.30%  |
| Precinct VBM           | 63                    | 26.25% | 25               | 10.42% | 2                    | 0.83% | 4                     | 1.67%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 1                     | 25.00% | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 88                    | 27.41% | 32               | 9.97%  | 2                    | 0.62% | 5                     | 1.56%  |
| 400020.08-CP 400020.08 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 20                    | 19.05% | 13               | 12.38% | 1                    | 0.95% | 2                     | 1.90%  |
| Precinct VBM           | 70                    | 22.65% | 22               | 7.12%  | 2                    | 0.65% | 5                     | 1.62%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 0                     | 0.00%  | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 90                    | 21.63% | 35               | 8.41%  | 3                    | 0.72% | 7                     | 1.68%  |
| 400021.07-CP 400021.07 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 23                    | 19.49% | 10               | 8.47%  | 0                    | 0.00% | 1                     | 0.85%  |
| Precinct VBM           | 185                   | 26.97% | 57               | 8.31%  | 10                   | 1.46% | 3                     | 0.44%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 3                     | 21.43% | 1                | 7.14%  | 1                    | 7.14% | 0                     | 0.00%  |
| Total                  | 211                   | 25.79% | 68               | 8.31%  | 11                   | 1.34% | 4                     | 0.49%  |
| 500001.10-CP 500001.10 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 26                    | 14.05% | 8                | 4.32%  | 4                    | 2.16% | 3                     | 1.62%  |
| Precinct VBM           | 78                    | 14.53% | 22               | 4.10%  | 5                    | 0.93% | 8                     | 1.49%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 1                     | 7.14%  | 0                | 0.00%  | 1                    | 7.14% | 0                     | 0.00%  |
| Total                  | 105                   | 14.27% | 30               | 4.08%  | 10                   | 1.36% | 11                    | 1.49%  |
| 500002.10-CP 500002.10 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 10                    | 8.33%  | 3                | 2.50%  | 2                    | 1.67% | 12                    | 10.00% |
| Precinct VBM           | 36                    | 9.70%  | 9                | 2.43%  | 1                    | 0.27% | 19                    | 5.12%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 1                     | 4.00%  | 0                | 0.00%  | 1                    | 4.00% | 2                     | 8.00%  |
| Total                  | 47                    | 9.11%  | 12               | 2.33%  | 4                    | 0.78% | 33                    | 6.40%  |
| 500003.10-CP 500003.10 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 10                    | 8.40%  | 1                | 0.84%  | 0                    | 0.00% | 8                     | 6.72%  |
| Precinct VBM           | 46                    | 15.23% | 7                | 2.32%  | 4                    | 1.32% | 7                     | 2.32%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 0                     | 0.00%  | 0                | 0.00%  | 0                    | 0.00% | 1                     | 14.29% |
| Total                  | 56                    | 13.08% | 8                | 1.87%  | 4                    | 0.93% | 16                    | 3.74%  |
| 500004.10-CP 500004.10 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 9                     | 8.41%  | 5                | 4.67%  | 1                    | 0.93% | 11                    | 10.28% |
| Precinct VBM           | 29                    | 12.61% | 11               | 4.78%  | 1                    | 0.43% | 12                    | 5.22%  |
| MB Precinct            | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 0                     | 0.00%  | 1                | 5.56%  | 0                    | 0.00% | 3                     | 16.67% |
| Total                  | 38                    | 10.70% | 17               | 4.79%  | 2                    | 0.56% | 26                    | 7.32%  |
| 500005.10-CP 500005.10 |                       |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                     | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 10                    | 10.31% | 3                | 3.09%  | 0                    | 0.00% | 8                     | 8.25%  |
| Precinct VBM           | 21                    | 9.59%  | 7                | 3.20%  | 1                    | 0.46% | 9                     | 4.11%  |

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|                        | DUF SUNDHEIM (REP) | PHIL WYMAN (REP) | GEORGE C. YANG (REP) | PAMELA ELIZONDO (GRN) |
|------------------------|--------------------|------------------|----------------------|-----------------------|
| MB Precinct            | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Provisional            | 0 0.00%            | 0 0.00%          | 0 0.00%              | 3 25.00%              |
| Total                  | 31 9.45%           | 10 3.05%         | 1 0.30%              | 20 6.10%              |
| 500006.10-CP 500006.10 |                    |                  |                      |                       |
| Precinct TSx           | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Precinct OS            | 11 6.71%           | 6 3.66%          | 1 0.61%              | 23 14.02%             |
| Precinct VBM           | 39 8.88%           | 14 3.19%         | 0 0.00%              | 29 6.61%              |
| MB Precinct            | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Provisional            | 3 6.12%            | 1 2.04%          | 0 0.00%              | 6 12.24%              |
| Total                  | 53 8.13%           | 21 3.22%         | 1 0.15%              | 58 8.90%              |
| 500007.10-CP 500007.10 |                    |                  |                      |                       |
| Precinct TSx           | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Precinct OS            | 30 15.96%          | 7 3.72%          | 0 0.00%              | 12 6.38%              |
| Precinct VBM           | 106 17.61%         | 41 6.81%         | 8 1.33%              | 24 3.99%              |
| MB Precinct            | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Provisional            | 2 11.11%           | 0 0.00%          | 0 0.00%              | 2 11.11%              |
| Total                  | 138 17.08%         | 48 5.94%         | 8 0.99%              | 38 4.70%              |
| 500009.10-CP 500009.10 |                    |                  |                      |                       |
| Precinct TSx           | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Precinct OS            | 3 2.78%            | 2 1.85%          | 0 0.00%              | 8 7.41%               |
| Precinct VBM           | 28 10.49%          | 6 2.25%          | 2 0.75%              | 14 5.24%              |
| MB Precinct            | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Provisional            | 1 3.23%            | 0 0.00%          | 1 3.23%              | 4 12.90%              |
| Total                  | 32 7.88%           | 8 1.97%          | 3 0.74%              | 26 6.40%              |
| 500011.10-CP 500011.10 |                    |                  |                      |                       |
| Precinct TSx           | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Precinct OS            | 18 11.04%          | 10 6.13%         | 1 0.61%              | 12 7.36%              |
| Precinct VBM           | 34 10.06%          | 14 4.14%         | 5 1.48%              | 18 5.33%              |
| MB Precinct            | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Provisional            | 1 3.03%            | 2 6.06%          | 0 0.00%              | 2 6.06%               |
| Total                  | 53 9.93%           | 26 4.87%         | 6 1.12%              | 32 5.99%              |
| 500012.10-CP 500012.10 |                    |                  |                      |                       |
| Precinct TSx           | 1 100.00%          | 0 0.00%          | 0 0.00%              | 0 0.00%               |
| Precinct OS            | 16 9.41%           | 5 2.94%          | 1 0.59%              | 15 8.82%              |
| Precinct VBM           | 69 12.73%          | 29 5.35%         | 6 1.11%              | 13 2.40%              |
| MB Precinct            | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Provisional            | 0 0.00%            | 1 3.33%          | 0 0.00%              | 5 16.67%              |
| Total                  | 86 11.57%          | 35 4.71%         | 7 0.94%              | 33 4.44%              |
| 500015.10-CP 500015.10 |                    |                  |                      |                       |
| Precinct TSx           | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Precinct OS            | 7 7.29%            | 4 4.17%          | 0 0.00%              | 3 3.13%               |
| Precinct VBM           | 34 9.26%           | 14 3.81%         | 3 0.82%              | 6 1.63%               |
| MB Precinct            | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Provisional            | 0 0.00%            | 1 10.00%         | 0 0.00%              | 1 10.00%              |
| Total                  | 41 8.67%           | 19 4.02%         | 3 0.63%              | 10 2.11%              |
| 500020.10-CP 500020.10 |                    |                  |                      |                       |
| Precinct TSx           | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Precinct OS            | 29 20.57%          | 12 8.51%         | 0 0.00%              | 0 0.00%               |
| Precinct VBM           | 107 17.63%         | 40 6.59%         | 1 0.16%              | 10 1.65%              |
| MB Precinct            | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Provisional            | 4 15.38%           | 2 7.69%          | 0 0.00%              | 2 7.69%               |
| Total                  | 140 18.09%         | 54 6.98%         | 1 0.13%              | 12 1.55%              |
| 500021.10-CP 500021.10 |                    |                  |                      |                       |
| Precinct TSx           | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Precinct OS            | 28 18.79%          | 16 10.74%        | 2 1.34%              | 3 2.01%               |
| Precinct VBM           | 120 20.98%         | 47 8.22%         | 6 1.05%              | 4 0.70%               |
| MB Precinct            | 0 -                | 0 -              | 0 -                  | 0 -                   |
| Provisional            | 2 8.00%            | 2 8.00%          | 1 4.00%              | 1 4.00%               |
| Total                  | 150 20.11%         | 65 8.71%         | 9 1.21%              | 8 1.07%               |
| 95U201.01-MB 95U201.01 |                    |                  |                      |                       |
| Precinct TSx           | 0 -                | 0 -              | 0 -                  | 0 -                   |

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|                        | DUF SUNDHEIM (REP)    |         | PHIL WYMAN (REP) |         | GEORGE C. YANG (REP) |       | PAMELA ELIZONDO (GRN) |        |
| Precinct OS            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| MB Precinct            | 3                     | 42.86%  | 0                | 0.00%   | 0                    | 0.00% | 0                     | 0.00%  |
| Provisional            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Total                  | 3                     | 42.86%  | 0                | 0.00%   | 0                    | 0.00% | 0                     | 0.00%  |
| 95U201.02-MB 95U201.02 |                       |         |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| MB Precinct            | 86                    | 28.29%  | 20               | 6.58%   | 4                    | 1.32% | 1                     | 0.33%  |
| Provisional            | 0                     | 0.00%   | 1                | 100.00% | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 86                    | 28.20%  | 21               | 6.89%   | 4                    | 1.31% | 1                     | 0.33%  |
| 95U202.02-MB 95U202.02 |                       |         |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| MB Precinct            | 107                   | 21.19%  | 35               | 6.93%   | 2                    | 0.40% | 5                     | 0.99%  |
| Provisional            | 0                     | 0.00%   | 0                | 0.00%   | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 107                   | 21.06%  | 35               | 6.89%   | 2                    | 0.39% | 5                     | 0.98%  |
| 95U203.02-MB 95U203.02 |                       |         |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| MB Precinct            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Provisional            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Total                  | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| 95U204.01-MB 95U204.01 |                       |         |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| MB Precinct            | 156                   | 26.13%  | 37               | 6.20%   | 8                    | 1.34% | 4                     | 0.67%  |
| Provisional            | 2                     | 100.00% | 0                | 0.00%   | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 158                   | 26.38%  | 37               | 6.18%   | 8                    | 1.34% | 4                     | 0.67%  |
| 95U301.05-MB 95U301.05 |                       |         |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| MB Precinct            | 27                    | 19.85%  | 10               | 7.35%   | 0                    | 0.00% | 5                     | 3.68%  |
| Provisional            | 0                     | 0.00%   | 0                | 0.00%   | 0                    | 0.00% | 1                     | 50.00% |
| Total                  | 27                    | 19.57%  | 10               | 7.25%   | 0                    | 0.00% | 6                     | 4.35%  |
| 95U302.04-MB 95U302.04 |                       |         |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| MB Precinct            | 0                     | 0.00%   | 0                | 0.00%   | 0                    | 0.00% | 0                     | 0.00%  |
| Provisional            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Total                  | 0                     | 0.00%   | 0                | 0.00%   | 0                    | 0.00% | 0                     | 0.00%  |
| 95U302.05-MB 95U302.05 |                       |         |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| MB Precinct            | 18                    | 22.22%  | 14               | 17.28%  | 0                    | 0.00% | 1                     | 1.23%  |
| Provisional            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Total                  | 18                    | 22.22%  | 14               | 17.28%  | 0                    | 0.00% | 1                     | 1.23%  |
| 95U303.03-MB 95U303.03 |                       |         |                  |         |                      |       |                       |        |
| Precinct TSx           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| MB Precinct            | 14                    | 17.95%  | 10               | 12.82%  | 0                    | 0.00% | 2                     | 2.56%  |
| Provisional            | 0                     | -       | 0                | -       | 0                    | -     | 0                     | -      |
| Total                  | 14                    | 17.95%  | 10               | 12.82%  | 0                    | 0.00% | 2                     | 2.56%  |



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|                        | DUF SUNDHEIM (REP) |        | PHIL WYMAN (REP) |        | GEORGE C. YANG (REP) |       | PAMELA ELIZONDO (GRN) |        |
|------------------------|--------------------|--------|------------------|--------|----------------------|-------|-----------------------|--------|
| 95U303.05-MB 95U303.05 |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct            | 16                 | 16.84% | 5                | 5.26%  | 1                    | 1.05% | 2                     | 2.11%  |
| Provisional            | 0                  | 0.00%  | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 16                 | 16.67% | 5                | 5.21%  | 1                    | 1.04% | 2                     | 2.08%  |
| 95U304.05-MB 95U304.05 |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Total                  | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| 95U305.05-MB 95U305.05 |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct            | 4                  | 57.14% | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Provisional            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Total                  | 4                  | 57.14% | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| 95U401.08-MB 95U401.08 |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct            | 6                  | 40.00% | 1                | 6.67%  | 0                    | 0.00% | 0                     | 0.00%  |
| Provisional            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Total                  | 6                  | 40.00% | 1                | 6.67%  | 0                    | 0.00% | 0                     | 0.00%  |
| 95U402.07-MB 95U402.07 |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct            | 58                 | 24.79% | 16               | 6.84%  | 0                    | 0.00% | 3                     | 1.28%  |
| Provisional            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Total                  | 58                 | 24.79% | 16               | 6.84%  | 0                    | 0.00% | 3                     | 1.28%  |
| 95U402.08-MB 95U402.08 |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct            | 127                | 25.20% | 46               | 9.13%  | 6                    | 1.19% | 7                     | 1.39%  |
| Provisional            | 0                  | 0.00%  | 1                | 33.33% | 0                    | 0.00% | 0                     | 0.00%  |
| Total                  | 127                | 25.05% | 47               | 9.27%  | 6                    | 1.18% | 7                     | 1.38%  |
| 95U403.08-MB 95U403.08 |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct            | 166                | 17.55% | 78               | 8.25%  | 10                   | 1.06% | 11                    | 1.16%  |
| Provisional            | 0                  | 0.00%  | 3                | 30.00% | 0                    | 0.00% | 2                     | 20.00% |
| Total                  | 166                | 17.36% | 81               | 8.47%  | 10                   | 1.05% | 13                    | 1.36%  |
| 95U404.06-MB 95U404.06 |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Provisional            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Total                  | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| 95U404.08-MB 95U404.08 |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM           | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct            | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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United States Senator

|                               | DUF SUNDHEIM (REP) |        | PHIL WYMAN (REP) |        | GEORGE C. YANG (REP) |       | PAMELA ELIZONDO (GRN) |        |
|-------------------------------|--------------------|--------|------------------|--------|----------------------|-------|-----------------------|--------|
| Provisional                   | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Total                         | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| 95U501.09-MB 95U501.09        |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx                  | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS                   | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM                  | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct                   | 0                  | 0.00%  | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Provisional                   | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Total                         | 0                  | 0.00%  | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| 95U501.10-MB 95U501.10        |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx                  | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS                   | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM                  | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct                   | 26                 | 16.25% | 12               | 7.50%  | 0                    | 0.00% | 1                     | 0.63%  |
| Provisional                   | 0                  | 0.00%  | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Total                         | 26                 | 16.05% | 12               | 7.41%  | 0                    | 0.00% | 1                     | 0.62%  |
| 95U598.10-MB 95U598.10        |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx                  | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS                   | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM                  | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct                   | 18                 | 8.41%  | 9                | 4.21%  | 0                    | 0.00% | 19                    | 8.88%  |
| Provisional                   | 0                  | 0.00%  | 1                | 50.00% | 0                    | 0.00% | 0                     | 0.00%  |
| Total                         | 18                 | 8.33%  | 10               | 4.63%  | 0                    | 0.00% | 19                    | 8.80%  |
| 95U599.10-MB 95U599.10        |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx                  | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS                   | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM                  | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct                   | 7                  | 20.59% | 2                | 5.88%  | 0                    | 0.00% | 0                     | 0.00%  |
| Provisional                   | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Total                         | 7                  | 20.59% | 2                | 5.88%  | 0                    | 0.00% | 0                     | 0.00%  |
| 96U101.11-MB 96U101.11        |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx                  | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS                   | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM                  | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct                   | 76                 | 20.71% | 26               | 7.08%  | 4                    | 1.09% | 3                     | 0.82%  |
| Provisional                   | 0                  | 0.00%  | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Total                         | 76                 | 20.38% | 26               | 6.97%  | 4                    | 1.07% | 3                     | 0.80%  |
| 96U401.14-MB 96U401.14        |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx                  | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS                   | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct VBM                  | 0                  | -      | 0                | -      | 0                    | -     | 0                     | -      |
| MB Precinct                   | 59                 | 18.32% | 35               | 10.87% | 3                    | 0.93% | 4                     | 1.24%  |
| Provisional                   | 0                  | 0.00%  | 0                | 0.00%  | 0                    | 0.00% | 1                     | 16.67% |
| Total                         | 59                 | 17.99% | 35               | 10.67% | 3                    | 0.91% | 5                     | 1.52%  |
| Total                         |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx                  | 2                  | 25.00% | 1                | 12.50% | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct OS                   | 1563               | 14.61% | 814              | 7.61%  | 89                   | 0.83% | 254                   | 2.37%  |
| Precinct VBM                  | 7617               | 18.63% | 2795             | 6.84%  | 356                  | 0.87% | 554                   | 1.36%  |
| MB Precinct                   | 974                | 21.13% | 356              | 7.72%  | 38                   | 0.82% | 68                    | 1.48%  |
| Provisional                   | 130                | 9.89%  | 112              | 8.52%  | 12                   | 0.91% | 86                    | 6.54%  |
| Total                         | 10286              | 17.89% | 4078             | 7.09%  | 495                  | 0.86% | 962                   | 1.67%  |
| STATE BOARD OF EQUALIZATION   |                    |        |                  |        |                      |       |                       |        |
| STATE BOARD OF EQUALIZATION 1 |                    |        |                  |        |                      |       |                       |        |
| Precinct TSx                  | 2                  | 25.00% | 1                | 12.50% | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct OS                   | 1563               | 14.61% | 814              | 7.61%  | 89                   | 0.83% | 254                   | 2.37%  |
| Precinct VBM                  | 7617               | 18.63% | 2795             | 6.84%  | 356                  | 0.87% | 554                   | 1.36%  |
| MB Precinct                   | 974                | 21.13% | 356              | 7.72%  | 38                   | 0.82% | 68                    | 1.48%  |
| Provisional                   | 130                | 9.89%  | 112              | 8.52%  | 12                   | 0.91% | 86                    | 6.54%  |
| Total                         | 10286              | 17.89% | 4078             | 7.09%  | 495                  | 0.86% | 962                   | 1.67%  |
| Total                         |                    |        |                  |        |                      |       |                       |        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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United States Senator

|                                     | DUF SUNDHEIM (REP) |               | PHIL WYMAN (REP) |              | GEORGE C. YANG (REP) |              | PAMELA ELIZONDO (GRN) |              |
|-------------------------------------|--------------------|---------------|------------------|--------------|----------------------|--------------|-----------------------|--------------|
| Precinct TSx                        | 2                  | 25.00%        | 1                | 12.50%       | 0                    | 0.00%        | 0                     | 0.00%        |
| Precinct OS                         | 1563               | 14.61%        | 814              | 7.61%        | 89                   | 0.83%        | 254                   | 2.37%        |
| Precinct VBM                        | 7617               | 18.63%        | 2795             | 6.84%        | 356                  | 0.87%        | 554                   | 1.36%        |
| MB Precinct                         | 974                | 21.13%        | 356              | 7.72%        | 38                   | 0.82%        | 68                    | 1.48%        |
| Provisional                         | 130                | 9.89%         | 112              | 8.52%        | 12                   | 0.91%        | 86                    | 6.54%        |
| <b>Total</b>                        | <b>10286</b>       | <b>17.89%</b> | <b>4078</b>      | <b>7.09%</b> | <b>495</b>           | <b>0.86%</b> | <b>962</b>            | <b>1.67%</b> |
| <b>UNITED STATES SENATE</b>         |                    |               |                  |              |                      |              |                       |              |
| <b>UNITED STATES SENATE</b>         |                    |               |                  |              |                      |              |                       |              |
| Precinct TSx                        | 2                  | 25.00%        | 1                | 12.50%       | 0                    | 0.00%        | 0                     | 0.00%        |
| Precinct OS                         | 1563               | 14.61%        | 814              | 7.61%        | 89                   | 0.83%        | 254                   | 2.37%        |
| Precinct VBM                        | 7617               | 18.63%        | 2795             | 6.84%        | 356                  | 0.87%        | 554                   | 1.36%        |
| MB Precinct                         | 974                | 21.13%        | 356              | 7.72%        | 38                   | 0.82%        | 68                    | 1.48%        |
| Provisional                         | 130                | 9.89%         | 112              | 8.52%        | 12                   | 0.91%        | 86                    | 6.54%        |
| <b>Total</b>                        | <b>10286</b>       | <b>17.89%</b> | <b>4078</b>      | <b>7.09%</b> | <b>495</b>           | <b>0.86%</b> | <b>962</b>            | <b>1.67%</b> |
| <b>Total</b>                        |                    |               |                  |              |                      |              |                       |              |
| Precinct TSx                        | 2                  | 25.00%        | 1                | 12.50%       | 0                    | 0.00%        | 0                     | 0.00%        |
| Precinct OS                         | 1563               | 14.61%        | 814              | 7.61%        | 89                   | 0.83%        | 254                   | 2.37%        |
| Precinct VBM                        | 7617               | 18.63%        | 2795             | 6.84%        | 356                  | 0.87%        | 554                   | 1.36%        |
| MB Precinct                         | 974                | 21.13%        | 356              | 7.72%        | 38                   | 0.82%        | 68                    | 1.48%        |
| Provisional                         | 130                | 9.89%         | 112              | 8.52%        | 12                   | 0.91%        | 86                    | 6.54%        |
| <b>Total</b>                        | <b>10286</b>       | <b>17.89%</b> | <b>4078</b>      | <b>7.09%</b> | <b>495</b>           | <b>0.86%</b> | <b>962</b>            | <b>1.67%</b> |
| <b>Total</b>                        |                    |               |                  |              |                      |              |                       |              |
| <b>UNITED STATES REPRESENTATIVE</b> |                    |               |                  |              |                      |              |                       |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                    |               |                  |              |                      |              |                       |              |
| Precinct TSx                        | 2                  | 25.00%        | 1                | 12.50%       | 0                    | 0.00%        | 0                     | 0.00%        |
| Precinct OS                         | 1563               | 14.61%        | 814              | 7.61%        | 89                   | 0.83%        | 254                   | 2.37%        |
| Precinct VBM                        | 7617               | 18.63%        | 2795             | 6.84%        | 356                  | 0.87%        | 554                   | 1.36%        |
| MB Precinct                         | 974                | 21.13%        | 356              | 7.72%        | 38                   | 0.82%        | 68                    | 1.48%        |
| Provisional                         | 130                | 9.89%         | 112              | 8.52%        | 12                   | 0.91%        | 86                    | 6.54%        |
| <b>Total</b>                        | <b>10286</b>       | <b>17.89%</b> | <b>4078</b>      | <b>7.09%</b> | <b>495</b>           | <b>0.86%</b> | <b>962</b>            | <b>1.67%</b> |
| <b>Total</b>                        |                    |               |                  |              |                      |              |                       |              |
| Precinct TSx                        | 2                  | 25.00%        | 1                | 12.50%       | 0                    | 0.00%        | 0                     | 0.00%        |
| Precinct OS                         | 1563               | 14.61%        | 814              | 7.61%        | 89                   | 0.83%        | 254                   | 2.37%        |
| Precinct VBM                        | 7617               | 18.63%        | 2795             | 6.84%        | 356                  | 0.87%        | 554                   | 1.36%        |
| MB Precinct                         | 974                | 21.13%        | 356              | 7.72%        | 38                   | 0.82%        | 68                    | 1.48%        |
| Provisional                         | 130                | 9.89%         | 112              | 8.52%        | 12                   | 0.91%        | 86                    | 6.54%        |
| <b>Total</b>                        | <b>10286</b>       | <b>17.89%</b> | <b>4078</b>      | <b>7.09%</b> | <b>495</b>           | <b>0.86%</b> | <b>962</b>            | <b>1.67%</b> |
| <b>Total</b>                        |                    |               |                  |              |                      |              |                       |              |
| <b>STATE SENATE</b>                 |                    |               |                  |              |                      |              |                       |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                    |               |                  |              |                      |              |                       |              |
| Precinct TSx                        | 2                  | 25.00%        | 1                | 12.50%       | 0                    | 0.00%        | 0                     | 0.00%        |
| Precinct OS                         | 1563               | 14.61%        | 814              | 7.61%        | 89                   | 0.83%        | 254                   | 2.37%        |
| Precinct VBM                        | 7617               | 18.63%        | 2795             | 6.84%        | 356                  | 0.87%        | 554                   | 1.36%        |
| MB Precinct                         | 974                | 21.13%        | 356              | 7.72%        | 38                   | 0.82%        | 68                    | 1.48%        |
| Provisional                         | 130                | 9.89%         | 112              | 8.52%        | 12                   | 0.91%        | 86                    | 6.54%        |
| <b>Total</b>                        | <b>10286</b>       | <b>17.89%</b> | <b>4078</b>      | <b>7.09%</b> | <b>495</b>           | <b>0.86%</b> | <b>962</b>            | <b>1.67%</b> |
| <b>Total</b>                        |                    |               |                  |              |                      |              |                       |              |
| Precinct TSx                        | 2                  | 25.00%        | 1                | 12.50%       | 0                    | 0.00%        | 0                     | 0.00%        |
| Precinct OS                         | 1563               | 14.61%        | 814              | 7.61%        | 89                   | 0.83%        | 254                   | 2.37%        |
| Precinct VBM                        | 7617               | 18.63%        | 2795             | 6.84%        | 356                  | 0.87%        | 554                   | 1.36%        |
| MB Precinct                         | 974                | 21.13%        | 356              | 7.72%        | 38                   | 0.82%        | 68                    | 1.48%        |
| Provisional                         | 130                | 9.89%         | 112              | 8.52%        | 12                   | 0.91%        | 86                    | 6.54%        |
| <b>Total</b>                        | <b>10286</b>       | <b>17.89%</b> | <b>4078</b>      | <b>7.09%</b> | <b>495</b>           | <b>0.86%</b> | <b>962</b>            | <b>1.67%</b> |
| <b>Total</b>                        |                    |               |                  |              |                      |              |                       |              |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                    |               |                  |              |                      |              |                       |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                    |               |                  |              |                      |              |                       |              |
| Precinct TSx                        | 2                  | 50.00%        | 1                | 25.00%       | 0                    | 0.00%        | 0                     | 0.00%        |
| Precinct OS                         | 1028               | 15.09%        | 558              | 8.19%        | 44                   | 0.65%        | 228                   | 3.35%        |
| Precinct VBM                        | 4890               | 19.26%        | 1845             | 7.27%        | 201                  | 0.79%        | 463                   | 1.82%        |
| MB Precinct                         | 839                | 21.40%        | 295              | 7.53%        | 31                   | 0.79%        | 61                    | 1.56%        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

Date:07/05/16  
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United States Senator

|                                   | DUF SUNDHEIM (REP) |         | PHIL WYMAN (REP) |        | GEORGE C. YANG (REP) |       | PAMELA ELIZONDO (GRN) |        |
|-----------------------------------|--------------------|---------|------------------|--------|----------------------|-------|-----------------------|--------|
| Provisional                       | 79                 | 9.19%   | 75               | 8.72%  | 9                    | 1.05% | 70                    | 8.14%  |
| Total                             | 6838               | 18.49%  | 2774             | 7.50%  | 285                  | 0.77% | 822                   | 2.22%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |                    |         |                  |        |                      |       |                       |        |
| Precinct TSx                      | 0                  | 0.00%   | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct OS                       | 535                | 13.76%  | 256              | 6.58%  | 45                   | 1.16% | 26                    | 0.67%  |
| Precinct VBM                      | 2727               | 17.61%  | 950              | 6.14%  | 155                  | 1.00% | 91                    | 0.59%  |
| MB Precinct                       | 135                | 19.59%  | 61               | 8.85%  | 7                    | 1.02% | 7                     | 1.02%  |
| Provisional                       | 51                 | 11.23%  | 37               | 8.15%  | 3                    | 0.66% | 16                    | 3.52%  |
| Total                             | 3448               | 16.81%  | 1304             | 6.36%  | 210                  | 1.02% | 140                   | 0.68%  |
| <b>Total</b>                      |                    |         |                  |        |                      |       |                       |        |
| Precinct TSx                      | 2                  | 25.00%  | 1                | 12.50% | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct OS                       | 1563               | 14.61%  | 814              | 7.61%  | 89                   | 0.83% | 254                   | 2.37%  |
| Precinct VBM                      | 7617               | 18.63%  | 2795             | 6.84%  | 356                  | 0.87% | 554                   | 1.36%  |
| MB Precinct                       | 974                | 21.13%  | 356              | 7.72%  | 38                   | 0.82% | 68                    | 1.48%  |
| Provisional                       | 130                | 9.89%   | 112              | 8.52%  | 12                   | 0.91% | 86                    | 6.54%  |
| Total                             | 10286              | 17.89%  | 4078             | 7.09%  | 495                  | 0.86% | 962                   | 1.67%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |                    |         |                  |        |                      |       |                       |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                    |         |                  |        |                      |       |                       |        |
| Precinct TSx                      | 0                  | -       | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS                       | 317                | 14.22%  | 135              | 6.06%  | 25                   | 1.12% | 9                     | 0.40%  |
| Precinct VBM                      | 1639               | 17.64%  | 551              | 5.93%  | 105                  | 1.13% | 44                    | 0.47%  |
| MB Precinct                       | 76                 | 20.71%  | 26               | 7.08%  | 4                    | 1.09% | 3                     | 0.82%  |
| Provisional                       | 33                 | 13.58%  | 11               | 4.53%  | 2                    | 0.82% | 10                    | 4.12%  |
| Total                             | 2065               | 17.03%  | 723              | 5.96%  | 136                  | 1.12% | 66                    | 0.54%  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                    |         |                  |        |                      |       |                       |        |
| Precinct TSx                      | 0                  | 0.00%   | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct OS                       | 343                | 15.23%  | 171              | 7.59%  | 26                   | 1.15% | 32                    | 1.42%  |
| Precinct VBM                      | 1635               | 18.92%  | 623              | 7.21%  | 72                   | 0.83% | 97                    | 1.12%  |
| MB Precinct                       | 352                | 24.91%  | 92               | 6.51%  | 14                   | 0.99% | 10                    | 0.71%  |
| Provisional                       | 32                 | 11.55%  | 30               | 10.83% | 4                    | 1.44% | 12                    | 4.33%  |
| Total                             | 2362               | 18.76%  | 916              | 7.28%  | 116                  | 0.92% | 151                   | 1.20%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                    |         |                  |        |                      |       |                       |        |
| Precinct TSx                      | 1                  | 33.33%  | 1                | 33.33% | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct OS                       | 323                | 15.24%  | 164              | 7.74%  | 14                   | 0.66% | 50                    | 2.36%  |
| Precinct VBM                      | 1747               | 19.29%  | 662              | 7.31%  | 68                   | 0.75% | 141                   | 1.56%  |
| MB Precinct                       | 79                 | 19.85%  | 39               | 9.80%  | 1                    | 0.25% | 10                    | 2.51%  |
| Provisional                       | 25                 | 10.55%  | 27               | 11.39% | 1                    | 0.42% | 22                    | 9.28%  |
| Total                             | 2175               | 18.41%  | 893              | 7.56%  | 84                   | 0.71% | 223                   | 1.89%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                    |         |                  |        |                      |       |                       |        |
| Precinct TSx                      | 0                  | -       | 0                | -      | 0                    | -     | 0                     | -      |
| Precinct OS                       | 373                | 16.26%  | 262              | 11.42% | 12                   | 0.52% | 45                    | 1.96%  |
| Precinct VBM                      | 1849               | 21.77%  | 698              | 8.22%  | 68                   | 0.80% | 99                    | 1.17%  |
| MB Precinct                       | 416                | 20.58%  | 176              | 8.71%  | 19                   | 0.94% | 25                    | 1.24%  |
| Provisional                       | 25                 | 9.80%   | 33               | 12.94% | 1                    | 0.39% | 10                    | 3.92%  |
| Total                             | 2663               | 20.38%  | 1169             | 8.95%  | 100                  | 0.77% | 179                   | 1.37%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                    |         |                  |        |                      |       |                       |        |
| Precinct TSx                      | 1                  | 100.00% | 0                | 0.00%  | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct OS                       | 207                | 11.46%  | 82               | 4.54%  | 12                   | 0.66% | 118                   | 6.53%  |
| Precinct VBM                      | 747                | 13.85%  | 261              | 4.84%  | 43                   | 0.80% | 173                   | 3.21%  |
| MB Precinct                       | 51                 | 12.44%  | 23               | 5.61%  | 0                    | 0.00% | 20                    | 4.88%  |
| Provisional                       | 15                 | 4.97%   | 11               | 3.64%  | 4                    | 1.32% | 32                    | 10.60% |
| Total                             | 1021               | 12.90%  | 377              | 4.76%  | 59                   | 0.75% | 343                   | 4.33%  |
| <b>Total</b>                      |                    |         |                  |        |                      |       |                       |        |
| Precinct TSx                      | 2                  | 25.00%  | 1                | 12.50% | 0                    | 0.00% | 0                     | 0.00%  |
| Precinct OS                       | 1563               | 14.61%  | 814              | 7.61%  | 89                   | 0.83% | 254                   | 2.37%  |
| Precinct VBM                      | 7617               | 18.63%  | 2795             | 6.84%  | 356                  | 0.87% | 554                   | 1.36%  |
| MB Precinct                       | 974                | 21.13%  | 356              | 7.72%  | 38                   | 0.82% | 68                    | 1.48%  |
| Provisional                       | 130                | 9.89%   | 112              | 8.52%  | 12                   | 0.91% | 86                    | 6.54%  |
| Total                             | 10286              | 17.89%  | 4078             | 7.09%  | 495                  | 0.86% | 962                   | 1.67%  |

**COUNTY OF EL DORADO**  
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|                                 | DUF SUNDHEIM (REP)  | PHIL WYMAN (REP)  | GEORGE C. YANG (REP) | PAMELA ELIZONDO (GRN) |
|---------------------------------|---------------------|-------------------|----------------------|-----------------------|
| <b>CITIES</b>                   |                     |                   |                      |                       |
| <b>CITY OF PLACERVILLE</b>      |                     |                   |                      |                       |
| Precinct TSx                    | 0 -                 | 0 -               | 0 -                  | 0 -                   |
| Precinct OS                     | 59 10.57%           | 35 6.27%          | 3 0.54%              | 25 4.48%              |
| Precinct VBM                    | 359 15.30%          | 138 5.88%         | 11 0.47%             | 45 1.92%              |
| MB Precinct                     | 0 -                 | 0 -               | 0 -                  | 0 -                   |
| Provisional                     | 3 4.29%             | 3 4.29%           | 0 0.00%              | 9 12.86%              |
| <b>Total</b>                    | <b>421 14.16%</b>   | <b>176 5.92%</b>  | <b>14 0.47%</b>      | <b>79 2.66%</b>       |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                     |                   |                      |                       |
| Precinct TSx                    | 0 -                 | 0 -               | 0 -                  | 0 -                   |
| Precinct OS                     | 91 9.61%            | 36 3.80%          | 5 0.53%              | 86 9.08%              |
| Precinct VBM                    | 293 11.88%          | 102 4.14%         | 18 0.73%             | 125 5.07%             |
| MB Precinct                     | 0 -                 | 0 -               | 0 -                  | 0 -                   |
| Provisional                     | 8 4.30%             | 4 2.15%           | 2 1.08%              | 22 11.83%             |
| <b>Total</b>                    | <b>392 10.89%</b>   | <b>142 3.95%</b>  | <b>25 0.69%</b>      | <b>233 6.47%</b>      |
| <b>UNINCORPORATED AREA</b>      |                     |                   |                      |                       |
| Precinct TSx                    | 2 25.00%            | 1 12.50%          | 0 0.00%              | 0 0.00%               |
| Precinct OS                     | 1413 15.37%         | 743 8.08%         | 81 0.88%             | 143 1.56%             |
| Precinct VBM                    | 6965 19.31%         | 2555 7.08%        | 327 0.91%            | 384 1.06%             |
| MB Precinct                     | 974 21.13%          | 356 7.72%         | 38 0.82%             | 68 1.48%              |
| Provisional                     | 119 11.25%          | 105 9.92%         | 10 0.95%             | 55 5.20%              |
| <b>Total</b>                    | <b>9473 18.60%</b>  | <b>3760 7.38%</b> | <b>456 0.90%</b>     | <b>650 1.28%</b>      |
| <b>Total</b>                    |                     |                   |                      |                       |
| Precinct TSx                    | 2 25.00%            | 1 12.50%          | 0 0.00%              | 0 0.00%               |
| Precinct OS                     | 1563 14.61%         | 814 7.61%         | 89 0.83%             | 254 2.37%             |
| Precinct VBM                    | 7617 18.63%         | 2795 6.84%        | 356 0.87%            | 554 1.36%             |
| MB Precinct                     | 974 21.13%          | 356 7.72%         | 38 0.82%             | 68 1.48%              |
| Provisional                     | 130 9.89%           | 112 8.52%         | 12 0.91%             | 86 6.54%              |
| <b>Total</b>                    | <b>10286 17.89%</b> | <b>4078 7.09%</b> | <b>495 0.86%</b>     | <b>962 1.67%</b>      |

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|                        | MARK MATTHEW<br>HERD (LIB) |       | JOHN T. PARKER<br>(PF) |       | MIKE BEITIKS<br>(NPP) |        | ELEANOR GARCIA<br>(NPP) |       |
| Jurisdiction Wide      |                            |       |                        |       |                       |        |                         |       |
| 100001.11-CP 100001.11 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 1                       | 0.64% |
| Precinct VBM           | 5                          | 0.71% | 1                      | 0.14% | 4                     | 0.57%  | 1                       | 0.14% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 5                          | 0.57% | 1                      | 0.11% | 4                     | 0.46%  | 2                       | 0.23% |
| 100002.11-CP 100002.11 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 1                      | 1.32% | 0                     | 0.00%  | 0                       | 0.00% |
| Precinct VBM           | 4                          | 0.99% | 0                      | 0.00% | 2                     | 0.50%  | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 1                          | 6.25% | 0                      | 0.00% | 2                     | 12.50% | 1                       | 6.25% |
| Total                  | 5                          | 1.01% | 1                      | 0.20% | 4                     | 0.81%  | 1                       | 0.20% |
| 100003.11-CP 100003.11 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Precinct VBM           | 3                          | 0.42% | 1                      | 0.14% | 2                     | 0.28%  | 2                       | 0.28% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 3                          | 0.36% | 1                      | 0.12% | 2                     | 0.24%  | 2                       | 0.24% |
| 100004.11-CP 100004.11 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 1                          | 0.43% | 0                      | 0.00% | 0                     | 0.00%  | 2                       | 0.85% |
| Precinct VBM           | 4                          | 0.60% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 5                          | 0.55% | 0                      | 0.00% | 0                     | 0.00%  | 2                       | 0.22% |
| 100005.11-CP 100005.11 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 1                          | 0.75% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Precinct VBM           | 1                          | 0.12% | 3                      | 0.37% | 0                     | 0.00%  | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 2                          | 0.21% | 3                      | 0.31% | 0                     | 0.00%  | 0                       | 0.00% |
| 100006.11-CP 100006.11 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 1                       | 0.45% |
| Precinct VBM           | 4                          | 0.47% | 2                      | 0.24% | 1                     | 0.12%  | 2                       | 0.24% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 4                          | 0.37% | 2                      | 0.18% | 1                     | 0.09%  | 3                       | 0.28% |
| 100008.11-CP 100008.11 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 1                          | 0.57% | 0                      | 0.00% | 1                     | 0.57%  | 1                       | 0.57% |
| Precinct VBM           | 5                          | 0.77% | 0                      | 0.00% | 1                     | 0.15%  | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 1                      | 9.09% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 6                          | 0.72% | 1                      | 0.12% | 2                     | 0.24%  | 1                       | 0.12% |
| 100009.11-CP 100009.11 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 2                          | 1.37% | 0                      | 0.00% | 0                     | 0.00%  | 1                       | 0.68% |
| Precinct VBM           | 3                          | 0.44% | 0                      | 0.00% | 2                     | 0.30%  | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 1                     | 5.56%  | 0                       | 0.00% |
| Total                  | 5                          | 0.60% | 0                      | 0.00% | 3                     | 0.36%  | 1                       | 0.12% |
| 100012.11-CP 100012.11 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 1                          | 0.44% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Precinct VBM           | 6                          | 0.70% | 0                      | 0.00% | 2                     | 0.23%  | 2                       | 0.23% |

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|------------------------|----------------------------|-------|------------------------|-------|-----------------------|-------|-------------------------|-------|
|                        | MARK MATTHEW<br>HERD (LIB) |       | JOHN T. PARKER<br>(PF) |       | MIKE BEITIKS<br>(NPP) |       | ELEANOR GARCIA<br>(NPP) |       |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 1                       | 3.57% |
| Total                  | 7                          | 0.63% | 0                      | 0.00% | 2                     | 0.18% | 3                       | 0.27% |
| 100013.11-CP 100013.11 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 6                          | 2.71% | 1                      | 0.45% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 6                          | 0.74% | 1                      | 0.12% | 0                     | 0.00% | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 1                          | 4.17% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 13                         | 1.23% | 2                      | 0.19% | 0                     | 0.00% | 0                       | 0.00% |
| 100014.11-CP 100014.11 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 3                          | 1.73% | 0                      | 0.00% | 2                     | 1.16% | 0                       | 0.00% |
| Precinct VBM           | 1                          | 0.16% | 4                      | 0.64% | 4                     | 0.64% | 2                       | 0.32% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 4                          | 0.49% | 4                      | 0.49% | 6                     | 0.73% | 2                       | 0.24% |
| 100022.11-CP 100022.11 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 2                          | 1.24% | 0                      | 0.00% | 1                     | 0.62% | 0                       | 0.00% |
| Precinct VBM           | 2                          | 0.33% | 1                      | 0.16% | 1                     | 0.16% | 1                       | 0.16% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 4                          | 0.51% | 1                      | 0.13% | 2                     | 0.26% | 1                       | 0.13% |
| 100023.11-CP 100023.11 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 1                          | 0.52% | 2                      | 1.04% | 1                     | 0.52% | 0                       | 0.00% |
| Precinct VBM           | 4                          | 0.43% | 1                      | 0.11% | 0                     | 0.00% | 2                       | 0.21% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 5                          | 0.43% | 3                      | 0.26% | 1                     | 0.09% | 2                       | 0.17% |
| 200003.12-CP 200003.12 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 1                      | 0.68% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 2                          | 0.40% | 3                      | 0.60% | 6                     | 1.19% | 3                       | 0.60% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 1                     | 3.85% | 0                       | 0.00% |
| Total                  | 2                          | 0.30% | 4                      | 0.59% | 7                     | 1.03% | 3                       | 0.44% |
| 200004.12-CP 200004.12 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 2                          | 1.21% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 1                          | 0.17% | 2                      | 0.33% | 1                     | 0.17% | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 1                     | 7.14% | 0                       | 0.00% |
| Total                  | 3                          | 0.38% | 2                      | 0.26% | 2                     | 0.26% | 0                       | 0.00% |
| 200005.02-CP 200005.02 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 1                          | 1.15% | 0                      | 0.00% | 1                     | 1.15% | 0                       | 0.00% |
| Precinct VBM           | 6                          | 1.48% | 3                      | 0.74% | 5                     | 1.23% | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 1                          | 5.26% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 8                          | 1.57% | 3                      | 0.59% | 6                     | 1.17% | 0                       | 0.00% |
| 200006.12-CP 200006.12 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 1                          | 0.93% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 2                          | 0.42% | 1                      | 0.21% | 0                     | 0.00% | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 3                          | 0.49% | 1                      | 0.16% | 0                     | 0.00% | 0                       | 0.00% |
| 200008.12-CP 200008.12 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |

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|                        | MARK MATTHEW<br>HERD (LIB) |       | JOHN T. PARKER<br>(PF) |       | MIKE BEITIKS<br>(NPP) |        | ELEANOR GARCIA<br>(NPP) |       |
| Precinct OS            | 3                          | 2.26% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Precinct VBM           | 3                          | 0.92% | 1                      | 0.31% | 1                     | 0.31%  | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 6                          | 1.28% | 1                      | 0.21% | 1                     | 0.21%  | 0                       | 0.00% |
| 200010.12-CP 200010.12 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Precinct OS            | 0                          | 0.00% | 0                      | 0.00% | 1                     | 0.56%  | 1                       | 0.56% |
| Precinct VBM           | 2                          | 0.27% | 2                      | 0.27% | 4                     | 0.53%  | 3                       | 0.40% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 2                          | 0.21% | 2                      | 0.21% | 5                     | 0.52%  | 4                       | 0.42% |
| 200012.12-CP 200012.12 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 1                          | 0.79% | 0                      | 0.00% | 1                     | 0.79%  | 0                       | 0.00% |
| Precinct VBM           | 1                          | 0.25% | 0                      | 0.00% | 1                     | 0.25%  | 1                       | 0.25% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 2                          | 0.37% | 0                      | 0.00% | 2                     | 0.37%  | 1                       | 0.18% |
| 200013.12-CP 200013.12 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 3                          | 1.57% | 0                      | 0.00% | 1                     | 0.52%  | 0                       | 0.00% |
| Precinct VBM           | 5                          | 0.78% | 1                      | 0.16% | 5                     | 0.78%  | 2                       | 0.31% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 8                          | 0.94% | 1                      | 0.12% | 6                     | 0.71%  | 2                       | 0.24% |
| 200014.12-CP 200014.12 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 2                          | 1.07% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1                      | 0.18% | 0                     | 0.00%  | 1                       | 0.18% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 1                          | 4.00% | 1                      | 4.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 3                          | 0.39% | 2                      | 0.26% | 0                     | 0.00%  | 1                       | 0.13% |
| 200015.02-CP 200015.02 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 2                          | 1.03% | 1                      | 0.51% | 2                     | 1.03%  | 1                       | 0.51% |
| Precinct VBM           | 7                          | 0.89% | 3                      | 0.38% | 8                     | 1.01%  | 2                       | 0.25% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 2                     | 10.00% | 0                       | 0.00% |
| Total                  | 9                          | 0.90% | 4                      | 0.40% | 12                    | 1.19%  | 3                       | 0.30% |
| 200016.12-CP 200016.12 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 1                          | 5.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Precinct VBM           | 2                          | 1.59% | 1                      | 0.79% | 0                     | 0.00%  | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 3                          | 2.01% | 1                      | 0.67% | 0                     | 0.00%  | 0                       | 0.00% |
| 200016.13-CP 200016.13 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 3                          | 4.17% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Precinct VBM           | 2                          | 0.68% | 1                      | 0.34% | 5                     | 1.70%  | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 5                          | 1.34% | 1                      | 0.27% | 5                     | 1.34%  | 0                       | 0.00% |
| 200017.02-CP 200017.02 |                            |       |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 1                          | 0.78% | 2                      | 1.56% | 1                     | 0.78%  | 1                       | 0.78% |
| Precinct VBM           | 4                          | 0.73% | 4                      | 0.73% | 4                     | 0.73%  | 4                       | 0.73% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 5                          | 0.73% | 6                      | 0.87% | 5                     | 0.73%  | 5                       | 0.73% |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | United States Senator      |       |                        |        |                       |       |                         |       |
|------------------------|----------------------------|-------|------------------------|--------|-----------------------|-------|-------------------------|-------|
|                        | MARK MATTHEW<br>HERD (LIB) |       | JOHN T. PARKER<br>(PF) |        | MIKE BEITIKS<br>(NPP) |       | ELEANOR GARCIA<br>(NPP) |       |
| 200018.02-CP 200018.02 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 1                          | 0.66% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 2                          | 0.32% | 2                      | 0.32%  | 3                     | 0.47% | 2                       | 0.32% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 1                          | 8.33% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 4                          | 0.50% | 2                      | 0.25%  | 3                     | 0.38% | 2                       | 0.25% |
| 200020.02-CP 200020.02 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 1                          | 0.81% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 4                          | 0.84% | 3                      | 0.63%  | 1                     | 0.21% | 1                       | 0.21% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 1                      | 5.88%  | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 5                          | 0.81% | 4                      | 0.65%  | 1                     | 0.16% | 1                       | 0.16% |
| 200021.01-CP 200021.01 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 3                          | 1.67% | 2                      | 1.11%  | 0                     | 0.00% | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 1                      | 20.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 3                          | 1.40% | 3                      | 1.40%  | 0                     | 0.00% | 0                       | 0.00% |
| 200021.02-CP 200021.02 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 1                          | 0.65% | 0                      | 0.00%  | 0                     | 0.00% | 1                       | 0.65% |
| Precinct VBM           | 2                          | 0.30% | 1                      | 0.15%  | 3                     | 0.44% | 3                       | 0.44% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00%  | 1                     | 6.67% | 0                       | 0.00% |
| Total                  | 3                          | 0.36% | 1                      | 0.12%  | 4                     | 0.47% | 4                       | 0.47% |
| 200024.02-CP 200024.02 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 1                      | 1.89%  | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 1                      | 0.38%  | 2                     | 0.77% | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 0                          | 0.00% | 2                      | 0.63%  | 2                     | 0.63% | 0                       | 0.00% |
| 300001.04-CP 300001.04 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0                      | 0.00%  | 1                     | 1.06% | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Total                  | 0                          | 0.00% | 0                      | 0.00%  | 1                     | 0.95% | 0                       | 0.00% |
| 300001.05-CP 300001.05 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 1                      | 1.47%  | 3                     | 4.41% | 1                       | 1.47% |
| Precinct VBM           | 3                          | 1.02% | 2                      | 0.68%  | 2                     | 0.68% | 1                       | 0.34% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 3                          | 0.81% | 3                      | 0.81%  | 5                     | 1.34% | 2                       | 0.54% |
| 300003.05-CP 300003.05 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 1                          | 0.56% | 0                      | 0.00%  | 3                     | 1.67% | 1                       | 0.56% |
| Precinct VBM           | 4                          | 0.48% | 10                     | 1.19%  | 8                     | 0.95% | 1                       | 0.12% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 5                          | 0.48% | 10                     | 0.96%  | 11                    | 1.05% | 2                       | 0.19% |
| 300004.05-CP 300004.05 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 1                          | 0.65% | 0                      | 0.00%  | 1                     | 0.65% | 1                       | 0.65% |
| Precinct VBM           | 6                          | 1.14% | 2                      | 0.38%  | 5                     | 0.95% | 3                       | 0.57% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |

**COUNTY OF EL DORADO**  
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|                        | United States Senator      |        |                        |       |                       |       |                         |       |
|------------------------|----------------------------|--------|------------------------|-------|-----------------------|-------|-------------------------|-------|
|                        | MARK MATTHEW<br>HERD (LIB) |        | JOHN T. PARKER<br>(PF) |       | MIKE BEITIKS<br>(NPP) |       | ELEANOR GARCIA<br>(NPP) |       |
| Provisional            | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 7                          | 1.00%  | 2                      | 0.29% | 6                     | 0.86% | 4                       | 0.57% |
| 300005.05-CP 300005.05 |                            |        |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 2                          | 1.39%  | 1                      | 0.69% | 1                     | 0.69% | 1                       | 0.69% |
| Precinct VBM           | 3                          | 0.51%  | 1                      | 0.17% | 7                     | 1.19% | 3                       | 0.51% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 1                          | 4.55%  | 1                      | 4.55% | 0                     | 0.00% | 1                       | 4.55% |
| Total                  | 6                          | 0.79%  | 3                      | 0.40% | 8                     | 1.06% | 5                       | 0.66% |
| 300008.05-CP 300008.05 |                            |        |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 2                          | 0.29%  | 0                      | 0.00% | 7                     | 1.03% | 2                       | 0.29% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 1                          | 9.09%  | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 3                          | 0.37%  | 0                      | 0.00% | 7                     | 0.85% | 2                       | 0.24% |
| 300009.04-CP 300009.04 |                            |        |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00%  | 1                      | 0.91% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 2                          | 0.58%  | 1                      | 0.29% | 1                     | 0.29% | 3                       | 0.87% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 2                          | 0.43%  | 2                      | 0.43% | 1                     | 0.21% | 3                       | 0.64% |
| 300010.04-CP 300010.04 |                            |        |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 3                          | 1.94%  | 0                      | 0.00% | 4                     | 2.58% | 0                       | 0.00% |
| Precinct VBM           | 1                          | 0.17%  | 0                      | 0.00% | 2                     | 0.34% | 1                       | 0.17% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 4                          | 0.53%  | 0                      | 0.00% | 6                     | 0.79% | 1                       | 0.13% |
| 300011.04-CP 300011.04 |                            |        |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 1                          | 0.77%  | 1                      | 0.77% | 1                     | 0.77% | 0                       | 0.00% |
| Precinct VBM           | 2                          | 0.31%  | 2                      | 0.31% | 6                     | 0.94% | 1                       | 0.16% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 2                          | 10.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 5                          | 0.63%  | 3                      | 0.38% | 7                     | 0.89% | 1                       | 0.13% |
| 300013.05-CP 300013.05 |                            |        |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct OS            | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00% | 1                       | 0.85% |
| Precinct VBM           | 3                          | 0.46%  | 2                      | 0.30% | 4                     | 0.61% | 2                       | 0.30% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 2                          | 18.18% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 5                          | 0.63%  | 2                      | 0.25% | 4                     | 0.51% | 3                       | 0.38% |
| 300015.04-CP 300015.04 |                            |        |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| 300015.05-CP 300015.05 |                            |        |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 1                          | 0.44%  | 0                      | 0.00% | 2                     | 0.88% | 0                       | 0.00% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 1                          | 0.34%  | 0                      | 0.00% | 2                     | 0.69% | 0                       | 0.00% |
| 300017.05-CP 300017.05 |                            |        |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 2                          | 1.83%  | 0                      | 0.00% | 2                     | 1.83% | 0                       | 0.00% |



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|                        | United States Senator      |       |                        |        |                       |       |                         |       |
|------------------------|----------------------------|-------|------------------------|--------|-----------------------|-------|-------------------------|-------|
|                        | MARK MATTHEW<br>HERD (LIB) |       | JOHN T. PARKER<br>(PF) |        | MIKE BEITIKS<br>(NPP) |       | ELEANOR GARCIA<br>(NPP) |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 4                          | 1.81% | 3                      | 1.36%  | 1                     | 0.45% | 1                       | 0.45% |
| Precinct VBM           | 3                          | 0.43% | 1                      | 0.14%  | 6                     | 0.86% | 2                       | 0.29% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 7                          | 0.74% | 4                      | 0.42%  | 7                     | 0.74% | 3                       | 0.32% |
| 400004.14-CP 400004.14 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 4                          | 2.35% | 0                      | 0.00%  | 1                     | 0.59% | 3                       | 1.76% |
| Precinct VBM           | 8                          | 1.31% | 5                      | 0.82%  | 2                     | 0.33% | 1                       | 0.16% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 1                          | 5.56% | 2                      | 11.11% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 13                         | 1.63% | 7                      | 0.88%  | 3                     | 0.38% | 4                       | 0.50% |
| 400005.08-CP 400005.08 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 0                      | 0.00%  | 1                     | 0.43% | 2                       | 0.86% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 0                          | 0.00% | 0                      | 0.00%  | 1                     | 0.33% | 2                       | 0.65% |
| 400006.08-CP 400006.08 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 4                          | 1.90% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 1                          | 0.16% | 3                      | 0.47%  | 2                     | 0.31% | 2                       | 0.31% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00%  | 1                     | 6.67% | 0                       | 0.00% |
| Total                  | 5                          | 0.58% | 3                      | 0.35%  | 3                     | 0.35% | 2                       | 0.23% |
| 400007.08-CP 400007.08 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 1                          | 0.47% | 0                      | 0.00%  | 4                     | 1.90% | 2                       | 0.95% |
| Precinct VBM           | 3                          | 0.42% | 1                      | 0.14%  | 8                     | 1.13% | 3                       | 0.42% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00%  | 1                     | 5.00% | 1                       | 5.00% |
| Total                  | 4                          | 0.43% | 1                      | 0.11%  | 13                    | 1.39% | 6                       | 0.64% |
| 400010.08-CP 400010.08 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 0                      | 0.00%  | 3                     | 1.36% | 2                       | 0.90% |
| Precinct VBM           | 7                          | 0.86% | 1                      | 0.12%  | 8                     | 0.99% | 3                       | 0.37% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 1                          | 4.76% | 1                      | 4.76%  | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 8                          | 0.76% | 2                      | 0.19%  | 11                    | 1.04% | 5                       | 0.47% |
| 400012.08-CP 400012.08 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 0                      | 0.00%  | 0                     | 0.00% | 1                       | 0.95% |
| Precinct VBM           | 0                          | 0.00% | 1                      | 0.26%  | 3                     | 0.77% | 1                       | 0.26% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 0                          | 0.00% | 1                      | 0.20%  | 3                     | 0.59% | 2                       | 0.39% |
| 400013.08-CP 400013.08 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 1                          | 0.93% | 0                      | 0.00%  | 1                     | 0.93% | 1                       | 0.93% |
| Precinct VBM           | 1                          | 0.34% | 1                      | 0.34%  | 2                     | 0.68% | 1                       | 0.34% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 2                          | 0.50% | 1                      | 0.25%  | 3                     | 0.75% | 2                       | 0.50% |
| 400014.08-CP 400014.08 |                            |       |                        |        |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Precinct OS            | 1                          | 0.51% | 0                      | 0.00%  | 0                     | 0.00% | 1                       | 0.51% |
| Precinct VBM           | 5                          | 0.75% | 0                      | 0.00%  | 3                     | 0.45% | 2                       | 0.30% |
| MB Precinct            | 0                          | -     | 0                      | -      | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00%  | 0                     | 0.00% | 0                       | 0.00% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|------------------------|----------------------------|--------|------------------------|-------|-----------------------|--------|-------------------------|-------|
|                        | MARK MATTHEW<br>HERD (LIB) |        | JOHN T. PARKER<br>(PF) |       | MIKE BEITIKS<br>(NPP) |        | ELEANOR GARCIA<br>(NPP) |       |
| Total                  | 6                          | 0.67%  | 0                      | 0.00% | 3                     | 0.34%  | 3                       | 0.34% |
| 400017.08-CP 400017.08 |                            |        |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 0                          | 0.00%  | 1                      | 0.65% | 0                     | 0.00%  | 1                       | 0.65% |
| Precinct VBM           | 11                         | 1.65%  | 0                      | 0.00% | 7                     | 1.05%  | 3                       | 0.45% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 1                          | 4.35%  | 1                      | 4.35% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 12                         | 1.42%  | 2                      | 0.24% | 7                     | 0.83%  | 4                       | 0.47% |
| 400018.08-CP 400018.08 |                            |        |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 1                          | 1.30%  | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Precinct VBM           | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 1                          | 0.31%  | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| 400020.08-CP 400020.08 |                            |        |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 2                          | 1.90%  | 0                      | 0.00% | 1                     | 0.95%  | 0                       | 0.00% |
| Precinct VBM           | 0                          | 0.00%  | 2                      | 0.65% | 1                     | 0.32%  | 1                       | 0.32% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 1                          | 50.00% | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 3                          | 0.72%  | 2                      | 0.48% | 2                     | 0.48%  | 1                       | 0.24% |
| 400021.07-CP 400021.07 |                            |        |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 1                          | 0.85%  | 1                      | 0.85% | 0                     | 0.00%  | 0                       | 0.00% |
| Precinct VBM           | 3                          | 0.44%  | 0                      | 0.00% | 3                     | 0.44%  | 2                       | 0.29% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 4                          | 0.49%  | 1                      | 0.12% | 3                     | 0.37%  | 2                       | 0.24% |
| 500001.10-CP 500001.10 |                            |        |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00%  | 1                       | 0.54% |
| Precinct VBM           | 2                          | 0.37%  | 0                      | 0.00% | 4                     | 0.74%  | 1                       | 0.19% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 0                          | 0.00%  | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 2                          | 0.27%  | 0                      | 0.00% | 4                     | 0.54%  | 2                       | 0.27% |
| 500002.10-CP 500002.10 |                            |        |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 2                          | 1.67%  | 0                      | 0.00% | 0                     | 0.00%  | 1                       | 0.83% |
| Precinct VBM           | 2                          | 0.54%  | 5                      | 1.35% | 6                     | 1.62%  | 2                       | 0.54% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 2                          | 8.00%  | 1                      | 4.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 6                          | 1.16%  | 6                      | 1.16% | 6                     | 1.16%  | 3                       | 0.58% |
| 500003.10-CP 500003.10 |                            |        |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 1                          | 0.84%  | 0                      | 0.00% | 0                     | 0.00%  | 0                       | 0.00% |
| Precinct VBM           | 2                          | 0.66%  | 4                      | 1.32% | 4                     | 1.32%  | 0                       | 0.00% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 1                          | 14.29% | 0                      | 0.00% | 1                     | 14.29% | 0                       | 0.00% |
| Total                  | 4                          | 0.93%  | 4                      | 0.93% | 5                     | 1.17%  | 0                       | 0.00% |
| 500004.10-CP 500004.10 |                            |        |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 1                          | 0.93%  | 0                      | 0.00% | 2                     | 1.87%  | 1                       | 0.93% |
| Precinct VBM           | 2                          | 0.87%  | 2                      | 0.87% | 0                     | 0.00%  | 2                       | 0.87% |
| MB Precinct            | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Provisional            | 1                          | 5.56%  | 1                      | 5.56% | 0                     | 0.00%  | 0                       | 0.00% |
| Total                  | 4                          | 1.13%  | 3                      | 0.85% | 2                     | 0.56%  | 3                       | 0.85% |
| 500005.10-CP 500005.10 |                            |        |                        |       |                       |        |                         |       |
| Precinct TSx           | 0                          | -      | 0                      | -     | 0                     | -      | 0                       | -     |
| Precinct OS            | 1                          | 1.03%  | 2                      | 2.06% | 1                     | 1.03%  | 1                       | 1.03% |
| Precinct VBM           | 3                          | 1.37%  | 1                      | 0.46% | 4                     | 1.83%  | 2                       | 0.91% |

**COUNTY OF EL DORADO**  
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|                        | United States Senator      |       |                        |       |                       |       |                         |       |
|------------------------|----------------------------|-------|------------------------|-------|-----------------------|-------|-------------------------|-------|
|                        | MARK MATTHEW<br>HERD (LIB) |       | JOHN T. PARKER<br>(PF) |       | MIKE BEITIKS<br>(NPP) |       | ELEANOR GARCIA<br>(NPP) |       |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 4                          | 1.22% | 3                      | 0.91% | 5                     | 1.52% | 3                       | 0.91% |
| 500006.10-CP 500006.10 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 3                          | 1.83% | 0                      | 0.00% | 4                     | 2.44% | 2                       | 1.22% |
| Precinct VBM           | 3                          | 0.68% | 2                      | 0.46% | 5                     | 1.14% | 1                       | 0.23% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 3                     | 6.12% | 0                       | 0.00% |
| Total                  | 6                          | 0.92% | 2                      | 0.31% | 12                    | 1.84% | 3                       | 0.46% |
| 500007.10-CP 500007.10 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 2                          | 1.06% | 0                      | 0.00% | 1                     | 0.53% | 1                       | 0.53% |
| Precinct VBM           | 1                          | 0.17% | 3                      | 0.50% | 8                     | 1.33% | 3                       | 0.50% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 3                          | 0.37% | 3                      | 0.37% | 9                     | 1.11% | 4                       | 0.50% |
| 500009.10-CP 500009.10 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 1                      | 0.93% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 3                      | 1.12% | 2                     | 0.75% | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 1                     | 3.23% | 0                       | 0.00% |
| Total                  | 0                          | 0.00% | 4                      | 0.99% | 3                     | 0.74% | 0                       | 0.00% |
| 500011.10-CP 500011.10 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 3                          | 1.84% | 0                      | 0.00% | 1                     | 0.61% | 2                       | 1.23% |
| Precinct VBM           | 3                          | 0.89% | 1                      | 0.30% | 6                     | 1.78% | 2                       | 0.59% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 2                     | 6.06% | 0                       | 0.00% |
| Total                  | 6                          | 1.12% | 1                      | 0.19% | 9                     | 1.69% | 4                       | 0.75% |
| 500012.10-CP 500012.10 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct OS            | 3                          | 1.76% | 2                      | 1.18% | 3                     | 1.76% | 0                       | 0.00% |
| Precinct VBM           | 3                          | 0.55% | 0                      | 0.00% | 4                     | 0.74% | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 6                          | 0.81% | 2                      | 0.27% | 7                     | 0.94% | 0                       | 0.00% |
| 500015.10-CP 500015.10 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 0                      | 0.00% | 2                     | 2.08% | 0                       | 0.00% |
| Precinct VBM           | 0                          | 0.00% | 4                      | 1.09% | 3                     | 0.82% | 3                       | 0.82% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 0                          | 0.00% | 4                      | 0.85% | 5                     | 1.06% | 3                       | 0.63% |
| 500020.10-CP 500020.10 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 2                          | 1.42% | 1                      | 0.71% | 0                     | 0.00% | 1                       | 0.71% |
| Precinct VBM           | 5                          | 0.82% | 5                      | 0.82% | 1                     | 0.16% | 0                       | 0.00% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 2                     | 7.69% | 0                       | 0.00% |
| Total                  | 7                          | 0.90% | 6                      | 0.78% | 3                     | 0.39% | 1                       | 0.13% |
| 500021.10-CP 500021.10 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | 0.00% | 1                      | 0.67% | 1                     | 0.67% | 1                       | 0.67% |
| Precinct VBM           | 7                          | 1.22% | 1                      | 0.17% | 3                     | 0.52% | 1                       | 0.17% |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 1                          | 4.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 8                          | 1.07% | 2                      | 0.27% | 4                     | 0.54% | 2                       | 0.27% |
| 95U201.01-MB 95U201.01 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |

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|------------------------|----------------------------|-------|------------------------|-------|-----------------------|-------|-------------------------|-------|
|                        | MARK MATTHEW<br>HERD (LIB) |       | JOHN T. PARKER<br>(PF) |       | MIKE BEITIKS<br>(NPP) |       | ELEANOR GARCIA<br>(NPP) |       |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Provisional            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Total                  | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| 95U201.02-MB 95U201.02 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 3                          | 0.99% | 1                      | 0.33% | 1                     | 0.33% | 1                       | 0.33% |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 3                          | 0.98% | 1                      | 0.33% | 1                     | 0.33% | 1                       | 0.33% |
| 95U202.02-MB 95U202.02 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 1                          | 0.20% | 0                      | 0.00% | 3                     | 0.59% | 3                       | 0.59% |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 1                          | 0.20% | 0                      | 0.00% | 3                     | 0.59% | 3                       | 0.59% |
| 95U203.02-MB 95U203.02 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Total                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| 95U204.01-MB 95U204.01 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 3                          | 0.50% | 3                      | 0.50% | 3                     | 0.50% | 0                       | 0.00% |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 3                          | 0.50% | 3                      | 0.50% | 3                     | 0.50% | 0                       | 0.00% |
| 95U301.05-MB 95U301.05 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 2                          | 1.47% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 2                          | 1.45% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| 95U302.04-MB 95U302.04 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Provisional            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Total                  | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| 95U302.05-MB 95U302.05 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Provisional            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Total                  | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| 95U303.03-MB 95U303.03 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 2                          | 2.56% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Provisional            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Total                  | 2                          | 2.56% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                        | United States Senator      |       |                        |       |                       |       |                         |       |
|------------------------|----------------------------|-------|------------------------|-------|-----------------------|-------|-------------------------|-------|
|                        | MARK MATTHEW<br>HERD (LIB) |       | JOHN T. PARKER<br>(PF) |       | MIKE BEITIKS<br>(NPP) |       | ELEANOR GARCIA<br>(NPP) |       |
| 95U303.05-MB 95U303.05 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 1                          | 1.05% | 0                      | 0.00% | 1                     | 1.05% | 2                       | 2.11% |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 1                          | 1.04% | 0                      | 0.00% | 1                     | 1.04% | 2                       | 2.08% |
| 95U304.05-MB 95U304.05 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Total                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| 95U305.05-MB 95U305.05 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Provisional            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Total                  | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| 95U401.08-MB 95U401.08 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Provisional            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Total                  | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| 95U402.07-MB 95U402.07 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 3                          | 1.28% | 2                      | 0.85% | 1                     | 0.43% | 0                       | 0.00% |
| Provisional            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Total                  | 3                          | 1.28% | 2                      | 0.85% | 1                     | 0.43% | 0                       | 0.00% |
| 95U402.08-MB 95U402.08 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 2                          | 0.40% | 0                      | 0.00% | 0                     | 0.00% | 1                       | 0.20% |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 2                          | 0.39% | 0                      | 0.00% | 0                     | 0.00% | 1                       | 0.20% |
| 95U403.08-MB 95U403.08 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 1                          | 0.11% | 1                      | 0.11% | 9                     | 0.95% | 3                       | 0.32% |
| Provisional            | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                  | 1                          | 0.10% | 1                      | 0.10% | 9                     | 0.94% | 3                       | 0.31% |
| 95U404.06-MB 95U404.06 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Provisional            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Total                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| 95U404.08-MB 95U404.08 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM           | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct            | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                               | United States Senator      |       |                        |       |                       |       |                         |       |
|-------------------------------|----------------------------|-------|------------------------|-------|-----------------------|-------|-------------------------|-------|
|                               | MARK MATTHEW<br>HERD (LIB) |       | JOHN T. PARKER<br>(PF) |       | MIKE BEITIKS<br>(NPP) |       | ELEANOR GARCIA<br>(NPP) |       |
| Provisional                   | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Total                         | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| 95U501.09-MB 95U501.09        |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS                   | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct                   | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Provisional                   | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Total                         | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| 95U501.10-MB 95U501.10        |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS                   | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct                   | 1                          | 0.63% | 0                      | 0.00% | 0                     | 0.00% | 3                       | 1.88% |
| Provisional                   | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                         | 1                          | 0.62% | 0                      | 0.00% | 0                     | 0.00% | 3                       | 1.85% |
| 95U598.10-MB 95U598.10        |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS                   | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct                   | 0                          | 0.00% | 1                      | 0.47% | 4                     | 1.87% | 0                       | 0.00% |
| Provisional                   | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                         | 0                          | 0.00% | 1                      | 0.46% | 4                     | 1.85% | 0                       | 0.00% |
| 95U599.10-MB 95U599.10        |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS                   | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct                   | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Provisional                   | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Total                         | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| 96U101.11-MB 96U101.11        |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS                   | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct                   | 1                          | 0.27% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Provisional                   | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                         | 1                          | 0.27% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| 96U401.14-MB 96U401.14        |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS                   | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct VBM                  | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| MB Precinct                   | 5                          | 1.55% | 2                      | 0.62% | 0                     | 0.00% | 0                       | 0.00% |
| Provisional                   | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Total                         | 5                          | 1.52% | 2                      | 0.61% | 0                     | 0.00% | 0                       | 0.00% |
| Total                         |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                  | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct OS                   | 101                        | 0.94% | 25                     | 0.23% | 56                    | 0.52% | 43                      | 0.40% |
| Precinct VBM                  | 228                        | 0.56% | 133                    | 0.33% | 225                   | 0.55% | 113                     | 0.28% |
| MB Precinct                   | 25                         | 0.54% | 10                     | 0.22% | 22                    | 0.48% | 13                      | 0.28% |
| Provisional                   | 21                         | 1.60% | 11                     | 0.84% | 19                    | 1.45% | 4                       | 0.30% |
| Total                         | 375                        | 0.65% | 179                    | 0.31% | 322                   | 0.56% | 173                     | 0.30% |
| STATE BOARD OF EQUALIZATION   |                            |       |                        |       |                       |       |                         |       |
| STATE BOARD OF EQUALIZATION 1 |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                  | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct OS                   | 101                        | 0.94% | 25                     | 0.23% | 56                    | 0.52% | 43                      | 0.40% |
| Precinct VBM                  | 228                        | 0.56% | 133                    | 0.33% | 225                   | 0.55% | 113                     | 0.28% |
| MB Precinct                   | 25                         | 0.54% | 10                     | 0.22% | 22                    | 0.48% | 13                      | 0.28% |
| Provisional                   | 21                         | 1.60% | 11                     | 0.84% | 19                    | 1.45% | 4                       | 0.30% |
| Total                         | 375                        | 0.65% | 179                    | 0.31% | 322                   | 0.56% | 173                     | 0.30% |
| Total                         |                            |       |                        |       |                       |       |                         |       |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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|                                     | United States Senator      |              |                        |              |                       |              |                         |              |
|-------------------------------------|----------------------------|--------------|------------------------|--------------|-----------------------|--------------|-------------------------|--------------|
|                                     | MARK MATTHEW<br>HERD (LIB) |              | JOHN T. PARKER<br>(PF) |              | MIKE BEITIKS<br>(NPP) |              | ELEANOR GARCIA<br>(NPP) |              |
| Precinct TSx                        | 0                          | 0.00%        | 0                      | 0.00%        | 0                     | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 101                        | 0.94%        | 25                     | 0.23%        | 56                    | 0.52%        | 43                      | 0.40%        |
| Precinct VBM                        | 228                        | 0.56%        | 133                    | 0.33%        | 225                   | 0.55%        | 113                     | 0.28%        |
| MB Precinct                         | 25                         | 0.54%        | 10                     | 0.22%        | 22                    | 0.48%        | 13                      | 0.28%        |
| Provisional                         | 21                         | 1.60%        | 11                     | 0.84%        | 19                    | 1.45%        | 4                       | 0.30%        |
| <b>Total</b>                        | <b>375</b>                 | <b>0.65%</b> | <b>179</b>             | <b>0.31%</b> | <b>322</b>            | <b>0.56%</b> | <b>173</b>              | <b>0.30%</b> |
| <b>UNITED STATES SENATE</b>         |                            |              |                        |              |                       |              |                         |              |
| <b>UNITED STATES SENATE</b>         |                            |              |                        |              |                       |              |                         |              |
| Precinct TSx                        | 0                          | 0.00%        | 0                      | 0.00%        | 0                     | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 101                        | 0.94%        | 25                     | 0.23%        | 56                    | 0.52%        | 43                      | 0.40%        |
| Precinct VBM                        | 228                        | 0.56%        | 133                    | 0.33%        | 225                   | 0.55%        | 113                     | 0.28%        |
| MB Precinct                         | 25                         | 0.54%        | 10                     | 0.22%        | 22                    | 0.48%        | 13                      | 0.28%        |
| Provisional                         | 21                         | 1.60%        | 11                     | 0.84%        | 19                    | 1.45%        | 4                       | 0.30%        |
| <b>Total</b>                        | <b>375</b>                 | <b>0.65%</b> | <b>179</b>             | <b>0.31%</b> | <b>322</b>            | <b>0.56%</b> | <b>173</b>              | <b>0.30%</b> |
| <b>Total</b>                        | <b>0</b>                   | <b>0.00%</b> | <b>0</b>               | <b>0.00%</b> | <b>0</b>              | <b>0.00%</b> | <b>0</b>                | <b>0.00%</b> |
| Precinct TSx                        | 0                          | 0.00%        | 0                      | 0.00%        | 0                     | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 101                        | 0.94%        | 25                     | 0.23%        | 56                    | 0.52%        | 43                      | 0.40%        |
| Precinct VBM                        | 228                        | 0.56%        | 133                    | 0.33%        | 225                   | 0.55%        | 113                     | 0.28%        |
| MB Precinct                         | 25                         | 0.54%        | 10                     | 0.22%        | 22                    | 0.48%        | 13                      | 0.28%        |
| Provisional                         | 21                         | 1.60%        | 11                     | 0.84%        | 19                    | 1.45%        | 4                       | 0.30%        |
| <b>Total</b>                        | <b>375</b>                 | <b>0.65%</b> | <b>179</b>             | <b>0.31%</b> | <b>322</b>            | <b>0.56%</b> | <b>173</b>              | <b>0.30%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |                            |              |                        |              |                       |              |                         |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                            |              |                        |              |                       |              |                         |              |
| Precinct TSx                        | 0                          | 0.00%        | 0                      | 0.00%        | 0                     | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 101                        | 0.94%        | 25                     | 0.23%        | 56                    | 0.52%        | 43                      | 0.40%        |
| Precinct VBM                        | 228                        | 0.56%        | 133                    | 0.33%        | 225                   | 0.55%        | 113                     | 0.28%        |
| MB Precinct                         | 25                         | 0.54%        | 10                     | 0.22%        | 22                    | 0.48%        | 13                      | 0.28%        |
| Provisional                         | 21                         | 1.60%        | 11                     | 0.84%        | 19                    | 1.45%        | 4                       | 0.30%        |
| <b>Total</b>                        | <b>375</b>                 | <b>0.65%</b> | <b>179</b>             | <b>0.31%</b> | <b>322</b>            | <b>0.56%</b> | <b>173</b>              | <b>0.30%</b> |
| <b>Total</b>                        | <b>0</b>                   | <b>0.00%</b> | <b>0</b>               | <b>0.00%</b> | <b>0</b>              | <b>0.00%</b> | <b>0</b>                | <b>0.00%</b> |
| Precinct TSx                        | 0                          | 0.00%        | 0                      | 0.00%        | 0                     | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 101                        | 0.94%        | 25                     | 0.23%        | 56                    | 0.52%        | 43                      | 0.40%        |
| Precinct VBM                        | 228                        | 0.56%        | 133                    | 0.33%        | 225                   | 0.55%        | 113                     | 0.28%        |
| MB Precinct                         | 25                         | 0.54%        | 10                     | 0.22%        | 22                    | 0.48%        | 13                      | 0.28%        |
| Provisional                         | 21                         | 1.60%        | 11                     | 0.84%        | 19                    | 1.45%        | 4                       | 0.30%        |
| <b>Total</b>                        | <b>375</b>                 | <b>0.65%</b> | <b>179</b>             | <b>0.31%</b> | <b>322</b>            | <b>0.56%</b> | <b>173</b>              | <b>0.30%</b> |
| <b>STATE SENATE</b>                 |                            |              |                        |              |                       |              |                         |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                            |              |                        |              |                       |              |                         |              |
| Precinct TSx                        | 0                          | 0.00%        | 0                      | 0.00%        | 0                     | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 101                        | 0.94%        | 25                     | 0.23%        | 56                    | 0.52%        | 43                      | 0.40%        |
| Precinct VBM                        | 228                        | 0.56%        | 133                    | 0.33%        | 225                   | 0.55%        | 113                     | 0.28%        |
| MB Precinct                         | 25                         | 0.54%        | 10                     | 0.22%        | 22                    | 0.48%        | 13                      | 0.28%        |
| Provisional                         | 21                         | 1.60%        | 11                     | 0.84%        | 19                    | 1.45%        | 4                       | 0.30%        |
| <b>Total</b>                        | <b>375</b>                 | <b>0.65%</b> | <b>179</b>             | <b>0.31%</b> | <b>322</b>            | <b>0.56%</b> | <b>173</b>              | <b>0.30%</b> |
| <b>Total</b>                        | <b>0</b>                   | <b>0.00%</b> | <b>0</b>               | <b>0.00%</b> | <b>0</b>              | <b>0.00%</b> | <b>0</b>                | <b>0.00%</b> |
| Precinct TSx                        | 0                          | 0.00%        | 0                      | 0.00%        | 0                     | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 101                        | 0.94%        | 25                     | 0.23%        | 56                    | 0.52%        | 43                      | 0.40%        |
| Precinct VBM                        | 228                        | 0.56%        | 133                    | 0.33%        | 225                   | 0.55%        | 113                     | 0.28%        |
| MB Precinct                         | 25                         | 0.54%        | 10                     | 0.22%        | 22                    | 0.48%        | 13                      | 0.28%        |
| Provisional                         | 21                         | 1.60%        | 11                     | 0.84%        | 19                    | 1.45%        | 4                       | 0.30%        |
| <b>Total</b>                        | <b>375</b>                 | <b>0.65%</b> | <b>179</b>             | <b>0.31%</b> | <b>322</b>            | <b>0.56%</b> | <b>173</b>              | <b>0.30%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                            |              |                        |              |                       |              |                         |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                            |              |                        |              |                       |              |                         |              |
| Precinct TSx                        | 0                          | 0.00%        | 0                      | 0.00%        | 0                     | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 61                         | 0.90%        | 20                     | 0.29%        | 47                    | 0.69%        | 33                      | 0.48%        |
| Precinct VBM                        | 147                        | 0.58%        | 94                     | 0.37%        | 180                   | 0.71%        | 87                      | 0.34%        |
| MB Precinct                         | 19                         | 0.48%        | 8                      | 0.20%        | 22                    | 0.56%        | 13                      | 0.33%        |

**COUNTY OF EL DORADO**  
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|                                   | United States Senator      |       |                        |       |                       |       |                         |       |
|-----------------------------------|----------------------------|-------|------------------------|-------|-----------------------|-------|-------------------------|-------|
|                                   | MARK MATTHEW<br>HERD (LIB) |       | JOHN T. PARKER<br>(PF) |       | MIKE BEITIKS<br>(NPP) |       | ELEANOR GARCIA<br>(NPP) |       |
| Provisional                       | 17                         | 1.98% | 7                      | 0.81% | 14                    | 1.63% | 2                       | 0.23% |
| Total                             | 244                        | 0.66% | 129                    | 0.35% | 263                   | 0.71% | 135                     | 0.36% |
| <b>6TH STATE ASSEMBLY DIST</b>    |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                      | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct OS                       | 40                         | 1.03% | 5                      | 0.13% | 9                     | 0.23% | 10                      | 0.26% |
| Precinct VBM                      | 81                         | 0.52% | 39                     | 0.25% | 45                    | 0.29% | 26                      | 0.17% |
| MB Precinct                       | 6                          | 0.87% | 2                      | 0.29% | 0                     | 0.00% | 0                       | 0.00% |
| Provisional                       | 4                          | 0.88% | 4                      | 0.88% | 5                     | 1.10% | 2                       | 0.44% |
| Total                             | 131                        | 0.64% | 50                     | 0.24% | 59                    | 0.29% | 38                      | 0.19% |
| <b>Total</b>                      |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                      | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct OS                       | 101                        | 0.94% | 25                     | 0.23% | 56                    | 0.52% | 43                      | 0.40% |
| Precinct VBM                      | 228                        | 0.56% | 133                    | 0.33% | 225                   | 0.55% | 113                     | 0.28% |
| MB Precinct                       | 25                         | 0.54% | 10                     | 0.22% | 22                    | 0.48% | 13                      | 0.28% |
| Provisional                       | 21                         | 1.60% | 11                     | 0.84% | 19                    | 1.45% | 4                       | 0.30% |
| Total                             | 375                        | 0.65% | 179                    | 0.31% | 322                   | 0.56% | 173                     | 0.30% |
| <b>SUPERVISORIAL DISTRICTS</b>    |                            |       |                        |       |                       |       |                         |       |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                      | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS                       | 18                         | 0.81% | 4                      | 0.18% | 5                     | 0.22% | 6                       | 0.27% |
| Precinct VBM                      | 48                         | 0.52% | 14                     | 0.15% | 19                    | 0.20% | 12                      | 0.13% |
| MB Precinct                       | 1                          | 0.27% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Provisional                       | 2                          | 0.82% | 1                      | 0.41% | 3                     | 1.23% | 2                       | 0.82% |
| Total                             | 69                         | 0.57% | 19                     | 0.16% | 27                    | 0.22% | 20                      | 0.16% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                      | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct OS                       | 23                         | 1.02% | 5                      | 0.22% | 7                     | 0.31% | 4                       | 0.18% |
| Precinct VBM                      | 48                         | 0.56% | 32                     | 0.37% | 49                    | 0.57% | 22                      | 0.25% |
| MB Precinct                       | 7                          | 0.50% | 4                      | 0.28% | 7                     | 0.50% | 4                       | 0.28% |
| Provisional                       | 3                          | 1.08% | 3                      | 1.08% | 5                     | 1.81% | 0                       | 0.00% |
| Total                             | 81                         | 0.64% | 44                     | 0.35% | 68                    | 0.54% | 30                      | 0.24% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                      | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct OS                       | 20                         | 0.94% | 4                      | 0.19% | 17                    | 0.80% | 10                      | 0.47% |
| Precinct VBM                      | 48                         | 0.53% | 31                     | 0.34% | 60                    | 0.66% | 33                      | 0.36% |
| MB Precinct                       | 5                          | 1.26% | 0                      | 0.00% | 1                     | 0.25% | 2                       | 0.50% |
| Provisional                       | 6                          | 2.53% | 1                      | 0.42% | 0                     | 0.00% | 1                       | 0.42% |
| Total                             | 79                         | 0.67% | 36                     | 0.30% | 78                    | 0.66% | 46                      | 0.39% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                      | 0                          | -     | 0                      | -     | 0                     | -     | 0                       | -     |
| Precinct OS                       | 22                         | 0.96% | 5                      | 0.22% | 12                    | 0.52% | 12                      | 0.52% |
| Precinct VBM                      | 51                         | 0.60% | 25                     | 0.29% | 47                    | 0.55% | 29                      | 0.34% |
| MB Precinct                       | 11                         | 0.54% | 5                      | 0.25% | 10                    | 0.49% | 4                       | 0.20% |
| Provisional                       | 5                          | 1.96% | 4                      | 1.57% | 2                     | 0.78% | 1                       | 0.39% |
| Total                             | 89                         | 0.68% | 39                     | 0.30% | 71                    | 0.54% | 46                      | 0.35% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                      | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct OS                       | 18                         | 1.00% | 7                      | 0.39% | 15                    | 0.83% | 11                      | 0.61% |
| Precinct VBM                      | 33                         | 0.61% | 31                     | 0.57% | 50                    | 0.93% | 17                      | 0.32% |
| MB Precinct                       | 1                          | 0.24% | 1                      | 0.24% | 4                     | 0.98% | 3                       | 0.73% |
| Provisional                       | 5                          | 1.66% | 2                      | 0.66% | 9                     | 2.98% | 0                       | 0.00% |
| Total                             | 57                         | 0.72% | 41                     | 0.52% | 78                    | 0.99% | 31                      | 0.39% |
| <b>Total</b>                      |                            |       |                        |       |                       |       |                         |       |
| Precinct TSx                      | 0                          | 0.00% | 0                      | 0.00% | 0                     | 0.00% | 0                       | 0.00% |
| Precinct OS                       | 101                        | 0.94% | 25                     | 0.23% | 56                    | 0.52% | 43                      | 0.40% |
| Precinct VBM                      | 228                        | 0.56% | 133                    | 0.33% | 225                   | 0.55% | 113                     | 0.28% |
| MB Precinct                       | 25                         | 0.54% | 10                     | 0.22% | 22                    | 0.48% | 13                      | 0.28% |
| Provisional                       | 21                         | 1.60% | 11                     | 0.84% | 19                    | 1.45% | 4                       | 0.30% |
| Total                             | 375                        | 0.65% | 179                    | 0.31% | 322                   | 0.56% | 173                     | 0.30% |

**COUNTY OF EL DORADO**  
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|                                 | United States Senator      |              |                        |              |                       |              |                         |              |
|---------------------------------|----------------------------|--------------|------------------------|--------------|-----------------------|--------------|-------------------------|--------------|
|                                 | MARK MATTHEW<br>HERD (LIB) |              | JOHN T. PARKER<br>(PF) |              | MIKE BEITIKS<br>(NPP) |              | ELEANOR GARCIA<br>(NPP) |              |
| <b>CITIES</b>                   |                            |              |                        |              |                       |              |                         |              |
| <b>CITY OF PLACERVILLE</b>      |                            |              |                        |              |                       |              |                         |              |
| Precinct TSx                    | 0                          | -            | 0                      | -            | 0                     | -            | 0                       | -            |
| Precinct OS                     | 4                          | 0.72%        | 2                      | 0.36%        | 8                     | 1.43%        | 4                       | 0.72%        |
| Precinct VBM                    | 16                         | 0.68%        | 15                     | 0.64%        | 23                    | 0.98%        | 8                       | 0.34%        |
| MB Precinct                     | 0                          | -            | 0                      | -            | 0                     | -            | 0                       | -            |
| Provisional                     | 1                          | 1.43%        | 1                      | 1.43%        | 0                     | 0.00%        | 1                       | 1.43%        |
| <b>Total</b>                    | <b>21</b>                  | <b>0.71%</b> | <b>18</b>              | <b>0.61%</b> | <b>31</b>             | <b>1.04%</b> | <b>13</b>               | <b>0.44%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                            |              |                        |              |                       |              |                         |              |
| Precinct TSx                    | 0                          | -            | 0                      | -            | 0                     | -            | 0                       | -            |
| Precinct OS                     | 12                         | 1.27%        | 3                      | 0.32%        | 9                     | 0.95%        | 8                       | 0.84%        |
| Precinct VBM                    | 14                         | 0.57%        | 17                     | 0.69%        | 31                    | 1.26%        | 12                      | 0.49%        |
| MB Precinct                     | 0                          | -            | 0                      | -            | 0                     | -            | 0                       | -            |
| Provisional                     | 3                          | 1.61%        | 2                      | 1.08%        | 6                     | 3.23%        | 0                       | 0.00%        |
| <b>Total</b>                    | <b>29</b>                  | <b>0.81%</b> | <b>22</b>              | <b>0.61%</b> | <b>46</b>             | <b>1.28%</b> | <b>20</b>               | <b>0.56%</b> |
| <b>UNINCORPORATED AREA</b>      |                            |              |                        |              |                       |              |                         |              |
| Precinct TSx                    | 0                          | 0.00%        | 0                      | 0.00%        | 0                     | 0.00%        | 0                       | 0.00%        |
| Precinct OS                     | 85                         | 0.92%        | 20                     | 0.22%        | 39                    | 0.42%        | 31                      | 0.34%        |
| Precinct VBM                    | 198                        | 0.55%        | 101                    | 0.28%        | 171                   | 0.47%        | 93                      | 0.26%        |
| MB Precinct                     | 25                         | 0.54%        | 10                     | 0.22%        | 22                    | 0.48%        | 13                      | 0.28%        |
| Provisional                     | 17                         | 1.61%        | 8                      | 0.76%        | 13                    | 1.23%        | 3                       | 0.28%        |
| <b>Total</b>                    | <b>325</b>                 | <b>0.64%</b> | <b>139</b>             | <b>0.27%</b> | <b>245</b>            | <b>0.48%</b> | <b>140</b>              | <b>0.27%</b> |
| <b>Total</b>                    |                            |              |                        |              |                       |              |                         |              |
| Precinct TSx                    | 0                          | 0.00%        | 0                      | 0.00%        | 0                     | 0.00%        | 0                       | 0.00%        |
| Precinct OS                     | 101                        | 0.94%        | 25                     | 0.23%        | 56                    | 0.52%        | 43                      | 0.40%        |
| Precinct VBM                    | 228                        | 0.56%        | 133                    | 0.33%        | 225                   | 0.55%        | 113                     | 0.28%        |
| MB Precinct                     | 25                         | 0.54%        | 10                     | 0.22%        | 22                    | 0.48%        | 13                      | 0.28%        |
| Provisional                     | 21                         | 1.60%        | 11                     | 0.84%        | 19                    | 1.45%        | 4                       | 0.30%        |
| <b>Total</b>                    | <b>375</b>                 | <b>0.65%</b> | <b>179</b>             | <b>0.31%</b> | <b>322</b>            | <b>0.56%</b> | <b>173</b>              | <b>0.30%</b> |

**COUNTY OF EL DORADO**  
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|                        | United States Senator  |       |                  |       |                        |       |                     |       |                    |       |
|------------------------|------------------------|-------|------------------|-------|------------------------|-------|---------------------|-------|--------------------|-------|
|                        | TIM GILDERSLEEVE (NPP) |       | CLIVE GREY (NPP) |       | DON J. GRUNDMANN (NPP) |       | JASON HANANIA (NPP) |       | PAUL MERRITT (NPP) |       |
| Jurisdiction Wide      |                        |       |                  |       |                        |       |                     |       |                    |       |
| 100001.11-CP 100001.11 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 1                      | 0.64% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 1                | 0.14% | 1                      | 0.14% | 3                   | 0.43% | 5                  | 0.71% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 6.25% | 0                  | 0.00% |
| Total                  | 1                      | 0.11% | 1                | 0.11% | 1                      | 0.11% | 4                   | 0.46% | 5                  | 0.57% |
| 100002.11-CP 100002.11 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 1.32% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 1                | 0.25% | 1                      | 0.25% | 1                   | 0.25% | 2                  | 0.50% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 1                | 0.20% | 1                      | 0.20% | 2                   | 0.40% | 2                  | 0.40% |
| 100003.11-CP 100003.11 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 1                      | 0.90% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 1                      | 0.14% | 4                | 0.56% | 1                      | 0.14% | 1                   | 0.14% | 2                  | 0.28% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 1                      | 0.12% | 4                | 0.48% | 2                      | 0.24% | 1                   | 0.12% | 2                  | 0.24% |
| 100004.11-CP 100004.11 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 0.43% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 1                | 0.15% | 3                      | 0.45% | 3                   | 0.45% | 0                  | 0.00% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 1                | 0.11% | 3                      | 0.33% | 4                   | 0.44% | 0                  | 0.00% |
| 100005.11-CP 100005.11 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.75% | 0                      | 0.00% | 0                   | 0.00% | 2                  | 1.49% |
| Precinct VBM           | 1                      | 0.12% | 0                | 0.00% | 0                      | 0.00% | 2                   | 0.25% | 2                  | 0.25% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 1                      | 0.10% | 1                | 0.10% | 0                      | 0.00% | 2                   | 0.21% | 4                  | 0.42% |
| 100006.11-CP 100006.11 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.45% | 0                      | 0.00% | 1                   | 0.45% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 1                | 0.12% | 0                      | 0.00% | 6                   | 0.71% | 0                  | 0.00% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 2                | 0.18% | 0                      | 0.00% | 7                   | 0.64% | 0                  | 0.00% |
| 100008.11-CP 100008.11 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 0.57% | 1                  | 0.57% |
| Precinct VBM           | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 0.15% | 2                  | 0.31% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 2                   | 0.24% | 3                  | 0.36% |
| 100009.11-CP 100009.11 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 1                  | 0.68% |
| Precinct VBM           | 0                      | 0.00% | 0                | 0.00% | 1                      | 0.15% | 2                   | 0.30% | 2                  | 0.30% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 0                | 0.00% | 1                      | 0.12% | 2                   | 0.24% | 3                  | 0.36% |
| 100012.11-CP 100012.11 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 2                | 0.87% | 1                      | 0.44% | 0                   | 0.00% | 1                  | 0.44% |
| Precinct VBM           | 0                      | 0.00% | 1                | 0.12% | 1                      | 0.12% | 5                   | 0.58% | 1                  | 0.12% |

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|                        | United States Senator  |       |                  |       |                        |       |                     |       |                    |       |
|------------------------|------------------------|-------|------------------|-------|------------------------|-------|---------------------|-------|--------------------|-------|
|                        | TIM GILDERSLEEVE (NPP) |       | CLIVE GREY (NPP) |       | DON J. GRUNDMANN (NPP) |       | JASON HANANIA (NPP) |       | PAUL MERRITT (NPP) |       |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 3.57% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 3                | 0.27% | 2                      | 0.18% | 6                   | 0.54% | 2                  | 0.18% |
| 100013.11-CP 100013.11 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 3                | 1.36% | 0                      | 0.00% | 1                   | 0.45% | 1                  | 0.45% |
| Precinct VBM           | 1                      | 0.12% | 4                | 0.49% | 1                      | 0.12% | 1                   | 0.12% | 6                  | 0.74% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 1                      | 0.09% | 7                | 0.66% | 1                      | 0.09% | 2                   | 0.19% | 7                  | 0.66% |
| 100014.11-CP 100014.11 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.58% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 2                | 0.32% | 1                      | 0.16% | 0                   | 0.00% | 2                  | 0.32% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 3                | 0.37% | 1                      | 0.12% | 0                   | 0.00% | 2                  | 0.24% |
| 100022.11-CP 100022.11 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 1                      | 0.62% | 0                | 0.00% | 0                      | 0.00% | 2                   | 1.24% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 1                      | 0.13% | 0                | 0.00% | 0                      | 0.00% | 2                   | 0.26% | 0                  | 0.00% |
| 100023.11-CP 100023.11 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.52% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 5                | 0.54% | 0                      | 0.00% | 2                   | 0.21% | 2                  | 0.21% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 1                | 3.57% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 7                | 0.61% | 0                      | 0.00% | 2                   | 0.17% | 2                  | 0.17% |
| 200003.12-CP 200003.12 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 1                      | 0.68% | 1                | 0.68% | 0                      | 0.00% | 2                   | 1.35% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 1                | 0.20% | 0                      | 0.00% | 3                   | 0.60% | 5                  | 0.99% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 1                      | 0.15% | 2                | 0.30% | 0                      | 0.00% | 5                   | 0.74% | 5                  | 0.74% |
| 200004.12-CP 200004.12 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 2                | 1.21% | 0                      | 0.00% | 0                   | 0.00% | 1                  | 0.61% |
| Precinct VBM           | 0                      | 0.00% | 2                | 0.33% | 0                      | 0.00% | 0                   | 0.00% | 2                  | 0.33% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 7.14% | 1                  | 7.14% |
| Total                  | 0                      | 0.00% | 4                | 0.51% | 0                      | 0.00% | 1                   | 0.13% | 4                  | 0.51% |
| 200005.02-CP 200005.02 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 2                | 0.49% | 0                      | 0.00% | 2                   | 0.49% | 2                  | 0.49% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 2                | 0.39% | 0                      | 0.00% | 2                   | 0.39% | 2                  | 0.39% |
| 200006.12-CP 200006.12 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.93% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 1                      | 0.21% | 1                | 0.21% | 1                      | 0.21% | 2                   | 0.42% | 1                  | 0.21% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 3.70% | 0                  | 0.00% |
| Total                  | 1                      | 0.16% | 2                | 0.33% | 1                      | 0.16% | 3                   | 0.49% | 1                  | 0.16% |
| 200008.12-CP 200008.12 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | United States Senator  |       |                  |       |                        |       |                     |        |                    |       |
|------------------------|------------------------|-------|------------------|-------|------------------------|-------|---------------------|--------|--------------------|-------|
|                        | TIM GILDERSLEEVE (NPP) |       | CLIVE GREY (NPP) |       | DON J. GRUNDMANN (NPP) |       | JASON HANANIA (NPP) |        | PAUL MERRITT (NPP) |       |
| Precinct OS            | 1                      | 0.75% | 1                | 0.75% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Precinct VBM           | 1                      | 0.31% | 3                | 0.92% | 0                      | 0.00% | 2                   | 0.61%  | 2                  | 0.61% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Total                  | 2                      | 0.43% | 4                | 0.85% | 0                      | 0.00% | 2                   | 0.43%  | 2                  | 0.43% |
| 200010.12-CP 200010.12 |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx           | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 25.00% | 0                  | 0.00% |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Precinct VBM           | 2                      | 0.27% | 1                | 0.13% | 1                      | 0.13% | 2                   | 0.27%  | 2                  | 0.27% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 3.85%  | 0                  | 0.00% |
| Total                  | 2                      | 0.21% | 1                | 0.10% | 1                      | 0.10% | 4                   | 0.42%  | 2                  | 0.21% |
| 200012.12-CP 200012.12 |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 2                | 1.59% | 1                      | 0.79% | 0                   | 0.00%  | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 0                | 0.00% | 3                      | 0.74% | 0                   | 0.00%  | 1                  | 0.25% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 2                | 0.37% | 4                      | 0.74% | 0                   | 0.00%  | 1                  | 0.18% |
| 200013.12-CP 200013.12 |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.52% | 0                      | 0.00% | 0                   | 0.00%  | 1                  | 0.52% |
| Precinct VBM           | 0                      | 0.00% | 3                | 0.47% | 0                      | 0.00% | 0                   | 0.00%  | 2                  | 0.31% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Provisional            | 0                      | 0.00% | 1                | 5.00% | 1                      | 5.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 5                | 0.59% | 1                      | 0.12% | 0                   | 0.00%  | 3                  | 0.35% |
| 200014.12-CP 200014.12 |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 2                      | 1.07% | 0                   | 0.00%  | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 2                | 0.36% | 2                      | 0.36% | 2                   | 0.36%  | 0                  | 0.00% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 2                | 0.26% | 4                      | 0.53% | 2                   | 0.26%  | 0                  | 0.00% |
| 200015.02-CP 200015.02 |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 2                | 1.03% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Precinct VBM           | 1                      | 0.13% | 0                | 0.00% | 1                      | 0.13% | 1                   | 0.13%  | 3                  | 0.38% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Provisional            | 0                      | 0.00% | 1                | 5.00% | 0                      | 0.00% | 1                   | 5.00%  | 1                  | 5.00% |
| Total                  | 1                      | 0.10% | 3                | 0.30% | 1                      | 0.10% | 2                   | 0.20%  | 4                  | 0.40% |
| 200016.12-CP 200016.12 |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 1                      | 5.00% | 0                   | 0.00%  | 1                  | 5.00% |
| Precinct VBM           | 4                      | 3.17% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Total                  | 4                      | 2.68% | 0                | 0.00% | 1                      | 0.67% | 0                   | 0.00%  | 1                  | 0.67% |
| 200016.13-CP 200016.13 |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 1                  | 1.39% |
| Precinct VBM           | 0                      | 0.00% | 0                | 0.00% | 2                      | 0.68% | 0                   | 0.00%  | 1                  | 0.34% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 0                | 0.00% | 2                      | 0.54% | 0                   | 0.00%  | 2                  | 0.54% |
| 200017.02-CP 200017.02 |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS            | 1                      | 0.78% | 1                | 0.78% | 0                      | 0.00% | 0                   | 0.00%  | 2                  | 1.56% |
| Precinct VBM           | 1                      | 0.18% | 4                | 0.73% | 0                      | 0.00% | 6                   | 1.10%  | 0                  | 0.00% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Total                  | 2                      | 0.29% | 5                | 0.73% | 0                      | 0.00% | 6                   | 0.87%  | 2                  | 0.29% |

**COUNTY OF EL DORADO**  
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|                        | United States Senator  |       |                  |       |                        |       |                     |       |                    |        |
|------------------------|------------------------|-------|------------------|-------|------------------------|-------|---------------------|-------|--------------------|--------|
|                        | TIM GILDERSLEEVE (NPP) |       | CLIVE GREY (NPP) |       | DON J. GRUNDMANN (NPP) |       | JASON HANANIA (NPP) |       | PAUL MERRITT (NPP) |        |
| 200018.02-CP 200018.02 |                        |       |                  |       |                        |       |                     |       |                    |        |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Precinct OS            | 0                      | 0.00% | 1                | 0.66% | 1                      | 0.66% | 1                   | 0.66% | 0                  | 0.00%  |
| Precinct VBM           | 0                      | 0.00% | 7                | 1.11% | 0                      | 0.00% | 0                   | 0.00% | 2                  | 0.32%  |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00%  |
| Total                  | 0                      | 0.00% | 8                | 1.01% | 1                      | 0.13% | 1                   | 0.13% | 2                  | 0.25%  |
| 200020.02-CP 200020.02 |                        |       |                  |       |                        |       |                     |       |                    |        |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 0.81% | 0                  | 0.00%  |
| Precinct VBM           | 1                      | 0.21% | 1                | 0.21% | 0                      | 0.00% | 1                   | 0.21% | 2                  | 0.42%  |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 1                      | 5.88% | 0                   | 0.00% | 0                  | 0.00%  |
| Total                  | 1                      | 0.16% | 1                | 0.16% | 1                      | 0.16% | 2                   | 0.32% | 2                  | 0.32%  |
| 200021.01-CP 200021.01 |                        |       |                  |       |                        |       |                     |       |                    |        |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 2                  | 6.67%  |
| Precinct VBM           | 0                      | 0.00% | 2                | 1.11% | 1                      | 0.56% | 0                   | 0.00% | 0                  | 0.00%  |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00%  |
| Total                  | 0                      | 0.00% | 2                | 0.93% | 1                      | 0.47% | 0                   | 0.00% | 2                  | 0.93%  |
| 200021.02-CP 200021.02 |                        |       |                  |       |                        |       |                     |       |                    |        |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Precinct OS            | 1                      | 0.65% | 2                | 1.30% | 0                      | 0.00% | 0                   | 0.00% | 1                  | 0.65%  |
| Precinct VBM           | 2                      | 0.30% | 6                | 0.89% | 0                      | 0.00% | 2                   | 0.30% | 2                  | 0.30%  |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00%  |
| Total                  | 3                      | 0.36% | 8                | 0.95% | 0                      | 0.00% | 2                   | 0.24% | 3                  | 0.36%  |
| 200024.02-CP 200024.02 |                        |       |                  |       |                        |       |                     |       |                    |        |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 2                      | 3.77% | 0                   | 0.00% | 0                  | 0.00%  |
| Precinct VBM           | 0                      | 0.00% | 1                | 0.38% | 0                      | 0.00% | 1                   | 0.38% | 2                  | 0.77%  |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00%  |
| Total                  | 0                      | 0.00% | 1                | 0.31% | 2                      | 0.63% | 1                   | 0.31% | 2                  | 0.63%  |
| 300001.04-CP 300001.04 |                        |       |                  |       |                        |       |                     |       |                    |        |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00%  |
| Precinct VBM           | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00%  |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Provisional            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Total                  | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00%  |
| 300001.05-CP 300001.05 |                        |       |                  |       |                        |       |                     |       |                    |        |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00%  |
| Precinct VBM           | 1                      | 0.34% | 0                | 0.00% | 1                      | 0.34% | 1                   | 0.34% | 2                  | 0.68%  |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 1                  | 11.11% |
| Total                  | 1                      | 0.27% | 0                | 0.00% | 1                      | 0.27% | 1                   | 0.27% | 3                  | 0.81%  |
| 300003.05-CP 300003.05 |                        |       |                  |       |                        |       |                     |       |                    |        |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Precinct OS            | 0                      | 0.00% | 1                | 0.56% | 0                      | 0.00% | 1                   | 0.56% | 1                  | 0.56%  |
| Precinct VBM           | 1                      | 0.12% | 1                | 0.12% | 1                      | 0.12% | 4                   | 0.48% | 2                  | 0.24%  |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00%  |
| Total                  | 1                      | 0.10% | 2                | 0.19% | 1                      | 0.10% | 5                   | 0.48% | 3                  | 0.29%  |
| 300004.05-CP 300004.05 |                        |       |                  |       |                        |       |                     |       |                    |        |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00%  |
| Precinct VBM           | 1                      | 0.19% | 2                | 0.38% | 0                      | 0.00% | 1                   | 0.19% | 0                  | 0.00%  |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -      |



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|                        | United States Senator  |       |                  |       |                        |       |                     |       |                    |       |
|------------------------|------------------------|-------|------------------|-------|------------------------|-------|---------------------|-------|--------------------|-------|
|                        | TIM GILDERSLEEVE (NPP) |       | CLIVE GREY (NPP) |       | DON J. GRUNDMANN (NPP) |       | JASON HANANIA (NPP) |       | PAUL MERRITT (NPP) |       |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 1                      | 0.14% | 2                | 0.29% | 0                      | 0.00% | 1                   | 0.14% | 0                  | 0.00% |
| 300005.05-CP 300005.05 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.69% | 0                      | 0.00% | 0                   | 0.00% | 1                  | 0.69% |
| Precinct VBM           | 0                      | 0.00% | 1                | 0.17% | 1                      | 0.17% | 0                   | 0.00% | 1                  | 0.17% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 2                | 0.26% | 1                      | 0.13% | 0                   | 0.00% | 2                  | 0.26% |
| 300008.05-CP 300008.05 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 1                      | 0.78% | 1                   | 0.78% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 7                | 1.03% | 1                      | 0.15% | 0                   | 0.00% | 3                  | 0.44% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 7                | 0.85% | 2                      | 0.24% | 1                   | 0.12% | 3                  | 0.37% |
| 300009.04-CP 300009.04 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 0.91% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 1                | 0.29% | 0                      | 0.00% | 0                   | 0.00% | 4                  | 1.16% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 1                | 0.21% | 0                      | 0.00% | 1                   | 0.21% | 4                  | 0.86% |
| 300010.04-CP 300010.04 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.65% | 0                      | 0.00% | 0                   | 0.00% | 1                  | 0.65% |
| Precinct VBM           | 2                      | 0.34% | 3                | 0.51% | 1                      | 0.17% | 2                   | 0.34% | 3                  | 0.51% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 9.09% | 0                  | 0.00% |
| Total                  | 2                      | 0.26% | 4                | 0.53% | 1                      | 0.13% | 3                   | 0.39% | 4                  | 0.53% |
| 300011.04-CP 300011.04 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 1                      | 0.77% | 1                   | 0.77% | 1                  | 0.77% |
| Precinct VBM           | 1                      | 0.16% | 3                | 0.47% | 0                      | 0.00% | 5                   | 0.78% | 2                  | 0.31% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 1                      | 0.13% | 3                | 0.38% | 1                      | 0.13% | 6                   | 0.76% | 3                  | 0.38% |
| 300013.05-CP 300013.05 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 1                      | 0.85% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 4                | 0.61% | 1                      | 0.15% | 2                   | 0.30% | 2                  | 0.30% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 4                | 0.51% | 2                      | 0.25% | 2                   | 0.25% | 2                  | 0.25% |
| 300015.04-CP 300015.04 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| 300015.05-CP 300015.05 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 2                | 0.88% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 2                | 0.69% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| 300017.05-CP 300017.05 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 1                      | 0.92% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 1                  | 0.92% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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|                        | United States Senator  |       |                  |       |                        |       |                     |       |                    |       |
|------------------------|------------------------|-------|------------------|-------|------------------------|-------|---------------------|-------|--------------------|-------|
|                        | TIM GILDERSLEEVE (NPP) |       | CLIVE GREY (NPP) |       | DON J. GRUNDMANN (NPP) |       | JASON HANANIA (NPP) |       | PAUL MERRITT (NPP) |       |
| Precinct VBM           | 4                      | 0.67% | 7                | 1.18% | 1                      | 0.17% | 4                   | 0.67% | 1                  | 0.17% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 5                      | 0.70% | 7                | 0.99% | 1                      | 0.14% | 4                   | 0.56% | 2                  | 0.28% |
| 300018.04-CP 300018.04 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 2                | 1.59% | 0                      | 0.00% | 1                   | 0.79% | 0                  | 0.00% |
| Precinct VBM           | 1                      | 0.22% | 2                | 0.44% | 1                      | 0.22% | 2                   | 0.44% | 2                  | 0.44% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 1                      | 4.00% | 0                   | 0.00% | 2                  | 8.00% |
| Total                  | 1                      | 0.17% | 4                | 0.66% | 2                      | 0.33% | 3                   | 0.50% | 4                  | 0.66% |
| 300020.04-CP 300020.04 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.69% | 0                      | 0.00% | 1                   | 0.69% | 1                  | 0.69% |
| Precinct VBM           | 2                      | 0.35% | 4                | 0.70% | 0                      | 0.00% | 2                   | 0.35% | 1                  | 0.18% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 2                      | 0.27% | 5                | 0.69% | 0                      | 0.00% | 3                   | 0.41% | 2                  | 0.27% |
| 300021.03-CP 300021.03 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 1                      | 0.75% | 1                   | 0.75% | 1                  | 0.75% |
| Precinct VBM           | 1                      | 0.18% | 5                | 0.89% | 1                      | 0.18% | 1                   | 0.18% | 3                  | 0.53% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 1                      | 0.14% | 5                | 0.71% | 2                      | 0.28% | 2                   | 0.28% | 4                  | 0.56% |
| 300022.05-CP 300022.05 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 1                      | 1.14% | 0                | 0.00% | 0                      | 0.00% | 1                   | 1.14% | 0                  | 0.00% |
| Precinct VBM           | 1                      | 0.31% | 1                | 0.31% | 0                      | 0.00% | 1                   | 0.31% | 1                  | 0.31% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 2                      | 0.47% | 1                | 0.24% | 0                      | 0.00% | 2                   | 0.47% | 1                  | 0.24% |
| 300023.03-CP 300023.03 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.48% | 0                      | 0.00% | 1                   | 0.48% | 1                  | 0.48% |
| Precinct VBM           | 4                      | 0.48% | 3                | 0.36% | 0                      | 0.00% | 1                   | 0.12% | 2                  | 0.24% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 1                | 5.26% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 4                      | 0.38% | 5                | 0.47% | 0                      | 0.00% | 2                   | 0.19% | 3                  | 0.28% |
| 300023.05-CP 300023.05 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 2.38% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 4                  | 2.31% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 1                | 0.45% | 0                      | 0.00% | 0                   | 0.00% | 4                  | 1.79% |
| 400001.14-CP 400001.14 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.63% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 2                      | 0.22% | 4                | 0.44% | 0                      | 0.00% | 5                   | 0.55% | 1                  | 0.11% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 2                      | 0.18% | 5                | 0.46% | 0                      | 0.00% | 5                   | 0.46% | 1                  | 0.09% |
| 400002.08-CP 400002.08 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 1                      | 0.57% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 2                  | 1.15% |
| Precinct VBM           | 3                      | 0.47% | 6                | 0.94% | 0                      | 0.00% | 2                   | 0.31% | 2                  | 0.31% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 1                | 7.69% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 4                      | 0.48% | 7                | 0.85% | 0                      | 0.00% | 2                   | 0.24% | 4                  | 0.48% |
| 400003.08-CP 400003.08 |                        |       |                  |       |                        |       |                     |       |                    |       |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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|                        | United States Senator  |              |                  |              |                        |              |                     |              |                    |              |
|------------------------|------------------------|--------------|------------------|--------------|------------------------|--------------|---------------------|--------------|--------------------|--------------|
|                        | TIM GILDERSLEEVE (NPP) |              | CLIVE GREY (NPP) |              | DON J. GRUNDMANN (NPP) |              | JASON HANANIA (NPP) |              | PAUL MERRITT (NPP) |              |
| Precinct TSx           | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Precinct OS            | 0                      | 0.00%        | 4                | 1.81%        | 3                      | 1.36%        | 1                   | 0.45%        | 0                  | 0.00%        |
| Precinct VBM           | 3                      | 0.43%        | 4                | 0.58%        | 1                      | 0.14%        | 4                   | 0.58%        | 2                  | 0.29%        |
| MB Precinct            | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Provisional            | 0                      | 0.00%        | 2                | 6.67%        | 0                      | 0.00%        | 0                   | 0.00%        | 1                  | 3.33%        |
| <b>Total</b>           | <b>3</b>               | <b>0.32%</b> | <b>10</b>        | <b>1.06%</b> | <b>4</b>               | <b>0.42%</b> | <b>5</b>            | <b>0.53%</b> | <b>3</b>           | <b>0.32%</b> |
| 400004.14-CP 400004.14 |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx           | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Precinct OS            | 1                      | 0.59%        | 1                | 0.59%        | 1                      | 0.59%        | 2                   | 1.18%        | 0                  | 0.00%        |
| Precinct VBM           | 0                      | 0.00%        | 4                | 0.66%        | 2                      | 0.33%        | 0                   | 0.00%        | 4                  | 0.66%        |
| MB Precinct            | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Provisional            | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 0                   | 0.00%        | 0                  | 0.00%        |
| <b>Total</b>           | <b>1</b>               | <b>0.13%</b> | <b>5</b>         | <b>0.63%</b> | <b>3</b>               | <b>0.38%</b> | <b>2</b>            | <b>0.25%</b> | <b>4</b>           | <b>0.50%</b> |
| 400005.08-CP 400005.08 |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx           | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Precinct OS            | 0                      | 0.00%        | 1                | 1.52%        | 1                      | 1.52%        | 0                   | 0.00%        | 0                  | 0.00%        |
| Precinct VBM           | 1                      | 0.43%        | 2                | 0.86%        | 0                      | 0.00%        | 0                   | 0.00%        | 1                  | 0.43%        |
| MB Precinct            | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Provisional            | 0                      | 0.00%        | 2                | 28.57%       | 0                      | 0.00%        | 0                   | 0.00%        | 0                  | 0.00%        |
| <b>Total</b>           | <b>1</b>               | <b>0.33%</b> | <b>5</b>         | <b>1.63%</b> | <b>1</b>               | <b>0.33%</b> | <b>0</b>            | <b>0.00%</b> | <b>1</b>           | <b>0.33%</b> |
| 400006.08-CP 400006.08 |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx           | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Precinct OS            | 0                      | 0.00%        | 1                | 0.48%        | 1                      | 0.48%        | 0                   | 0.00%        | 4                  | 1.90%        |
| Precinct VBM           | 0                      | 0.00%        | 2                | 0.31%        | 1                      | 0.16%        | 3                   | 0.47%        | 4                  | 0.62%        |
| MB Precinct            | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Provisional            | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 0                   | 0.00%        | 0                  | 0.00%        |
| <b>Total</b>           | <b>0</b>               | <b>0.00%</b> | <b>3</b>         | <b>0.35%</b> | <b>2</b>               | <b>0.23%</b> | <b>3</b>            | <b>0.35%</b> | <b>8</b>           | <b>0.92%</b> |
| 400007.08-CP 400007.08 |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx           | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Precinct OS            | 1                      | 0.47%        | 0                | 0.00%        | 1                      | 0.47%        | 0                   | 0.00%        | 0                  | 0.00%        |
| Precinct VBM           | 0                      | 0.00%        | 9                | 1.27%        | 0                      | 0.00%        | 1                   | 0.14%        | 3                  | 0.42%        |
| MB Precinct            | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Provisional            | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 0                   | 0.00%        | 1                  | 5.00%        |
| <b>Total</b>           | <b>1</b>               | <b>0.11%</b> | <b>9</b>         | <b>0.96%</b> | <b>1</b>               | <b>0.11%</b> | <b>1</b>            | <b>0.11%</b> | <b>4</b>           | <b>0.43%</b> |
| 400010.08-CP 400010.08 |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx           | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Precinct OS            | 1                      | 0.45%        | 2                | 0.90%        | 1                      | 0.45%        | 0                   | 0.00%        | 1                  | 0.45%        |
| Precinct VBM           | 1                      | 0.12%        | 4                | 0.49%        | 0                      | 0.00%        | 3                   | 0.37%        | 6                  | 0.74%        |
| MB Precinct            | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Provisional            | 0                      | 0.00%        | 0                | 0.00%        | 1                      | 4.76%        | 1                   | 4.76%        | 0                  | 0.00%        |
| <b>Total</b>           | <b>2</b>               | <b>0.19%</b> | <b>6</b>         | <b>0.57%</b> | <b>2</b>               | <b>0.19%</b> | <b>4</b>            | <b>0.38%</b> | <b>7</b>           | <b>0.66%</b> |
| 400012.08-CP 400012.08 |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx           | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Precinct OS            | 0                      | 0.00%        | 1                | 0.95%        | 0                      | 0.00%        | 0                   | 0.00%        | 0                  | 0.00%        |
| Precinct VBM           | 0                      | 0.00%        | 2                | 0.51%        | 0                      | 0.00%        | 6                   | 1.53%        | 1                  | 0.26%        |
| MB Precinct            | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Provisional            | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 0                   | 0.00%        | 0                  | 0.00%        |
| <b>Total</b>           | <b>0</b>               | <b>0.00%</b> | <b>3</b>         | <b>0.59%</b> | <b>0</b>               | <b>0.00%</b> | <b>6</b>            | <b>1.17%</b> | <b>1</b>           | <b>0.20%</b> |
| 400013.08-CP 400013.08 |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx           | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Precinct OS            | 1                      | 0.93%        | 2                | 1.87%        | 0                      | 0.00%        | 0                   | 0.00%        | 0                  | 0.00%        |
| Precinct VBM           | 1                      | 0.34%        | 1                | 0.34%        | 0                      | 0.00%        | 0                   | 0.00%        | 0                  | 0.00%        |
| MB Precinct            | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Provisional            | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 0                   | 0.00%        | 0                  | 0.00%        |
| <b>Total</b>           | <b>2</b>               | <b>0.50%</b> | <b>3</b>         | <b>0.75%</b> | <b>0</b>               | <b>0.00%</b> | <b>0</b>            | <b>0.00%</b> | <b>0</b>           | <b>0.00%</b> |
| 400014.08-CP 400014.08 |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx           | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Precinct OS            | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 0                   | 0.00%        | 1                  | 0.51%        |
| Precinct VBM           | 1                      | 0.15%        | 3                | 0.45%        | 0                      | 0.00%        | 0                   | 0.00%        | 4                  | 0.60%        |
| MB Precinct            | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Provisional            | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 0                   | 0.00%        | 0                  | 0.00%        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | United States Senator  |       |                  |       |                        |       |                     |       |                    |       |
|------------------------|------------------------|-------|------------------|-------|------------------------|-------|---------------------|-------|--------------------|-------|
|                        | TIM GILDERSLEEVE (NPP) |       | CLIVE GREY (NPP) |       | DON J. GRUNDMANN (NPP) |       | JASON HANANIA (NPP) |       | PAUL MERRITT (NPP) |       |
| Total                  | 1                      | 0.11% | 3                | 0.34% | 0                      | 0.00% | 0                   | 0.00% | 5                  | 0.56% |
| 400017.08-CP 400017.08 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 2                | 1.31% | 0                      | 0.00% | 0                   | 0.00% | 2                  | 1.31% |
| Precinct VBM           | 0                      | 0.00% | 3                | 0.45% | 0                      | 0.00% | 2                   | 0.30% | 2                  | 0.30% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 5                | 0.59% | 0                      | 0.00% | 2                   | 0.24% | 4                  | 0.47% |
| 400018.08-CP 400018.08 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 1.30% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 1                  | 0.42% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 1                | 0.31% | 0                      | 0.00% | 0                   | 0.00% | 1                  | 0.31% |
| 400020.08-CP 400020.08 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.95% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 3                | 0.97% | 0                      | 0.00% | 3                   | 0.97% | 0                  | 0.00% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 4                | 0.96% | 0                      | 0.00% | 3                   | 0.72% | 0                  | 0.00% |
| 400021.07-CP 400021.07 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 1                      | 0.85% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 1                | 0.15% | 2                      | 0.29% | 2                   | 0.29% | 1                  | 0.15% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 1                | 0.12% | 3                      | 0.37% | 2                   | 0.24% | 1                  | 0.12% |
| 500001.10-CP 500001.10 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 4                | 2.16% | 0                      | 0.00% | 1                   | 0.54% | 0                  | 0.00% |
| Precinct VBM           | 1                      | 0.19% | 5                | 0.93% | 0                      | 0.00% | 1                   | 0.19% | 5                  | 0.93% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 1                      | 0.14% | 9                | 1.22% | 0                      | 0.00% | 2                   | 0.27% | 5                  | 0.68% |
| 500002.10-CP 500002.10 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 1                  | 0.83% |
| Precinct VBM           | 0                      | 0.00% | 6                | 1.62% | 0                      | 0.00% | 3                   | 0.81% | 0                  | 0.00% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 1                | 4.00% | 1                      | 4.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 7                | 1.36% | 1                      | 0.19% | 3                   | 0.58% | 1                  | 0.19% |
| 500003.10-CP 500003.10 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.84% | 1                      | 0.84% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 1                      | 0.33% | 2                | 0.66% | 0                      | 0.00% | 4                   | 1.32% | 3                  | 0.99% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 1                      | 0.23% | 3                | 0.70% | 1                      | 0.23% | 4                   | 0.93% | 3                  | 0.70% |
| 500004.10-CP 500004.10 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.93% | 2                      | 1.87% | 2                   | 1.87% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 4                | 1.74% | 0                      | 0.00% | 0                   | 0.00% | 2                  | 0.87% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 5                | 1.41% | 2                      | 0.56% | 2                   | 0.56% | 2                  | 0.56% |
| 500005.10-CP 500005.10 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 1                      | 1.03% | 3                | 3.09% | 1                      | 1.03% | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 1                      | 0.46% | 1                | 0.46% | 0                      | 0.00% | 1                   | 0.46% | 2                  | 0.91% |

**COUNTY OF EL DORADO**  
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|                        | United States Senator  |       |                  |       |                        |        |                     |       |                    |       |
|------------------------|------------------------|-------|------------------|-------|------------------------|--------|---------------------|-------|--------------------|-------|
|                        | TIM GILDERSLEEVE (NPP) |       | CLIVE GREY (NPP) |       | DON J. GRUNDMANN (NPP) |        | JASON HANANIA (NPP) |       | PAUL MERRITT (NPP) |       |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00%  | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 2                      | 0.61% | 4                | 1.22% | 1                      | 0.30%  | 1                   | 0.30% | 2                  | 0.61% |
| 500006.10-CP 500006.10 |                        |       |                  |       |                        |        |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 1                | 0.61% | 1                      | 0.61%  | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 0                | 0.00% | 2                      | 0.46%  | 0                   | 0.00% | 3                  | 0.68% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 3                | 6.12% | 1                      | 2.04%  | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 4                | 0.61% | 4                      | 0.61%  | 0                   | 0.00% | 3                  | 0.46% |
| 500007.10-CP 500007.10 |                        |       |                  |       |                        |        |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 1                      | 0.53%  | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 2                | 0.33% | 0                      | 0.00%  | 1                   | 0.17% | 3                  | 0.50% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00%  | 0                   | 0.00% | 1                  | 5.56% |
| Total                  | 0                      | 0.00% | 2                | 0.25% | 1                      | 0.12%  | 1                   | 0.12% | 4                  | 0.50% |
| 500009.10-CP 500009.10 |                        |       |                  |       |                        |        |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00%  | 0                   | 0.00% | 0                  | 0.00% |
| Precinct VBM           | 1                      | 0.37% | 1                | 0.37% | 0                      | 0.00%  | 0                   | 0.00% | 0                  | 0.00% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00%  | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 1                      | 0.25% | 1                | 0.25% | 0                      | 0.00%  | 0                   | 0.00% | 0                  | 0.00% |
| 500011.10-CP 500011.10 |                        |       |                  |       |                        |        |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Precinct OS            | 1                      | 0.61% | 5                | 3.07% | 0                      | 0.00%  | 1                   | 0.61% | 0                  | 0.00% |
| Precinct VBM           | 0                      | 0.00% | 3                | 0.89% | 1                      | 0.30%  | 1                   | 0.30% | 1                  | 0.30% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00%  | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 1                      | 0.19% | 8                | 1.50% | 1                      | 0.19%  | 2                   | 0.37% | 1                  | 0.19% |
| 500012.10-CP 500012.10 |                        |       |                  |       |                        |        |                     |       |                    |       |
| Precinct TSx           | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00%  | 0                   | 0.00% | 0                  | 0.00% |
| Precinct OS            | 0                      | 0.00% | 1                | 0.59% | 2                      | 1.18%  | 0                   | 0.00% | 1                  | 0.59% |
| Precinct VBM           | 0                      | 0.00% | 2                | 0.37% | 1                      | 0.18%  | 3                   | 0.55% | 3                  | 0.55% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00%  | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 3                | 0.40% | 3                      | 0.40%  | 3                   | 0.40% | 4                  | 0.54% |
| 500015.10-CP 500015.10 |                        |       |                  |       |                        |        |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00%  | 0                   | 0.00% | 1                  | 1.04% |
| Precinct VBM           | 0                      | 0.00% | 2                | 0.54% | 0                      | 0.00%  | 0                   | 0.00% | 0                  | 0.00% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 1                      | 10.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 2                | 0.42% | 1                      | 0.21%  | 0                   | 0.00% | 1                  | 0.21% |
| 500020.10-CP 500020.10 |                        |       |                  |       |                        |        |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 2                | 1.42% | 0                      | 0.00%  | 3                   | 2.13% | 1                  | 0.71% |
| Precinct VBM           | 2                      | 0.33% | 4                | 0.66% | 2                      | 0.33%  | 0                   | 0.00% | 3                  | 0.49% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 1                | 3.85% | 0                      | 0.00%  | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 2                      | 0.26% | 7                | 0.90% | 2                      | 0.26%  | 3                   | 0.39% | 4                  | 0.52% |
| 500021.10-CP 500021.10 |                        |       |                  |       |                        |        |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00%  | 1                   | 0.67% | 2                  | 1.34% |
| Precinct VBM           | 1                      | 0.17% | 4                | 0.70% | 0                      | 0.00%  | 3                   | 0.52% | 2                  | 0.35% |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 1                      | 4.00%  | 0                   | 0.00% | 1                  | 4.00% |
| Total                  | 1                      | 0.13% | 4                | 0.54% | 1                      | 0.13%  | 4                   | 0.54% | 5                  | 0.67% |
| 95U201.01-MB 95U201.01 |                        |       |                  |       |                        |        |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -      | 0                   | -     | 0                  | -     |

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|                        | United States Senator  |       |                  |       |                        |       |                     |       |                    |       |
|------------------------|------------------------|-------|------------------|-------|------------------------|-------|---------------------|-------|--------------------|-------|
|                        | TIM GILDERSLEEVE (NPP) |       | CLIVE GREY (NPP) |       | DON J. GRUNDMANN (NPP) |       | JASON HANANIA (NPP) |       | PAUL MERRITT (NPP) |       |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Provisional            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Total                  | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| 95U201.02-MB 95U201.02 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | 0.00% | 6                | 1.97% | 0                      | 0.00% | 2                   | 0.66% | 0                  | 0.00% |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 6                | 1.97% | 0                      | 0.00% | 2                   | 0.66% | 0                  | 0.00% |
| 95U202.02-MB 95U202.02 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 2                      | 0.40% | 3                | 0.59% | 0                      | 0.00% | 0                   | 0.00% | 4                  | 0.79% |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 2                      | 0.39% | 3                | 0.59% | 0                      | 0.00% | 0                   | 0.00% | 4                  | 0.79% |
| 95U203.02-MB 95U203.02 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Total                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| 95U204.01-MB 95U204.01 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 1                      | 0.17% | 4                | 0.67% | 0                      | 0.00% | 1                   | 0.17% | 1                  | 0.17% |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 1                      | 0.17% | 4                | 0.67% | 0                      | 0.00% | 1                   | 0.17% | 1                  | 0.17% |
| 95U301.05-MB 95U301.05 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | 0.00% | 4                | 2.94% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 4                | 2.90% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| 95U302.04-MB 95U302.04 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Provisional            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Total                  | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| 95U302.05-MB 95U302.05 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 1                  | 1.23% |
| Provisional            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Total                  | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 1                  | 1.23% |
| 95U303.03-MB 95U303.03 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Provisional            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Total                  | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                        | United States Senator  |       |                  |       |                        |       |                     |       |                    |       |
|------------------------|------------------------|-------|------------------|-------|------------------------|-------|---------------------|-------|--------------------|-------|
|                        | TIM GILDERSLEEVE (NPP) |       | CLIVE GREY (NPP) |       | DON J. GRUNDMANN (NPP) |       | JASON HANANIA (NPP) |       | PAUL MERRITT (NPP) |       |
| 95U303.05-MB 95U303.05 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | 0.00% | 2                | 2.11% | 3                      | 3.16% | 0                   | 0.00% | 2                  | 2.11% |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 2                | 2.08% | 3                      | 3.13% | 0                   | 0.00% | 2                  | 2.08% |
| 95U304.05-MB 95U304.05 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Total                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| 95U305.05-MB 95U305.05 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Provisional            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Total                  | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| 95U401.08-MB 95U401.08 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Provisional            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Total                  | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| 95U402.07-MB 95U402.07 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Provisional            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Total                  | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| 95U402.08-MB 95U402.08 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | 0.00% | 9                | 1.79% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 9                | 1.78% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| 95U403.08-MB 95U403.08 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | 0.00% | 8                | 0.85% | 0                      | 0.00% | 0                   | 0.00% | 2                  | 0.21% |
| Provisional            | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00% | 0                  | 0.00% |
| Total                  | 0                      | 0.00% | 8                | 0.84% | 0                      | 0.00% | 0                   | 0.00% | 2                  | 0.21% |
| 95U404.06-MB 95U404.06 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Provisional            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Total                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| 95U404.08-MB 95U404.08 |                        |       |                  |       |                        |       |                     |       |                    |       |
| Precinct TSx           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct OS            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| Precinct VBM           | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |
| MB Precinct            | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -     | 0                  | -     |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                               | United States Senator  |       |                  |       |                        |       |                     |        |                    |       |
|-------------------------------|------------------------|-------|------------------|-------|------------------------|-------|---------------------|--------|--------------------|-------|
|                               | TIM GILDERSLEEVE (NPP) |       | CLIVE GREY (NPP) |       | DON J. GRUNDMANN (NPP) |       | JASON HANANIA (NPP) |        | PAUL MERRITT (NPP) |       |
| Provisional                   | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Total                         | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| 95U501.09-MB 95U501.09        |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS                   | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct VBM                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| MB Precinct                   | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Provisional                   | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Total                         | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| 95U501.10-MB 95U501.10        |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS                   | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct VBM                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| MB Precinct                   | 0                      | 0.00% | 4                | 2.50% | 1                      | 0.63% | 1                   | 0.63%  | 2                  | 1.25% |
| Provisional                   | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Total                         | 0                      | 0.00% | 4                | 2.47% | 1                      | 0.62% | 1                   | 0.62%  | 2                  | 1.23% |
| 95U598.10-MB 95U598.10        |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS                   | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct VBM                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| MB Precinct                   | 1                      | 0.47% | 1                | 0.47% | 0                      | 0.00% | 2                   | 0.93%  | 0                  | 0.00% |
| Provisional                   | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Total                         | 1                      | 0.46% | 1                | 0.46% | 0                      | 0.00% | 2                   | 0.93%  | 0                  | 0.00% |
| 95U599.10-MB 95U599.10        |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS                   | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct VBM                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| MB Precinct                   | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Provisional                   | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Total                         | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| 96U101.11-MB 96U101.11        |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS                   | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct VBM                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| MB Precinct                   | 1                      | 0.27% | 1                | 0.27% | 0                      | 0.00% | 1                   | 0.27%  | 1                  | 0.27% |
| Provisional                   | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Total                         | 1                      | 0.27% | 1                | 0.27% | 0                      | 0.00% | 1                   | 0.27%  | 1                  | 0.27% |
| 96U401.14-MB 96U401.14        |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS                   | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct VBM                  | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| MB Precinct                   | 1                      | 0.31% | 1                | 0.31% | 0                      | 0.00% | 0                   | 0.00%  | 1                  | 0.31% |
| Provisional                   | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Total                         | 1                      | 0.30% | 1                | 0.30% | 0                      | 0.00% | 0                   | 0.00%  | 1                  | 0.30% |
| Total                         |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                  | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 12.50% | 0                  | 0.00% |
| Precinct OS                   | 15                     | 0.14% | 66               | 0.62% | 30                     | 0.28% | 31                  | 0.29%  | 39                 | 0.36% |
| Precinct VBM                  | 54                     | 0.13% | 186              | 0.46% | 42                     | 0.10% | 125                 | 0.31%  | 147                | 0.36% |
| MB Precinct                   | 6                      | 0.13% | 43               | 0.93% | 4                      | 0.09% | 7                   | 0.15%  | 14                 | 0.30% |
| Provisional                   | 0                      | 0.00% | 14               | 1.07% | 8                      | 0.61% | 8                   | 0.61%  | 9                  | 0.68% |
| Total                         | 75                     | 0.13% | 309              | 0.54% | 84                     | 0.15% | 172                 | 0.30%  | 209                | 0.36% |
| STATE BOARD OF EQUALIZATION   |                        |       |                  |       |                        |       |                     |        |                    |       |
| STATE BOARD OF EQUALIZATION 1 |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                  | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 12.50% | 0                  | 0.00% |
| Precinct OS                   | 15                     | 0.14% | 66               | 0.62% | 30                     | 0.28% | 31                  | 0.29%  | 39                 | 0.36% |
| Precinct VBM                  | 54                     | 0.13% | 186              | 0.46% | 42                     | 0.10% | 125                 | 0.31%  | 147                | 0.36% |
| MB Precinct                   | 6                      | 0.13% | 43               | 0.93% | 4                      | 0.09% | 7                   | 0.15%  | 14                 | 0.30% |
| Provisional                   | 0                      | 0.00% | 14               | 1.07% | 8                      | 0.61% | 8                   | 0.61%  | 9                  | 0.68% |
| Total                         | 75                     | 0.13% | 309              | 0.54% | 84                     | 0.15% | 172                 | 0.30%  | 209                | 0.36% |
| Total                         |                        |       |                  |       |                        |       |                     |        |                    |       |



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|                                     | United States Senator  |              |                  |              |                        |              |                     |              |                    |              |
|-------------------------------------|------------------------|--------------|------------------|--------------|------------------------|--------------|---------------------|--------------|--------------------|--------------|
|                                     | TIM GILDERSLEEVE (NPP) |              | CLIVE GREY (NPP) |              | DON J. GRUNDMANN (NPP) |              | JASON HANANIA (NPP) |              | PAUL MERRITT (NPP) |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 1                   | 12.50%       | 0                  | 0.00%        |
| Precinct OS                         | 15                     | 0.14%        | 66               | 0.62%        | 30                     | 0.28%        | 31                  | 0.29%        | 39                 | 0.36%        |
| Precinct VBM                        | 54                     | 0.13%        | 186              | 0.46%        | 42                     | 0.10%        | 125                 | 0.31%        | 147                | 0.36%        |
| MB Precinct                         | 6                      | 0.13%        | 43               | 0.93%        | 4                      | 0.09%        | 7                   | 0.15%        | 14                 | 0.30%        |
| Provisional                         | 0                      | 0.00%        | 14               | 1.07%        | 8                      | 0.61%        | 8                   | 0.61%        | 9                  | 0.68%        |
| <b>Total</b>                        | <b>75</b>              | <b>0.13%</b> | <b>309</b>       | <b>0.54%</b> | <b>84</b>              | <b>0.15%</b> | <b>172</b>          | <b>0.30%</b> | <b>209</b>         | <b>0.36%</b> |
| <b>UNITED STATES SENATE</b>         |                        |              |                  |              |                        |              |                     |              |                    |              |
| <b>UNITED STATES SENATE</b>         |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 1                   | 12.50%       | 0                  | 0.00%        |
| Precinct OS                         | 15                     | 0.14%        | 66               | 0.62%        | 30                     | 0.28%        | 31                  | 0.29%        | 39                 | 0.36%        |
| Precinct VBM                        | 54                     | 0.13%        | 186              | 0.46%        | 42                     | 0.10%        | 125                 | 0.31%        | 147                | 0.36%        |
| MB Precinct                         | 6                      | 0.13%        | 43               | 0.93%        | 4                      | 0.09%        | 7                   | 0.15%        | 14                 | 0.30%        |
| Provisional                         | 0                      | 0.00%        | 14               | 1.07%        | 8                      | 0.61%        | 8                   | 0.61%        | 9                  | 0.68%        |
| <b>Total</b>                        | <b>75</b>              | <b>0.13%</b> | <b>309</b>       | <b>0.54%</b> | <b>84</b>              | <b>0.15%</b> | <b>172</b>          | <b>0.30%</b> | <b>209</b>         | <b>0.36%</b> |
| <b>Total</b>                        |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 1                   | 12.50%       | 0                  | 0.00%        |
| Precinct OS                         | 15                     | 0.14%        | 66               | 0.62%        | 30                     | 0.28%        | 31                  | 0.29%        | 39                 | 0.36%        |
| Precinct VBM                        | 54                     | 0.13%        | 186              | 0.46%        | 42                     | 0.10%        | 125                 | 0.31%        | 147                | 0.36%        |
| MB Precinct                         | 6                      | 0.13%        | 43               | 0.93%        | 4                      | 0.09%        | 7                   | 0.15%        | 14                 | 0.30%        |
| Provisional                         | 0                      | 0.00%        | 14               | 1.07%        | 8                      | 0.61%        | 8                   | 0.61%        | 9                  | 0.68%        |
| <b>Total</b>                        | <b>75</b>              | <b>0.13%</b> | <b>309</b>       | <b>0.54%</b> | <b>84</b>              | <b>0.15%</b> | <b>172</b>          | <b>0.30%</b> | <b>209</b>         | <b>0.36%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |                        |              |                  |              |                        |              |                     |              |                    |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 1                   | 12.50%       | 0                  | 0.00%        |
| Precinct OS                         | 15                     | 0.14%        | 66               | 0.62%        | 30                     | 0.28%        | 31                  | 0.29%        | 39                 | 0.36%        |
| Precinct VBM                        | 54                     | 0.13%        | 186              | 0.46%        | 42                     | 0.10%        | 125                 | 0.31%        | 147                | 0.36%        |
| MB Precinct                         | 6                      | 0.13%        | 43               | 0.93%        | 4                      | 0.09%        | 7                   | 0.15%        | 14                 | 0.30%        |
| Provisional                         | 0                      | 0.00%        | 14               | 1.07%        | 8                      | 0.61%        | 8                   | 0.61%        | 9                  | 0.68%        |
| <b>Total</b>                        | <b>75</b>              | <b>0.13%</b> | <b>309</b>       | <b>0.54%</b> | <b>84</b>              | <b>0.15%</b> | <b>172</b>          | <b>0.30%</b> | <b>209</b>         | <b>0.36%</b> |
| <b>Total</b>                        |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 1                   | 12.50%       | 0                  | 0.00%        |
| Precinct OS                         | 15                     | 0.14%        | 66               | 0.62%        | 30                     | 0.28%        | 31                  | 0.29%        | 39                 | 0.36%        |
| Precinct VBM                        | 54                     | 0.13%        | 186              | 0.46%        | 42                     | 0.10%        | 125                 | 0.31%        | 147                | 0.36%        |
| MB Precinct                         | 6                      | 0.13%        | 43               | 0.93%        | 4                      | 0.09%        | 7                   | 0.15%        | 14                 | 0.30%        |
| Provisional                         | 0                      | 0.00%        | 14               | 1.07%        | 8                      | 0.61%        | 8                   | 0.61%        | 9                  | 0.68%        |
| <b>Total</b>                        | <b>75</b>              | <b>0.13%</b> | <b>309</b>       | <b>0.54%</b> | <b>84</b>              | <b>0.15%</b> | <b>172</b>          | <b>0.30%</b> | <b>209</b>         | <b>0.36%</b> |
| <b>STATE SENATE</b>                 |                        |              |                  |              |                        |              |                     |              |                    |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 1                   | 12.50%       | 0                  | 0.00%        |
| Precinct OS                         | 15                     | 0.14%        | 66               | 0.62%        | 30                     | 0.28%        | 31                  | 0.29%        | 39                 | 0.36%        |
| Precinct VBM                        | 54                     | 0.13%        | 186              | 0.46%        | 42                     | 0.10%        | 125                 | 0.31%        | 147                | 0.36%        |
| MB Precinct                         | 6                      | 0.13%        | 43               | 0.93%        | 4                      | 0.09%        | 7                   | 0.15%        | 14                 | 0.30%        |
| Provisional                         | 0                      | 0.00%        | 14               | 1.07%        | 8                      | 0.61%        | 8                   | 0.61%        | 9                  | 0.68%        |
| <b>Total</b>                        | <b>75</b>              | <b>0.13%</b> | <b>309</b>       | <b>0.54%</b> | <b>84</b>              | <b>0.15%</b> | <b>172</b>          | <b>0.30%</b> | <b>209</b>         | <b>0.36%</b> |
| <b>Total</b>                        |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 1                   | 12.50%       | 0                  | 0.00%        |
| Precinct OS                         | 15                     | 0.14%        | 66               | 0.62%        | 30                     | 0.28%        | 31                  | 0.29%        | 39                 | 0.36%        |
| Precinct VBM                        | 54                     | 0.13%        | 186              | 0.46%        | 42                     | 0.10%        | 125                 | 0.31%        | 147                | 0.36%        |
| MB Precinct                         | 6                      | 0.13%        | 43               | 0.93%        | 4                      | 0.09%        | 7                   | 0.15%        | 14                 | 0.30%        |
| Provisional                         | 0                      | 0.00%        | 14               | 1.07%        | 8                      | 0.61%        | 8                   | 0.61%        | 9                  | 0.68%        |
| <b>Total</b>                        | <b>75</b>              | <b>0.13%</b> | <b>309</b>       | <b>0.54%</b> | <b>84</b>              | <b>0.15%</b> | <b>172</b>          | <b>0.30%</b> | <b>209</b>         | <b>0.36%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                        |              |                  |              |                        |              |                     |              |                    |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx                        | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 0                   | 0.00%        | 0                  | 0.00%        |
| Precinct OS                         | 10                     | 0.15%        | 47               | 0.69%        | 23                     | 0.34%        | 20                  | 0.29%        | 29                 | 0.43%        |
| Precinct VBM                        | 41                     | 0.16%        | 145              | 0.57%        | 21                     | 0.08%        | 82                  | 0.32%        | 100                | 0.39%        |
| MB Precinct                         | 4                      | 0.10%        | 41               | 1.05%        | 4                      | 0.10%        | 6                   | 0.15%        | 12                 | 0.31%        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                   | United States Senator  |       |                  |       |                        |       |                     |        |                    |       |
|-----------------------------------|------------------------|-------|------------------|-------|------------------------|-------|---------------------|--------|--------------------|-------|
|                                   | TIM GILDERSLEEVE (NPP) |       | CLIVE GREY (NPP) |       | DON J. GRUNDMANN (NPP) |       | JASON HANANIA (NPP) |        | PAUL MERRITT (NPP) |       |
| Provisional                       | 0                      | 0.00% | 12               | 1.40% | 7                      | 0.81% | 3                   | 0.35%  | 8                  | 0.93% |
| Total                             | 55                     | 0.15% | 245              | 0.66% | 55                     | 0.15% | 111                 | 0.30%  | 149                | 0.40% |
| <b>6TH STATE ASSEMBLY DIST</b>    |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                      | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 25.00% | 0                  | 0.00% |
| Precinct OS                       | 5                      | 0.13% | 19               | 0.49% | 7                      | 0.18% | 11                  | 0.28%  | 10                 | 0.26% |
| Precinct VBM                      | 13                     | 0.08% | 41               | 0.26% | 21                     | 0.14% | 43                  | 0.28%  | 47                 | 0.30% |
| MB Precinct                       | 2                      | 0.29% | 2                | 0.29% | 0                      | 0.00% | 1                   | 0.15%  | 2                  | 0.29% |
| Provisional                       | 0                      | 0.00% | 2                | 0.44% | 1                      | 0.22% | 5                   | 1.10%  | 1                  | 0.22% |
| Total                             | 20                     | 0.10% | 64               | 0.31% | 29                     | 0.14% | 61                  | 0.30%  | 60                 | 0.29% |
| <b>Total</b>                      |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                      | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 12.50% | 0                  | 0.00% |
| Precinct OS                       | 15                     | 0.14% | 66               | 0.62% | 30                     | 0.28% | 31                  | 0.29%  | 39                 | 0.36% |
| Precinct VBM                      | 54                     | 0.13% | 186              | 0.46% | 42                     | 0.10% | 125                 | 0.31%  | 147                | 0.36% |
| MB Precinct                       | 6                      | 0.13% | 43               | 0.93% | 4                      | 0.09% | 7                   | 0.15%  | 14                 | 0.30% |
| Provisional                       | 0                      | 0.00% | 14               | 1.07% | 8                      | 0.61% | 8                   | 0.61%  | 9                  | 0.68% |
| Total                             | 75                     | 0.13% | 309              | 0.54% | 84                     | 0.15% | 172                 | 0.30%  | 209                | 0.36% |
| <b>SUPERVISORIAL DISTRICTS</b>    |                        |       |                  |       |                        |       |                     |        |                    |       |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                      | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS                       | 2                      | 0.09% | 9                | 0.40% | 2                      | 0.09% | 7                   | 0.31%  | 6                  | 0.27% |
| Precinct VBM                      | 3                      | 0.03% | 20               | 0.22% | 10                     | 0.11% | 27                  | 0.29%  | 26                 | 0.28% |
| MB Precinct                       | 1                      | 0.27% | 1                | 0.27% | 0                      | 0.00% | 1                   | 0.27%  | 1                  | 0.27% |
| Provisional                       | 0                      | 0.00% | 1                | 0.41% | 0                      | 0.00% | 2                   | 0.82%  | 0                  | 0.00% |
| Total                             | 6                      | 0.05% | 31               | 0.26% | 12                     | 0.10% | 37                  | 0.31%  | 33                 | 0.27% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                      | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 25.00% | 0                  | 0.00% |
| Precinct OS                       | 4                      | 0.18% | 14               | 0.62% | 7                      | 0.31% | 4                   | 0.18%  | 9                  | 0.40% |
| Precinct VBM                      | 13                     | 0.15% | 36               | 0.42% | 11                     | 0.13% | 24                  | 0.28%  | 29                 | 0.34% |
| MB Precinct                       | 3                      | 0.21% | 13               | 0.92% | 0                      | 0.00% | 3                   | 0.21%  | 5                  | 0.35% |
| Provisional                       | 0                      | 0.00% | 2                | 0.72% | 2                      | 0.72% | 4                   | 1.44%  | 2                  | 0.72% |
| Total                             | 20                     | 0.16% | 65               | 0.52% | 20                     | 0.16% | 36                  | 0.29%  | 45                 | 0.36% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                      | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Precinct OS                       | 2                      | 0.09% | 8                | 0.38% | 4                      | 0.19% | 9                   | 0.42%  | 8                  | 0.38% |
| Precinct VBM                      | 19                     | 0.21% | 46               | 0.51% | 9                      | 0.10% | 26                  | 0.29%  | 33                 | 0.36% |
| MB Precinct                       | 0                      | 0.00% | 6                | 1.51% | 3                      | 0.75% | 0                   | 0.00%  | 3                  | 0.75% |
| Provisional                       | 0                      | 0.00% | 1                | 0.42% | 1                      | 0.42% | 1                   | 0.42%  | 3                  | 1.27% |
| Total                             | 21                     | 0.18% | 61               | 0.52% | 17                     | 0.14% | 36                  | 0.30%  | 47                 | 0.40% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                      | 0                      | -     | 0                | -     | 0                      | -     | 0                   | -      | 0                  | -     |
| Precinct OS                       | 5                      | 0.22% | 17               | 0.74% | 9                      | 0.39% | 3                   | 0.13%  | 10                 | 0.44% |
| Precinct VBM                      | 12                     | 0.14% | 48               | 0.57% | 6                      | 0.07% | 31                  | 0.36%  | 32                 | 0.38% |
| MB Precinct                       | 1                      | 0.05% | 18               | 0.89% | 0                      | 0.00% | 0                   | 0.00%  | 3                  | 0.15% |
| Provisional                       | 0                      | 0.00% | 5                | 1.96% | 1                      | 0.39% | 1                   | 0.39%  | 2                  | 0.78% |
| Total                             | 18                     | 0.14% | 88               | 0.67% | 16                     | 0.12% | 35                  | 0.27%  | 47                 | 0.36% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                      | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 0                   | 0.00%  | 0                  | 0.00% |
| Precinct OS                       | 2                      | 0.11% | 18               | 1.00% | 8                      | 0.44% | 8                   | 0.44%  | 6                  | 0.33% |
| Precinct VBM                      | 7                      | 0.13% | 36               | 0.67% | 6                      | 0.11% | 17                  | 0.32%  | 27                 | 0.50% |
| MB Precinct                       | 1                      | 0.24% | 5                | 1.22% | 1                      | 0.24% | 3                   | 0.73%  | 2                  | 0.49% |
| Provisional                       | 0                      | 0.00% | 5                | 1.66% | 4                      | 1.32% | 0                   | 0.00%  | 2                  | 0.66% |
| Total                             | 10                     | 0.13% | 64               | 0.81% | 19                     | 0.24% | 28                  | 0.35%  | 37                 | 0.47% |
| <b>Total</b>                      |                        |       |                  |       |                        |       |                     |        |                    |       |
| Precinct TSx                      | 0                      | 0.00% | 0                | 0.00% | 0                      | 0.00% | 1                   | 12.50% | 0                  | 0.00% |
| Precinct OS                       | 15                     | 0.14% | 66               | 0.62% | 30                     | 0.28% | 31                  | 0.29%  | 39                 | 0.36% |
| Precinct VBM                      | 54                     | 0.13% | 186              | 0.46% | 42                     | 0.10% | 125                 | 0.31%  | 147                | 0.36% |
| MB Precinct                       | 6                      | 0.13% | 43               | 0.93% | 4                      | 0.09% | 7                   | 0.15%  | 14                 | 0.30% |
| Provisional                       | 0                      | 0.00% | 14               | 1.07% | 8                      | 0.61% | 8                   | 0.61%  | 9                  | 0.68% |
| Total                             | 75                     | 0.13% | 309              | 0.54% | 84                     | 0.15% | 172                 | 0.30%  | 209                | 0.36% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                 | United States Senator  |              |                  |              |                        |              |                     |              |                    |              |
|---------------------------------|------------------------|--------------|------------------|--------------|------------------------|--------------|---------------------|--------------|--------------------|--------------|
|                                 | TIM GILDERSLEEVE (NPP) |              | CLIVE GREY (NPP) |              | DON J. GRUNDMANN (NPP) |              | JASON HANANIA (NPP) |              | PAUL MERRITT (NPP) |              |
| <b>CITIES</b>                   |                        |              |                  |              |                        |              |                     |              |                    |              |
| <b>CITY OF PLACERVILLE</b>      |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx                    | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Precinct OS                     | 0                      | 0.00%        | 2                | 0.36%        | 0                      | 0.00%        | 1                   | 0.18%        | 2                  | 0.36%        |
| Precinct VBM                    | 3                      | 0.13%        | 4                | 0.17%        | 3                      | 0.13%        | 6                   | 0.26%        | 5                  | 0.21%        |
| MB Precinct                     | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Provisional                     | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 0                   | 0.00%        | 1                  | 1.43%        |
| <b>Total</b>                    | <b>3</b>               | <b>0.10%</b> | <b>6</b>         | <b>0.20%</b> | <b>3</b>               | <b>0.10%</b> | <b>7</b>            | <b>0.24%</b> | <b>8</b>           | <b>0.27%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx                    | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Precinct OS                     | 2                      | 0.21%        | 10               | 1.06%        | 5                      | 0.53%        | 3                   | 0.32%        | 1                  | 0.11%        |
| Precinct VBM                    | 2                      | 0.08%        | 17               | 0.69%        | 3                      | 0.12%        | 6                   | 0.24%        | 11                 | 0.45%        |
| MB Precinct                     | 0                      | -            | 0                | -            | 0                      | -            | 0                   | -            | 0                  | -            |
| Provisional                     | 0                      | 0.00%        | 4                | 2.15%        | 2                      | 1.08%        | 0                   | 0.00%        | 1                  | 0.54%        |
| <b>Total</b>                    | <b>4</b>               | <b>0.11%</b> | <b>31</b>        | <b>0.86%</b> | <b>10</b>              | <b>0.28%</b> | <b>9</b>            | <b>0.25%</b> | <b>13</b>          | <b>0.36%</b> |
| <b>UNINCORPORATED AREA</b>      |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx                    | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 1                   | 12.50%       | 0                  | 0.00%        |
| Precinct OS                     | 13                     | 0.14%        | 54               | 0.59%        | 25                     | 0.27%        | 27                  | 0.29%        | 36                 | 0.39%        |
| Precinct VBM                    | 49                     | 0.14%        | 165              | 0.46%        | 36                     | 0.10%        | 113                 | 0.31%        | 131                | 0.36%        |
| MB Precinct                     | 6                      | 0.13%        | 43               | 0.93%        | 4                      | 0.09%        | 7                   | 0.15%        | 14                 | 0.30%        |
| Provisional                     | 0                      | 0.00%        | 10               | 0.95%        | 6                      | 0.57%        | 8                   | 0.76%        | 7                  | 0.66%        |
| <b>Total</b>                    | <b>68</b>              | <b>0.13%</b> | <b>272</b>       | <b>0.53%</b> | <b>71</b>              | <b>0.14%</b> | <b>156</b>          | <b>0.31%</b> | <b>188</b>         | <b>0.37%</b> |
| <b>Total</b>                    |                        |              |                  |              |                        |              |                     |              |                    |              |
| Precinct TSx                    | 0                      | 0.00%        | 0                | 0.00%        | 0                      | 0.00%        | 1                   | 12.50%       | 0                  | 0.00%        |
| Precinct OS                     | 15                     | 0.14%        | 66               | 0.62%        | 30                     | 0.28%        | 31                  | 0.29%        | 39                 | 0.36%        |
| Precinct VBM                    | 54                     | 0.13%        | 186              | 0.46%        | 42                     | 0.10%        | 125                 | 0.31%        | 147                | 0.36%        |
| MB Precinct                     | 6                      | 0.13%        | 43               | 0.93%        | 4                      | 0.09%        | 7                   | 0.15%        | 14                 | 0.30%        |
| Provisional                     | 0                      | 0.00%        | 14               | 1.07%        | 8                      | 0.61%        | 8                   | 0.61%        | 9                  | 0.68%        |
| <b>Total</b>                    | <b>75</b>              | <b>0.13%</b> | <b>309</b>       | <b>0.54%</b> | <b>84</b>              | <b>0.15%</b> | <b>172</b>          | <b>0.30%</b> | <b>209</b>         | <b>0.36%</b> |

**COUNTY OF EL DORADO**  
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|                        | United States Senator |       |                     |       |                         |       |                         |       |
|------------------------|-----------------------|-------|---------------------|-------|-------------------------|-------|-------------------------|-------|
|                        | GAR MYERS (NPP)       |       | LING LING SHI (NPP) |       | SCOTT A. VINEBERG (NPP) |       | GAIL K. LIGHTFOOT (LIB) |       |
| Jurisdiction Wide      |                       |       |                     |       |                         |       |                         |       |
| 100001.11-CP 100001.11 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 2                       | 1.27% |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 4                       | 0.57% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 6.25% |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 7                       | 0.80% |
| 100002.11-CP 100002.11 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 2                   | 2.63% | 0                       | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 0                     | 0.00% | 1                   | 0.25% | 0                       | 0.00% | 4                       | 0.99% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 3                   | 0.61% | 0                       | 0.00% | 4                       | 0.81% |
| 100003.11-CP 100003.11 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 1                     | 0.90% | 1                   | 0.90% | 0                       | 0.00% | 1                       | 0.90% |
| Precinct VBM           | 0                     | 0.00% | 3                   | 0.42% | 1                       | 0.14% | 9                       | 1.27% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 1                     | 0.12% | 4                   | 0.48% | 1                       | 0.12% | 10                      | 1.19% |
| 100004.11-CP 100004.11 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.43% | 2                       | 0.85% | 2                       | 0.85% |
| Precinct VBM           | 0                     | 0.00% | 2                   | 0.30% | 2                       | 0.30% | 6                       | 0.90% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 3                   | 0.33% | 4                       | 0.44% | 8                       | 0.89% |
| 100005.11-CP 100005.11 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 0.75% |
| Precinct VBM           | 1                     | 0.12% | 3                   | 0.37% | 0                       | 0.00% | 7                       | 0.87% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 1                     | 0.10% | 3                   | 0.31% | 0                       | 0.00% | 8                       | 0.83% |
| 100006.11-CP 100006.11 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 3                   | 1.36% | 0                       | 0.00% | 3                       | 1.36% |
| Precinct VBM           | 0                     | 0.00% | 3                   | 0.35% | 1                       | 0.12% | 10                      | 1.18% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 6                   | 0.55% | 1                       | 0.09% | 13                      | 1.20% |
| 100008.11-CP 100008.11 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 2                   | 1.15% | 1                       | 0.57% | 2                       | 1.15% |
| Precinct VBM           | 0                     | 0.00% | 2                   | 0.31% | 1                       | 0.15% | 2                       | 0.31% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 4                   | 0.48% | 2                       | 0.24% | 4                       | 0.48% |
| 100009.11-CP 100009.11 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.68% | 3                       | 2.05% | 0                       | 0.00% |
| Precinct VBM           | 1                     | 0.15% | 0                   | 0.00% | 0                       | 0.00% | 15                      | 2.22% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 1                     | 0.12% | 1                   | 0.12% | 3                       | 0.36% | 15                      | 1.79% |
| 100012.11-CP 100012.11 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.44% | 0                       | 0.00% | 3                       | 1.31% |
| Precinct VBM           | 0                     | 0.00% | 3                   | 0.35% | 2                       | 0.23% | 7                       | 0.82% |

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|                        | GAR MYERS (NPP)       |       | LING LING SHI (NPP) |        | SCOTT A. VINEBERG (NPP) |       | GAIL K. LIGHTFOOT (LIB) |       |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 1                     | 3.57% | 2                   | 7.14%  | 0                       | 0.00% | 1                       | 3.57% |
| Total                  | 1                     | 0.09% | 6                   | 0.54%  | 2                       | 0.18% | 11                      | 0.99% |
| 100013.11-CP 100013.11 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00%  | 2                       | 0.90% | 2                       | 0.90% |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00%  | 1                       | 0.12% | 12                      | 1.48% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 0                   | 0.00%  | 3                       | 0.28% | 14                      | 1.33% |
| 100014.11-CP 100014.11 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.58%  | 1                       | 0.58% | 2                       | 1.16% |
| Precinct VBM           | 0                     | 0.00% | 4                   | 0.64%  | 3                       | 0.48% | 4                       | 0.64% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 5                   | 0.61%  | 4                       | 0.49% | 6                       | 0.73% |
| 100022.11-CP 100022.11 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.62%  | 1                       | 0.62% | 3                       | 1.86% |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 5                       | 0.82% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 1                   | 0.13%  | 1                       | 0.13% | 8                       | 1.02% |
| 100023.11-CP 100023.11 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00%  | 1                       | 0.52% | 1                       | 0.52% |
| Precinct VBM           | 0                     | 0.00% | 3                   | 0.32%  | 1                       | 0.11% | 13                      | 1.39% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 1                       | 3.57% |
| Total                  | 0                     | 0.00% | 3                   | 0.26%  | 2                       | 0.17% | 15                      | 1.30% |
| 200003.12-CP 200003.12 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 2                   | 1.35%  | 0                       | 0.00% | 5                       | 3.38% |
| Precinct VBM           | 0                     | 0.00% | 4                   | 0.80%  | 1                       | 0.20% | 11                      | 2.19% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 3                   | 11.54% | 0                       | 0.00% | 2                       | 7.69% |
| Total                  | 0                     | 0.00% | 9                   | 1.33%  | 1                       | 0.15% | 18                      | 2.66% |
| 200004.12-CP 200004.12 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.61%  | 0                       | 0.00% | 1                       | 0.61% |
| Precinct VBM           | 0                     | 0.00% | 5                   | 0.83%  | 1                       | 0.17% | 3                       | 0.50% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 1                       | 7.14% |
| Total                  | 0                     | 0.00% | 6                   | 0.77%  | 1                       | 0.13% | 5                       | 0.64% |
| 200005.02-CP 200005.02 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00%  | 1                       | 1.15% | 2                       | 2.30% |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 5                       | 1.23% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 0                   | 0.00%  | 1                       | 0.20% | 7                       | 1.37% |
| 200006.12-CP 200006.12 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 2                       | 1.85% |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 6                       | 1.25% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 8                       | 1.30% |
| 200008.12-CP 200008.12 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |

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|                        | GAR MYERS (NPP)       |       | LING LING SHI (NPP) |       | SCOTT A. VINEBERG (NPP) |        | GAIL K. LIGHTFOOT (LIB) |        |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 4                       | 3.01%  |
| Precinct VBM           | 0                     | 0.00% | 1                   | 0.31% | 1                       | 0.31%  | 4                       | 1.22%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 0                       | 0.00%  |
| Total                  | 0                     | 0.00% | 1                   | 0.21% | 1                       | 0.21%  | 8                       | 1.71%  |
| 200010.12-CP 200010.12 |                       |       |                     |       |                         |        |                         |        |
| Precinct TSx           | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 0                       | 0.00%  |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.56%  | 6                       | 3.35%  |
| Precinct VBM           | 0                     | 0.00% | 3                   | 0.40% | 0                       | 0.00%  | 17                      | 2.26%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 1                       | 3.85%  |
| Total                  | 0                     | 0.00% | 3                   | 0.31% | 1                       | 0.10%  | 24                      | 2.49%  |
| 200012.12-CP 200012.12 |                       |       |                     |       |                         |        |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 3                       | 2.38%  |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 4                       | 0.99%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Provisional            | 0                     | 0.00% | 1                   | 8.33% | 0                       | 0.00%  | 0                       | 0.00%  |
| Total                  | 0                     | 0.00% | 1                   | 0.18% | 0                       | 0.00%  | 7                       | 1.29%  |
| 200013.12-CP 200013.12 |                       |       |                     |       |                         |        |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.52%  | 5                       | 2.62%  |
| Precinct VBM           | 0                     | 0.00% | 1                   | 0.16% | 1                       | 0.16%  | 8                       | 1.25%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 0                       | 0.00%  |
| Total                  | 0                     | 0.00% | 1                   | 0.12% | 2                       | 0.24%  | 13                      | 1.53%  |
| 200014.12-CP 200014.12 |                       |       |                     |       |                         |        |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.53%  | 6                       | 3.21%  |
| Precinct VBM           | 0                     | 0.00% | 3                   | 0.55% | 3                       | 0.55%  | 6                       | 1.09%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 1                       | 4.00%  |
| Total                  | 0                     | 0.00% | 3                   | 0.39% | 4                       | 0.53%  | 13                      | 1.71%  |
| 200015.02-CP 200015.02 |                       |       |                     |       |                         |        |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.51% | 0                       | 0.00%  | 3                       | 1.54%  |
| Precinct VBM           | 2                     | 0.25% | 5                   | 0.63% | 1                       | 0.13%  | 22                      | 2.78%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 1                       | 5.00%  |
| Total                  | 2                     | 0.20% | 6                   | 0.60% | 1                       | 0.10%  | 26                      | 2.59%  |
| 200016.12-CP 200016.12 |                       |       |                     |       |                         |        |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 0                       | 0.00%  |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 0                       | 0.00%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 1                       | 33.33% | 0                       | 0.00%  |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.67%  | 0                       | 0.00%  |
| 200016.13-CP 200016.13 |                       |       |                     |       |                         |        |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 0                       | 0.00%  |
| Precinct VBM           | 0                     | 0.00% | 2                   | 0.68% | 0                       | 0.00%  | 2                       | 0.68%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 0                       | 0.00%  |
| Total                  | 0                     | 0.00% | 2                   | 0.54% | 0                       | 0.00%  | 2                       | 0.54%  |
| 200017.02-CP 200017.02 |                       |       |                     |       |                         |        |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.78% | 0                       | 0.00%  | 2                       | 1.56%  |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 14                      | 2.56%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -      | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00%  | 2                       | 18.18% |
| Total                  | 0                     | 0.00% | 1                   | 0.15% | 0                       | 0.00%  | 18                      | 2.62%  |

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|                        | GAR MYERS (NPP)       |       | LING LING SHI (NPP) |       | SCOTT A. VINEBERG (NPP) |       | GAIL K. LIGHTFOOT (LIB) |       |
| 200018.02-CP 200018.02 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.66% | 0                       | 0.00% | 6                       | 3.95% |
| Precinct VBM           | 0                     | 0.00% | 1                   | 0.16% | 4                       | 0.63% | 15                      | 2.37% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 8.33% |
| Total                  | 0                     | 0.00% | 2                   | 0.25% | 4                       | 0.50% | 22                      | 2.76% |
| 200020.02-CP 200020.02 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 5                       | 4.03% |
| Precinct VBM           | 0                     | 0.00% | 1                   | 0.21% | 1                       | 0.21% | 2                       | 0.42% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 5.88% |
| Total                  | 0                     | 0.00% | 1                   | 0.16% | 1                       | 0.16% | 8                       | 1.30% |
| 200021.01-CP 200021.01 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 2                       | 6.67% |
| Precinct VBM           | 0                     | 0.00% | 2                   | 1.11% | 0                       | 0.00% | 6                       | 3.33% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 2                   | 0.93% | 0                       | 0.00% | 8                       | 3.72% |
| 200021.02-CP 200021.02 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 1                     | 0.65% | 0                   | 0.00% | 0                       | 0.00% | 3                       | 1.95% |
| Precinct VBM           | 2                     | 0.30% | 0                   | 0.00% | 1                       | 0.15% | 10                      | 1.48% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 6.67% |
| Total                  | 3                     | 0.36% | 0                   | 0.00% | 1                       | 0.12% | 14                      | 1.66% |
| 200024.02-CP 200024.02 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 4                       | 7.55% |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.38% | 7                       | 2.69% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.31% | 11                      | 3.46% |
| 300001.04-CP 300001.04 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 0                     | 0.00% | 1                   | 1.06% | 0                       | 0.00% | 2                       | 2.13% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Total                  | 0                     | 0.00% | 1                   | 0.95% | 0                       | 0.00% | 2                       | 1.90% |
| 300001.05-CP 300001.05 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 2                       | 2.94% |
| Precinct VBM           | 0                     | 0.00% | 3                   | 1.02% | 1                       | 0.34% | 3                       | 1.02% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 3                   | 0.81% | 1                       | 0.27% | 5                       | 1.34% |
| 300003.05-CP 300003.05 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.56% | 9                       | 5.00% |
| Precinct VBM           | 0                     | 0.00% | 1                   | 0.12% | 3                       | 0.36% | 14                      | 1.66% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 4.35% |
| Total                  | 0                     | 0.00% | 1                   | 0.10% | 4                       | 0.38% | 24                      | 2.30% |
| 300004.05-CP 300004.05 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 2                   | 1.29% | 2                       | 1.29% | 4                       | 2.58% |
| Precinct VBM           | 0                     | 0.00% | 2                   | 0.38% | 0                       | 0.00% | 7                       | 1.33% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |

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|                        | GAR MYERS (NPP)       |       | LING LING SHI (NPP) |        | SCOTT A. VINEBERG (NPP) |       | GAIL K. LIGHTFOOT (LIB) |       |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 1                       | 6.25% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 4                   | 0.57%  | 3                       | 0.43% | 11                      | 1.58% |
| 300005.05-CP 300005.05 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 4                       | 2.78% |
| Precinct VBM           | 0                     | 0.00% | 2                   | 0.34%  | 0                       | 0.00% | 9                       | 1.53% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 1                       | 4.55% |
| Total                  | 0                     | 0.00% | 2                   | 0.26%  | 0                       | 0.00% | 14                      | 1.85% |
| 300008.05-CP 300008.05 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 5                       | 3.91% |
| Precinct VBM           | 0                     | 0.00% | 1                   | 0.15%  | 0                       | 0.00% | 9                       | 1.32% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 1                   | 9.09%  | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 2                   | 0.24%  | 0                       | 0.00% | 14                      | 1.71% |
| 300009.04-CP 300009.04 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00%  | 1                       | 0.91% | 2                       | 1.82% |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 2                       | 0.58% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 0                   | 0.00%  | 1                       | 0.21% | 4                       | 0.86% |
| 300010.04-CP 300010.04 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.65%  | 0                       | 0.00% | 1                       | 0.65% |
| Precinct VBM           | 1                     | 0.17% | 0                   | 0.00%  | 0                       | 0.00% | 6                       | 1.01% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 1                       | 9.09% |
| Total                  | 1                     | 0.13% | 1                   | 0.13%  | 0                       | 0.00% | 8                       | 1.05% |
| 300011.04-CP 300011.04 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.77%  | 0                       | 0.00% | 4                       | 3.08% |
| Precinct VBM           | 0                     | 0.00% | 3                   | 0.47%  | 1                       | 0.16% | 10                      | 1.56% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 1                       | 5.00% | 1                       | 5.00% |
| Total                  | 0                     | 0.00% | 4                   | 0.51%  | 2                       | 0.25% | 15                      | 1.90% |
| 300013.05-CP 300013.05 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 0                       | 0.00% |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00%  | 1                       | 0.85% | 3                       | 2.56% |
| Precinct VBM           | 0                     | 0.00% | 1                   | 0.15%  | 0                       | 0.00% | 20                      | 3.04% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 1                   | 0.13%  | 1                       | 0.13% | 23                      | 2.92% |
| 300015.04-CP 300015.04 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 1                   | 12.50% | 0                       | 0.00% | 0                       | 0.00% |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 3                       | 5.88% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 1                   | 1.59%  | 0                       | 0.00% | 3                       | 4.76% |
| 300015.05-CP 300015.05 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 1                       | 1.64% |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00%  | 1                       | 0.44% | 1                       | 0.44% |
| MB Precinct            | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00%  | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 0                   | 0.00%  | 1                       | 0.34% | 2                       | 0.69% |
| 300017.05-CP 300017.05 |                       |       |                     |        |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -      | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00%  | 1                       | 0.92% | 2                       | 1.83% |





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|                        | GAR MYERS (NPP)       |       | LING LING SHI (NPP) |       | SCOTT A. VINEBERG (NPP) |       | GAIL K. LIGHTFOOT (LIB) |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.45% | 4                       | 1.81%  |
| Precinct VBM           | 0                     | 0.00% | 1                   | 0.14% | 2                       | 0.29% | 14                      | 2.01%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00%  |
| Total                  | 0                     | 0.00% | 1                   | 0.11% | 3                       | 0.32% | 18                      | 1.90%  |
| 400004.14-CP 400004.14 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.59% | 1                       | 0.59% | 2                       | 1.18%  |
| Precinct VBM           | 0                     | 0.00% | 3                   | 0.49% | 6                       | 0.98% | 4                       | 0.66%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 3                       | 16.67% |
| Total                  | 0                     | 0.00% | 4                   | 0.50% | 7                       | 0.88% | 9                       | 1.13%  |
| 400005.08-CP 400005.08 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 2                       | 3.03%  |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 6                       | 2.58%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00%  |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 8                       | 2.61%  |
| 400006.08-CP 400006.08 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.48% | 0                       | 0.00% | 5                       | 2.38%  |
| Precinct VBM           | 1                     | 0.16% | 0                   | 0.00% | 1                       | 0.16% | 12                      | 1.86%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 2                       | 13.33% |
| Total                  | 1                     | 0.12% | 1                   | 0.12% | 1                       | 0.12% | 19                      | 2.19%  |
| 400007.08-CP 400007.08 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 1                     | 0.47% | 0                   | 0.00% | 0                       | 0.00% | 6                       | 2.84%  |
| Precinct VBM           | 0                     | 0.00% | 7                   | 0.99% | 1                       | 0.14% | 23                      | 3.25%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 5.00%  |
| Total                  | 1                     | 0.11% | 7                   | 0.75% | 1                       | 0.11% | 30                      | 3.20%  |
| 400010.08-CP 400010.08 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.45% | 0                       | 0.00% | 5                       | 2.26%  |
| Precinct VBM           | 0                     | 0.00% | 3                   | 0.37% | 0                       | 0.00% | 13                      | 1.60%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 4.76%  |
| Total                  | 0                     | 0.00% | 4                   | 0.38% | 0                       | 0.00% | 19                      | 1.80%  |
| 400012.08-CP 400012.08 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.95% | 0                       | 0.00% | 1                       | 0.95%  |
| Precinct VBM           | 0                     | 0.00% | 3                   | 0.77% | 0                       | 0.00% | 9                       | 2.30%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00%  |
| Total                  | 0                     | 0.00% | 4                   | 0.78% | 0                       | 0.00% | 10                      | 1.95%  |
| 400013.08-CP 400013.08 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.93% | 0                       | 0.00%  |
| Precinct VBM           | 1                     | 0.34% | 0                   | 0.00% | 0                       | 0.00% | 2                       | 0.68%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00%  |
| Total                  | 1                     | 0.25% | 0                   | 0.00% | 1                       | 0.25% | 2                       | 0.50%  |
| 400014.08-CP 400014.08 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 3                       | 1.53%  |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 4                       | 0.60%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00%  |

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|                        | GAR MYERS (NPP)       |       | LING LING SHI (NPP) |       | SCOTT A. VINEBERG (NPP) |       | GAIL K. LIGHTFOOT (LIB) |        |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 7                       | 0.79%  |
| 400017.08-CP 400017.08 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 2                       | 1.31%  |
| Precinct VBM           | 0                     | 0.00% | 1                   | 0.15% | 1                       | 0.15% | 7                       | 1.05%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 4.35%  |
| Total                  | 0                     | 0.00% | 1                   | 0.12% | 1                       | 0.12% | 10                      | 1.18%  |
| 400018.08-CP 400018.08 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 4                       | 5.19%  |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 0.42%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00%  |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 5                       | 1.56%  |
| 400020.08-CP 400020.08 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 6                       | 5.71%  |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.32% | 5                       | 1.62%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00%  |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.24% | 11                      | 2.64%  |
| 400021.07-CP 400021.07 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 1                     | 0.85% | 0                   | 0.00% | 0                       | 0.00% | 3                       | 2.54%  |
| Precinct VBM           | 2                     | 0.29% | 4                   | 0.58% | 4                       | 0.58% | 16                      | 2.33%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00%  |
| Total                  | 3                     | 0.37% | 4                   | 0.49% | 4                       | 0.49% | 19                      | 2.32%  |
| 500001.10-CP 500001.10 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 3                       | 1.62%  |
| Precinct VBM           | 0                     | 0.00% | 4                   | 0.74% | 0                       | 0.00% | 4                       | 0.74%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 7.14%  |
| Total                  | 0                     | 0.00% | 4                   | 0.54% | 0                       | 0.00% | 8                       | 1.09%  |
| 500002.10-CP 500002.10 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 4                       | 3.33%  |
| Precinct VBM           | 0                     | 0.00% | 3                   | 0.81% | 1                       | 0.27% | 5                       | 1.35%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00%  |
| Total                  | 0                     | 0.00% | 3                   | 0.58% | 1                       | 0.19% | 9                       | 1.74%  |
| 500003.10-CP 500003.10 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 3                       | 2.52%  |
| Precinct VBM           | 0                     | 0.00% | 1                   | 0.33% | 1                       | 0.33% | 9                       | 2.98%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 14.29% |
| Total                  | 0                     | 0.00% | 1                   | 0.23% | 1                       | 0.23% | 13                      | 3.04%  |
| 500004.10-CP 500004.10 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 3                       | 2.80%  |
| Precinct VBM           | 0                     | 0.00% | 1                   | 0.43% | 0                       | 0.00% | 7                       | 3.04%  |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 5.56%  |
| Total                  | 0                     | 0.00% | 1                   | 0.28% | 0                       | 0.00% | 11                      | 3.10%  |
| 500005.10-CP 500005.10 |                       |       |                     |       |                         |       |                         |        |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -      |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 1.03%  |
| Precinct VBM           | 0                     | 0.00% | 1                   | 0.46% | 0                       | 0.00% | 8                       | 3.65%  |

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|                        | GAR MYERS (NPP)       |       | LING LING SHI (NPP) |       | SCOTT A. VINEBERG (NPP) |       | GAIL K. LIGHTFOOT (LIB) |       |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 1                   | 8.33% | 0                       | 0.00% | 1                       | 8.33% |
| Total                  | 0                     | 0.00% | 2                   | 0.61% | 0                       | 0.00% | 10                      | 3.05% |
| 500006.10-CP 500006.10 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 3                   | 1.83% | 0                       | 0.00% | 6                       | 3.66% |
| Precinct VBM           | 0                     | 0.00% | 2                   | 0.46% | 1                       | 0.23% | 12                      | 2.73% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 2.04% |
| Total                  | 0                     | 0.00% | 5                   | 0.77% | 1                       | 0.15% | 19                      | 2.91% |
| 500007.10-CP 500007.10 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 0.53% |
| Precinct VBM           | 0                     | 0.00% | 3                   | 0.50% | 1                       | 0.17% | 8                       | 1.33% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 5.56% |
| Total                  | 0                     | 0.00% | 3                   | 0.37% | 1                       | 0.12% | 10                      | 1.24% |
| 500009.10-CP 500009.10 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.93% | 3                       | 2.78% |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.37% | 11                      | 4.12% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 2                       | 6.45% |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 2                       | 0.49% | 16                      | 3.94% |
| 500011.10-CP 500011.10 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 1                     | 0.61% | 0                   | 0.00% | 1                       | 0.61% | 3                       | 1.84% |
| Precinct VBM           | 1                     | 0.30% | 4                   | 1.18% | 1                       | 0.30% | 6                       | 1.78% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 1                   | 3.03% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 2                     | 0.37% | 5                   | 0.94% | 2                       | 0.37% | 9                       | 1.69% |
| 500012.10-CP 500012.10 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.59% | 1                       | 0.59% | 3                       | 1.76% |
| Precinct VBM           | 0                     | 0.00% | 1                   | 0.18% | 0                       | 0.00% | 6                       | 1.11% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 2                   | 0.27% | 1                       | 0.13% | 9                       | 1.21% |
| 500015.10-CP 500015.10 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 2                       | 2.08% |
| Precinct VBM           | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 3                       | 0.82% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 5                       | 1.06% |
| 500020.10-CP 500020.10 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.71% | 0                       | 0.00% | 4                       | 2.84% |
| Precinct VBM           | 0                     | 0.00% | 3                   | 0.49% | 0                       | 0.00% | 9                       | 1.48% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 1                       | 3.85% | 1                       | 3.85% |
| Total                  | 0                     | 0.00% | 4                   | 0.52% | 1                       | 0.13% | 14                      | 1.81% |
| 500021.10-CP 500021.10 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | 0.00% | 1                   | 0.67% | 0                       | 0.00% | 5                       | 3.36% |
| Precinct VBM           | 0                     | 0.00% | 4                   | 0.70% | 1                       | 0.17% | 11                      | 1.92% |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 5                   | 0.67% | 1                       | 0.13% | 16                      | 2.14% |
| 95U201.01-MB 95U201.01 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |

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|                        | GAR MYERS (NPP)       |       | LING LING SHI (NPP) |       | SCOTT A. VINEBERG (NPP) |       | GAIL K. LIGHTFOOT (LIB) |       |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Provisional            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| 95U201.02-MB 95U201.02 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 7                       | 2.30% |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 7                       | 2.30% |
| 95U202.02-MB 95U202.02 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 1                     | 0.20% | 0                   | 0.00% | 0                       | 0.00% | 11                      | 2.18% |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 1                     | 0.20% | 0                   | 0.00% | 0                       | 0.00% | 11                      | 2.17% |
| 95U203.02-MB 95U203.02 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Total                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| 95U204.01-MB 95U204.01 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 9                       | 1.51% |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 9                       | 1.50% |
| 95U301.05-MB 95U301.05 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | 0.00% | 1                   | 0.74% | 0                       | 0.00% | 7                       | 5.15% |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 1                   | 0.72% | 0                       | 0.00% | 7                       | 5.07% |
| 95U302.04-MB 95U302.04 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Provisional            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| 95U302.05-MB 95U302.05 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | 0.00% | 0                   | 0.00% | 1                       | 1.23% | 1                       | 1.23% |
| Provisional            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 1                       | 1.23% | 1                       | 1.23% |
| 95U303.03-MB 95U303.03 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 1.28% |
| Provisional            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 1.28% |

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|                        | United States Senator |       |                     |       |                         |       |                         |       |
|------------------------|-----------------------|-------|---------------------|-------|-------------------------|-------|-------------------------|-------|
|                        | GAR MYERS (NPP)       |       | LING LING SHI (NPP) |       | SCOTT A. VINEBERG (NPP) |       | GAIL K. LIGHTFOOT (LIB) |       |
| 95U303.05-MB 95U303.05 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | 0.00% | 1                   | 1.05% | 0                       | 0.00% | 1                       | 1.05% |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 1                   | 1.04% | 0                       | 0.00% | 1                       | 1.04% |
| 95U304.05-MB 95U304.05 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Total                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| 95U305.05-MB 95U305.05 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Provisional            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| 95U401.08-MB 95U401.08 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Provisional            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| 95U402.07-MB 95U402.07 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.43% | 4                       | 1.71% |
| Provisional            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Total                  | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.43% | 4                       | 1.71% |
| 95U402.08-MB 95U402.08 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | 0.00% | 2                   | 0.40% | 0                       | 0.00% | 10                      | 1.98% |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 2                   | 0.39% | 0                       | 0.00% | 10                      | 1.97% |
| 95U403.08-MB 95U403.08 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | 0.00% | 2                   | 0.21% | 0                       | 0.00% | 16                      | 1.69% |
| Provisional            | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                  | 0                     | 0.00% | 2                   | 0.21% | 0                       | 0.00% | 16                      | 1.67% |
| 95U404.06-MB 95U404.06 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Provisional            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Total                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| 95U404.08-MB 95U404.08 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM           | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct            | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |

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|                               | GAR MYERS (NPP)       |       | LING LING SHI (NPP) |       | SCOTT A. VINEBERG (NPP) |       | GAIL K. LIGHTFOOT (LIB) |       |
| Provisional                   | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Total                         | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| 95U501.09-MB 95U501.09        |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS                   | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct                   | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Provisional                   | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Total                         | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| 95U501.10-MB 95U501.10        |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS                   | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct                   | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.63% | 3                       | 1.88% |
| Provisional                   | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                         | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.62% | 3                       | 1.85% |
| 95U598.10-MB 95U598.10        |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS                   | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct                   | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 2                       | 0.93% |
| Provisional                   | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                         | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 2                       | 0.93% |
| 95U599.10-MB 95U599.10        |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS                   | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct                   | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 2.94% |
| Provisional                   | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Total                         | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 1                       | 2.94% |
| 96U101.11-MB 96U101.11        |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS                   | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct                   | 0                     | 0.00% | 0                   | 0.00% | 2                       | 0.54% | 12                      | 3.27% |
| Provisional                   | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                         | 0                     | 0.00% | 0                   | 0.00% | 2                       | 0.54% | 12                      | 3.22% |
| 96U401.14-MB 96U401.14        |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS                   | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct VBM                  | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| MB Precinct                   | 0                     | 0.00% | 2                   | 0.62% | 1                       | 0.31% | 6                       | 1.86% |
| Provisional                   | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Total                         | 0                     | 0.00% | 2                   | 0.61% | 1                       | 0.30% | 6                       | 1.83% |
| Total                         |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                  | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Precinct OS                   | 6                     | 0.06% | 37                  | 0.35% | 29                      | 0.27% | 224                     | 2.09% |
| Precinct VBM                  | 14                    | 0.03% | 119                 | 0.29% | 63                      | 0.15% | 618                     | 1.51% |
| MB Precinct                   | 1                     | 0.02% | 8                   | 0.17% | 6                       | 0.13% | 91                      | 1.97% |
| Provisional                   | 2                     | 0.15% | 10                  | 0.76% | 6                       | 0.46% | 39                      | 2.97% |
| Total                         | 23                    | 0.04% | 174                 | 0.30% | 104                     | 0.18% | 972                     | 1.69% |
| STATE BOARD OF EQUALIZATION   |                       |       |                     |       |                         |       |                         |       |
| STATE BOARD OF EQUALIZATION 1 |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                  | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Precinct OS                   | 6                     | 0.06% | 37                  | 0.35% | 29                      | 0.27% | 224                     | 2.09% |
| Precinct VBM                  | 14                    | 0.03% | 119                 | 0.29% | 63                      | 0.15% | 618                     | 1.51% |
| MB Precinct                   | 1                     | 0.02% | 8                   | 0.17% | 6                       | 0.13% | 91                      | 1.97% |
| Provisional                   | 2                     | 0.15% | 10                  | 0.76% | 6                       | 0.46% | 39                      | 2.97% |
| Total                         | 23                    | 0.04% | 174                 | 0.30% | 104                     | 0.18% | 972                     | 1.69% |
| Total                         |                       |       |                     |       |                         |       |                         |       |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                                     | United States Senator |              |                     |              |                         |              |                         |              |
|-------------------------------------|-----------------------|--------------|---------------------|--------------|-------------------------|--------------|-------------------------|--------------|
|                                     | GAR MYERS (NPP)       |              | LING LING SHI (NPP) |              | SCOTT A. VINEBERG (NPP) |              | GAIL K. LIGHTFOOT (LIB) |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                   | 0.00%        | 0                       | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 6                     | 0.06%        | 37                  | 0.35%        | 29                      | 0.27%        | 224                     | 2.09%        |
| Precinct VBM                        | 14                    | 0.03%        | 119                 | 0.29%        | 63                      | 0.15%        | 618                     | 1.51%        |
| MB Precinct                         | 1                     | 0.02%        | 8                   | 0.17%        | 6                       | 0.13%        | 91                      | 1.97%        |
| Provisional                         | 2                     | 0.15%        | 10                  | 0.76%        | 6                       | 0.46%        | 39                      | 2.97%        |
| <b>Total</b>                        | <b>23</b>             | <b>0.04%</b> | <b>174</b>          | <b>0.30%</b> | <b>104</b>              | <b>0.18%</b> | <b>972</b>              | <b>1.69%</b> |
| <b>UNITED STATES SENATE</b>         |                       |              |                     |              |                         |              |                         |              |
| <b>UNITED STATES SENATE</b>         |                       |              |                     |              |                         |              |                         |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                   | 0.00%        | 0                       | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 6                     | 0.06%        | 37                  | 0.35%        | 29                      | 0.27%        | 224                     | 2.09%        |
| Precinct VBM                        | 14                    | 0.03%        | 119                 | 0.29%        | 63                      | 0.15%        | 618                     | 1.51%        |
| MB Precinct                         | 1                     | 0.02%        | 8                   | 0.17%        | 6                       | 0.13%        | 91                      | 1.97%        |
| Provisional                         | 2                     | 0.15%        | 10                  | 0.76%        | 6                       | 0.46%        | 39                      | 2.97%        |
| <b>Total</b>                        | <b>23</b>             | <b>0.04%</b> | <b>174</b>          | <b>0.30%</b> | <b>104</b>              | <b>0.18%</b> | <b>972</b>              | <b>1.69%</b> |
| <b>Total</b>                        |                       |              |                     |              |                         |              |                         |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                   | 0.00%        | 0                       | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 6                     | 0.06%        | 37                  | 0.35%        | 29                      | 0.27%        | 224                     | 2.09%        |
| Precinct VBM                        | 14                    | 0.03%        | 119                 | 0.29%        | 63                      | 0.15%        | 618                     | 1.51%        |
| MB Precinct                         | 1                     | 0.02%        | 8                   | 0.17%        | 6                       | 0.13%        | 91                      | 1.97%        |
| Provisional                         | 2                     | 0.15%        | 10                  | 0.76%        | 6                       | 0.46%        | 39                      | 2.97%        |
| <b>Total</b>                        | <b>23</b>             | <b>0.04%</b> | <b>174</b>          | <b>0.30%</b> | <b>104</b>              | <b>0.18%</b> | <b>972</b>              | <b>1.69%</b> |
| <b>Total</b>                        |                       |              |                     |              |                         |              |                         |              |
| <b>UNITED STATES REPRESENTATIVE</b> |                       |              |                     |              |                         |              |                         |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                       |              |                     |              |                         |              |                         |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                   | 0.00%        | 0                       | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 6                     | 0.06%        | 37                  | 0.35%        | 29                      | 0.27%        | 224                     | 2.09%        |
| Precinct VBM                        | 14                    | 0.03%        | 119                 | 0.29%        | 63                      | 0.15%        | 618                     | 1.51%        |
| MB Precinct                         | 1                     | 0.02%        | 8                   | 0.17%        | 6                       | 0.13%        | 91                      | 1.97%        |
| Provisional                         | 2                     | 0.15%        | 10                  | 0.76%        | 6                       | 0.46%        | 39                      | 2.97%        |
| <b>Total</b>                        | <b>23</b>             | <b>0.04%</b> | <b>174</b>          | <b>0.30%</b> | <b>104</b>              | <b>0.18%</b> | <b>972</b>              | <b>1.69%</b> |
| <b>Total</b>                        |                       |              |                     |              |                         |              |                         |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                   | 0.00%        | 0                       | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 6                     | 0.06%        | 37                  | 0.35%        | 29                      | 0.27%        | 224                     | 2.09%        |
| Precinct VBM                        | 14                    | 0.03%        | 119                 | 0.29%        | 63                      | 0.15%        | 618                     | 1.51%        |
| MB Precinct                         | 1                     | 0.02%        | 8                   | 0.17%        | 6                       | 0.13%        | 91                      | 1.97%        |
| Provisional                         | 2                     | 0.15%        | 10                  | 0.76%        | 6                       | 0.46%        | 39                      | 2.97%        |
| <b>Total</b>                        | <b>23</b>             | <b>0.04%</b> | <b>174</b>          | <b>0.30%</b> | <b>104</b>              | <b>0.18%</b> | <b>972</b>              | <b>1.69%</b> |
| <b>Total</b>                        |                       |              |                     |              |                         |              |                         |              |
| <b>STATE SENATE</b>                 |                       |              |                     |              |                         |              |                         |              |
| <b>1ST STATE SENATE DISTRICT</b>    |                       |              |                     |              |                         |              |                         |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                   | 0.00%        | 0                       | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 6                     | 0.06%        | 37                  | 0.35%        | 29                      | 0.27%        | 224                     | 2.09%        |
| Precinct VBM                        | 14                    | 0.03%        | 119                 | 0.29%        | 63                      | 0.15%        | 618                     | 1.51%        |
| MB Precinct                         | 1                     | 0.02%        | 8                   | 0.17%        | 6                       | 0.13%        | 91                      | 1.97%        |
| Provisional                         | 2                     | 0.15%        | 10                  | 0.76%        | 6                       | 0.46%        | 39                      | 2.97%        |
| <b>Total</b>                        | <b>23</b>             | <b>0.04%</b> | <b>174</b>          | <b>0.30%</b> | <b>104</b>              | <b>0.18%</b> | <b>972</b>              | <b>1.69%</b> |
| <b>Total</b>                        |                       |              |                     |              |                         |              |                         |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                   | 0.00%        | 0                       | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 6                     | 0.06%        | 37                  | 0.35%        | 29                      | 0.27%        | 224                     | 2.09%        |
| Precinct VBM                        | 14                    | 0.03%        | 119                 | 0.29%        | 63                      | 0.15%        | 618                     | 1.51%        |
| MB Precinct                         | 1                     | 0.02%        | 8                   | 0.17%        | 6                       | 0.13%        | 91                      | 1.97%        |
| Provisional                         | 2                     | 0.15%        | 10                  | 0.76%        | 6                       | 0.46%        | 39                      | 2.97%        |
| <b>Total</b>                        | <b>23</b>             | <b>0.04%</b> | <b>174</b>          | <b>0.30%</b> | <b>104</b>              | <b>0.18%</b> | <b>972</b>              | <b>1.69%</b> |
| <b>Total</b>                        |                       |              |                     |              |                         |              |                         |              |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                       |              |                     |              |                         |              |                         |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |                       |              |                     |              |                         |              |                         |              |
| Precinct TSx                        | 0                     | 0.00%        | 0                   | 0.00%        | 0                       | 0.00%        | 0                       | 0.00%        |
| Precinct OS                         | 5                     | 0.07%        | 20                  | 0.29%        | 14                      | 0.21%        | 164                     | 2.41%        |
| Precinct VBM                        | 12                    | 0.05%        | 73                  | 0.29%        | 37                      | 0.15%        | 443                     | 1.74%        |
| MB Precinct                         | 1                     | 0.03%        | 6                   | 0.15%        | 3                       | 0.08%        | 73                      | 1.86%        |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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|                                   | United States Senator |       |                     |       |                         |       |                         |       |
|-----------------------------------|-----------------------|-------|---------------------|-------|-------------------------|-------|-------------------------|-------|
|                                   | GAR MYERS (NPP)       |       | LING LING SHI (NPP) |       | SCOTT A. VINEBERG (NPP) |       | GAIL K. LIGHTFOOT (LIB) |       |
| Provisional                       | 1                     | 0.12% | 3                   | 0.35% | 5                       | 0.58% | 28                      | 3.26% |
| Total                             | 19                    | 0.05% | 102                 | 0.28% | 59                      | 0.16% | 708                     | 1.91% |
| <b>6TH STATE ASSEMBLY DIST</b>    |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                      | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Precinct OS                       | 1                     | 0.03% | 17                  | 0.44% | 15                      | 0.39% | 60                      | 1.54% |
| Precinct VBM                      | 2                     | 0.01% | 46                  | 0.30% | 26                      | 0.17% | 175                     | 1.13% |
| MB Precinct                       | 0                     | 0.00% | 2                   | 0.29% | 3                       | 0.44% | 18                      | 2.61% |
| Provisional                       | 1                     | 0.22% | 7                   | 1.54% | 1                       | 0.22% | 11                      | 2.42% |
| Total                             | 4                     | 0.02% | 72                  | 0.35% | 45                      | 0.22% | 264                     | 1.29% |
| <b>Total</b>                      |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                      | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Precinct OS                       | 6                     | 0.06% | 37                  | 0.35% | 29                      | 0.27% | 224                     | 2.09% |
| Precinct VBM                      | 14                    | 0.03% | 119                 | 0.29% | 63                      | 0.15% | 618                     | 1.51% |
| MB Precinct                       | 1                     | 0.02% | 8                   | 0.17% | 6                       | 0.13% | 91                      | 1.97% |
| Provisional                       | 2                     | 0.15% | 10                  | 0.76% | 6                       | 0.46% | 39                      | 2.97% |
| Total                             | 23                    | 0.04% | 174                 | 0.30% | 104                     | 0.18% | 972                     | 1.69% |
| <b>SUPERVISORIAL DISTRICTS</b>    |                       |       |                     |       |                         |       |                         |       |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                      | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS                       | 1                     | 0.04% | 13                  | 0.58% | 11                      | 0.49% | 22                      | 0.99% |
| Precinct VBM                      | 2                     | 0.02% | 24                  | 0.26% | 12                      | 0.13% | 98                      | 1.05% |
| MB Precinct                       | 0                     | 0.00% | 0                   | 0.00% | 2                       | 0.54% | 12                      | 3.27% |
| Provisional                       | 1                     | 0.41% | 2                   | 0.82% | 0                       | 0.00% | 3                       | 1.23% |
| Total                             | 4                     | 0.03% | 39                  | 0.32% | 25                      | 0.21% | 135                     | 1.11% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                      | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Precinct OS                       | 1                     | 0.04% | 6                   | 0.27% | 4                       | 0.18% | 59                      | 2.62% |
| Precinct VBM                      | 4                     | 0.05% | 28                  | 0.32% | 15                      | 0.17% | 142                     | 1.64% |
| MB Precinct                       | 1                     | 0.07% | 0                   | 0.00% | 0                       | 0.00% | 27                      | 1.91% |
| Provisional                       | 0                     | 0.00% | 4                   | 1.44% | 1                       | 0.36% | 11                      | 3.97% |
| Total                             | 6                     | 0.05% | 38                  | 0.30% | 20                      | 0.16% | 239                     | 1.90% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                      | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Precinct OS                       | 0                     | 0.00% | 7                   | 0.33% | 8                       | 0.38% | 53                      | 2.50% |
| Precinct VBM                      | 3                     | 0.03% | 18                  | 0.20% | 12                      | 0.13% | 143                     | 1.58% |
| MB Precinct                       | 0                     | 0.00% | 2                   | 0.50% | 1                       | 0.25% | 10                      | 2.51% |
| Provisional                       | 1                     | 0.42% | 1                   | 0.42% | 4                       | 1.69% | 8                       | 3.38% |
| Total                             | 4                     | 0.03% | 28                  | 0.24% | 25                      | 0.21% | 214                     | 1.81% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                      | 0                     | -     | 0                   | -     | 0                       | -     | 0                       | -     |
| Precinct OS                       | 3                     | 0.13% | 5                   | 0.22% | 3                       | 0.13% | 49                      | 2.14% |
| Precinct VBM                      | 4                     | 0.05% | 22                  | 0.26% | 17                      | 0.20% | 136                     | 1.60% |
| MB Precinct                       | 0                     | 0.00% | 6                   | 0.30% | 2                       | 0.10% | 36                      | 1.78% |
| Provisional                       | 0                     | 0.00% | 1                   | 0.39% | 0                       | 0.00% | 8                       | 3.14% |
| Total                             | 7                     | 0.05% | 34                  | 0.26% | 22                      | 0.17% | 229                     | 1.75% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                      | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Precinct OS                       | 1                     | 0.06% | 6                   | 0.33% | 3                       | 0.17% | 41                      | 2.27% |
| Precinct VBM                      | 1                     | 0.02% | 27                  | 0.50% | 7                       | 0.13% | 99                      | 1.84% |
| MB Precinct                       | 0                     | 0.00% | 0                   | 0.00% | 1                       | 0.24% | 6                       | 1.46% |
| Provisional                       | 0                     | 0.00% | 2                   | 0.66% | 1                       | 0.33% | 9                       | 2.98% |
| Total                             | 2                     | 0.03% | 35                  | 0.44% | 12                      | 0.15% | 155                     | 1.96% |
| <b>Total</b>                      |                       |       |                     |       |                         |       |                         |       |
| Precinct TSx                      | 0                     | 0.00% | 0                   | 0.00% | 0                       | 0.00% | 0                       | 0.00% |
| Precinct OS                       | 6                     | 0.06% | 37                  | 0.35% | 29                      | 0.27% | 224                     | 2.09% |
| Precinct VBM                      | 14                    | 0.03% | 119                 | 0.29% | 63                      | 0.15% | 618                     | 1.51% |
| MB Precinct                       | 1                     | 0.02% | 8                   | 0.17% | 6                       | 0.13% | 91                      | 1.97% |
| Provisional                       | 2                     | 0.15% | 10                  | 0.76% | 6                       | 0.46% | 39                      | 2.97% |
| Total                             | 23                    | 0.04% | 174                 | 0.30% | 104                     | 0.18% | 972                     | 1.69% |

**COUNTY OF EL DORADO**  
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|                                 | United States Senator |              |                     |              |                         |              |                         |              |
|---------------------------------|-----------------------|--------------|---------------------|--------------|-------------------------|--------------|-------------------------|--------------|
|                                 | GAR MYERS (NPP)       |              | LING LING SHI (NPP) |              | SCOTT A. VINEBERG (NPP) |              | GAIL K. LIGHTFOOT (LIB) |              |
| <b>CITIES</b>                   |                       |              |                     |              |                         |              |                         |              |
| <b>CITY OF PLACERVILLE</b>      |                       |              |                     |              |                         |              |                         |              |
| Precinct TSx                    | 0                     | -            | 0                   | -            | 0                       | -            | 0                       | -            |
| Precinct OS                     | 0                     | 0.00%        | 2                   | 0.36%        | 3                       | 0.54%        | 19                      | 3.41%        |
| Precinct VBM                    | 0                     | 0.00%        | 9                   | 0.38%        | 4                       | 0.17%        | 35                      | 1.49%        |
| MB Precinct                     | 0                     | -            | 0                   | -            | 0                       | -            | 0                       | -            |
| Provisional                     | 0                     | 0.00%        | 0                   | 0.00%        | 1                       | 1.43%        | 2                       | 2.86%        |
| <b>Total</b>                    | <b>0</b>              | <b>0.00%</b> | <b>11</b>           | <b>0.37%</b> | <b>8</b>                | <b>0.27%</b> | <b>56</b>               | <b>1.88%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                       |              |                     |              |                         |              |                         |              |
| Precinct TSx                    | 0                     | -            | 0                   | -            | 0                       | -            | 0                       | -            |
| Precinct OS                     | 1                     | 0.11%        | 3                   | 0.32%        | 2                       | 0.21%        | 21                      | 2.22%        |
| Precinct VBM                    | 1                     | 0.04%        | 14                  | 0.57%        | 5                       | 0.20%        | 57                      | 2.31%        |
| MB Precinct                     | 0                     | -            | 0                   | -            | 0                       | -            | 0                       | -            |
| Provisional                     | 0                     | 0.00%        | 2                   | 1.08%        | 0                       | 0.00%        | 6                       | 3.23%        |
| <b>Total</b>                    | <b>2</b>              | <b>0.06%</b> | <b>19</b>           | <b>0.53%</b> | <b>7</b>                | <b>0.19%</b> | <b>84</b>               | <b>2.33%</b> |
| <b>UNINCORPORATED AREA</b>      |                       |              |                     |              |                         |              |                         |              |
| Precinct TSx                    | 0                     | 0.00%        | 0                   | 0.00%        | 0                       | 0.00%        | 0                       | 0.00%        |
| Precinct OS                     | 5                     | 0.05%        | 32                  | 0.35%        | 24                      | 0.26%        | 184                     | 2.00%        |
| Precinct VBM                    | 13                    | 0.04%        | 96                  | 0.27%        | 54                      | 0.15%        | 526                     | 1.46%        |
| MB Precinct                     | 1                     | 0.02%        | 8                   | 0.17%        | 6                       | 0.13%        | 91                      | 1.97%        |
| Provisional                     | 2                     | 0.19%        | 8                   | 0.76%        | 5                       | 0.47%        | 31                      | 2.93%        |
| <b>Total</b>                    | <b>21</b>             | <b>0.04%</b> | <b>144</b>          | <b>0.28%</b> | <b>89</b>               | <b>0.17%</b> | <b>832</b>              | <b>1.63%</b> |
| <b>Total</b>                    |                       |              |                     |              |                         |              |                         |              |
| Precinct TSx                    | 0                     | 0.00%        | 0                   | 0.00%        | 0                       | 0.00%        | 0                       | 0.00%        |
| Precinct OS                     | 6                     | 0.06%        | 37                  | 0.35%        | 29                      | 0.27%        | 224                     | 2.09%        |
| Precinct VBM                    | 14                    | 0.03%        | 119                 | 0.29%        | 63                      | 0.15%        | 618                     | 1.51%        |
| MB Precinct                     | 1                     | 0.02%        | 8                   | 0.17%        | 6                       | 0.13%        | 91                      | 1.97%        |
| Provisional                     | 2                     | 0.15%        | 10                  | 0.76%        | 6                       | 0.46%        | 39                      | 2.97%        |
| <b>Total</b>                    | <b>23</b>             | <b>0.04%</b> | <b>174</b>          | <b>0.30%</b> | <b>104</b>              | <b>0.18%</b> | <b>972</b>              | <b>1.69%</b> |

**COUNTY OF EL DORADO**  
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|                        | United States Senator |        |                          |       |                        |        |
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|                        | RON UNZ (REP)         |        | JARRELL WILLIAMSON (REP) |       | KAMALA D. HARRIS (DEM) |        |
| Jurisdiction Wide      |                       |        |                          |       |                        |        |
| 100001.11-CP 100001.11 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 2                     | 1.27%  | 1                        | 0.64% | 60                     | 38.22% |
| Precinct VBM           | 38                    | 5.41%  | 10                       | 1.42% | 214                    | 30.44% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 7                      | 43.75% |
| Total                  | 40                    | 4.57%  | 11                       | 1.26% | 281                    | 32.08% |
| 100002.11-CP 100002.11 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 1                     | 1.32%  | 0                        | 0.00% | 29                     | 38.16% |
| Precinct VBM           | 15                    | 3.72%  | 7                        | 1.74% | 141                    | 34.99% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 2                     | 12.50% | 0                        | 0.00% | 3                      | 18.75% |
| Total                  | 18                    | 3.64%  | 7                        | 1.41% | 173                    | 34.95% |
| 100003.11-CP 100003.11 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 2                     | 1.80%  | 0                        | 0.00% | 37                     | 33.33% |
| Precinct VBM           | 19                    | 2.68%  | 6                        | 0.85% | 279                    | 39.41% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 1                     | 4.55%  | 1                        | 4.55% | 8                      | 36.36% |
| Total                  | 22                    | 2.62%  | 7                        | 0.83% | 324                    | 38.53% |
| 100004.11-CP 100004.11 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 12                    | 5.13%  | 2                        | 0.85% | 96                     | 41.03% |
| Precinct VBM           | 18                    | 2.70%  | 4                        | 0.60% | 266                    | 39.94% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 0                      | 0.00%  |
| Total                  | 30                    | 3.33%  | 6                        | 0.67% | 362                    | 40.13% |
| 100005.11-CP 100005.11 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 2                     | 1.49%  | 1                        | 0.75% | 38                     | 28.36% |
| Precinct VBM           | 34                    | 4.21%  | 6                        | 0.74% | 251                    | 31.10% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 2                     | 10.53% | 0                        | 0.00% | 3                      | 15.79% |
| Total                  | 38                    | 3.96%  | 7                        | 0.73% | 292                    | 30.42% |
| 100006.11-CP 100006.11 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 13                    | 5.91%  | 4                        | 1.82% | 71                     | 32.27% |
| Precinct VBM           | 17                    | 2.01%  | 8                        | 0.95% | 296                    | 34.99% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 8                      | 38.10% |
| Total                  | 30                    | 2.76%  | 12                       | 1.10% | 375                    | 34.50% |
| 100008.11-CP 100008.11 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 7                     | 4.02%  | 0                        | 0.00% | 72                     | 41.38% |
| Precinct VBM           | 16                    | 2.48%  | 7                        | 1.08% | 228                    | 35.29% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 1                        | 9.09% | 5                      | 45.45% |
| Total                  | 23                    | 2.77%  | 8                        | 0.96% | 305                    | 36.70% |
| 100009.11-CP 100009.11 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 4                     | 2.74%  | 0                        | 0.00% | 60                     | 41.10% |
| Precinct VBM           | 18                    | 2.66%  | 0                        | 0.00% | 237                    | 35.06% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 3                      | 16.67% |
| Total                  | 22                    | 2.62%  | 0                        | 0.00% | 300                    | 35.71% |
| 100012.11-CP 100012.11 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 7                     | 3.06%  | 1                        | 0.44% | 94                     | 41.05% |
| Precinct VBM           | 28                    | 3.27%  | 4                        | 0.47% | 287                    | 33.49% |

**COUNTY OF EL DORADO**  
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|                        | United States Senator |        |                          |       |                        |        |
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|                        | RON UNZ (REP)         |        | JARRELL WILLIAMSON (REP) |       | KAMALA D. HARRIS (DEM) |        |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 5                      | 17.86% |
| Total                  | 35                    | 3.14%  | 5                        | 0.45% | 386                    | 34.65% |
| 100013.11-CP 100013.11 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 3                     | 1.36%  | 1                        | 0.45% | 72                     | 32.58% |
| Precinct VBM           | 18                    | 2.22%  | 7                        | 0.87% | 274                    | 33.87% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 3                     | 12.50% | 1                        | 4.17% | 9                      | 37.50% |
| Total                  | 24                    | 2.28%  | 9                        | 0.85% | 355                    | 33.68% |
| 100014.11-CP 100014.11 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 3                     | 1.73%  | 1                        | 0.58% | 53                     | 30.64% |
| Precinct VBM           | 23                    | 3.68%  | 4                        | 0.64% | 218                    | 34.88% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 5                      | 22.73% |
| Total                  | 26                    | 3.17%  | 5                        | 0.61% | 276                    | 33.66% |
| 100022.11-CP 100022.11 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 4                     | 2.48%  | 1                        | 0.62% | 60                     | 37.27% |
| Precinct VBM           | 11                    | 1.80%  | 8                        | 1.31% | 189                    | 30.88% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 3                      | 30.00% |
| Total                  | 15                    | 1.92%  | 9                        | 1.15% | 252                    | 32.18% |
| 100023.11-CP 100023.11 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 3                     | 1.55%  | 1                        | 0.52% | 50                     | 25.91% |
| Precinct VBM           | 50                    | 5.36%  | 7                        | 0.75% | 275                    | 29.51% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 1                     | 3.57%  | 1                        | 3.57% | 9                      | 32.14% |
| Total                  | 54                    | 4.68%  | 9                        | 0.78% | 334                    | 28.97% |
| 200003.12-CP 200003.12 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 0                     | 0.00%  | 2                        | 1.35% | 46                     | 31.08% |
| Precinct VBM           | 17                    | 3.38%  | 6                        | 1.19% | 151                    | 30.02% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 1                     | 3.85%  | 0                        | 0.00% | 3                      | 11.54% |
| Total                  | 18                    | 2.66%  | 8                        | 1.18% | 200                    | 29.54% |
| 200004.12-CP 200004.12 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 4                     | 2.42%  | 3                        | 1.82% | 50                     | 30.30% |
| Precinct VBM           | 14                    | 2.32%  | 1                        | 0.17% | 216                    | 35.82% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 3                     | 21.43% | 0                        | 0.00% | 1                      | 7.14%  |
| Total                  | 21                    | 2.69%  | 4                        | 0.51% | 267                    | 34.14% |
| 200005.02-CP 200005.02 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 4                     | 4.60%  | 0                        | 0.00% | 32                     | 36.78% |
| Precinct VBM           | 21                    | 5.19%  | 5                        | 1.23% | 125                    | 30.86% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 7                      | 36.84% |
| Total                  | 25                    | 4.89%  | 5                        | 0.98% | 164                    | 32.09% |
| 200006.12-CP 200006.12 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 6                     | 5.56%  | 0                        | 0.00% | 25                     | 23.15% |
| Precinct VBM           | 14                    | 2.92%  | 7                        | 1.46% | 132                    | 27.56% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 1                     | 3.70%  | 0                        | 0.00% | 9                      | 33.33% |
| Total                  | 21                    | 3.42%  | 7                        | 1.14% | 166                    | 27.04% |
| 200008.12-CP 200008.12 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |

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|                        | RON UNZ (REP)         |        | JARRELL WILLIAMSON (REP) |       | KAMALA D. HARRIS (DEM) |        |
| Precinct OS            | 0                     | 0.00%  | 0                        | 0.00% | 41                     | 30.83% |
| Precinct VBM           | 9                     | 2.75%  | 1                        | 0.31% | 139                    | 42.51% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 1                     | 12.50% | 0                        | 0.00% | 1                      | 12.50% |
| Total                  | 10                    | 2.14%  | 1                        | 0.21% | 181                    | 38.68% |
| 200010.12-CP 200010.12 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | 0.00%  | 0                        | 0.00% | 1                      | 25.00% |
| Precinct OS            | 7                     | 3.91%  | 2                        | 1.12% | 62                     | 34.64% |
| Precinct VBM           | 7                     | 0.93%  | 9                        | 1.20% | 272                    | 36.12% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 2                     | 7.69%  | 1                        | 3.85% | 9                      | 34.62% |
| Total                  | 16                    | 1.66%  | 12                       | 1.25% | 344                    | 35.76% |
| 200012.12-CP 200012.12 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 10                    | 7.94%  | 1                        | 0.79% | 41                     | 32.54% |
| Precinct VBM           | 2                     | 0.50%  | 2                        | 0.50% | 144                    | 35.64% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 3                      | 25.00% |
| Total                  | 12                    | 2.21%  | 3                        | 0.55% | 188                    | 34.69% |
| 200013.12-CP 200013.12 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 9                     | 4.71%  | 3                        | 1.57% | 60                     | 31.41% |
| Precinct VBM           | 18                    | 2.82%  | 5                        | 0.78% | 203                    | 31.77% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 8                      | 40.00% |
| Total                  | 27                    | 3.18%  | 8                        | 0.94% | 271                    | 31.88% |
| 200014.12-CP 200014.12 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 8                     | 4.28%  | 2                        | 1.07% | 57                     | 30.48% |
| Precinct VBM           | 11                    | 2.00%  | 3                        | 0.55% | 198                    | 36.07% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 1                        | 4.00% | 9                      | 36.00% |
| Total                  | 19                    | 2.50%  | 6                        | 0.79% | 264                    | 34.69% |
| 200015.02-CP 200015.02 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 11                    | 5.64%  | 0                        | 0.00% | 54                     | 27.69% |
| Precinct VBM           | 19                    | 2.41%  | 4                        | 0.51% | 263                    | 33.29% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 3                      | 15.00% |
| Total                  | 30                    | 2.99%  | 4                        | 0.40% | 320                    | 31.84% |
| 200016.12-CP 200016.12 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 0                     | 0.00%  | 0                        | 0.00% | 6                      | 30.00% |
| Precinct VBM           | 1                     | 0.79%  | 0                        | 0.00% | 29                     | 23.02% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 0                      | 0.00%  |
| Total                  | 1                     | 0.67%  | 0                        | 0.00% | 35                     | 23.49% |
| 200016.13-CP 200016.13 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 2                     | 2.78%  | 1                        | 1.39% | 18                     | 25.00% |
| Precinct VBM           | 9                     | 3.06%  | 2                        | 0.68% | 74                     | 25.17% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 1                      | 16.67% |
| Total                  | 11                    | 2.96%  | 3                        | 0.81% | 93                     | 25.00% |
| 200017.02-CP 200017.02 |                       |        |                          |       |                        |        |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -      |
| Precinct OS            | 1                     | 0.78%  | 3                        | 2.34% | 45                     | 35.16% |
| Precinct VBM           | 9                     | 1.65%  | 2                        | 0.37% | 181                    | 33.09% |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 2                      | 18.18% |
| Total                  | 10                    | 1.46%  | 5                        | 0.73% | 228                    | 33.24% |

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|                        | RON UNZ (REP)         |       | JARRELL WILLIAMSON (REP) |        | KAMALA D. HARRIS (DEM) |        |
| 200018.02-CP 200018.02 |                       |       |                          |        |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -      | 0                      | -      |
| Precinct OS            | 9                     | 5.92% | 2                        | 1.32%  | 35                     | 23.03% |
| Precinct VBM           | 20                    | 3.16% | 3                        | 0.47%  | 181                    | 28.64% |
| MB Precinct            | 0                     | -     | 0                        | -      | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00%  | 1                      | 8.33%  |
| Total                  | 29                    | 3.64% | 5                        | 0.63%  | 217                    | 27.26% |
| 200020.02-CP 200020.02 |                       |       |                          |        |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -      | 0                      | -      |
| Precinct OS            | 3                     | 2.42% | 0                        | 0.00%  | 31                     | 25.00% |
| Precinct VBM           | 20                    | 4.20% | 3                        | 0.63%  | 127                    | 26.68% |
| MB Precinct            | 0                     | -     | 0                        | -      | 0                      | -      |
| Provisional            | 1                     | 5.88% | 0                        | 0.00%  | 1                      | 5.88%  |
| Total                  | 24                    | 3.89% | 3                        | 0.49%  | 159                    | 25.77% |
| 200021.01-CP 200021.01 |                       |       |                          |        |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -      | 0                      | -      |
| Precinct OS            | 0                     | 0.00% | 0                        | 0.00%  | 8                      | 26.67% |
| Precinct VBM           | 0                     | 0.00% | 1                        | 0.56%  | 52                     | 28.89% |
| MB Precinct            | 0                     | -     | 0                        | -      | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00%  | 1                      | 20.00% |
| Total                  | 0                     | 0.00% | 1                        | 0.47%  | 61                     | 28.37% |
| 200021.02-CP 200021.02 |                       |       |                          |        |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -      | 0                      | -      |
| Precinct OS            | 7                     | 4.55% | 1                        | 0.65%  | 42                     | 27.27% |
| Precinct VBM           | 27                    | 4.00% | 9                        | 1.33%  | 206                    | 30.52% |
| MB Precinct            | 0                     | -     | 0                        | -      | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00%  | 4                      | 26.67% |
| Total                  | 34                    | 4.03% | 10                       | 1.18%  | 252                    | 29.86% |
| 200024.02-CP 200024.02 |                       |       |                          |        |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -      | 0                      | -      |
| Precinct OS            | 3                     | 5.66% | 1                        | 1.89%  | 8                      | 15.09% |
| Precinct VBM           | 10                    | 3.85% | 8                        | 3.08%  | 68                     | 26.15% |
| MB Precinct            | 0                     | -     | 0                        | -      | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00%  | 2                      | 40.00% |
| Total                  | 13                    | 4.09% | 9                        | 2.83%  | 78                     | 24.53% |
| 300001.04-CP 300001.04 |                       |       |                          |        |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -      | 0                      | -      |
| Precinct OS            | 0                     | 0.00% | 0                        | 0.00%  | 3                      | 27.27% |
| Precinct VBM           | 0                     | 0.00% | 2                        | 2.13%  | 30                     | 31.91% |
| MB Precinct            | 0                     | -     | 0                        | -      | 0                      | -      |
| Provisional            | 0                     | -     | 0                        | -      | 0                      | -      |
| Total                  | 0                     | 0.00% | 2                        | 1.90%  | 33                     | 31.43% |
| 300001.05-CP 300001.05 |                       |       |                          |        |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -      | 0                      | -      |
| Precinct OS            | 0                     | 0.00% | 0                        | 0.00%  | 20                     | 29.41% |
| Precinct VBM           | 7                     | 2.37% | 0                        | 0.00%  | 99                     | 33.56% |
| MB Precinct            | 0                     | -     | 0                        | -      | 0                      | -      |
| Provisional            | 0                     | 0.00% | 1                        | 11.11% | 2                      | 22.22% |
| Total                  | 7                     | 1.88% | 1                        | 0.27%  | 121                    | 32.53% |
| 300003.05-CP 300003.05 |                       |       |                          |        |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -      | 0                      | -      |
| Precinct OS            | 4                     | 2.22% | 0                        | 0.00%  | 63                     | 35.00% |
| Precinct VBM           | 19                    | 2.26% | 11                       | 1.31%  | 339                    | 40.31% |
| MB Precinct            | 0                     | -     | 0                        | -      | 0                      | -      |
| Provisional            | 2                     | 8.70% | 0                        | 0.00%  | 6                      | 26.09% |
| Total                  | 25                    | 2.39% | 11                       | 1.05%  | 408                    | 39.08% |
| 300004.05-CP 300004.05 |                       |       |                          |        |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -      | 0                      | -      |
| Precinct OS            | 2                     | 1.29% | 0                        | 0.00%  | 65                     | 41.94% |
| Precinct VBM           | 5                     | 0.95% | 7                        | 1.33%  | 202                    | 38.40% |
| MB Precinct            | 0                     | -     | 0                        | -      | 0                      | -      |

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|                        | RON UNZ (REP)         |       | JARRELL WILLIAMSON (REP) |       | KAMALA D. HARRIS (DEM) |        |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 3                      | 18.75% |
| Total                  | 7                     | 1.00% | 7                        | 1.00% | 270                    | 38.74% |
| 300005.05-CP 300005.05 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 2                     | 1.39% | 1                        | 0.69% | 44                     | 30.56% |
| Precinct VBM           | 10                    | 1.69% | 6                        | 1.02% | 211                    | 35.76% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 4                      | 18.18% |
| Total                  | 12                    | 1.59% | 7                        | 0.93% | 259                    | 34.26% |
| 300008.05-CP 300008.05 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 5                     | 3.91% | 0                        | 0.00% | 35                     | 27.34% |
| Precinct VBM           | 22                    | 3.23% | 7                        | 1.03% | 216                    | 31.67% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 1                     | 9.09% | 0                        | 0.00% | 2                      | 18.18% |
| Total                  | 28                    | 3.41% | 7                        | 0.85% | 253                    | 30.82% |
| 300009.04-CP 300009.04 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 1                     | 0.91% | 1                        | 0.91% | 26                     | 23.64% |
| Precinct VBM           | 16                    | 4.65% | 3                        | 0.87% | 109                    | 31.69% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 4                      | 33.33% |
| Total                  | 17                    | 3.65% | 4                        | 0.86% | 139                    | 29.83% |
| 300010.04-CP 300010.04 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 8                     | 5.16% | 1                        | 0.65% | 27                     | 17.42% |
| Precinct VBM           | 17                    | 2.86% | 4                        | 0.67% | 202                    | 34.01% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 1                      | 9.09%  |
| Total                  | 25                    | 3.29% | 5                        | 0.66% | 230                    | 30.26% |
| 300011.04-CP 300011.04 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 1                     | 0.77% | 2                        | 1.54% | 37                     | 28.46% |
| Precinct VBM           | 12                    | 1.88% | 2                        | 0.31% | 201                    | 31.41% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 4                      | 20.00% |
| Total                  | 13                    | 1.65% | 4                        | 0.51% | 242                    | 30.63% |
| 300013.05-CP 300013.05 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | 0.00% | 0                        | 0.00% | 0                      | 0.00%  |
| Precinct OS            | 3                     | 2.56% | 1                        | 0.85% | 43                     | 36.75% |
| Precinct VBM           | 11                    | 1.67% | 2                        | 0.30% | 198                    | 30.14% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 2                      | 18.18% |
| Total                  | 14                    | 1.78% | 3                        | 0.38% | 243                    | 30.84% |
| 300015.04-CP 300015.04 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 0                     | 0.00% | 0                        | 0.00% | 1                      | 12.50% |
| Precinct VBM           | 1                     | 1.96% | 0                        | 0.00% | 17                     | 33.33% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 1                      | 25.00% |
| Total                  | 1                     | 1.59% | 0                        | 0.00% | 19                     | 30.16% |
| 300015.05-CP 300015.05 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 0                     | 0.00% | 0                        | 0.00% | 24                     | 39.34% |
| Precinct VBM           | 3                     | 1.32% | 3                        | 1.32% | 94                     | 41.23% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 0                      | 0.00%  |
| Total                  | 3                     | 1.03% | 3                        | 1.03% | 118                    | 40.69% |
| 300017.05-CP 300017.05 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 5                     | 4.59% | 1                        | 0.92% | 34                     | 31.19% |

**COUNTY OF EL DORADO**  
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|                        | United States Senator |       |                          |       |                        |        |
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|                        | RON UNZ (REP)         |       | JARRELL WILLIAMSON (REP) |       | KAMALA D. HARRIS (DEM) |        |
| Precinct VBM           | 22                    | 3.70% | 3                        | 0.50% | 190                    | 31.93% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 3                      | 50.00% |
| Total                  | 27                    | 3.80% | 4                        | 0.56% | 227                    | 31.97% |
| 300018.04-CP 300018.04 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 2                     | 1.59% | 1                        | 0.79% | 22                     | 17.46% |
| Precinct VBM           | 6                     | 1.32% | 2                        | 0.44% | 145                    | 32.01% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 5                      | 20.00% |
| Total                  | 8                     | 1.32% | 3                        | 0.50% | 172                    | 28.48% |
| 300020.04-CP 300020.04 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 3                     | 2.07% | 1                        | 0.69% | 42                     | 28.97% |
| Precinct VBM           | 13                    | 2.28% | 2                        | 0.35% | 167                    | 29.25% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 1                     | 8.33% | 1                        | 8.33% | 0                      | 0.00%  |
| Total                  | 17                    | 2.34% | 4                        | 0.55% | 209                    | 28.71% |
| 300021.03-CP 300021.03 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 3                     | 2.26% | 1                        | 0.75% | 45                     | 33.83% |
| Precinct VBM           | 14                    | 2.48% | 4                        | 0.71% | 193                    | 34.22% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 1                     | 8.33% | 0                        | 0.00% | 4                      | 33.33% |
| Total                  | 18                    | 2.54% | 5                        | 0.71% | 242                    | 34.13% |
| 300022.05-CP 300022.05 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 7                     | 7.95% | 2                        | 2.27% | 19                     | 21.59% |
| Precinct VBM           | 1                     | 0.31% | 0                        | 0.00% | 120                    | 36.92% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 3                      | 25.00% |
| Total                  | 8                     | 1.88% | 2                        | 0.47% | 142                    | 33.41% |
| 300023.03-CP 300023.03 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 4                     | 1.91% | 0                        | 0.00% | 79                     | 37.80% |
| Precinct VBM           | 29                    | 3.49% | 9                        | 1.08% | 297                    | 35.70% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 5                      | 26.32% |
| Total                  | 33                    | 3.11% | 9                        | 0.85% | 381                    | 35.94% |
| 300023.05-CP 300023.05 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 0                     | 0.00% | 2                        | 4.76% | 13                     | 30.95% |
| Precinct VBM           | 5                     | 2.89% | 0                        | 0.00% | 64                     | 36.99% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 3                      | 37.50% |
| Total                  | 5                     | 2.24% | 2                        | 0.90% | 80                     | 35.87% |
| 400001.14-CP 400001.14 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 3                     | 1.88% | 0                        | 0.00% | 43                     | 26.88% |
| Precinct VBM           | 31                    | 3.43% | 5                        | 0.55% | 281                    | 31.05% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 4                      | 20.00% |
| Total                  | 34                    | 3.13% | 5                        | 0.46% | 328                    | 30.23% |
| 400002.08-CP 400002.08 |                       |       |                          |       |                        |        |
| Precinct TSx           | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS            | 6                     | 3.45% | 3                        | 1.72% | 48                     | 27.59% |
| Precinct VBM           | 24                    | 3.76% | 6                        | 0.94% | 166                    | 26.02% |
| MB Precinct            | 0                     | -     | 0                        | -     | 0                      | -      |
| Provisional            | 0                     | 0.00% | 0                        | 0.00% | 4                      | 30.77% |
| Total                  | 30                    | 3.64% | 9                        | 1.09% | 218                    | 26.42% |
| 400003.08-CP 400003.08 |                       |       |                          |       |                        |        |



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|                        | United States Senator |       |                             |       |                           |        |
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|                        | RON UNZ (REP)         |       | JARRELL<br>WILLIAMSON (REP) |       | KAMALA D. HARRIS<br>(DEM) |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 11                    | 4.98% | 2                           | 0.90% | 41                        | 18.55% |
| Precinct VBM           | 21                    | 3.02% | 9                           | 1.29% | 203                       | 29.21% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 2                     | 6.67% | 1                           | 3.33% | 7                         | 23.33% |
| Total                  | 34                    | 3.59% | 12                          | 1.27% | 251                       | 26.53% |
| 400004.14-CP 400004.14 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 2                     | 1.18% | 1                           | 0.59% | 41                        | 24.12% |
| Precinct VBM           | 9                     | 1.48% | 8                           | 1.31% | 179                       | 29.34% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 2                         | 11.11% |
| Total                  | 11                    | 1.38% | 9                           | 1.13% | 222                       | 27.82% |
| 400005.08-CP 400005.08 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 1                     | 1.52% | 1                           | 1.52% | 21                        | 31.82% |
| Precinct VBM           | 10                    | 4.29% | 2                           | 0.86% | 81                        | 34.76% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 1                         | 14.29% |
| Total                  | 11                    | 3.59% | 3                           | 0.98% | 103                       | 33.66% |
| 400006.08-CP 400006.08 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 15                    | 7.14% | 5                           | 2.38% | 59                        | 28.10% |
| Precinct VBM           | 16                    | 2.48% | 7                           | 1.09% | 256                       | 39.75% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 3                         | 20.00% |
| Total                  | 31                    | 3.57% | 12                          | 1.38% | 318                       | 36.59% |
| 400007.08-CP 400007.08 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 9                     | 4.27% | 1                           | 0.47% | 59                        | 27.96% |
| Precinct VBM           | 18                    | 2.55% | 6                           | 0.85% | 186                       | 26.31% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 3                         | 15.00% |
| Total                  | 27                    | 2.88% | 7                           | 0.75% | 248                       | 26.44% |
| 400010.08-CP 400010.08 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 7                     | 3.17% | 1                           | 0.45% | 69                        | 31.22% |
| Precinct VBM           | 31                    | 3.82% | 8                           | 0.99% | 289                       | 35.59% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 7                         | 33.33% |
| Total                  | 38                    | 3.61% | 9                           | 0.85% | 365                       | 34.63% |
| 400012.08-CP 400012.08 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 4                     | 3.81% | 2                           | 1.90% | 32                        | 30.48% |
| Precinct VBM           | 10                    | 2.55% | 3                           | 0.77% | 140                       | 35.71% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 5                         | 33.33% |
| Total                  | 14                    | 2.73% | 5                           | 0.98% | 177                       | 34.57% |
| 400013.08-CP 400013.08 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 2                     | 1.87% | 3                           | 2.80% | 16                        | 14.95% |
| Precinct VBM           | 7                     | 2.40% | 8                           | 2.74% | 97                        | 33.22% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 2                         | 66.67% |
| Total                  | 9                     | 2.24% | 11                          | 2.74% | 115                       | 28.61% |
| 400014.08-CP 400014.08 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 9                     | 4.59% | 2                           | 1.02% | 64                        | 32.65% |
| Precinct VBM           | 18                    | 2.71% | 3                           | 0.45% | 217                       | 32.68% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 2                     | 6.45% | 0                           | 0.00% | 11                        | 35.48% |

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|                        | United States Senator |       |                             |       |                           |        |
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|                        | RON UNZ (REP)         |       | JARRELL<br>WILLIAMSON (REP) |       | KAMALA D. HARRIS<br>(DEM) |        |
| Total                  | 29                    | 3.25% | 5                           | 0.56% | 292                       | 32.77% |
| 400017.08-CP 400017.08 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 3                     | 1.96% | 0                           | 0.00% | 32                        | 20.92% |
| Precinct VBM           | 26                    | 3.89% | 3                           | 0.45% | 176                       | 26.35% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 3                         | 13.04% |
| Total                  | 29                    | 3.44% | 3                           | 0.36% | 211                       | 25.00% |
| 400018.08-CP 400018.08 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 2                     | 2.60% | 0                           | 0.00% | 20                        | 25.97% |
| Precinct VBM           | 13                    | 5.42% | 3                           | 1.25% | 67                        | 27.92% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 2                         | 50.00% |
| Total                  | 15                    | 4.67% | 3                           | 0.93% | 89                        | 27.73% |
| 400020.08-CP 400020.08 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 2                     | 1.90% | 1                           | 0.95% | 16                        | 15.24% |
| Precinct VBM           | 7                     | 2.27% | 6                           | 1.94% | 88                        | 28.48% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 1                         | 50.00% |
| Total                  | 9                     | 2.16% | 7                           | 1.68% | 105                       | 25.24% |
| 400021.07-CP 400021.07 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 4                     | 3.39% | 0                           | 0.00% | 39                        | 33.05% |
| Precinct VBM           | 18                    | 2.62% | 6                           | 0.87% | 179                       | 26.09% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 1                           | 7.14% | 2                         | 14.29% |
| Total                  | 22                    | 2.69% | 7                           | 0.86% | 220                       | 26.89% |
| 500001.10-CP 500001.10 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 3                     | 1.62% | 2                           | 1.08% | 73                        | 39.46% |
| Precinct VBM           | 3                     | 0.56% | 3                           | 0.56% | 219                       | 40.78% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 3                         | 21.43% |
| Total                  | 6                     | 0.82% | 5                           | 0.68% | 295                       | 40.08% |
| 500002.10-CP 500002.10 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 1                     | 0.83% | 1                           | 0.83% | 34                        | 28.33% |
| Precinct VBM           | 5                     | 1.35% | 6                           | 1.62% | 133                       | 35.85% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 3                         | 12.00% |
| Total                  | 6                     | 1.16% | 7                           | 1.36% | 170                       | 32.95% |
| 500003.10-CP 500003.10 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 0                     | 0.00% | 2                           | 1.68% | 49                        | 41.18% |
| Precinct VBM           | 1                     | 0.33% | 5                           | 1.66% | 116                       | 38.41% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 1                         | 14.29% |
| Total                  | 1                     | 0.23% | 7                           | 1.64% | 166                       | 38.79% |
| 500004.10-CP 500004.10 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 0                     | 0.00% | 0                           | 0.00% | 19                        | 17.76% |
| Precinct VBM           | 3                     | 1.30% | 2                           | 0.87% | 77                        | 33.48% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 1                         | 5.56%  |
| Total                  | 3                     | 0.85% | 2                           | 0.56% | 97                        | 27.32% |
| 500005.10-CP 500005.10 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 0                     | 0.00% | 0                           | 0.00% | 27                        | 27.84% |
| Precinct VBM           | 1                     | 0.46% | 2                           | 0.91% | 74                        | 33.79% |

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|                        | United States Senator |       |                             |       |                           |        |
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|                        | RON UNZ (REP)         |       | JARRELL<br>WILLIAMSON (REP) |       | KAMALA D. HARRIS<br>(DEM) |        |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 0                         | 0.00%  |
| Total                  | 1                     | 0.30% | 2                           | 0.61% | 101                       | 30.79% |
| 500006.10-CP 500006.10 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 1                     | 0.61% | 0                           | 0.00% | 37                        | 22.56% |
| Precinct VBM           | 1                     | 0.23% | 6                           | 1.37% | 163                       | 37.13% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 9                         | 18.37% |
| Total                  | 2                     | 0.31% | 6                           | 0.92% | 209                       | 32.06% |
| 500007.10-CP 500007.10 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 1                     | 0.53% | 0                           | 0.00% | 54                        | 28.72% |
| Precinct VBM           | 8                     | 1.33% | 1                           | 0.17% | 209                       | 34.72% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 4                         | 22.22% |
| Total                  | 9                     | 1.11% | 1                           | 0.12% | 267                       | 33.04% |
| 500009.10-CP 500009.10 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 0                     | 0.00% | 1                           | 0.93% | 32                        | 29.63% |
| Precinct VBM           | 3                     | 1.12% | 1                           | 0.37% | 100                       | 37.45% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 8                         | 25.81% |
| Total                  | 3                     | 0.74% | 2                           | 0.49% | 140                       | 34.48% |
| 500011.10-CP 500011.10 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 2                     | 1.23% | 2                           | 1.23% | 25                        | 15.34% |
| Precinct VBM           | 1                     | 0.30% | 2                           | 0.59% | 105                       | 31.07% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 6                         | 18.18% |
| Total                  | 3                     | 0.56% | 4                           | 0.75% | 136                       | 25.47% |
| 500012.10-CP 500012.10 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | 0.00% | 0                           | 0.00% | 0                         | 0.00%  |
| Precinct OS            | 0                     | 0.00% | 1                           | 0.59% | 71                        | 41.76% |
| Precinct VBM           | 4                     | 0.74% | 6                           | 1.11% | 215                       | 39.67% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 1                     | 3.33% | 0                           | 0.00% | 10                        | 33.33% |
| Total                  | 5                     | 0.67% | 7                           | 0.94% | 296                       | 39.84% |
| 500015.10-CP 500015.10 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 0                     | 0.00% | 0                           | 0.00% | 41                        | 42.71% |
| Precinct VBM           | 5                     | 1.36% | 1                           | 0.27% | 177                       | 48.23% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 3                         | 30.00% |
| Total                  | 5                     | 1.06% | 1                           | 0.21% | 221                       | 46.72% |
| 500020.10-CP 500020.10 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 2                     | 1.42% | 1                           | 0.71% | 57                        | 40.43% |
| Precinct VBM           | 21                    | 3.46% | 6                           | 0.99% | 209                       | 34.43% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 7                         | 26.92% |
| Total                  | 23                    | 2.97% | 7                           | 0.90% | 273                       | 35.27% |
| 500021.10-CP 500021.10 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |
| Precinct OS            | 6                     | 4.03% | 1                           | 0.67% | 40                        | 26.85% |
| Precinct VBM           | 19                    | 3.32% | 1                           | 0.17% | 181                       | 31.64% |
| MB Precinct            | 0                     | -     | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | 0.00% | 0                           | 0.00% | 7                         | 28.00% |
| Total                  | 25                    | 3.35% | 2                           | 0.27% | 228                       | 30.56% |
| 95U201.01-MB 95U201.01 |                       |       |                             |       |                           |        |
| Precinct TSx           | 0                     | -     | 0                           | -     | 0                         | -      |

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|                        | United States Senator |        |                          |       |                        |         |
|------------------------|-----------------------|--------|--------------------------|-------|------------------------|---------|
|                        | RON UNZ (REP)         |        | JARRELL WILLIAMSON (REP) |       | KAMALA D. HARRIS (DEM) |         |
| Precinct OS            | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct VBM           | 0                     | -      | 0                        | -     | 0                      | -       |
| MB Precinct            | 2                     | 28.57% | 0                        | 0.00% | 1                      | 14.29%  |
| Provisional            | 0                     | -      | 0                        | -     | 0                      | -       |
| Total                  | 2                     | 28.57% | 0                        | 0.00% | 1                      | 14.29%  |
| 95U201.02-MB 95U201.02 |                       |        |                          |       |                        |         |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct OS            | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct VBM           | 0                     | -      | 0                        | -     | 0                      | -       |
| MB Precinct            | 8                     | 2.63%  | 4                        | 1.32% | 81                     | 26.64%  |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 0                      | 0.00%   |
| Total                  | 8                     | 2.62%  | 4                        | 1.31% | 81                     | 26.56%  |
| 95U202.02-MB 95U202.02 |                       |        |                          |       |                        |         |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct OS            | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct VBM           | 0                     | -      | 0                        | -     | 0                      | -       |
| MB Precinct            | 16                    | 3.17%  | 5                        | 0.99% | 167                    | 33.07%  |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 0                      | 0.00%   |
| Total                  | 16                    | 3.15%  | 5                        | 0.98% | 167                    | 32.87%  |
| 95U203.02-MB 95U203.02 |                       |        |                          |       |                        |         |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct OS            | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct VBM           | 0                     | -      | 0                        | -     | 0                      | -       |
| MB Precinct            | 0                     | -      | 0                        | -     | 0                      | -       |
| Provisional            | 0                     | -      | 0                        | -     | 0                      | -       |
| Total                  | 0                     | -      | 0                        | -     | 0                      | -       |
| 95U204.01-MB 95U204.01 |                       |        |                          |       |                        |         |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct OS            | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct VBM           | 0                     | -      | 0                        | -     | 0                      | -       |
| MB Precinct            | 25                    | 4.19%  | 4                        | 0.67% | 170                    | 28.48%  |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 0                      | 0.00%   |
| Total                  | 25                    | 4.17%  | 4                        | 0.67% | 170                    | 28.38%  |
| 95U301.05-MB 95U301.05 |                       |        |                          |       |                        |         |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct OS            | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct VBM           | 0                     | -      | 0                        | -     | 0                      | -       |
| MB Precinct            | 5                     | 3.68%  | 0                        | 0.00% | 33                     | 24.26%  |
| Provisional            | 0                     | 0.00%  | 0                        | 0.00% | 0                      | 0.00%   |
| Total                  | 5                     | 3.62%  | 0                        | 0.00% | 33                     | 23.91%  |
| 95U302.04-MB 95U302.04 |                       |        |                          |       |                        |         |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct OS            | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct VBM           | 0                     | -      | 0                        | -     | 0                      | -       |
| MB Precinct            | 0                     | 0.00%  | 0                        | 0.00% | 1                      | 100.00% |
| Provisional            | 0                     | -      | 0                        | -     | 0                      | -       |
| Total                  | 0                     | 0.00%  | 0                        | 0.00% | 1                      | 100.00% |
| 95U302.05-MB 95U302.05 |                       |        |                          |       |                        |         |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct OS            | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct VBM           | 0                     | -      | 0                        | -     | 0                      | -       |
| MB Precinct            | 0                     | 0.00%  | 0                        | 0.00% | 27                     | 33.33%  |
| Provisional            | 0                     | -      | 0                        | -     | 0                      | -       |
| Total                  | 0                     | 0.00%  | 0                        | 0.00% | 27                     | 33.33%  |
| 95U303.03-MB 95U303.03 |                       |        |                          |       |                        |         |
| Precinct TSx           | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct OS            | 0                     | -      | 0                        | -     | 0                      | -       |
| Precinct VBM           | 0                     | -      | 0                        | -     | 0                      | -       |
| MB Precinct            | 1                     | 1.28%  | 1                        | 1.28% | 16                     | 20.51%  |
| Provisional            | 0                     | -      | 0                        | -     | 0                      | -       |
| Total                  | 1                     | 1.28%  | 1                        | 1.28% | 16                     | 20.51%  |

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|                        | United States Senator |         |                             |       |                           |        |
|------------------------|-----------------------|---------|-----------------------------|-------|---------------------------|--------|
|                        | RON UNZ (REP)         |         | JARRELL<br>WILLIAMSON (REP) |       | KAMALA D. HARRIS<br>(DEM) |        |
| 95U303.05-MB 95U303.05 |                       |         |                             |       |                           |        |
| Precinct TSx           | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct OS            | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct VBM           | 0                     | -       | 0                           | -     | 0                         | -      |
| MB Precinct            | 3                     | 3.16%   | 1                           | 1.05% | 27                        | 28.42% |
| Provisional            | 1                     | 100.00% | 0                           | 0.00% | 0                         | 0.00%  |
| Total                  | 4                     | 4.17%   | 1                           | 1.04% | 27                        | 28.13% |
| 95U304.05-MB 95U304.05 |                       |         |                             |       |                           |        |
| Precinct TSx           | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct OS            | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct VBM           | 0                     | -       | 0                           | -     | 0                         | -      |
| MB Precinct            | 0                     | -       | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | -       | 0                           | -     | 0                         | -      |
| Total                  | 0                     | -       | 0                           | -     | 0                         | -      |
| 95U305.05-MB 95U305.05 |                       |         |                             |       |                           |        |
| Precinct TSx           | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct OS            | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct VBM           | 0                     | -       | 0                           | -     | 0                         | -      |
| MB Precinct            | 0                     | 0.00%   | 0                           | 0.00% | 1                         | 14.29% |
| Provisional            | 0                     | -       | 0                           | -     | 0                         | -      |
| Total                  | 0                     | 0.00%   | 0                           | 0.00% | 1                         | 14.29% |
| 95U401.08-MB 95U401.08 |                       |         |                             |       |                           |        |
| Precinct TSx           | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct OS            | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct VBM           | 0                     | -       | 0                           | -     | 0                         | -      |
| MB Precinct            | 0                     | 0.00%   | 0                           | 0.00% | 4                         | 26.67% |
| Provisional            | 0                     | -       | 0                           | -     | 0                         | -      |
| Total                  | 0                     | 0.00%   | 0                           | 0.00% | 4                         | 26.67% |
| 95U402.07-MB 95U402.07 |                       |         |                             |       |                           |        |
| Precinct TSx           | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct OS            | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct VBM           | 0                     | -       | 0                           | -     | 0                         | -      |
| MB Precinct            | 11                    | 4.70%   | 2                           | 0.85% | 72                        | 30.77% |
| Provisional            | 0                     | -       | 0                           | -     | 0                         | -      |
| Total                  | 11                    | 4.70%   | 2                           | 0.85% | 72                        | 30.77% |
| 95U402.08-MB 95U402.08 |                       |         |                             |       |                           |        |
| Precinct TSx           | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct OS            | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct VBM           | 0                     | -       | 0                           | -     | 0                         | -      |
| MB Precinct            | 17                    | 3.37%   | 4                           | 0.79% | 131                       | 25.99% |
| Provisional            | 0                     | 0.00%   | 0                           | 0.00% | 0                         | 0.00%  |
| Total                  | 17                    | 3.35%   | 4                           | 0.79% | 131                       | 25.84% |
| 95U403.08-MB 95U403.08 |                       |         |                             |       |                           |        |
| Precinct TSx           | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct OS            | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct VBM           | 0                     | -       | 0                           | -     | 0                         | -      |
| MB Precinct            | 23                    | 2.43%   | 6                           | 0.63% | 336                       | 35.52% |
| Provisional            | 1                     | 10.00%  | 0                           | 0.00% | 1                         | 10.00% |
| Total                  | 24                    | 2.51%   | 6                           | 0.63% | 337                       | 35.25% |
| 95U404.06-MB 95U404.06 |                       |         |                             |       |                           |        |
| Precinct TSx           | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct OS            | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct VBM           | 0                     | -       | 0                           | -     | 0                         | -      |
| MB Precinct            | 0                     | -       | 0                           | -     | 0                         | -      |
| Provisional            | 0                     | -       | 0                           | -     | 0                         | -      |
| Total                  | 0                     | -       | 0                           | -     | 0                         | -      |
| 95U404.08-MB 95U404.08 |                       |         |                             |       |                           |        |
| Precinct TSx           | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct OS            | 0                     | -       | 0                           | -     | 0                         | -      |
| Precinct VBM           | 0                     | -       | 0                           | -     | 0                         | -      |
| MB Precinct            | 0                     | -       | 0                           | -     | 0                         | -      |

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|                               | RON UNZ (REP)         |        | JARRELL<br>WILLIAMSON (REP) |        | KAMALA D. HARRIS<br>(DEM) |
| Provisional                   | 0                     | -      | 0                           | -      | 0                         |
| Total                         | 0                     | -      | 0                           | -      | 0                         |
| 95U501.09-MB 95U501.09        |                       |        |                             |        |                           |
| Precinct TSx                  | 0                     | -      | 0                           | -      | 0                         |
| Precinct OS                   | 0                     | -      | 0                           | -      | 0                         |
| Precinct VBM                  | 0                     | -      | 0                           | -      | 0                         |
| MB Precinct                   | 0                     | 0.00%  | 0                           | 0.00%  | 2 100.00%                 |
| Provisional                   | 0                     | -      | 0                           | -      | 0                         |
| Total                         | 0                     | 0.00%  | 0                           | 0.00%  | 2 100.00%                 |
| 95U501.10-MB 95U501.10        |                       |        |                             |        |                           |
| Precinct TSx                  | 0                     | -      | 0                           | -      | 0                         |
| Precinct OS                   | 0                     | -      | 0                           | -      | 0                         |
| Precinct VBM                  | 0                     | -      | 0                           | -      | 0                         |
| MB Precinct                   | 9                     | 5.63%  | 0                           | 0.00%  | 60 37.50%                 |
| Provisional                   | 1                     | 50.00% | 0                           | 0.00%  | 0 0.00%                   |
| Total                         | 10                    | 6.17%  | 0                           | 0.00%  | 60 37.04%                 |
| 95U598.10-MB 95U598.10        |                       |        |                             |        |                           |
| Precinct TSx                  | 0                     | -      | 0                           | -      | 0                         |
| Precinct OS                   | 0                     | -      | 0                           | -      | 0                         |
| Precinct VBM                  | 0                     | -      | 0                           | -      | 0                         |
| MB Precinct                   | 2                     | 0.93%  | 0                           | 0.00%  | 93 43.46%                 |
| Provisional                   | 1                     | 50.00% | 0                           | 0.00%  | 0 0.00%                   |
| Total                         | 3                     | 1.39%  | 0                           | 0.00%  | 93 43.06%                 |
| 95U599.10-MB 95U599.10        |                       |        |                             |        |                           |
| Precinct TSx                  | 0                     | -      | 0                           | -      | 0                         |
| Precinct OS                   | 0                     | -      | 0                           | -      | 0                         |
| Precinct VBM                  | 0                     | -      | 0                           | -      | 0                         |
| MB Precinct                   | 0                     | 0.00%  | 0                           | 0.00%  | 16 47.06%                 |
| Provisional                   | 0                     | -      | 0                           | -      | 0                         |
| Total                         | 0                     | 0.00%  | 0                           | 0.00%  | 16 47.06%                 |
| 96U101.11-MB 96U101.11        |                       |        |                             |        |                           |
| Precinct TSx                  | 0                     | -      | 0                           | -      | 0                         |
| Precinct OS                   | 0                     | -      | 0                           | -      | 0                         |
| Precinct VBM                  | 0                     | -      | 0                           | -      | 0                         |
| MB Precinct                   | 12                    | 3.27%  | 1                           | 0.27%  | 106 28.88%                |
| Provisional                   | 1                     | 16.67% | 0                           | 0.00%  | 2 33.33%                  |
| Total                         | 13                    | 3.49%  | 1                           | 0.27%  | 108 28.95%                |
| 96U401.14-MB 96U401.14        |                       |        |                             |        |                           |
| Precinct TSx                  | 0                     | -      | 0                           | -      | 0                         |
| Precinct OS                   | 0                     | -      | 0                           | -      | 0                         |
| Precinct VBM                  | 0                     | -      | 0                           | -      | 0                         |
| MB Precinct                   | 7                     | 2.17%  | 3                           | 0.93%  | 92 28.57%                 |
| Provisional                   | 0                     | 0.00%  | 1                           | 16.67% | 2 33.33%                  |
| Total                         | 7                     | 2.13%  | 4                           | 1.22%  | 94 28.66%                 |
| Total                         |                       |        |                             |        |                           |
| Precinct TSx                  | 0                     | 0.00%  | 0                           | 0.00%  | 1 12.50%                  |
| Precinct OS                   | 293                   | 2.74%  | 81                          | 0.76%  | 3254 30.41%               |
| Precinct VBM                  | 1080                  | 2.64%  | 341                         | 0.83%  | 13593 33.26%              |
| MB Precinct                   | 141                   | 3.06%  | 31                          | 0.67%  | 1436 31.16%               |
| Provisional                   | 33                    | 2.51%  | 11                          | 0.84%  | 309 23.52%                |
| Total                         | 1547                  | 2.69%  | 464                         | 0.81%  | 18593 32.33%              |
| STATE BOARD OF EQUALIZATION   |                       |        |                             |        |                           |
| STATE BOARD OF EQUALIZATION 1 |                       |        |                             |        |                           |
| Precinct TSx                  | 0                     | 0.00%  | 0                           | 0.00%  | 1 12.50%                  |
| Precinct OS                   | 293                   | 2.74%  | 81                          | 0.76%  | 3254 30.41%               |
| Precinct VBM                  | 1080                  | 2.64%  | 341                         | 0.83%  | 13593 33.26%              |
| MB Precinct                   | 141                   | 3.06%  | 31                          | 0.67%  | 1436 31.16%               |
| Provisional                   | 33                    | 2.51%  | 11                          | 0.84%  | 309 23.52%                |
| Total                         | 1547                  | 2.69%  | 464                         | 0.81%  | 18593 32.33%              |
| Total                         |                       |        |                             |        |                           |

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|                                     | United States Senator |              |                          |              |                        |               |
|-------------------------------------|-----------------------|--------------|--------------------------|--------------|------------------------|---------------|
|                                     | RON UNZ (REP)         |              | JARRELL WILLIAMSON (REP) |              | KAMALA D. HARRIS (DEM) |               |
| Precinct TSx                        | 0                     | 0.00%        | 0                        | 0.00%        | 1                      | 12.50%        |
| Precinct OS                         | 293                   | 2.74%        | 81                       | 0.76%        | 3254                   | 30.41%        |
| Precinct VBM                        | 1080                  | 2.64%        | 341                      | 0.83%        | 13593                  | 33.26%        |
| MB Precinct                         | 141                   | 3.06%        | 31                       | 0.67%        | 1436                   | 31.16%        |
| Provisional                         | 33                    | 2.51%        | 11                       | 0.84%        | 309                    | 23.52%        |
| <b>Total</b>                        | <b>1547</b>           | <b>2.69%</b> | <b>464</b>               | <b>0.81%</b> | <b>18593</b>           | <b>32.33%</b> |
| <b>UNITED STATES SENATE</b>         |                       |              |                          |              |                        |               |
| <b>UNITED STATES SENATE</b>         |                       |              |                          |              |                        |               |
| Precinct TSx                        | 0                     | 0.00%        | 0                        | 0.00%        | 1                      | 12.50%        |
| Precinct OS                         | 293                   | 2.74%        | 81                       | 0.76%        | 3254                   | 30.41%        |
| Precinct VBM                        | 1080                  | 2.64%        | 341                      | 0.83%        | 13593                  | 33.26%        |
| MB Precinct                         | 141                   | 3.06%        | 31                       | 0.67%        | 1436                   | 31.16%        |
| Provisional                         | 33                    | 2.51%        | 11                       | 0.84%        | 309                    | 23.52%        |
| <b>Total</b>                        | <b>1547</b>           | <b>2.69%</b> | <b>464</b>               | <b>0.81%</b> | <b>18593</b>           | <b>32.33%</b> |
| <b>Total</b>                        |                       |              |                          |              |                        |               |
| Precinct TSx                        | 0                     | 0.00%        | 0                        | 0.00%        | 1                      | 12.50%        |
| Precinct OS                         | 293                   | 2.74%        | 81                       | 0.76%        | 3254                   | 30.41%        |
| Precinct VBM                        | 1080                  | 2.64%        | 341                      | 0.83%        | 13593                  | 33.26%        |
| MB Precinct                         | 141                   | 3.06%        | 31                       | 0.67%        | 1436                   | 31.16%        |
| Provisional                         | 33                    | 2.51%        | 11                       | 0.84%        | 309                    | 23.52%        |
| <b>Total</b>                        | <b>1547</b>           | <b>2.69%</b> | <b>464</b>               | <b>0.81%</b> | <b>18593</b>           | <b>32.33%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |                       |              |                          |              |                        |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                       |              |                          |              |                        |               |
| Precinct TSx                        | 0                     | 0.00%        | 0                        | 0.00%        | 1                      | 12.50%        |
| Precinct OS                         | 293                   | 2.74%        | 81                       | 0.76%        | 3254                   | 30.41%        |
| Precinct VBM                        | 1080                  | 2.64%        | 341                      | 0.83%        | 13593                  | 33.26%        |
| MB Precinct                         | 141                   | 3.06%        | 31                       | 0.67%        | 1436                   | 31.16%        |
| Provisional                         | 33                    | 2.51%        | 11                       | 0.84%        | 309                    | 23.52%        |
| <b>Total</b>                        | <b>1547</b>           | <b>2.69%</b> | <b>464</b>               | <b>0.81%</b> | <b>18593</b>           | <b>32.33%</b> |
| <b>Total</b>                        |                       |              |                          |              |                        |               |
| Precinct TSx                        | 0                     | 0.00%        | 0                        | 0.00%        | 1                      | 12.50%        |
| Precinct OS                         | 293                   | 2.74%        | 81                       | 0.76%        | 3254                   | 30.41%        |
| Precinct VBM                        | 1080                  | 2.64%        | 341                      | 0.83%        | 13593                  | 33.26%        |
| MB Precinct                         | 141                   | 3.06%        | 31                       | 0.67%        | 1436                   | 31.16%        |
| Provisional                         | 33                    | 2.51%        | 11                       | 0.84%        | 309                    | 23.52%        |
| <b>Total</b>                        | <b>1547</b>           | <b>2.69%</b> | <b>464</b>               | <b>0.81%</b> | <b>18593</b>           | <b>32.33%</b> |
| <b>STATE SENATE</b>                 |                       |              |                          |              |                        |               |
| <b>1ST STATE SENATE DISTRICT</b>    |                       |              |                          |              |                        |               |
| Precinct TSx                        | 0                     | 0.00%        | 0                        | 0.00%        | 1                      | 12.50%        |
| Precinct OS                         | 293                   | 2.74%        | 81                       | 0.76%        | 3254                   | 30.41%        |
| Precinct VBM                        | 1080                  | 2.64%        | 341                      | 0.83%        | 13593                  | 33.26%        |
| MB Precinct                         | 141                   | 3.06%        | 31                       | 0.67%        | 1436                   | 31.16%        |
| Provisional                         | 33                    | 2.51%        | 11                       | 0.84%        | 309                    | 23.52%        |
| <b>Total</b>                        | <b>1547</b>           | <b>2.69%</b> | <b>464</b>               | <b>0.81%</b> | <b>18593</b>           | <b>32.33%</b> |
| <b>Total</b>                        |                       |              |                          |              |                        |               |
| Precinct TSx                        | 0                     | 0.00%        | 0                        | 0.00%        | 1                      | 12.50%        |
| Precinct OS                         | 293                   | 2.74%        | 81                       | 0.76%        | 3254                   | 30.41%        |
| Precinct VBM                        | 1080                  | 2.64%        | 341                      | 0.83%        | 13593                  | 33.26%        |
| MB Precinct                         | 141                   | 3.06%        | 31                       | 0.67%        | 1436                   | 31.16%        |
| Provisional                         | 33                    | 2.51%        | 11                       | 0.84%        | 309                    | 23.52%        |
| <b>Total</b>                        | <b>1547</b>           | <b>2.69%</b> | <b>464</b>               | <b>0.81%</b> | <b>18593</b>           | <b>32.33%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                       |              |                          |              |                        |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |                       |              |                          |              |                        |               |
| Precinct TSx                        | 0                     | 0.00%        | 0                        | 0.00%        | 0                      | 0.00%         |
| Precinct OS                         | 179                   | 2.63%        | 53                       | 0.78%        | 1972                   | 28.94%        |
| Precinct VBM                        | 633                   | 2.49%        | 214                      | 0.84%        | 8420                   | 33.16%        |
| MB Precinct                         | 122                   | 3.11%        | 27                       | 0.69%        | 1238                   | 31.58%        |

**COUNTY OF EL DORADO**  
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|                                   | United States Senator |       |                          |       |                        |        |
|-----------------------------------|-----------------------|-------|--------------------------|-------|------------------------|--------|
|                                   | RON UNZ (REP)         |       | JARRELL WILLIAMSON (REP) |       | KAMALA D. HARRIS (DEM) |        |
| Provisional                       | 15                    | 1.74% | 4                        | 0.47% | 187                    | 21.74% |
| Total                             | 949                   | 2.57% | 298                      | 0.81% | 11817                  | 31.95% |
| <b>6TH STATE ASSEMBLY DIST</b>    |                       |       |                          |       |                        |        |
| Precinct TSx                      | 0                     | 0.00% | 0                        | 0.00% | 1                      | 25.00% |
| Precinct OS                       | 114                   | 2.93% | 28                       | 0.72% | 1282                   | 32.97% |
| Precinct VBM                      | 447                   | 2.89% | 127                      | 0.82% | 5173                   | 33.41% |
| MB Precinct                       | 19                    | 2.76% | 4                        | 0.58% | 198                    | 28.74% |
| Provisional                       | 18                    | 3.96% | 7                        | 1.54% | 122                    | 26.87% |
| Total                             | 598                   | 2.91% | 166                      | 0.81% | 6776                   | 33.03% |
| <b>Total</b>                      |                       |       |                          |       |                        |        |
| Precinct TSx                      | 0                     | 0.00% | 0                        | 0.00% | 1                      | 12.50% |
| Precinct OS                       | 293                   | 2.74% | 81                       | 0.76% | 3254                   | 30.41% |
| Precinct VBM                      | 1080                  | 2.64% | 341                      | 0.83% | 13593                  | 33.26% |
| MB Precinct                       | 141                   | 3.06% | 31                       | 0.67% | 1436                   | 31.16% |
| Provisional                       | 33                    | 2.51% | 11                       | 0.84% | 309                    | 23.52% |
| Total                             | 1547                  | 2.69% | 464                      | 0.81% | 18593                  | 32.33% |
| <b>SUPERVISORIAL DISTRICTS</b>    |                       |       |                          |       |                        |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                       |       |                          |       |                        |        |
| Precinct TSx                      | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS                       | 63                    | 2.83% | 13                       | 0.58% | 792                    | 35.53% |
| Precinct VBM                      | 305                   | 3.28% | 78                       | 0.84% | 3155                   | 33.96% |
| MB Precinct                       | 12                    | 3.27% | 1                        | 0.27% | 106                    | 28.88% |
| Provisional                       | 10                    | 4.12% | 4                        | 1.65% | 70                     | 28.81% |
| Total                             | 390                   | 3.22% | 96                       | 0.79% | 4123                   | 33.99% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                       |       |                          |       |                        |        |
| Precinct TSx                      | 0                     | 0.00% | 0                        | 0.00% | 1                      | 25.00% |
| Precinct OS                       | 84                    | 3.73% | 21                       | 0.93% | 661                    | 29.35% |
| Precinct VBM                      | 228                   | 2.64% | 71                       | 0.82% | 2761                   | 31.95% |
| MB Precinct                       | 51                    | 3.61% | 13                       | 0.92% | 419                    | 29.65% |
| Provisional                       | 9                     | 3.25% | 2                        | 0.72% | 65                     | 23.47% |
| Total                             | 372                   | 2.96% | 107                      | 0.85% | 3907                   | 31.04% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                       |       |                          |       |                        |        |
| Precinct TSx                      | 0                     | 0.00% | 0                        | 0.00% | 0                      | 0.00%  |
| Precinct OS                       | 50                    | 2.36% | 14                       | 0.66% | 642                    | 30.30% |
| Precinct VBM                      | 213                   | 2.35% | 67                       | 0.74% | 3094                   | 34.17% |
| MB Precinct                       | 9                     | 2.26% | 2                        | 0.50% | 105                    | 26.38% |
| Provisional                       | 6                     | 2.53% | 2                        | 0.84% | 52                     | 21.94% |
| Total                             | 278                   | 2.35% | 85                       | 0.72% | 3893                   | 32.96% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                       |       |                          |       |                        |        |
| Precinct TSx                      | 0                     | -     | 0                        | -     | 0                      | -      |
| Precinct OS                       | 80                    | 3.49% | 22                       | 0.96% | 600                    | 26.16% |
| Precinct VBM                      | 259                   | 3.05% | 83                       | 0.98% | 2605                   | 30.67% |
| MB Precinct                       | 58                    | 2.87% | 15                       | 0.74% | 635                    | 31.42% |
| Provisional                       | 5                     | 1.96% | 3                        | 1.18% | 60                     | 23.53% |
| Total                             | 402                   | 3.08% | 123                      | 0.94% | 3900                   | 29.85% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                       |       |                          |       |                        |        |
| Precinct TSx                      | 0                     | 0.00% | 0                        | 0.00% | 0                      | 0.00%  |
| Precinct OS                       | 16                    | 0.89% | 11                       | 0.61% | 559                    | 30.94% |
| Precinct VBM                      | 75                    | 1.39% | 42                       | 0.78% | 1978                   | 36.68% |
| MB Precinct                       | 11                    | 2.68% | 0                        | 0.00% | 171                    | 41.71% |
| Provisional                       | 3                     | 0.99% | 0                        | 0.00% | 62                     | 20.53% |
| Total                             | 105                   | 1.33% | 53                       | 0.67% | 2770                   | 35.01% |
| <b>Total</b>                      |                       |       |                          |       |                        |        |
| Precinct TSx                      | 0                     | 0.00% | 0                        | 0.00% | 1                      | 12.50% |
| Precinct OS                       | 293                   | 2.74% | 81                       | 0.76% | 3254                   | 30.41% |
| Precinct VBM                      | 1080                  | 2.64% | 341                      | 0.83% | 13593                  | 33.26% |
| MB Precinct                       | 141                   | 3.06% | 31                       | 0.67% | 1436                   | 31.16% |
| Provisional                       | 33                    | 2.51% | 11                       | 0.84% | 309                    | 23.52% |
| Total                             | 1547                  | 2.69% | 464                      | 0.81% | 18593                  | 32.33% |



**COUNTY OF EL DORADO**  
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|                                 | United States Senator |              |                             |              |                           |               |
|---------------------------------|-----------------------|--------------|-----------------------------|--------------|---------------------------|---------------|
|                                 | RON UNZ (REP)         |              | JARRELL<br>WILLIAMSON (REP) |              | KAMALA D. HARRIS<br>(DEM) |               |
| <b>CITIES</b>                   |                       |              |                             |              |                           |               |
| <b>CITY OF PLACERVILLE</b>      |                       |              |                             |              |                           |               |
| Precinct TSx                    | 0                     | -            | 0                           | -            | 0                         | -             |
| Precinct OS                     | 8                     | 1.43%        | 1                           | 0.18%        | 195                       | 34.95%        |
| Precinct VBM                    | 41                    | 1.75%        | 26                          | 1.11%        | 881                       | 37.55%        |
| MB Precinct                     | 0                     | -            | 0                           | -            | 0                         | -             |
| Provisional                     | 2                     | 2.86%        | 1                           | 1.43%        | 15                        | 21.43%        |
| <b>Total</b>                    | <b>51</b>             | <b>1.71%</b> | <b>28</b>                   | <b>0.94%</b> | <b>1091</b>               | <b>36.68%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                       |              |                             |              |                           |               |
| Precinct TSx                    | 0                     | -            | 0                           | -            | 0                         | -             |
| Precinct OS                     | 5                     | 0.53%        | 4                           | 0.42%        | 228                       | 24.08%        |
| Precinct VBM                    | 22                    | 0.89%        | 20                          | 0.81%        | 861                       | 34.91%        |
| MB Precinct                     | 0                     | -            | 0                           | -            | 0                         | -             |
| Provisional                     | 0                     | 0.00%        | 0                           | 0.00%        | 31                        | 16.67%        |
| <b>Total</b>                    | <b>27</b>             | <b>0.75%</b> | <b>24</b>                   | <b>0.67%</b> | <b>1120</b>               | <b>31.12%</b> |
| <b>UNINCORPORATED AREA</b>      |                       |              |                             |              |                           |               |
| Precinct TSx                    | 0                     | 0.00%        | 0                           | 0.00%        | 1                         | 12.50%        |
| Precinct OS                     | 280                   | 3.04%        | 76                          | 0.83%        | 2831                      | 30.79%        |
| Precinct VBM                    | 1017                  | 2.82%        | 295                         | 0.82%        | 11851                     | 32.86%        |
| MB Precinct                     | 141                   | 3.06%        | 31                          | 0.67%        | 1436                      | 31.16%        |
| Provisional                     | 31                    | 2.93%        | 10                          | 0.95%        | 263                       | 24.86%        |
| <b>Total</b>                    | <b>1469</b>           | <b>2.88%</b> | <b>412</b>                  | <b>0.81%</b> | <b>16382</b>              | <b>32.16%</b> |
| <b>Total</b>                    |                       |              |                             |              |                           |               |
| Precinct TSx                    | 0                     | 0.00%        | 0                           | 0.00%        | 1                         | 12.50%        |
| Precinct OS                     | 293                   | 2.74%        | 81                          | 0.76%        | 3254                      | 30.41%        |
| Precinct VBM                    | 1080                  | 2.64%        | 341                         | 0.83%        | 13593                     | 33.26%        |
| MB Precinct                     | 141                   | 3.06%        | 31                          | 0.67%        | 1436                      | 31.16%        |
| Provisional                     | 33                    | 2.51%        | 11                          | 0.84%        | 309                       | 23.52%        |
| <b>Total</b>                    | <b>1547</b>           | <b>2.69%</b> | <b>464</b>                  | <b>0.81%</b> | <b>18593</b>              | <b>32.33%</b> |

**COUNTY OF EL DORADO**  
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|                          | United States Representative 4th District |          |               |             |                           |        |  |
|--------------------------|---|----------|---------------|-------------|---------------------------|--------|--|
|                          | Reg. Voters                               | Vote For | Times Counted | Total Votes | ROBERT W. DERLET<br>(DEM) |        |  |
| <b>Jurisdiction Wide</b> |   |          |               |             |                           |        |  |
| 100001.11-CP 100001.11   |   |          |               |             |                           |        |  |
| Precinct TSx             | 1615                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS              | 1615                                      | 1        | 190           | 173         | 54                        | 31.21% |  |
| Precinct VBM             | 1615                                      | 1        | 781           | 743         | 184                       | 24.76% |  |
| MB Precinct              | 1615                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional              | 1615                                      | 1        | 17            | 15          | 4                         | 26.67% |  |
| Total                    | 1615                                      | 5        | 988           | 931         | 242                       | 25.99% |  |
| 100002.11-CP 100002.11   |   |          |               |             |                           |        |  |
| Precinct TSx             | 1000                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS              | 1000                                      | 1        | 89            | 81          | 28                        | 34.57% |  |
| Precinct VBM             | 1000                                      | 1        | 476           | 441         | 121                       | 27.44% |  |
| MB Precinct              | 1000                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional              | 1000                                      | 1        | 20            | 19          | 4                         | 21.05% |  |
| Total                    | 1000                                      | 5        | 585           | 541         | 153                       | 28.28% |  |
| 100003.11-CP 100003.11   |   |          |               |             |                           |        |  |
| Precinct TSx             | 1269                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS              | 1269                                      | 1        | 123           | 114         | 24                        | 21.05% |  |
| Precinct VBM             | 1269                                      | 1        | 773           | 736         | 220                       | 29.89% |  |
| MB Precinct              | 1269                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional              | 1269                                      | 1        | 24            | 21          | 4                         | 19.05% |  |
| Total                    | 1269                                      | 5        | 920           | 871         | 248                       | 28.47% |  |
| 100004.11-CP 100004.11   |   |          |               |             |                           |        |  |
| Precinct TSx             | 1633                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS              | 1633                                      | 1        | 254           | 235         | 78                        | 33.19% |  |
| Precinct VBM             | 1633                                      | 1        | 738           | 693         | 216                       | 31.17% |  |
| MB Precinct              | 1633                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional              | 1633                                      | 1        | 4             | 2           | 1                         | 50.00% |  |
| Total                    | 1633                                      | 5        | 996           | 930         | 295                       | 31.72% |  |
| 100005.11-CP 100005.11   |   |          |               |             |                           |        |  |
| Precinct TSx             | 1586                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS              | 1586                                      | 1        | 146           | 133         | 30                        | 22.56% |  |
| Precinct VBM             | 1586                                      | 1        | 895           | 859         | 191                       | 22.24% |  |
| MB Precinct              | 1586                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional              | 1586                                      | 1        | 23            | 22          | 7                         | 31.82% |  |
| Total                    | 1586                                      | 5        | 1064          | 1014        | 228                       | 22.49% |  |
| 100006.11-CP 100006.11   |   |          |               |             |                           |        |  |
| Precinct TSx             | 1928                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS              | 1928                                      | 1        | 252           | 234         | 58                        | 24.79% |  |
| Precinct VBM             | 1928                                      | 1        | 944           | 902         | 293                       | 32.48% |  |
| MB Precinct              | 1928                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional              | 1928                                      | 1        | 31            | 24          | 3                         | 12.50% |  |
| Total                    | 1928                                      | 5        | 1227          | 1160        | 354                       | 30.52% |  |
| 100008.11-CP 100008.11   |   |          |               |             |                           |        |  |
| Precinct TSx             | 1671                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS              | 1671                                      | 1        | 197           | 177         | 41                        | 23.16% |  |
| Precinct VBM             | 1671                                      | 1        | 740           | 693         | 177                       | 25.54% |  |
| MB Precinct              | 1671                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional              | 1671                                      | 1        | 17            | 15          | 3                         | 20.00% |  |
| Total                    | 1671                                      | 5        | 954           | 885         | 221                       | 24.97% |  |
| 100009.11-CP 100009.11   |   |          |               |             |                           |        |  |
| Precinct TSx             | 1838                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS              | 1838                                      | 1        | 169           | 156         | 51                        | 32.69% |  |
| Precinct VBM             | 1838                                      | 1        | 751           | 706         | 202                       | 28.61% |  |
| MB Precinct              | 1838                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional              | 1838                                      | 1        | 26            | 21          | 2                         | 9.52%  |  |
| Total                    | 1838                                      | 5        | 946           | 883         | 255                       | 28.88% |  |
| 100012.11-CP 100012.11   |   |          |               |             |                           |        |  |
| Precinct TSx             | 2108                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS              | 2108                                      | 1        | 255           | 241         | 76                        | 31.54% |  |
| Precinct VBM             | 2108                                      | 1        | 968           | 898         | 250                       | 27.84% |  |

**COUNTY OF EL DORADO**  
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|                        |  | United States Representative 4th District |          |               |             |                           |        |  |
|------------------------|--|---|----------|---------------|-------------|---------------------------|--------|--|
|                        |  | Reg. Voters                               | Vote For | Times Counted | Total Votes | ROBERT W. DERLET<br>(DEM) |        |  |
| MB Precinct            |  | 2108                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 2108                                      | 1        | 29            | 28          | 2                         | 7.14%  |  |
| Total                  |  | 2108                                      | 5        | 1252          | 1167        | 328                       | 28.11% |  |
| 100013.11-CP 100013.11 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 2087                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 2087                                      | 1        | 250           | 234         | 65                        | 27.78% |  |
| Precinct VBM           |  | 2087                                      | 1        | 908           | 866         | 205                       | 23.67% |  |
| MB Precinct            |  | 2087                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 2087                                      | 1        | 28            | 25          | 7                         | 28.00% |  |
| Total                  |  | 2087                                      | 5        | 1186          | 1125        | 277                       | 24.62% |  |
| 100014.11-CP 100014.11 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 1659                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 1659                                      | 1        | 211           | 191         | 49                        | 25.65% |  |
| Precinct VBM           |  | 1659                                      | 1        | 706           | 667         | 165                       | 24.74% |  |
| MB Precinct            |  | 1659                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 1659                                      | 1        | 30            | 25          | 8                         | 32.00% |  |
| Total                  |  | 1659                                      | 5        | 947           | 883         | 222                       | 25.14% |  |
| 100022.11-CP 100022.11 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 1432                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 1432                                      | 1        | 177           | 163         | 47                        | 28.83% |  |
| Precinct VBM           |  | 1432                                      | 1        | 678           | 648         | 157                       | 24.23% |  |
| MB Precinct            |  | 1432                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 1432                                      | 1        | 13            | 11          | 2                         | 18.18% |  |
| Total                  |  | 1432                                      | 5        | 868           | 822         | 206                       | 25.06% |  |
| 100023.11-CP 100023.11 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 2135                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 2135                                      | 1        | 217           | 207         | 37                        | 17.87% |  |
| Precinct VBM           |  | 2135                                      | 1        | 1053          | 996         | 255                       | 25.60% |  |
| MB Precinct            |  | 2135                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 2135                                      | 1        | 33            | 28          | 7                         | 25.00% |  |
| Total                  |  | 2135                                      | 5        | 1303          | 1231        | 299                       | 24.29% |  |
| 200003.12-CP 200003.12 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 1362                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 1362                                      | 1        | 166           | 153         | 31                        | 20.26% |  |
| Precinct VBM           |  | 1362                                      | 1        | 558           | 528         | 151                       | 28.60% |  |
| MB Precinct            |  | 1362                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 1362                                      | 1        | 31            | 27          | 6                         | 22.22% |  |
| Total                  |  | 1362                                      | 5        | 755           | 708         | 188                       | 26.55% |  |
| 200004.12-CP 200004.12 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 1507                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 1507                                      | 1        | 182           | 173         | 42                        | 24.28% |  |
| Precinct VBM           |  | 1507                                      | 1        | 662           | 627         | 182                       | 29.03% |  |
| MB Precinct            |  | 1507                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 1507                                      | 1        | 14            | 13          | 2                         | 15.38% |  |
| Total                  |  | 1507                                      | 5        | 858           | 813         | 226                       | 27.80% |  |
| 200005.02-CP 200005.02 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 867                                       | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 867                                       | 1        | 94            | 91          | 20                        | 21.98% |  |
| Precinct VBM           |  | 867                                       | 1        | 452           | 432         | 116                       | 26.85% |  |
| MB Precinct            |  | 867                                       | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 867                                       | 1        | 21            | 18          | 7                         | 38.89% |  |
| Total                  |  | 867                                       | 5        | 567           | 541         | 143                       | 26.43% |  |
| 200006.12-CP 200006.12 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 1086                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 1086                                      | 1        | 117           | 114         | 21                        | 18.42% |  |
| Precinct VBM           |  | 1086                                      | 1        | 532           | 503         | 97                        | 19.28% |  |
| MB Precinct            |  | 1086                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 1086                                      | 1        | 29            | 27          | 8                         | 29.63% |  |
| Total                  |  | 1086                                      | 5        | 678           | 644         | 126                       | 19.57% |  |
| 200008.12-CP 200008.12 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 999                                       | 1        | 0             | 0           | 0                         | -      |  |

**COUNTY OF EL DORADO**  
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|                        | United States Representative 4th District |          |               |             |     | ROBERT W. DERLET<br>(DEM) |  |
|------------------------|---|----------|---------------|-------------|-----|---------------------------|--|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes |     |                           |  |
| Precinct OS            | 999                                       | 1        | 155           | 146         | 38  | 26.03%                    |  |
| Precinct VBM           | 999                                       | 1        | 379           | 353         | 95  | 26.91%                    |  |
| MB Precinct            | 999                                       | 1        | 0             | 0           | 0   | -                         |  |
| Provisional            | 999                                       | 1        | 10            | 10          | 2   | 20.00%                    |  |
| Total                  | 999                                       | 5        | 544           | 509         | 135 | 26.52%                    |  |
| 200010.12-CP 200010.12 |   |          |               |             |     |                           |  |
| Precinct TSx           | 1976                                      | 1        | 4             | 4           | 2   | 50.00%                    |  |
| Precinct OS            | 1976                                      | 1        | 197           | 179         | 58  | 32.40%                    |  |
| Precinct VBM           | 1976                                      | 1        | 859           | 789         | 212 | 26.87%                    |  |
| MB Precinct            | 1976                                      | 1        | 0             | 0           | 0   | -                         |  |
| Provisional            | 1976                                      | 1        | 34            | 28          | 6   | 21.43%                    |  |
| Total                  | 1976                                      | 5        | 1094          | 1000        | 278 | 27.80%                    |  |
| 200012.12-CP 200012.12 |   |          |               |             |     |                           |  |
| Precinct TSx           | 1064                                      | 1        | 0             | 0           | 0   | -                         |  |
| Precinct OS            | 1064                                      | 1        | 145           | 131         | 35  | 26.72%                    |  |
| Precinct VBM           | 1064                                      | 1        | 464           | 444         | 114 | 25.68%                    |  |
| MB Precinct            | 1064                                      | 1        | 0             | 0           | 0   | -                         |  |
| Provisional            | 1064                                      | 1        | 14            | 12          | 4   | 33.33%                    |  |
| Total                  | 1064                                      | 5        | 623           | 587         | 153 | 26.06%                    |  |
| 200013.12-CP 200013.12 |   |          |               |             |     |                           |  |
| Precinct TSx           | 1574                                      | 1        | 0             | 0           | 0   | -                         |  |
| Precinct OS            | 1574                                      | 1        | 211           | 197         | 50  | 25.38%                    |  |
| Precinct VBM           | 1574                                      | 1        | 701           | 664         | 159 | 23.95%                    |  |
| MB Precinct            | 1574                                      | 1        | 0             | 0           | 0   | -                         |  |
| Provisional            | 1574                                      | 1        | 23            | 19          | 3   | 15.79%                    |  |
| Total                  | 1574                                      | 5        | 935           | 880         | 212 | 24.09%                    |  |
| 200014.12-CP 200014.12 |   |          |               |             |     |                           |  |
| Precinct TSx           | 1438                                      | 1        | 0             | 0           | 0   | -                         |  |
| Precinct OS            | 1438                                      | 1        | 201           | 187         | 38  | 20.32%                    |  |
| Precinct VBM           | 1438                                      | 1        | 613           | 572         | 174 | 30.42%                    |  |
| MB Precinct            | 1438                                      | 1        | 0             | 0           | 0   | -                         |  |
| Provisional            | 1438                                      | 1        | 30            | 27          | 8   | 29.63%                    |  |
| Total                  | 1438                                      | 5        | 844           | 786         | 220 | 27.99%                    |  |
| 200015.02-CP 200015.02 |   |          |               |             |     |                           |  |
| Precinct TSx           | 1832                                      | 1        | 0             | 0           | 0   | -                         |  |
| Precinct OS            | 1832                                      | 1        | 218           | 209         | 56  | 26.79%                    |  |
| Precinct VBM           | 1832                                      | 1        | 886           | 836         | 245 | 29.31%                    |  |
| MB Precinct            | 1832                                      | 1        | 0             | 0           | 0   | -                         |  |
| Provisional            | 1832                                      | 1        | 22            | 21          | 3   | 14.29%                    |  |
| Total                  | 1832                                      | 5        | 1126          | 1066        | 304 | 28.52%                    |  |
| 200016.12-CP 200016.12 |   |          |               |             |     |                           |  |
| Precinct TSx           | 301                                       | 1        | 0             | 0           | 0   | -                         |  |
| Precinct OS            | 301                                       | 1        | 26            | 23          | 5   | 21.74%                    |  |
| Precinct VBM           | 301                                       | 1        | 139           | 133         | 16  | 12.03%                    |  |
| MB Precinct            | 301                                       | 1        | 0             | 0           | 0   | -                         |  |
| Provisional            | 301                                       | 1        | 6             | 5           | 0   | 0.00%                     |  |
| Total                  | 301                                       | 5        | 171           | 161         | 21  | 13.04%                    |  |
| 200016.13-CP 200016.13 |   |          |               |             |     |                           |  |
| Precinct TSx           | 565                                       | 1        | 0             | 0           | 0   | -                         |  |
| Precinct OS            | 565                                       | 1        | 80            | 76          | 15  | 19.74%                    |  |
| Precinct VBM           | 565                                       | 1        | 338           | 321         | 62  | 19.31%                    |  |
| MB Precinct            | 565                                       | 1        | 0             | 0           | 0   | -                         |  |
| Provisional            | 565                                       | 1        | 6             | 6           | 1   | 16.67%                    |  |
| Total                  | 565                                       | 5        | 424           | 403         | 78  | 19.35%                    |  |
| 200017.02-CP 200017.02 |   |          |               |             |     |                           |  |
| Precinct TSx           | 1286                                      | 1        | 0             | 0           | 0   | -                         |  |
| Precinct OS            | 1286                                      | 1        | 146           | 139         | 46  | 33.09%                    |  |
| Precinct VBM           | 1286                                      | 1        | 594           | 554         | 174 | 31.41%                    |  |
| MB Precinct            | 1286                                      | 1        | 0             | 0           | 0   | -                         |  |
| Provisional            | 1286                                      | 1        | 12            | 12          | 2   | 16.67%                    |  |
| Total                  | 1286                                      | 5        | 752           | 705         | 222 | 31.49%                    |  |

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|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | ROBERT W. DERLET<br>(DEM) |        |
| 200018.02-CP 200018.02 |   |          |               |             |                           |        |
| Precinct TSx           | 1454                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1454                                      | 1        | 169           | 160         | 31                        | 19.38% |
| Precinct VBM           | 1454                                      | 1        | 704           | 670         | 173                       | 25.82% |
| MB Precinct            | 1454                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1454                                      | 1        | 13            | 12          | 2                         | 16.67% |
| Total                  | 1454                                      | 5        | 886           | 842         | 206                       | 24.47% |
| 200020.02-CP 200020.02 |   |          |               |             |                           |        |
| Precinct TSx           | 1213                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1213                                      | 1        | 139           | 127         | 27                        | 21.26% |
| Precinct VBM           | 1213                                      | 1        | 526           | 501         | 130                       | 25.95% |
| MB Precinct            | 1213                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1213                                      | 1        | 19            | 17          | 3                         | 17.65% |
| Total                  | 1213                                      | 5        | 684           | 645         | 160                       | 24.81% |
| 200021.01-CP 200021.01 |   |          |               |             |                           |        |
| Precinct TSx           | 436                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 436                                       | 1        | 38            | 35          | 6                         | 17.14% |
| Precinct VBM           | 436                                       | 1        | 206           | 193         | 52                        | 26.94% |
| MB Precinct            | 436                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 436                                       | 1        | 7             | 6           | 1                         | 16.67% |
| Total                  | 436                                       | 5        | 251           | 234         | 59                        | 25.21% |
| 200021.02-CP 200021.02 |   |          |               |             |                           |        |
| Precinct TSx           | 1296                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1296                                      | 1        | 170           | 162         | 30                        | 18.52% |
| Precinct VBM           | 1296                                      | 1        | 752           | 703         | 205                       | 29.16% |
| MB Precinct            | 1296                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1296                                      | 1        | 20            | 17          | 5                         | 29.41% |
| Total                  | 1296                                      | 5        | 942           | 882         | 240                       | 27.21% |
| 200024.02-CP 200024.02 |   |          |               |             |                           |        |
| Precinct TSx           | 555                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 555                                       | 1        | 55            | 55          | 6                         | 10.91% |
| Precinct VBM           | 555                                       | 1        | 282           | 268         | 58                        | 21.64% |
| MB Precinct            | 555                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 555                                       | 1        | 9             | 7           | 0                         | 0.00%  |
| Total                  | 555                                       | 5        | 346           | 330         | 64                        | 19.39% |
| 300001.04-CP 300001.04 |   |          |               |             |                           |        |
| Precinct TSx           | 259                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 259                                       | 1        | 12            | 12          | 0                         | 0.00%  |
| Precinct VBM           | 259                                       | 1        | 100           | 96          | 33                        | 34.38% |
| MB Precinct            | 259                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 259                                       | 1        | 0             | 0           | 0                         | -      |
| Total                  | 259                                       | 5        | 112           | 108         | 33                        | 30.56% |
| 300001.05-CP 300001.05 |   |          |               |             |                           |        |
| Precinct TSx           | 731                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 731                                       | 1        | 76            | 72          | 19                        | 26.39% |
| Precinct VBM           | 731                                       | 1        | 330           | 308         | 103                       | 33.44% |
| MB Precinct            | 731                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 731                                       | 1        | 10            | 10          | 2                         | 20.00% |
| Total                  | 731                                       | 5        | 416           | 390         | 124                       | 31.79% |
| 300003.05-CP 300003.05 |   |          |               |             |                           |        |
| Precinct TSx           | 1932                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1932                                      | 1        | 205           | 184         | 71                        | 38.59% |
| Precinct VBM           | 1932                                      | 1        | 924           | 860         | 340                       | 39.53% |
| MB Precinct            | 1932                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1932                                      | 1        | 27            | 25          | 7                         | 28.00% |
| Total                  | 1932                                      | 5        | 1156          | 1069        | 418                       | 39.10% |
| 300004.05-CP 300004.05 |   |          |               |             |                           |        |
| Precinct TSx           | 1273                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1273                                      | 1        | 175           | 157         | 48                        | 30.57% |
| Precinct VBM           | 1273                                      | 1        | 576           | 523         | 188                       | 35.95% |
| MB Precinct            | 1273                                      | 1        | 0             | 0           | 0                         | -      |

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|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | ROBERT W. DERLET<br>(DEM) |        |
| Provisional            | 1273                                      | 1        | 17            | 14          | 4                         | 28.57% |
| Total                  | 1273                                      | 5        | 768           | 694         | 240                       | 34.58% |
| 300005.05-CP 300005.05 |   |          |               |             |                           |        |
| Precinct TSx           | 1374                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1374                                      | 1        | 163           | 148         | 47                        | 31.76% |
| Precinct VBM           | 1374                                      | 1        | 660           | 615         | 201                       | 32.68% |
| MB Precinct            | 1374                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1374                                      | 1        | 26            | 21          | 5                         | 23.81% |
| Total                  | 1374                                      | 5        | 849           | 784         | 253                       | 32.27% |
| 300008.05-CP 300008.05 |   |          |               |             |                           |        |
| Precinct TSx           | 1366                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1366                                      | 1        | 139           | 135         | 30                        | 22.22% |
| Precinct VBM           | 1366                                      | 1        | 740           | 709         | 191                       | 26.94% |
| MB Precinct            | 1366                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1366                                      | 1        | 14            | 13          | 1                         | 7.69%  |
| Total                  | 1366                                      | 5        | 893           | 857         | 222                       | 25.90% |
| 300009.04-CP 300009.04 |   |          |               |             |                           |        |
| Precinct TSx           | 873                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 873                                       | 1        | 121           | 115         | 27                        | 23.48% |
| Precinct VBM           | 873                                       | 1        | 395           | 377         | 83                        | 22.02% |
| MB Precinct            | 873                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 873                                       | 1        | 13            | 12          | 3                         | 25.00% |
| Total                  | 873                                       | 5        | 529           | 504         | 113                       | 22.42% |
| 300010.04-CP 300010.04 |   |          |               |             |                           |        |
| Precinct TSx           | 1340                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1340                                      | 1        | 171           | 161         | 30                        | 18.63% |
| Precinct VBM           | 1340                                      | 1        | 656           | 625         | 176                       | 28.16% |
| MB Precinct            | 1340                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1340                                      | 1        | 11            | 11          | 2                         | 18.18% |
| Total                  | 1340                                      | 5        | 838           | 797         | 208                       | 26.10% |
| 300011.04-CP 300011.04 |   |          |               |             |                           |        |
| Precinct TSx           | 1491                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1491                                      | 1        | 142           | 135         | 30                        | 22.22% |
| Precinct VBM           | 1491                                      | 1        | 711           | 672         | 166                       | 24.70% |
| MB Precinct            | 1491                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1491                                      | 1        | 22            | 21          | 3                         | 14.29% |
| Total                  | 1491                                      | 5        | 875           | 828         | 199                       | 24.03% |
| 300013.05-CP 300013.05 |   |          |               |             |                           |        |
| Precinct TSx           | 1490                                      | 1        | 3             | 3           | 0                         | 0.00%  |
| Precinct OS            | 1490                                      | 1        | 128           | 125         | 33                        | 26.40% |
| Precinct VBM           | 1490                                      | 1        | 721           | 692         | 189                       | 27.31% |
| MB Precinct            | 1490                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1490                                      | 1        | 15            | 12          | 3                         | 25.00% |
| Total                  | 1490                                      | 5        | 867           | 832         | 225                       | 27.04% |
| 300015.04-CP 300015.04 |   |          |               |             |                           |        |
| Precinct TSx           | 119                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 119                                       | 1        | 10            | 10          | 2                         | 20.00% |
| Precinct VBM           | 119                                       | 1        | 63            | 56          | 19                        | 33.93% |
| MB Precinct            | 119                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 119                                       | 1        | 4             | 4           | 1                         | 25.00% |
| Total                  | 119                                       | 5        | 77            | 70          | 22                        | 31.43% |
| 300015.05-CP 300015.05 |   |          |               |             |                           |        |
| Precinct TSx           | 503                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 503                                       | 1        | 66            | 62          | 20                        | 32.26% |
| Precinct VBM           | 503                                       | 1        | 266           | 247         | 96                        | 38.87% |
| MB Precinct            | 503                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 503                                       | 1        | 2             | 2           | 0                         | 0.00%  |
| Total                  | 503                                       | 5        | 334           | 311         | 116                       | 37.30% |
| 300017.05-CP 300017.05 |   |          |               |             |                           |        |
| Precinct TSx           | 1240                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1240                                      | 1        | 117           | 110         | 27                        | 24.55% |

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|------------------------|--|---|----------|---------------|-------------|---------------------------|--------|--|
|                        |  | Reg. Voters                               | Vote For | Times Counted | Total Votes | ROBERT W. DERLET<br>(DEM) |        |  |
| Precinct VBM           |  | 1240                                      | 1        | 655           | 617         | 174                       | 28.20% |  |
| MB Precinct            |  | 1240                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 1240                                      | 1        | 9             | 8           | 2                         | 25.00% |  |
| Total                  |  | 1240                                      | 5        | 781           | 735         | 203                       | 27.62% |  |
| 300018.04-CP 300018.04 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 1306                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 1306                                      | 1        | 148           | 137         | 21                        | 15.33% |  |
| Precinct VBM           |  | 1306                                      | 1        | 505           | 485         | 138                       | 28.45% |  |
| MB Precinct            |  | 1306                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 1306                                      | 1        | 31            | 24          | 5                         | 20.83% |  |
| Total                  |  | 1306                                      | 5        | 684           | 646         | 164                       | 25.39% |  |
| 300020.04-CP 300020.04 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 1401                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 1401                                      | 1        | 168           | 153         | 37                        | 24.18% |  |
| Precinct VBM           |  | 1401                                      | 1        | 632           | 593         | 157                       | 26.48% |  |
| MB Precinct            |  | 1401                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 1401                                      | 1        | 14            | 14          | 2                         | 14.29% |  |
| Total                  |  | 1401                                      | 5        | 814           | 760         | 196                       | 25.79% |  |
| 300021.03-CP 300021.03 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 1234                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 1234                                      | 1        | 153           | 143         | 44                        | 30.77% |  |
| Precinct VBM           |  | 1234                                      | 1        | 614           | 586         | 170                       | 29.01% |  |
| MB Precinct            |  | 1234                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 1234                                      | 1        | 13            | 11          | 5                         | 45.45% |  |
| Total                  |  | 1234                                      | 5        | 780           | 740         | 219                       | 29.59% |  |
| 300022.05-CP 300022.05 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 872                                       | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 872                                       | 1        | 101           | 94          | 18                        | 19.15% |  |
| Precinct VBM           |  | 872                                       | 1        | 356           | 340         | 112                       | 32.94% |  |
| MB Precinct            |  | 872                                       | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 872                                       | 1        | 12            | 12          | 3                         | 25.00% |  |
| Total                  |  | 872                                       | 5        | 469           | 446         | 133                       | 29.82% |  |
| 300023.03-CP 300023.03 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 1819                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 1819                                      | 1        | 228           | 219         | 73                        | 33.33% |  |
| Precinct VBM           |  | 1819                                      | 1        | 924           | 876         | 254                       | 29.00% |  |
| MB Precinct            |  | 1819                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 1819                                      | 1        | 26            | 23          | 3                         | 13.04% |  |
| Total                  |  | 1819                                      | 5        | 1178          | 1118        | 330                       | 29.52% |  |
| 300023.05-CP 300023.05 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 386                                       | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 386                                       | 1        | 45            | 44          | 9                         | 20.45% |  |
| Precinct VBM           |  | 386                                       | 1        | 193           | 184         | 54                        | 29.35% |  |
| MB Precinct            |  | 386                                       | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 386                                       | 1        | 8             | 8           | 2                         | 25.00% |  |
| Total                  |  | 386                                       | 5        | 246           | 236         | 65                        | 27.54% |  |
| 400001.14-CP 400001.14 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 1957                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 1957                                      | 1        | 178           | 165         | 34                        | 20.61% |  |
| Precinct VBM           |  | 1957                                      | 1        | 992           | 935         | 229                       | 24.49% |  |
| MB Precinct            |  | 1957                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 1957                                      | 1        | 23            | 22          | 6                         | 27.27% |  |
| Total                  |  | 1957                                      | 5        | 1193          | 1122        | 269                       | 23.98% |  |
| 400002.08-CP 400002.08 |  |   |          |               |             |                           |        |  |
| Precinct TSx           |  | 1508                                      | 1        | 0             | 0           | 0                         | -      |  |
| Precinct OS            |  | 1508                                      | 1        | 200           | 187         | 40                        | 21.39% |  |
| Precinct VBM           |  | 1508                                      | 1        | 730           | 699         | 149                       | 21.32% |  |
| MB Precinct            |  | 1508                                      | 1        | 0             | 0           | 0                         | -      |  |
| Provisional            |  | 1508                                      | 1        | 15            | 15          | 4                         | 26.67% |  |
| Total                  |  | 1508                                      | 5        | 945           | 901         | 193                       | 21.42% |  |
| 400003.08-CP 400003.08 |  |   |          |               |             |                           |        |  |

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|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | ROBERT W. DERLET<br>(DEM) |        |
| Precinct TSx           | 1800                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1800                                      | 1        | 245           | 229         | 51                        | 22.27% |
| Precinct VBM           | 1800                                      | 1        | 773           | 730         | 229                       | 31.37% |
| MB Precinct            | 1800                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1800                                      | 1        | 35            | 32          | 8                         | 25.00% |
| Total                  | 1800                                      | 5        | 1053          | 991         | 288                       | 29.06% |
| 400004.14-CP 400004.14 |   |          |               |             |                           |        |
| Precinct TSx           | 1536                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1536                                      | 1        | 194           | 179         | 40                        | 22.35% |
| Precinct VBM           | 1536                                      | 1        | 662           | 627         | 136                       | 21.69% |
| MB Precinct            | 1536                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1536                                      | 1        | 19            | 18          | 1                         | 5.56%  |
| Total                  | 1536                                      | 5        | 875           | 824         | 177                       | 21.48% |
| 400005.08-CP 400005.08 |   |          |               |             |                           |        |
| Precinct TSx           | 655                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 655                                       | 1        | 79            | 75          | 20                        | 26.67% |
| Precinct VBM           | 655                                       | 1        | 262           | 248         | 78                        | 31.45% |
| MB Precinct            | 655                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 655                                       | 1        | 11            | 11          | 1                         | 9.09%  |
| Total                  | 655                                       | 5        | 352           | 334         | 99                        | 29.64% |
| 400006.08-CP 400006.08 |   |          |               |             |                           |        |
| Precinct TSx           | 1537                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1537                                      | 1        | 228           | 213         | 56                        | 26.29% |
| Precinct VBM           | 1537                                      | 1        | 722           | 672         | 243                       | 36.16% |
| MB Precinct            | 1537                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1537                                      | 1        | 19            | 16          | 3                         | 18.75% |
| Total                  | 1537                                      | 5        | 969           | 901         | 302                       | 33.52% |
| 400007.08-CP 400007.08 |   |          |               |             |                           |        |
| Precinct TSx           | 1766                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1766                                      | 1        | 228           | 220         | 48                        | 21.82% |
| Precinct VBM           | 1766                                      | 1        | 765           | 737         | 157                       | 21.30% |
| MB Precinct            | 1766                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1766                                      | 1        | 22            | 22          | 3                         | 13.64% |
| Total                  | 1766                                      | 5        | 1015          | 979         | 208                       | 21.25% |
| 400010.08-CP 400010.08 |   |          |               |             |                           |        |
| Precinct TSx           | 1875                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1875                                      | 1        | 244           | 226         | 64                        | 28.32% |
| Precinct VBM           | 1875                                      | 1        | 869           | 843         | 272                       | 32.27% |
| MB Precinct            | 1875                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1875                                      | 1        | 31            | 26          | 6                         | 23.08% |
| Total                  | 1875                                      | 5        | 1144          | 1095        | 342                       | 31.23% |
| 400012.08-CP 400012.08 |   |          |               |             |                           |        |
| Precinct TSx           | 877                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 877                                       | 1        | 113           | 105         | 32                        | 30.48% |
| Precinct VBM           | 877                                       | 1        | 434           | 408         | 120                       | 29.41% |
| MB Precinct            | 877                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 877                                       | 1        | 22            | 17          | 5                         | 29.41% |
| Total                  | 877                                       | 5        | 569           | 530         | 157                       | 29.62% |
| 400013.08-CP 400013.08 |   |          |               |             |                           |        |
| Precinct TSx           | 758                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 758                                       | 1        | 127           | 116         | 23                        | 19.83% |
| Precinct VBM           | 758                                       | 1        | 315           | 294         | 93                        | 31.63% |
| MB Precinct            | 758                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 758                                       | 1        | 4             | 3           | 1                         | 33.33% |
| Total                  | 758                                       | 5        | 446           | 413         | 117                       | 28.33% |
| 400014.08-CP 400014.08 |   |          |               |             |                           |        |
| Precinct TSx           | 1526                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1526                                      | 1        | 230           | 215         | 49                        | 22.79% |
| Precinct VBM           | 1526                                      | 1        | 736           | 701         | 210                       | 29.96% |
| MB Precinct            | 1526                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1526                                      | 1        | 36            | 34          | 6                         | 17.65% |



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|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | ROBERT W. DERLET<br>(DEM) |        |
| Total                  | 1526                                      | 5        | 1002          | 950         | 265                       | 27.89% |
| 400017.08-CP 400017.08 |   |          |               |             |                           |        |
| Precinct TSx           | 1495                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1495                                      | 1        | 175           | 168         | 32                        | 19.05% |
| Precinct VBM           | 1495                                      | 1        | 752           | 719         | 173                       | 24.06% |
| MB Precinct            | 1495                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1495                                      | 1        | 28            | 26          | 6                         | 23.08% |
| Total                  | 1495                                      | 5        | 955           | 913         | 211                       | 23.11% |
| 400018.08-CP 400018.08 |   |          |               |             |                           |        |
| Precinct TSx           | 597                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 597                                       | 1        | 86            | 82          | 16                        | 19.51% |
| Precinct VBM           | 597                                       | 1        | 276           | 249         | 50                        | 20.08% |
| MB Precinct            | 597                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 597                                       | 1        | 7             | 5           | 0                         | 0.00%  |
| Total                  | 597                                       | 5        | 369           | 336         | 66                        | 19.64% |
| 400020.08-CP 400020.08 |   |          |               |             |                           |        |
| Precinct TSx           | 724                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 724                                       | 1        | 114           | 108         | 14                        | 12.96% |
| Precinct VBM           | 724                                       | 1        | 343           | 328         | 81                        | 24.70% |
| MB Precinct            | 724                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 724                                       | 1        | 2             | 2           | 1                         | 50.00% |
| Total                  | 724                                       | 5        | 459           | 438         | 96                        | 21.92% |
| 400021.07-CP 400021.07 |   |          |               |             |                           |        |
| Precinct TSx           | 1332                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1332                                      | 1        | 127           | 117         | 24                        | 20.51% |
| Precinct VBM           | 1332                                      | 1        | 767           | 721         | 209                       | 28.99% |
| MB Precinct            | 1332                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1332                                      | 1        | 18            | 18          | 6                         | 33.33% |
| Total                  | 1332                                      | 5        | 912           | 856         | 239                       | 27.92% |
| 500001.10-CP 500001.10 |   |          |               |             |                           |        |
| Precinct TSx           | 1717                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1717                                      | 1        | 210           | 188         | 80                        | 42.55% |
| Precinct VBM           | 1717                                      | 1        | 617           | 565         | 259                       | 45.84% |
| MB Precinct            | 1717                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1717                                      | 1        | 19            | 13          | 6                         | 46.15% |
| Total                  | 1717                                      | 5        | 846           | 766         | 345                       | 45.04% |
| 500002.10-CP 500002.10 |   |          |               |             |                           |        |
| Precinct TSx           | 1381                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1381                                      | 1        | 137           | 121         | 52                        | 42.98% |
| Precinct VBM           | 1381                                      | 1        | 412           | 387         | 174                       | 44.96% |
| MB Precinct            | 1381                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1381                                      | 1        | 31            | 25          | 11                        | 44.00% |
| Total                  | 1381                                      | 5        | 580           | 533         | 237                       | 44.47% |
| 500003.10-CP 500003.10 |   |          |               |             |                           |        |
| Precinct TSx           | 1065                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1065                                      | 1        | 131           | 117         | 47                        | 40.17% |
| Precinct VBM           | 1065                                      | 1        | 346           | 315         | 142                       | 45.08% |
| MB Precinct            | 1065                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1065                                      | 1        | 11            | 6           | 2                         | 33.33% |
| Total                  | 1065                                      | 5        | 488           | 438         | 191                       | 43.61% |
| 500004.10-CP 500004.10 |   |          |               |             |                           |        |
| Precinct TSx           | 981                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 981                                       | 1        | 122           | 108         | 40                        | 37.04% |
| Precinct VBM           | 981                                       | 1        | 264           | 231         | 89                        | 38.53% |
| MB Precinct            | 981                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 981                                       | 1        | 21            | 18          | 7                         | 38.89% |
| Total                  | 981                                       | 5        | 407           | 357         | 136                       | 38.10% |
| 500005.10-CP 500005.10 |   |          |               |             |                           |        |
| Precinct TSx           | 946                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 946                                       | 1        | 107           | 94          | 44                        | 46.81% |
| Precinct VBM           | 946                                       | 1        | 250           | 232         | 113                       | 48.71% |

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|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | ROBERT W. DERLET<br>(DEM) |        |
| MB Precinct            | 946                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 946                                       | 1        | 16            | 12          | 6                         | 50.00% |
| Total                  | 946                                       | 5        | 373           | 338         | 163                       | 48.22% |
| 500006.10-CP 500006.10 |   |          |               |             |                           |        |
| Precinct TSx           | 1661                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1661                                      | 1        | 190           | 162         | 63                        | 38.89% |
| Precinct VBM           | 1661                                      | 1        | 498           | 455         | 178                       | 39.12% |
| MB Precinct            | 1661                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1661                                      | 1        | 58            | 49          | 19                        | 38.78% |
| Total                  | 1661                                      | 5        | 746           | 666         | 260                       | 39.04% |
| 500007.10-CP 500007.10 |   |          |               |             |                           |        |
| Precinct TSx           | 1788                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1788                                      | 1        | 208           | 186         | 69                        | 37.10% |
| Precinct VBM           | 1788                                      | 1        | 688           | 632         | 249                       | 39.40% |
| MB Precinct            | 1788                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1788                                      | 1        | 20            | 19          | 10                        | 52.63% |
| Total                  | 1788                                      | 5        | 916           | 837         | 328                       | 39.19% |
| 500009.10-CP 500009.10 |   |          |               |             |                           |        |
| Precinct TSx           | 1073                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1073                                      | 1        | 121           | 109         | 44                        | 40.37% |
| Precinct VBM           | 1073                                      | 1        | 304           | 264         | 117                       | 44.32% |
| MB Precinct            | 1073                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1073                                      | 1        | 38            | 32          | 6                         | 18.75% |
| Total                  | 1073                                      | 5        | 463           | 405         | 167                       | 41.23% |
| 500011.10-CP 500011.10 |   |          |               |             |                           |        |
| Precinct TSx           | 1635                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1635                                      | 1        | 178           | 166         | 65                        | 39.16% |
| Precinct VBM           | 1635                                      | 1        | 380           | 353         | 161                       | 45.61% |
| MB Precinct            | 1635                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1635                                      | 1        | 40            | 34          | 13                        | 38.24% |
| Total                  | 1635                                      | 5        | 598           | 553         | 239                       | 43.22% |
| 500012.10-CP 500012.10 |   |          |               |             |                           |        |
| Precinct TSx           | 1574                                      | 1        | 1             | 1           | 0                         | 0.00%  |
| Precinct OS            | 1574                                      | 1        | 195           | 172         | 78                        | 45.35% |
| Precinct VBM           | 1574                                      | 1        | 601           | 567         | 248                       | 43.74% |
| MB Precinct            | 1574                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1574                                      | 1        | 37            | 29          | 11                        | 37.93% |
| Total                  | 1574                                      | 5        | 834           | 769         | 337                       | 43.82% |
| 500015.10-CP 500015.10 |   |          |               |             |                           |        |
| Precinct TSx           | 978                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 978                                       | 1        | 105           | 99          | 51                        | 51.52% |
| Precinct VBM           | 978                                       | 1        | 407           | 373         | 188                       | 50.40% |
| MB Precinct            | 978                                       | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 978                                       | 1        | 13            | 13          | 3                         | 23.08% |
| Total                  | 978                                       | 5        | 525           | 485         | 242                       | 49.90% |
| 500020.10-CP 500020.10 |   |          |               |             |                           |        |
| Precinct TSx           | 1530                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1530                                      | 1        | 161           | 152         | 47                        | 30.92% |
| Precinct VBM           | 1530                                      | 1        | 659           | 633         | 182                       | 28.75% |
| MB Precinct            | 1530                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1530                                      | 1        | 27            | 25          | 3                         | 12.00% |
| Total                  | 1530                                      | 5        | 847           | 810         | 232                       | 28.64% |
| 500021.10-CP 500021.10 |   |          |               |             |                           |        |
| Precinct TSx           | 1404                                      | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 1404                                      | 1        | 169           | 156         | 42                        | 26.92% |
| Precinct VBM           | 1404                                      | 1        | 632           | 602         | 170                       | 28.24% |
| MB Precinct            | 1404                                      | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 1404                                      | 1        | 28            | 26          | 9                         | 34.62% |
| Total                  | 1404                                      | 5        | 829           | 784         | 221                       | 28.19% |
| 95U201.01-MB 95U201.01 |   |          |               |             |                           |        |
| Precinct TSx           | 8   | 1        | 0             | 0           | 0                         | -      |

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|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | ROBERT W. DERLET<br>(DEM) |        |
| Precinct OS            | 8   | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM           | 8   | 1        | 0             | 0           | 0                         | -      |
| MB Precinct            | 8   | 1        | 7             | 7           | 1                         | 14.29% |
| Provisional            | 8   | 1        | 0             | 0           | 0                         | -      |
| Total                  | 8   | 5        | 7             | 7           | 1                         | 14.29% |
| 95U201.02-MB 95U201.02 |   |          |               |             |                           |        |
| Precinct TSx           | 541                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 541                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM           | 541                                       | 1        | 0             | 0           | 0                         | -      |
| MB Precinct            | 541                                       | 1        | 358           | 330         | 96                        | 29.09% |
| Provisional            | 541                                       | 1        | 1             | 1           | 0                         | 0.00%  |
| Total                  | 541                                       | 5        | 359           | 331         | 96                        | 29.00% |
| 95U202.02-MB 95U202.02 |   |          |               |             |                           |        |
| Precinct TSx           | 859                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 859                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM           | 859                                       | 1        | 0             | 0           | 0                         | -      |
| MB Precinct            | 859                                       | 1        | 574           | 554         | 149                       | 26.90% |
| Provisional            | 859                                       | 1        | 4             | 3           | 1                         | 33.33% |
| Total                  | 859                                       | 5        | 578           | 557         | 150                       | 26.93% |
| 95U203.02-MB 95U203.02 |   |          |               |             |                           |        |
| Precinct TSx           | 0   | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 0   | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM           | 0   | 1        | 0             | 0           | 0                         | -      |
| MB Precinct            | 0   | 1        | 0             | 0           | 0                         | -      |
| Provisional            | 0   | 1        | 0             | 0           | 0                         | -      |
| Total                  | 0   | 5        | 0             | 0           | 0                         | -      |
| 95U204.01-MB 95U204.01 |   |          |               |             |                           |        |
| Precinct TSx           | 895                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 895                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM           | 895                                       | 1        | 0             | 0           | 0                         | -      |
| MB Precinct            | 895                                       | 1        | 651           | 628         | 157                       | 25.00% |
| Provisional            | 895                                       | 1        | 2             | 2           | 0                         | 0.00%  |
| Total                  | 895                                       | 5        | 653           | 630         | 157                       | 24.92% |
| 95U301.05-MB 95U301.05 |   |          |               |             |                           |        |
| Precinct TSx           | 244                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 244                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM           | 244                                       | 1        | 0             | 0           | 0                         | -      |
| MB Precinct            | 244                                       | 1        | 151           | 144         | 43                        | 29.86% |
| Provisional            | 244                                       | 1        | 2             | 1           | 0                         | 0.00%  |
| Total                  | 244                                       | 5        | 153           | 145         | 43                        | 29.66% |
| 95U302.04-MB 95U302.04 |   |          |               |             |                           |        |
| Precinct TSx           | 0   | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 0   | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM           | 0   | 1        | 0             | 0           | 0                         | -      |
| MB Precinct            | 0   | 1        | 1             | 1           | 0                         | 0.00%  |
| Provisional            | 0   | 1        | 0             | 0           | 0                         | -      |
| Total                  | 0   | 5        | 1             | 1           | 0                         | 0.00%  |
| 95U302.05-MB 95U302.05 |   |          |               |             |                           |        |
| Precinct TSx           | 128                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 128                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM           | 128                                       | 1        | 0             | 0           | 0                         | -      |
| MB Precinct            | 128                                       | 1        | 86            | 84          | 19                        | 22.62% |
| Provisional            | 128                                       | 1        | 0             | 0           | 0                         | -      |
| Total                  | 128                                       | 5        | 86            | 84          | 19                        | 22.62% |
| 95U303.03-MB 95U303.03 |   |          |               |             |                           |        |
| Precinct TSx           | 144                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS            | 144                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM           | 144                                       | 1        | 0             | 0           | 0                         | -      |
| MB Precinct            | 144                                       | 1        | 84            | 80          | 24                        | 30.00% |
| Provisional            | 144                                       | 1        | 0             | 0           | 0                         | -      |
| Total                  | 144                                       | 5        | 84            | 80          | 24                        | 30.00% |

**COUNTY OF EL DORADO**  
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|                        | United States Representative 4th District |          |               |             |                           |         |
|------------------------|---|----------|---------------|-------------|---------------------------|---------|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | ROBERT W. DERLET<br>(DEM) |         |
| 95U303.05-MB 95U303.05 |   |          |               |             |                           |         |
| Precinct TSx           | 159                                       | 1        | 0             | 0           | 0                         | -       |
| Precinct OS            | 159                                       | 1        | 0             | 0           | 0                         | -       |
| Precinct VBM           | 159                                       | 1        | 0             | 0           | 0                         | -       |
| MB Precinct            | 159                                       | 1        | 111           | 102         | 24                        | 23.53%  |
| Provisional            | 159                                       | 1        | 1             | 1           | 0                         | 0.00%   |
| Total                  | 159                                       | 5        | 112           | 103         | 24                        | 23.30%  |
| 95U304.05-MB 95U304.05 |   |          |               |             |                           |         |
| Precinct TSx           | 0   | 1        | 0             | 0           | 0                         | -       |
| Precinct OS            | 0   | 1        | 0             | 0           | 0                         | -       |
| Precinct VBM           | 0   | 1        | 0             | 0           | 0                         | -       |
| MB Precinct            | 0   | 1        | 0             | 0           | 0                         | -       |
| Provisional            | 0   | 1        | 0             | 0           | 0                         | -       |
| Total                  | 0   | 5        | 0             | 0           | 0                         | -       |
| 95U305.05-MB 95U305.05 |   |          |               |             |                           |         |
| Precinct TSx           | 7   | 1        | 0             | 0           | 0                         | -       |
| Precinct OS            | 7   | 1        | 0             | 0           | 0                         | -       |
| Precinct VBM           | 7   | 1        | 0             | 0           | 0                         | -       |
| MB Precinct            | 7   | 1        | 7             | 7           | 1                         | 14.29%  |
| Provisional            | 7   | 1        | 0             | 0           | 0                         | -       |
| Total                  | 7   | 5        | 7             | 7           | 1                         | 14.29%  |
| 95U401.08-MB 95U401.08 |   |          |               |             |                           |         |
| Precinct TSx           | 39  | 1        | 0             | 0           | 0                         | -       |
| Precinct OS            | 39  | 1        | 0             | 0           | 0                         | -       |
| Precinct VBM           | 39  | 1        | 0             | 0           | 0                         | -       |
| MB Precinct            | 39  | 1        | 16            | 14          | 4                         | 28.57%  |
| Provisional            | 39  | 1        | 0             | 0           | 0                         | -       |
| Total                  | 39  | 5        | 16            | 14          | 4                         | 28.57%  |
| 95U402.07-MB 95U402.07 |   |          |               |             |                           |         |
| Precinct TSx           | 413                                       | 1        | 0             | 0           | 0                         | -       |
| Precinct OS            | 413                                       | 1        | 0             | 0           | 0                         | -       |
| Precinct VBM           | 413                                       | 1        | 0             | 0           | 0                         | -       |
| MB Precinct            | 413                                       | 1        | 256           | 243         | 68                        | 27.98%  |
| Provisional            | 413                                       | 1        | 1             | 1           | 1                         | 100.00% |
| Total                  | 413                                       | 5        | 257           | 244         | 69                        | 28.28%  |
| 95U402.08-MB 95U402.08 |   |          |               |             |                           |         |
| Precinct TSx           | 873                                       | 1        | 0             | 0           | 0                         | -       |
| Precinct OS            | 873                                       | 1        | 0             | 0           | 0                         | -       |
| Precinct VBM           | 873                                       | 1        | 0             | 0           | 0                         | -       |
| MB Precinct            | 873                                       | 1        | 566           | 528         | 107                       | 20.27%  |
| Provisional            | 873                                       | 1        | 3             | 3           | 1                         | 33.33%  |
| Total                  | 873                                       | 5        | 569           | 531         | 108                       | 20.34%  |
| 95U403.08-MB 95U403.08 |   |          |               |             |                           |         |
| Precinct TSx           | 1591                                      | 1        | 0             | 0           | 0                         | -       |
| Precinct OS            | 1591                                      | 1        | 0             | 0           | 0                         | -       |
| Precinct VBM           | 1591                                      | 1        | 0             | 0           | 0                         | -       |
| MB Precinct            | 1591                                      | 1        | 1042          | 992         | 322                       | 32.46%  |
| Provisional            | 1591                                      | 1        | 13            | 12          | 3                         | 25.00%  |
| Total                  | 1591                                      | 5        | 1055          | 1004        | 325                       | 32.37%  |
| 95U404.06-MB 95U404.06 |   |          |               |             |                           |         |
| Precinct TSx           | 0   | 1        | 0             | 0           | 0                         | -       |
| Precinct OS            | 0   | 1        | 0             | 0           | 0                         | -       |
| Precinct VBM           | 0   | 1        | 0             | 0           | 0                         | -       |
| MB Precinct            | 0   | 1        | 0             | 0           | 0                         | -       |
| Provisional            | 0   | 1        | 0             | 0           | 0                         | -       |
| Total                  | 0   | 5        | 0             | 0           | 0                         | -       |
| 95U404.08-MB 95U404.08 |   |          |               |             |                           |         |
| Precinct TSx           | 0   | 1        | 0             | 0           | 0                         | -       |
| Precinct OS            | 0   | 1        | 0             | 0           | 0                         | -       |
| Precinct VBM           | 0   | 1        | 0             | 0           | 0                         | -       |
| MB Precinct            | 0   | 1        | 0             | 0           | 0                         | -       |

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|                               | United States Representative 4th District |          |               |             |                           |        |
|-------------------------------|---|----------|---------------|-------------|---------------------------|--------|
|                               | Reg. Voters                               | Vote For | Times Counted | Total Votes | ROBERT W. DERLET<br>(DEM) |        |
| Provisional                   | 0   | 1        | 0             | 0           | 0                         | -      |
| Total                         | 0   | 5        | 0             | 0           | 0                         | -      |
| 95U501.09-MB 95U501.09        |   |          |               |             |                           |        |
| Precinct TSx                  | 0   | 1        | 0             | 0           | 0                         | -      |
| Precinct OS                   | 0   | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM                  | 0   | 1        | 0             | 0           | 0                         | -      |
| MB Precinct                   | 0   | 1        | 2             | 2           | 0                         | 0.00%  |
| Provisional                   | 0   | 1        | 0             | 0           | 0                         | -      |
| Total                         | 0   | 5        | 2             | 2           | 0                         | 0.00%  |
| 95U501.10-MB 95U501.10        |   |          |               |             |                           |        |
| Precinct TSx                  | 286                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS                   | 286                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM                  | 286                                       | 1        | 0             | 0           | 0                         | -      |
| MB Precinct                   | 286                                       | 1        | 172           | 165         | 50                        | 30.30% |
| Provisional                   | 286                                       | 1        | 2             | 2           | 1                         | 50.00% |
| Total                         | 286                                       | 5        | 174           | 167         | 51                        | 30.54% |
| 95U598.10-MB 95U598.10        |   |          |               |             |                           |        |
| Precinct TSx                  | 568                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS                   | 568                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM                  | 568                                       | 1        | 0             | 0           | 0                         | -      |
| MB Precinct                   | 568                                       | 1        | 239           | 212         | 95                        | 44.81% |
| Provisional                   | 568                                       | 1        | 3             | 1           | 0                         | 0.00%  |
| Total                         | 568                                       | 5        | 242           | 213         | 95                        | 44.60% |
| 95U599.10-MB 95U599.10        |   |          |               |             |                           |        |
| Precinct TSx                  | 75  | 1        | 0             | 0           | 0                         | -      |
| Precinct OS                   | 75  | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM                  | 75  | 1        | 0             | 0           | 0                         | -      |
| MB Precinct                   | 75  | 1        | 38            | 35          | 17                        | 48.57% |
| Provisional                   | 75  | 1        | 0             | 0           | 0                         | -      |
| Total                         | 75  | 5        | 38            | 35          | 17                        | 48.57% |
| 96U101.11-MB 96U101.11        |   |          |               |             |                           |        |
| Precinct TSx                  | 626                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS                   | 626                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM                  | 626                                       | 1        | 0             | 0           | 0                         | -      |
| MB Precinct                   | 626                                       | 1        | 423           | 402         | 90                        | 22.39% |
| Provisional                   | 626                                       | 1        | 6             | 6           | 0                         | 0.00%  |
| Total                         | 626                                       | 5        | 429           | 408         | 90                        | 22.06% |
| 96U401.14-MB 96U401.14        |   |          |               |             |                           |        |
| Precinct TSx                  | 566                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct OS                   | 566                                       | 1        | 0             | 0           | 0                         | -      |
| Precinct VBM                  | 566                                       | 1        | 0             | 0           | 0                         | -      |
| MB Precinct                   | 566                                       | 1        | 359           | 333         | 62                        | 18.62% |
| Provisional                   | 566                                       | 1        | 6             | 6           | 1                         | 16.67% |
| Total                         | 566                                       | 5        | 365           | 339         | 63                        | 18.58% |
| Total                         |   |          |               |             |                           |        |
| Precinct TSx                  | 109479                                    | 102      | 8             | 8           | 2                         | 25.00% |
| Precinct OS                   | 109479                                    | 102      | 12009         | 11147       | 3044                      | 27.31% |
| Precinct VBM                  | 109479                                    | 102      | 45535         | 42920       | 12594                     | 29.34% |
| MB Precinct                   | 109479                                    | 102      | 5143          | 4863        | 1329                      | 27.33% |
| Provisional                   | 109479                                    | 102      | 1584          | 1392        | 341                       | 24.50% |
| Total                         | 109479                                    | 510      | 64279         | 60330       | 17310                     | 28.69% |
| STATE BOARD OF EQUALIZATION   |   |          |               |             |                           |        |
| STATE BOARD OF EQUALIZATION 1 |   |          |               |             |                           |        |
| Precinct TSx                  | 109479                                    | 102      | 8             | 8           | 2                         | 25.00% |
| Precinct OS                   | 109479                                    | 102      | 12009         | 11147       | 3044                      | 27.31% |
| Precinct VBM                  | 109479                                    | 102      | 45535         | 42920       | 12594                     | 29.34% |
| MB Precinct                   | 109479                                    | 102      | 5143          | 4863        | 1329                      | 27.33% |
| Provisional                   | 109479                                    | 102      | 1584          | 1392        | 341                       | 24.50% |
| Total                         | 109479                                    | 510      | 64279         | 60330       | 17310                     | 28.69% |
| Total                         |   |          |               |             |                           |        |

**COUNTY OF EL DORADO**  
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| United States Representative 4th District |               |            |               |              |                           |               |
|---|---------------|------------|---------------|--------------|---------------------------|---------------|
|   | Reg. Voters   | Vote For   | Times Counted | Total Votes  | ROBERT W. DERLET<br>(DEM) |               |
| Precinct TSx                              | 109479        | 102        | 8             | 8            | 2                         | 25.00%        |
| Precinct OS                               | 109479        | 102        | 12009         | 11147        | 3044                      | 27.31%        |
| Precinct VBM                              | 109479        | 102        | 45535         | 42920        | 12594                     | 29.34%        |
| MB Precinct                               | 109479        | 102        | 5143          | 4863         | 1329                      | 27.33%        |
| Provisional                               | 109479        | 102        | 1584          | 1392         | 341                       | 24.50%        |
| <b>Total</b>                              | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>60330</b> | <b>17310</b>              | <b>28.69%</b> |
| <b>UNITED STATES SENATE</b>               |               |            |               |              |                           |               |
| <b>UNITED STATES SENATE</b>               |               |            |               |              |                           |               |
| Precinct TSx                              | 109479        | 102        | 8             | 8            | 2                         | 25.00%        |
| Precinct OS                               | 109479        | 102        | 12009         | 11147        | 3044                      | 27.31%        |
| Precinct VBM                              | 109479        | 102        | 45535         | 42920        | 12594                     | 29.34%        |
| MB Precinct                               | 109479        | 102        | 5143          | 4863         | 1329                      | 27.33%        |
| Provisional                               | 109479        | 102        | 1584          | 1392         | 341                       | 24.50%        |
| <b>Total</b>                              | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>60330</b> | <b>17310</b>              | <b>28.69%</b> |
| <b>Total</b>                              |               |            |               |              |                           |               |
| Precinct TSx                              | 109479        | 102        | 8             | 8            | 2                         | 25.00%        |
| Precinct OS                               | 109479        | 102        | 12009         | 11147        | 3044                      | 27.31%        |
| Precinct VBM                              | 109479        | 102        | 45535         | 42920        | 12594                     | 29.34%        |
| MB Precinct                               | 109479        | 102        | 5143          | 4863         | 1329                      | 27.33%        |
| Provisional                               | 109479        | 102        | 1584          | 1392         | 341                       | 24.50%        |
| <b>Total</b>                              | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>60330</b> | <b>17310</b>              | <b>28.69%</b> |
| <b>Total</b>                              |               |            |               |              |                           |               |
| <b>UNITED STATES REPRESENTATIVE</b>       |               |            |               |              |                           |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>         |               |            |               |              |                           |               |
| Precinct TSx                              | 109479        | 102        | 8             | 8            | 2                         | 25.00%        |
| Precinct OS                               | 109479        | 102        | 12009         | 11147        | 3044                      | 27.31%        |
| Precinct VBM                              | 109479        | 102        | 45535         | 42920        | 12594                     | 29.34%        |
| MB Precinct                               | 109479        | 102        | 5143          | 4863         | 1329                      | 27.33%        |
| Provisional                               | 109479        | 102        | 1584          | 1392         | 341                       | 24.50%        |
| <b>Total</b>                              | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>60330</b> | <b>17310</b>              | <b>28.69%</b> |
| <b>Total</b>                              |               |            |               |              |                           |               |
| Precinct TSx                              | 109479        | 102        | 8             | 8            | 2                         | 25.00%        |
| Precinct OS                               | 109479        | 102        | 12009         | 11147        | 3044                      | 27.31%        |
| Precinct VBM                              | 109479        | 102        | 45535         | 42920        | 12594                     | 29.34%        |
| MB Precinct                               | 109479        | 102        | 5143          | 4863         | 1329                      | 27.33%        |
| Provisional                               | 109479        | 102        | 1584          | 1392         | 341                       | 24.50%        |
| <b>Total</b>                              | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>60330</b> | <b>17310</b>              | <b>28.69%</b> |
| <b>Total</b>                              |               |            |               |              |                           |               |
| <b>STATE SENATE</b>                       |               |            |               |              |                           |               |
| <b>1ST STATE SENATE DISTRICT</b>          |               |            |               |              |                           |               |
| Precinct TSx                              | 109479        | 102        | 8             | 8            | 2                         | 25.00%        |
| Precinct OS                               | 109479        | 102        | 12009         | 11147        | 3044                      | 27.31%        |
| Precinct VBM                              | 109479        | 102        | 45535         | 42920        | 12594                     | 29.34%        |
| MB Precinct                               | 109479        | 102        | 5143          | 4863         | 1329                      | 27.33%        |
| Provisional                               | 109479        | 102        | 1584          | 1392         | 341                       | 24.50%        |
| <b>Total</b>                              | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>60330</b> | <b>17310</b>              | <b>28.69%</b> |
| <b>Total</b>                              |               |            |               |              |                           |               |
| Precinct TSx                              | 109479        | 102        | 8             | 8            | 2                         | 25.00%        |
| Precinct OS                               | 109479        | 102        | 12009         | 11147        | 3044                      | 27.31%        |
| Precinct VBM                              | 109479        | 102        | 45535         | 42920        | 12594                     | 29.34%        |
| MB Precinct                               | 109479        | 102        | 5143          | 4863         | 1329                      | 27.33%        |
| Provisional                               | 109479        | 102        | 1584          | 1392         | 341                       | 24.50%        |
| <b>Total</b>                              | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>60330</b> | <b>17310</b>              | <b>28.69%</b> |
| <b>Total</b>                              |               |            |               |              |                           |               |
| <b>STATE ASSEMBLY DISTRICTS</b>           |               |            |               |              |                           |               |
| <b>5TH STATE ASSEMBLY DIST</b>            |               |            |               |              |                           |               |
| Precinct TSx                              | 70961         | 75         | 4             | 4            | 0                         | 0.00%         |
| Precinct OS                               | 70961         | 75         | 7627          | 7085         | 1999                      | 28.21%        |
| Precinct VBM                              | 70961         | 75         | 28225         | 26576        | 8331                      | 31.35%        |
| MB Precinct                               | 70961         | 75         | 4361          | 4128         | 1177                      | 28.51%        |

**COUNTY OF EL DORADO**  
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|---|-------------|----------|---------------|-------------|---------------------------|--------|
|   | Reg. Voters | Vote For | Times Counted | Total Votes | ROBERT W. DERLET<br>(DEM) |        |
| Provisional                               | 70961       | 75       | 1038          | 910         | 239                       | 26.26% |
| Total                                     | 70961       | 375      | 41255         | 38703       | 11746                     | 30.35% |
| <b>6TH STATE ASSEMBLY DIST</b>            |             |          |               |             |                           |        |
| Precinct TSx                              | 38518       | 27       | 4             | 4           | 2                         | 50.00% |
| Precinct OS                               | 38518       | 27       | 4382          | 4062        | 1045                      | 25.73% |
| Precinct VBM                              | 38518       | 27       | 17310         | 16344       | 4263                      | 26.08% |
| MB Precinct                               | 38518       | 27       | 782           | 735         | 152                       | 20.68% |
| Provisional                               | 38518       | 27       | 546           | 482         | 102                       | 21.16% |
| Total                                     | 38518       | 135      | 23024         | 21627       | 5564                      | 25.73% |
| <b>Total</b>                              |             |          |               |             |                           |        |
| Precinct TSx                              | 109479      | 102      | 8             | 8           | 2                         | 25.00% |
| Precinct OS                               | 109479      | 102      | 12009         | 11147       | 3044                      | 27.31% |
| Precinct VBM                              | 109479      | 102      | 45535         | 42920       | 12594                     | 29.34% |
| MB Precinct                               | 109479      | 102      | 5143          | 4863        | 1329                      | 27.33% |
| Provisional                               | 109479      | 102      | 1584          | 1392        | 341                       | 24.50% |
| Total                                     | 109479      | 510      | 64279         | 60330       | 17310                     | 28.69% |
| <b>SUPERVISORIAL DISTRICTS</b>            |             |          |               |             |                           |        |
| <b>1ST SUPERVISORIAL DISTRICT</b>         |             |          |               |             |                           |        |
| Precinct TSx                              | 22587       | 14       | 0             | 0           | 0                         | -      |
| Precinct OS                               | 22587       | 14       | 2530          | 2339        | 638                       | 27.28% |
| Precinct VBM                              | 22587       | 14       | 10411         | 9848        | 2636                      | 26.77% |
| MB Precinct                               | 22587       | 14       | 423           | 402         | 90                        | 22.39% |
| Provisional                               | 22587       | 14       | 301           | 262         | 54                        | 20.61% |
| Total                                     | 22587       | 70       | 13665         | 12851       | 3418                      | 26.60% |
| <b>2ND SUPERVISORIAL DISTRICT</b>         |             |          |               |             |                           |        |
| Precinct TSx                              | 23114       | 23       | 4             | 4           | 2                         | 50.00% |
| Precinct OS                               | 23114       | 23       | 2509          | 2357        | 555                       | 23.55% |
| Precinct VBM                              | 23114       | 23       | 9647          | 9091        | 2415                      | 26.56% |
| MB Precinct                               | 23114       | 23       | 1590          | 1519        | 403                       | 26.53% |
| Provisional                               | 23114       | 23       | 327           | 290         | 64                        | 22.07% |
| Total                                     | 23114       | 115      | 14077         | 13261       | 3439                      | 25.93% |
| <b>3RD SUPERVISORIAL DISTRICT</b>         |             |          |               |             |                           |        |
| Precinct TSx                              | 21691       | 26       | 3             | 3           | 0                         | 0.00%  |
| Precinct OS                               | 21691       | 26       | 2368          | 2216        | 586                       | 26.44% |
| Precinct VBM                              | 21691       | 26       | 10021         | 9461        | 2844                      | 30.06% |
| MB Precinct                               | 21691       | 26       | 440           | 418         | 111                       | 26.56% |
| Provisional                               | 21691       | 26       | 277           | 247         | 53                        | 21.46% |
| Total                                     | 21691       | 130      | 13109         | 12345       | 3594                      | 29.11% |
| <b>4TH SUPERVISORIAL DISTRICT</b>         |             |          |               |             |                           |        |
| Precinct TSx                              | 23425       | 22       | 0             | 0           | 0                         | -      |
| Precinct OS                               | 23425       | 22       | 2568          | 2405        | 543                       | 22.58% |
| Precinct VBM                              | 23425       | 22       | 9398          | 8911        | 2429                      | 27.26% |
| MB Precinct                               | 23425       | 22       | 2239          | 2110        | 563                       | 26.68% |
| Provisional                               | 23425       | 22       | 315           | 289         | 63                        | 21.80% |
| Total                                     | 23425       | 110      | 14520         | 13715       | 3598                      | 26.23% |
| <b>5TH SUPERVISORIAL DISTRICT</b>         |             |          |               |             |                           |        |
| Precinct TSx                              | 18662       | 17       | 1             | 1           | 0                         | 0.00%  |
| Precinct OS                               | 18662       | 17       | 2034          | 1830        | 722                       | 39.45% |
| Precinct VBM                              | 18662       | 17       | 6058          | 5609        | 2270                      | 40.47% |
| MB Precinct                               | 18662       | 17       | 451           | 414         | 162                       | 39.13% |
| Provisional                               | 18662       | 17       | 364           | 304         | 107                       | 35.20% |
| Total                                     | 18662       | 85       | 8908          | 8158        | 3261                      | 39.97% |
| <b>Total</b>                              |             |          |               |             |                           |        |
| Precinct TSx                              | 109479      | 102      | 8             | 8           | 2                         | 25.00% |
| Precinct OS                               | 109479      | 102      | 12009         | 11147       | 3044                      | 27.31% |
| Precinct VBM                              | 109479      | 102      | 45535         | 42920       | 12594                     | 29.34% |
| MB Precinct                               | 109479      | 102      | 5143          | 4863        | 1329                      | 27.33% |
| Provisional                               | 109479      | 102      | 1584          | 1392        | 341                       | 24.50% |
| Total                                     | 109479      | 510      | 64279         | 60330       | 17310                     | 28.69% |

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|---------------------------------|--|---|------------|---------------|--------------|---------------------------|
|                                 |  | Reg. Voters                               | Vote For   | Times Counted | Total Votes  | ROBERT W. DERLET<br>(DEM) |
| <b>CITIES</b>                   |  |   |            |               |              |                           |
| <b>CITY OF PLACERVILLE</b>      |  |   |            |               |              |                           |
| Precinct TSx                    |  | 5569                                      | 5          | 0             | 0            | 0 -                       |
| Precinct OS                     |  | 5569                                      | 5          | 631           | 573          | 185 32.29%                |
| Precinct VBM                    |  | 5569                                      | 5          | 2590          | 2402         | 865 36.01%                |
| MB Precinct                     |  | 5569                                      | 5          | 0             | 0            | 0 -                       |
| Provisional                     |  | 5569                                      | 5          | 80            | 70           | 18 25.71%                 |
| <b>Total</b>                    |  | <b>5569</b>                               | <b>25</b>  | <b>3301</b>   | <b>3045</b>  | <b>1068 35.07%</b>        |
| <b>CITY OF SOUTH LAKE TAHOE</b> |  |   |            |               |              |                           |
| Precinct TSx                    |  | 9465                                      | 7          | 0             | 0            | 0 -                       |
| Precinct OS                     |  | 9465                                      | 7          | 1063          | 946          | 377 39.85%                |
| Precinct VBM                    |  | 9465                                      | 7          | 2796          | 2554         | 1081 42.33%               |
| MB Precinct                     |  | 9465                                      | 7          | 0             | 0            | 0 -                       |
| Provisional                     |  | 9465                                      | 7          | 224           | 189          | 72 38.10%                 |
| <b>Total</b>                    |  | <b>9465</b>                               | <b>35</b>  | <b>4083</b>   | <b>3689</b>  | <b>1530 41.47%</b>        |
| <b>UNINCORPORATED AREA</b>      |  |   |            |               |              |                           |
| Precinct TSx                    |  | 94445                                     | 90         | 8             | 8            | 2 25.00%                  |
| Precinct OS                     |  | 94445                                     | 90         | 10315         | 9628         | 2482 25.78%               |
| Precinct VBM                    |  | 94445                                     | 90         | 40149         | 37964        | 10648 28.05%              |
| MB Precinct                     |  | 94445                                     | 90         | 5143          | 4863         | 1329 27.33%               |
| Provisional                     |  | 94445                                     | 90         | 1280          | 1133         | 251 22.15%                |
| <b>Total</b>                    |  | <b>94445</b>                              | <b>450</b> | <b>56895</b>  | <b>53596</b> | <b>14712 27.45%</b>       |
| <b>Total</b>                    |  |   |            |               |              |                           |
| Precinct TSx                    |  | 109479                                    | 102        | 8             | 8            | 2 25.00%                  |
| Precinct OS                     |  | 109479                                    | 102        | 12009         | 11147        | 3044 27.31%               |
| Precinct VBM                    |  | 109479                                    | 102        | 45535         | 42920        | 12594 29.34%              |
| MB Precinct                     |  | 109479                                    | 102        | 5143          | 4863         | 1329 27.33%               |
| Provisional                     |  | 109479                                    | 102        | 1584          | 1392         | 341 24.50%                |
| <b>Total</b>                    |  | <b>109479</b>                             | <b>510</b> | <b>64279</b>  | <b>60330</b> | <b>17310 28.69%</b>       |



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|                        | SEAN WHITE (DEM)                          |        | TOM MCCLINTOCK (REP) |        |
| Jurisdiction Wide      |   |        |                      |        |
| 100001.11-CP 100001.11 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 11  | 6.36%  | 108                  | 62.43% |
| Precinct VBM           | 47  | 6.33%  | 512                  | 68.91% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 2   | 13.33% | 9                    | 60.00% |
| Total                  | 60  | 6.44%  | 629                  | 67.56% |
| 100002.11-CP 100002.11 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 7   | 8.64%  | 46                   | 56.79% |
| Precinct VBM           | 33  | 7.48%  | 287                  | 65.08% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 5   | 26.32% | 10                   | 52.63% |
| Total                  | 45  | 8.32%  | 343                  | 63.40% |
| 100003.11-CP 100003.11 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 9   | 7.89%  | 81                   | 71.05% |
| Precinct VBM           | 63  | 8.56%  | 453                  | 61.55% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 3   | 14.29% | 13                   | 61.90% |
| Total                  | 75  | 8.61%  | 547                  | 62.80% |
| 100004.11-CP 100004.11 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 27  | 11.49% | 129                  | 54.89% |
| Precinct VBM           | 56  | 8.08%  | 420                  | 60.61% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 0   | 0.00%  | 1                    | 50.00% |
| Total                  | 83  | 8.92%  | 550                  | 59.14% |
| 100005.11-CP 100005.11 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 13  | 9.77%  | 90                   | 67.67% |
| Precinct VBM           | 37  | 4.31%  | 629                  | 73.22% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 2   | 9.09%  | 13                   | 59.09% |
| Total                  | 52  | 5.13%  | 732                  | 72.19% |
| 100006.11-CP 100006.11 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 25  | 10.68% | 150                  | 64.10% |
| Precinct VBM           | 58  | 6.43%  | 547                  | 60.64% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 7   | 29.17% | 14                   | 58.33% |
| Total                  | 90  | 7.76%  | 711                  | 61.29% |
| 100008.11-CP 100008.11 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 29  | 16.38% | 107                  | 60.45% |
| Precinct VBM           | 49  | 7.07%  | 465                  | 67.10% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 4   | 26.67% | 8                    | 53.33% |
| Total                  | 82  | 9.27%  | 580                  | 65.54% |
| 100009.11-CP 100009.11 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 11  | 7.05%  | 94                   | 60.26% |
| Precinct VBM           | 55  | 7.79%  | 447                  | 63.31% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 5   | 23.81% | 14                   | 66.67% |
| Total                  | 71  | 8.04%  | 555                  | 62.85% |
| 100012.11-CP 100012.11 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 25  | 10.37% | 140                  | 58.09% |
| Precinct VBM           | 73  | 8.13%  | 571                  | 63.59% |

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|                        | SEAN WHITE (DEM)                          |        | TOM MCCLINTOCK (REP) |        |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 6   | 21.43% | 20                   | 71.43% |
| Total                  | 104                                       | 8.91%  | 731                  | 62.64% |
| 100013.11-CP 100013.11 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 23  | 9.83%  | 145                  | 61.97% |
| Precinct VBM           | 76  | 8.78%  | 583                  | 67.32% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 2   | 8.00%  | 16                   | 64.00% |
| Total                  | 101                                       | 8.98%  | 744                  | 66.13% |
| 100014.11-CP 100014.11 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 13  | 6.81%  | 129                  | 67.54% |
| Precinct VBM           | 44  | 6.60%  | 455                  | 68.22% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 2   | 8.00%  | 15                   | 60.00% |
| Total                  | 59  | 6.68%  | 599                  | 67.84% |
| 100022.11-CP 100022.11 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 13  | 7.98%  | 103                  | 63.19% |
| Precinct VBM           | 47  | 7.25%  | 441                  | 68.06% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 2   | 18.18% | 7                    | 63.64% |
| Total                  | 62  | 7.54%  | 551                  | 67.03% |
| 100023.11-CP 100023.11 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 23  | 11.11% | 147                  | 71.01% |
| Precinct VBM           | 53  | 5.32%  | 687                  | 68.98% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 4   | 14.29% | 17                   | 60.71% |
| Total                  | 80  | 6.50%  | 851                  | 69.13% |
| 200003.12-CP 200003.12 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 30  | 19.61% | 92                   | 60.13% |
| Precinct VBM           | 64  | 12.12% | 310                  | 58.71% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 5   | 18.52% | 16                   | 59.26% |
| Total                  | 99  | 13.98% | 418                  | 59.04% |
| 200004.12-CP 200004.12 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 20  | 11.56% | 111                  | 64.16% |
| Precinct VBM           | 57  | 9.09%  | 385                  | 61.40% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 2   | 15.38% | 9                    | 69.23% |
| Total                  | 79  | 9.72%  | 505                  | 62.12% |
| 200005.02-CP 200005.02 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 15  | 16.48% | 56                   | 61.54% |
| Precinct VBM           | 44  | 10.19% | 271                  | 62.73% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 1   | 5.56%  | 10                   | 55.56% |
| Total                  | 60  | 11.09% | 337                  | 62.29% |
| 200006.12-CP 200006.12 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 13  | 11.40% | 80                   | 70.18% |
| Precinct VBM           | 30  | 5.96%  | 376                  | 74.75% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 3   | 11.11% | 16                   | 59.26% |
| Total                  | 46  | 7.14%  | 472                  | 73.29% |
| 200008.12-CP 200008.12 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |

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|                        | SEAN WHITE (DEM)                          |        | TOM MCCLINTOCK (REP) |         |
| Precinct OS            | 18  | 12.33% | 89                   | 60.96%  |
| Precinct VBM           | 38  | 10.76% | 219                  | 62.04%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 1   | 10.00% | 7                    | 70.00%  |
| Total                  | 57  | 11.20% | 315                  | 61.89%  |
| 200010.12-CP 200010.12 |   |        |                      |         |
| Precinct TSx           | 0   | 0.00%  | 2                    | 50.00%  |
| Precinct OS            | 28  | 15.64% | 93                   | 51.96%  |
| Precinct VBM           | 65  | 8.24%  | 510                  | 64.64%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 2   | 7.14%  | 20                   | 71.43%  |
| Total                  | 95  | 9.50%  | 625                  | 62.50%  |
| 200012.12-CP 200012.12 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 24  | 18.32% | 72                   | 54.96%  |
| Precinct VBM           | 34  | 7.66%  | 296                  | 66.67%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 2   | 16.67% | 6                    | 50.00%  |
| Total                  | 60  | 10.22% | 374                  | 63.71%  |
| 200013.12-CP 200013.12 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 29  | 14.72% | 118                  | 59.90%  |
| Precinct VBM           | 64  | 9.64%  | 439                  | 66.11%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 5   | 26.32% | 11                   | 57.89%  |
| Total                  | 98  | 11.14% | 568                  | 64.55%  |
| 200014.12-CP 200014.12 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 25  | 13.37% | 124                  | 66.31%  |
| Precinct VBM           | 56  | 9.79%  | 342                  | 59.79%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 4   | 14.81% | 15                   | 55.56%  |
| Total                  | 85  | 10.81% | 481                  | 61.20%  |
| 200015.02-CP 200015.02 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 20  | 9.57%  | 133                  | 63.64%  |
| Precinct VBM           | 97  | 11.60% | 492                  | 58.85%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 4   | 19.05% | 14                   | 66.67%  |
| Total                  | 121                                       | 11.35% | 639                  | 59.94%  |
| 200016.12-CP 200016.12 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 0   | 0.00%  | 18                   | 78.26%  |
| Precinct VBM           | 11  | 8.27%  | 106                  | 79.70%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 0   | 0.00%  | 5                    | 100.00% |
| Total                  | 11  | 6.83%  | 129                  | 80.12%  |
| 200016.13-CP 200016.13 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 4   | 5.26%  | 57                   | 75.00%  |
| Precinct VBM           | 27  | 8.41%  | 231                  | 71.96%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 0   | 0.00%  | 5                    | 83.33%  |
| Total                  | 31  | 7.69%  | 293                  | 72.70%  |
| 200017.02-CP 200017.02 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 11  | 7.91%  | 82                   | 58.99%  |
| Precinct VBM           | 66  | 11.91% | 312                  | 56.32%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 3   | 25.00% | 7                    | 58.33%  |
| Total                  | 80  | 11.35% | 401                  | 56.88%  |

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|                        | SEAN WHITE (DEM)                          |        | TOM MCCLINTOCK (REP) |         |
| 200018.02-CP 200018.02 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 9   | 5.63%  | 120                  | 75.00%  |
| Precinct VBM           | 45  | 6.72%  | 451                  | 67.31%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 2   | 16.67% | 8                    | 66.67%  |
| Total                  | 56  | 6.65%  | 579                  | 68.76%  |
| 200020.02-CP 200020.02 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 6   | 4.72%  | 94                   | 74.02%  |
| Precinct VBM           | 32  | 6.39%  | 336                  | 67.07%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 4   | 23.53% | 10                   | 58.82%  |
| Total                  | 42  | 6.51%  | 440                  | 68.22%  |
| 200021.01-CP 200021.01 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 5   | 14.29% | 24                   | 68.57%  |
| Precinct VBM           | 10  | 5.18%  | 130                  | 67.36%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 0   | 0.00%  | 5                    | 83.33%  |
| Total                  | 15  | 6.41%  | 159                  | 67.95%  |
| 200021.02-CP 200021.02 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 13  | 8.02%  | 119                  | 73.46%  |
| Precinct VBM           | 36  | 5.12%  | 461                  | 65.58%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 1   | 5.88%  | 11                   | 64.71%  |
| Total                  | 50  | 5.67%  | 591                  | 67.01%  |
| 200024.02-CP 200024.02 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 5   | 9.09%  | 44                   | 80.00%  |
| Precinct VBM           | 13  | 4.85%  | 196                  | 73.13%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 0   | 0.00%  | 7                    | 100.00% |
| Total                  | 18  | 5.45%  | 247                  | 74.85%  |
| 300001.04-CP 300001.04 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 2   | 16.67% | 10                   | 83.33%  |
| Precinct VBM           | 12  | 12.50% | 50                   | 52.08%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 0   | -      | 0                    | -       |
| Total                  | 14  | 12.96% | 60                   | 55.56%  |
| 300001.05-CP 300001.05 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 8   | 11.11% | 45                   | 62.50%  |
| Precinct VBM           | 41  | 13.31% | 164                  | 53.25%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 4   | 40.00% | 4                    | 40.00%  |
| Total                  | 53  | 13.59% | 213                  | 54.62%  |
| 300003.05-CP 300003.05 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 24  | 13.04% | 89                   | 48.37%  |
| Precinct VBM           | 89  | 10.35% | 430                  | 50.00%  |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 9   | 36.00% | 9                    | 36.00%  |
| Total                  | 122                                       | 11.41% | 528                  | 49.39%  |
| 300004.05-CP 300004.05 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 38  | 24.20% | 71                   | 45.22%  |
| Precinct VBM           | 51  | 9.75%  | 284                  | 54.30%  |
| MB Precinct            | 0   | -      | 0                    | -       |

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|                        | SEAN WHITE (DEM)                          | TOM MCCLINTOCK (REP) |
| Provisional            | 2 14.29%                                  | 8 57.14%             |
| Total                  | 91 13.11%                                 | 363 52.31%           |
| 300005.05-CP 300005.05 |   |                      |
| Precinct TSx           | 0 -                                       | 0 -                  |
| Precinct OS            | 17 11.49%                                 | 84 56.76%            |
| Precinct VBM           | 62 10.08%                                 | 347 56.42%           |
| MB Precinct            | 0 -                                       | 0 -                  |
| Provisional            | 8 38.10%                                  | 8 38.10%             |
| Total                  | 87 11.10%                                 | 439 55.99%           |
| 300008.05-CP 300008.05 |   |                      |
| Precinct TSx           | 0 -                                       | 0 -                  |
| Precinct OS            | 14 10.37%                                 | 91 67.41%            |
| Precinct VBM           | 41 5.78%                                  | 477 67.28%           |
| MB Precinct            | 0 -                                       | 0 -                  |
| Provisional            | 2 15.38%                                  | 10 76.92%            |
| Total                  | 57 6.65%                                  | 578 67.44%           |
| 300009.04-CP 300009.04 |   |                      |
| Precinct TSx           | 0 -                                       | 0 -                  |
| Precinct OS            | 10 8.70%                                  | 78 67.83%            |
| Precinct VBM           | 32 8.49%                                  | 262 69.50%           |
| MB Precinct            | 0 -                                       | 0 -                  |
| Provisional            | 3 25.00%                                  | 6 50.00%             |
| Total                  | 45 8.93%                                  | 346 68.65%           |
| 300010.04-CP 300010.04 |   |                      |
| Precinct TSx           | 0 -                                       | 0 -                  |
| Precinct OS            | 18 11.18%                                 | 113 70.19%           |
| Precinct VBM           | 60 9.60%                                  | 386 61.76%           |
| MB Precinct            | 0 -                                       | 0 -                  |
| Provisional            | 2 18.18%                                  | 7 63.64%             |
| Total                  | 80 10.04%                                 | 506 63.49%           |
| 300011.04-CP 300011.04 |   |                      |
| Precinct TSx           | 0 -                                       | 0 -                  |
| Precinct OS            | 9 6.67%                                   | 96 71.11%            |
| Precinct VBM           | 62 9.23%                                  | 439 65.33%           |
| MB Precinct            | 0 -                                       | 0 -                  |
| Provisional            | 6 28.57%                                  | 12 57.14%            |
| Total                  | 77 9.30%                                  | 547 66.06%           |
| 300013.05-CP 300013.05 |   |                      |
| Precinct TSx           | 1 33.33%                                  | 2 66.67%             |
| Precinct OS            | 15 12.00%                                 | 77 61.60%            |
| Precinct VBM           | 50 7.23%                                  | 452 65.32%           |
| MB Precinct            | 0 -                                       | 0 -                  |
| Provisional            | 1 8.33%                                   | 8 66.67%             |
| Total                  | 67 8.05%                                  | 539 64.78%           |
| 300015.04-CP 300015.04 |   |                      |
| Precinct TSx           | 0 -                                       | 0 -                  |
| Precinct OS            | 0 0.00%                                   | 8 80.00%             |
| Precinct VBM           | 2 3.57%                                   | 35 62.50%            |
| MB Precinct            | 0 -                                       | 0 -                  |
| Provisional            | 1 25.00%                                  | 2 50.00%             |
| Total                  | 3 4.29%                                   | 45 64.29%            |
| 300015.05-CP 300015.05 |   |                      |
| Precinct TSx           | 0 -                                       | 0 -                  |
| Precinct OS            | 10 16.13%                                 | 32 51.61%            |
| Precinct VBM           | 20 8.10%                                  | 131 53.04%           |
| MB Precinct            | 0 -                                       | 0 -                  |
| Provisional            | 1 50.00%                                  | 1 50.00%             |
| Total                  | 31 9.97%                                  | 164 52.73%           |
| 300017.05-CP 300017.05 |   |                      |
| Precinct TSx           | 0 -                                       | 0 -                  |
| Precinct OS            | 15 13.64%                                 | 67 60.91%            |

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|                        | SEAN WHITE (DEM)                          |        | TOM MCCLINTOCK (REP) |        |
| Precinct VBM           | 42  | 6.81%  | 401                  | 64.99% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 2   | 25.00% | 4                    | 50.00% |
| Total                  | 59  | 8.03%  | 472                  | 64.22% |
| 300018.04-CP 300018.04 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 22  | 16.06% | 93                   | 67.88% |
| Precinct VBM           | 54  | 11.13% | 292                  | 60.21% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 5   | 20.83% | 14                   | 58.33% |
| Total                  | 81  | 12.54% | 399                  | 61.76% |
| 300020.04-CP 300020.04 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 14  | 9.15%  | 100                  | 65.36% |
| Precinct VBM           | 52  | 8.77%  | 383                  | 64.59% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 2   | 14.29% | 10                   | 71.43% |
| Total                  | 68  | 8.95%  | 493                  | 64.87% |
| 300021.03-CP 300021.03 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 15  | 10.49% | 83                   | 58.04% |
| Precinct VBM           | 48  | 8.19%  | 368                  | 62.80% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 0   | 0.00%  | 6                    | 54.55% |
| Total                  | 63  | 8.51%  | 457                  | 61.76% |
| 300022.05-CP 300022.05 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 10  | 10.64% | 66                   | 70.21% |
| Precinct VBM           | 41  | 12.06% | 186                  | 54.71% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 2   | 16.67% | 7                    | 58.33% |
| Total                  | 53  | 11.88% | 259                  | 58.07% |
| 300023.03-CP 300023.03 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 15  | 6.85%  | 131                  | 59.82% |
| Precinct VBM           | 61  | 6.96%  | 561                  | 64.04% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 7   | 30.43% | 13                   | 56.52% |
| Total                  | 83  | 7.42%  | 705                  | 63.06% |
| 300023.05-CP 300023.05 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 4   | 9.09%  | 31                   | 70.45% |
| Precinct VBM           | 11  | 5.98%  | 119                  | 64.67% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 3   | 37.50% | 3                    | 37.50% |
| Total                  | 18  | 7.63%  | 153                  | 64.83% |
| 400001.14-CP 400001.14 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 18  | 10.91% | 113                  | 68.48% |
| Precinct VBM           | 75  | 8.02%  | 631                  | 67.49% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 3   | 13.64% | 13                   | 59.09% |
| Total                  | 96  | 8.56%  | 757                  | 67.47% |
| 400002.08-CP 400002.08 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 15  | 8.02%  | 132                  | 70.59% |
| Precinct VBM           | 41  | 5.87%  | 507                  | 72.53% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 2   | 13.33% | 9                    | 60.00% |
| Total                  | 58  | 6.44%  | 648                  | 71.92% |
| 400003.08-CP 400003.08 |   |        |                      |        |

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|                        | SEAN WHITE (DEM)                          |        | TOM MCCLINTOCK (REP) |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 24  | 10.48% | 153                  | 66.81% |
| Precinct VBM           | 64  | 8.77%  | 435                  | 59.59% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 7   | 21.88% | 17                   | 53.13% |
| Total                  | 95  | 9.59%  | 605                  | 61.05% |
| 400004.14-CP 400004.14 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 24  | 13.41% | 115                  | 64.25% |
| Precinct VBM           | 76  | 12.12% | 415                  | 66.19% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 4   | 22.22% | 13                   | 72.22% |
| Total                  | 104                                       | 12.62% | 543                  | 65.90% |
| 400005.08-CP 400005.08 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 9   | 12.00% | 46                   | 61.33% |
| Precinct VBM           | 22  | 8.87%  | 147                  | 59.27% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 1   | 9.09%  | 9                    | 81.82% |
| Total                  | 32  | 9.58%  | 202                  | 60.48% |
| 400006.08-CP 400006.08 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 30  | 14.08% | 127                  | 59.62% |
| Precinct VBM           | 51  | 7.59%  | 377                  | 56.10% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 3   | 18.75% | 10                   | 62.50% |
| Total                  | 84  | 9.32%  | 514                  | 57.05% |
| 400007.08-CP 400007.08 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 21  | 9.55%  | 151                  | 68.64% |
| Precinct VBM           | 65  | 8.82%  | 515                  | 69.88% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 5   | 22.73% | 14                   | 63.64% |
| Total                  | 91  | 9.30%  | 680                  | 69.46% |
| 400010.08-CP 400010.08 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 34  | 15.04% | 128                  | 56.64% |
| Precinct VBM           | 93  | 11.03% | 477                  | 56.58% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 5   | 19.23% | 15                   | 57.69% |
| Total                  | 132                                       | 12.05% | 620                  | 56.62% |
| 400012.08-CP 400012.08 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 10  | 9.52%  | 62                   | 59.05% |
| Precinct VBM           | 41  | 10.05% | 245                  | 60.05% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 1   | 5.88%  | 11                   | 64.71% |
| Total                  | 52  | 9.81%  | 318                  | 60.00% |
| 400013.08-CP 400013.08 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 8   | 6.90%  | 85                   | 73.28% |
| Precinct VBM           | 19  | 6.46%  | 179                  | 60.88% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 1   | 33.33% | 1                    | 33.33% |
| Total                  | 28  | 6.78%  | 265                  | 64.16% |
| 400014.08-CP 400014.08 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 16  | 7.44%  | 150                  | 69.77% |
| Precinct VBM           | 39  | 5.56%  | 450                  | 64.19% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 8   | 23.53% | 20                   | 58.82% |

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|                        | SEAN WHITE (DEM)                          |        | TOM MCCLINTOCK (REP) |        |
| Total                  | 63  | 6.63%  | 620                  | 65.26% |
| 400017.08-CP 400017.08 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 9   | 5.36%  | 127                  | 75.60% |
| Precinct VBM           | 43  | 5.98%  | 502                  | 69.82% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 3   | 11.54% | 17                   | 65.38% |
| Total                  | 55  | 6.02%  | 646                  | 70.76% |
| 400018.08-CP 400018.08 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 10  | 12.20% | 55                   | 67.07% |
| Precinct VBM           | 11  | 4.42%  | 188                  | 75.50% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 1   | 20.00% | 4                    | 80.00% |
| Total                  | 22  | 6.55%  | 247                  | 73.51% |
| 400020.08-CP 400020.08 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 9   | 8.33%  | 85                   | 78.70% |
| Precinct VBM           | 32  | 9.76%  | 215                  | 65.55% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 0   | 0.00%  | 1                    | 50.00% |
| Total                  | 41  | 9.36%  | 301                  | 68.72% |
| 400021.07-CP 400021.07 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 12  | 10.26% | 81                   | 69.23% |
| Precinct VBM           | 34  | 4.72%  | 474                  | 65.74% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 2   | 11.11% | 10                   | 55.56% |
| Total                  | 48  | 5.61%  | 565                  | 66.00% |
| 500001.10-CP 500001.10 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 39  | 20.74% | 69                   | 36.70% |
| Precinct VBM           | 70  | 12.39% | 234                  | 41.42% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 5   | 38.46% | 2                    | 15.38% |
| Total                  | 114                                       | 14.88% | 305                  | 39.82% |
| 500002.10-CP 500002.10 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 30  | 24.79% | 39                   | 32.23% |
| Precinct VBM           | 83  | 21.45% | 130                  | 33.59% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 9   | 36.00% | 5                    | 20.00% |
| Total                  | 122                                       | 22.89% | 174                  | 32.65% |
| 500003.10-CP 500003.10 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 27  | 23.08% | 42                   | 35.90% |
| Precinct VBM           | 53  | 16.83% | 119                  | 37.78% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 2   | 33.33% | 2                    | 33.33% |
| Total                  | 82  | 18.72% | 163                  | 37.21% |
| 500004.10-CP 500004.10 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 33  | 30.56% | 35                   | 32.41% |
| Precinct VBM           | 54  | 23.38% | 87                   | 37.66% |
| MB Precinct            | 0   | -      | 0                    | -      |
| Provisional            | 5   | 27.78% | 6                    | 33.33% |
| Total                  | 92  | 25.77% | 128                  | 35.85% |
| 500005.10-CP 500005.10 |   |        |                      |        |
| Precinct TSx           | 0   | -      | 0                    | -      |
| Precinct OS            | 24  | 25.53% | 25                   | 26.60% |
| Precinct VBM           | 48  | 20.69% | 70                   | 30.17% |



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|                        | SEAN WHITE (DEM)                          | TOM MCCLINTOCK (REP) |
| MB Precinct            | 0   | 0                    |
| Provisional            | 3 25.00%                                  | 3 25.00%             |
| Total                  | 75 22.19%                                 | 98 28.99%            |
| 500006.10-CP 500006.10 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 57 35.19%                                 | 41 25.31%            |
| Precinct VBM           | 122 26.81%                                | 155 34.07%           |
| MB Precinct            | 0   | 0                    |
| Provisional            | 20 40.82%                                 | 10 20.41%            |
| Total                  | 199 29.88%                                | 206 30.93%           |
| 500007.10-CP 500007.10 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 34 18.28%                                 | 83 44.62%            |
| Precinct VBM           | 78 12.34%                                 | 304 48.10%           |
| MB Precinct            | 0   | 0                    |
| Provisional            | 4 21.05%                                  | 5 26.32%             |
| Total                  | 116 13.86%                                | 392 46.83%           |
| 500009.10-CP 500009.10 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 41 37.61%                                 | 23 21.10%            |
| Precinct VBM           | 46 17.42%                                 | 101 38.26%           |
| MB Precinct            | 0   | 0                    |
| Provisional            | 15 46.88%                                 | 11 34.38%            |
| Total                  | 102 25.19%                                | 135 33.33%           |
| 500011.10-CP 500011.10 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 51 30.72%                                 | 49 29.52%            |
| Precinct VBM           | 69 19.55%                                 | 120 33.99%           |
| MB Precinct            | 0   | 0                    |
| Provisional            | 12 35.29%                                 | 9 26.47%             |
| Total                  | 132 23.87%                                | 178 32.19%           |
| 500012.10-CP 500012.10 |   |                      |
| Precinct TSx           | 0 0.00%                                   | 1 100.00%            |
| Precinct OS            | 43 25.00%                                 | 51 29.65%            |
| Precinct VBM           | 86 15.17%                                 | 231 40.74%           |
| MB Precinct            | 0   | 0                    |
| Provisional            | 14 48.28%                                 | 4 13.79%             |
| Total                  | 143 18.60%                                | 287 37.32%           |
| 500015.10-CP 500015.10 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 22 22.22%                                 | 26 26.26%            |
| Precinct VBM           | 55 14.75%                                 | 130 34.85%           |
| MB Precinct            | 0   | 0                    |
| Provisional            | 2 15.38%                                  | 8 61.54%             |
| Total                  | 79 16.29%                                 | 164 33.81%           |
| 500020.10-CP 500020.10 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 16 10.53%                                 | 88 57.89%            |
| Precinct VBM           | 60 9.48%                                  | 390 61.61%           |
| MB Precinct            | 0   | 0                    |
| Provisional            | 6 24.00%                                  | 16 64.00%            |
| Total                  | 82 10.12%                                 | 494 60.99%           |
| 500021.10-CP 500021.10 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 17 10.90%                                 | 97 62.18%            |
| Precinct VBM           | 55 9.14%                                  | 375 62.29%           |
| MB Precinct            | 0   | 0                    |
| Provisional            | 7 26.92%                                  | 10 38.46%            |
| Total                  | 79 10.08%                                 | 482 61.48%           |
| 95U201.01-MB 95U201.01 |   |                      |
| Precinct TSx           | 0   | 0                    |

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|------------------------|---|----------------------|
|                        | SEAN WHITE (DEM)                          | TOM MCCLINTOCK (REP) |
| Precinct OS            | 0   | 0                    |
| Precinct VBM           | 0   | 0                    |
| MB Precinct            | 0 0.00%                                   | 6 85.71%             |
| Provisional            | 0   | 0                    |
| Total                  | 0 0.00%                                   | 6 85.71%             |
| 95U201.02-MB 95U201.02 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 0   | 0                    |
| Precinct VBM           | 0   | 0                    |
| MB Precinct            | 13 3.94%                                  | 221 66.97%           |
| Provisional            | 0 0.00%                                   | 1 100.00%            |
| Total                  | 13 3.93%                                  | 222 67.07%           |
| 95U202.02-MB 95U202.02 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 0   | 0                    |
| Precinct VBM           | 0   | 0                    |
| MB Precinct            | 56 10.11%                                 | 345 62.27%           |
| Provisional            | 1 33.33%                                  | 1 33.33%             |
| Total                  | 57 10.23%                                 | 346 62.12%           |
| 95U203.02-MB 95U203.02 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 0   | 0                    |
| Precinct VBM           | 0   | 0                    |
| MB Precinct            | 0   | 0                    |
| Provisional            | 0   | 0                    |
| Total                  | 0   | 0                    |
| 95U204.01-MB 95U204.01 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 0   | 0                    |
| Precinct VBM           | 0   | 0                    |
| MB Precinct            | 44 7.01%                                  | 427 67.99%           |
| Provisional            | 0 0.00%                                   | 2 100.00%            |
| Total                  | 44 6.98%                                  | 429 68.10%           |
| 95U301.05-MB 95U301.05 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 0   | 0                    |
| Precinct VBM           | 0   | 0                    |
| MB Precinct            | 10 6.94%                                  | 91 63.19%            |
| Provisional            | 0 0.00%                                   | 1 100.00%            |
| Total                  | 10 6.90%                                  | 92 63.45%            |
| 95U302.04-MB 95U302.04 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 0   | 0                    |
| Precinct VBM           | 0   | 0                    |
| MB Precinct            | 1 100.00%                                 | 0 0.00%              |
| Provisional            | 0   | 0                    |
| Total                  | 1 100.00%                                 | 0 0.00%              |
| 95U302.05-MB 95U302.05 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 0   | 0                    |
| Precinct VBM           | 0   | 0                    |
| MB Precinct            | 13 15.48%                                 | 52 61.90%            |
| Provisional            | 0   | 0                    |
| Total                  | 13 15.48%                                 | 52 61.90%            |
| 95U303.03-MB 95U303.03 |   |                      |
| Precinct TSx           | 0   | 0                    |
| Precinct OS            | 0   | 0                    |
| Precinct VBM           | 0   | 0                    |
| MB Precinct            | 4 5.00%                                   | 52 65.00%            |
| Provisional            | 0   | 0                    |
| Total                  | 4 5.00%                                   | 52 65.00%            |

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|                        | SEAN WHITE (DEM)                          |        | TOM MCCLINTOCK (REP) |         |
| 95U303.05-MB 95U303.05 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 0   | -      | 0                    | -       |
| Precinct VBM           | 0   | -      | 0                    | -       |
| MB Precinct            | 7   | 6.86%  | 71                   | 69.61%  |
| Provisional            | 0   | 0.00%  | 1                    | 100.00% |
| Total                  | 7   | 6.80%  | 72                   | 69.90%  |
| 95U304.05-MB 95U304.05 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 0   | -      | 0                    | -       |
| Precinct VBM           | 0   | -      | 0                    | -       |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 0   | -      | 0                    | -       |
| Total                  | 0   | -      | 0                    | -       |
| 95U305.05-MB 95U305.05 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 0   | -      | 0                    | -       |
| Precinct VBM           | 0   | -      | 0                    | -       |
| MB Precinct            | 0   | 0.00%  | 6                    | 85.71%  |
| Provisional            | 0   | -      | 0                    | -       |
| Total                  | 0   | 0.00%  | 6                    | 85.71%  |
| 95U401.08-MB 95U401.08 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 0   | -      | 0                    | -       |
| Precinct VBM           | 0   | -      | 0                    | -       |
| MB Precinct            | 1   | 7.14%  | 9                    | 64.29%  |
| Provisional            | 0   | -      | 0                    | -       |
| Total                  | 1   | 7.14%  | 9                    | 64.29%  |
| 95U402.07-MB 95U402.07 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 0   | -      | 0                    | -       |
| Precinct VBM           | 0   | -      | 0                    | -       |
| MB Precinct            | 19  | 7.82%  | 155                  | 63.79%  |
| Provisional            | 0   | 0.00%  | 0                    | 0.00%   |
| Total                  | 19  | 7.79%  | 155                  | 63.52%  |
| 95U402.08-MB 95U402.08 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 0   | -      | 0                    | -       |
| Precinct VBM           | 0   | -      | 0                    | -       |
| MB Precinct            | 37  | 7.01%  | 384                  | 72.73%  |
| Provisional            | 0   | 0.00%  | 2                    | 66.67%  |
| Total                  | 37  | 6.97%  | 386                  | 72.69%  |
| 95U403.08-MB 95U403.08 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 0   | -      | 0                    | -       |
| Precinct VBM           | 0   | -      | 0                    | -       |
| MB Precinct            | 110                                       | 11.09% | 557                  | 56.15%  |
| Provisional            | 4   | 33.33% | 5                    | 41.67%  |
| Total                  | 114                                       | 11.35% | 562                  | 55.98%  |
| 95U404.06-MB 95U404.06 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 0   | -      | 0                    | -       |
| Precinct VBM           | 0   | -      | 0                    | -       |
| MB Precinct            | 0   | -      | 0                    | -       |
| Provisional            | 0   | -      | 0                    | -       |
| Total                  | 0   | -      | 0                    | -       |
| 95U404.08-MB 95U404.08 |   |        |                      |         |
| Precinct TSx           | 0   | -      | 0                    | -       |
| Precinct OS            | 0   | -      | 0                    | -       |
| Precinct VBM           | 0   | -      | 0                    | -       |
| MB Precinct            | 0   | -      | 0                    | -       |

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|-------------------------------|---|----------------------|
|                               | SEAN WHITE (DEM)                          | TOM MCCLINTOCK (REP) |
| Provisional                   | 0   | 0                    |
| Total                         | 0   | 0                    |
| 95U501.09-MB 95U501.09        |   |                      |
| Precinct TSx                  | 0   | 0                    |
| Precinct OS                   | 0   | 0                    |
| Precinct VBM                  | 0   | 0                    |
| MB Precinct                   | 2 100.00%                                 | 0 0.00%              |
| Provisional                   | 0   | 0                    |
| Total                         | 2 100.00%                                 | 0 0.00%              |
| 95U501.10-MB 95U501.10        |   |                      |
| Precinct TSx                  | 0   | 0                    |
| Precinct OS                   | 0   | 0                    |
| Precinct VBM                  | 0   | 0                    |
| MB Precinct                   | 18 10.91%                                 | 97 58.79%            |
| Provisional                   | 0 0.00%                                   | 1 50.00%             |
| Total                         | 18 10.78%                                 | 98 58.68%            |
| 95U598.10-MB 95U598.10        |   |                      |
| Precinct TSx                  | 0   | 0                    |
| Precinct OS                   | 0   | 0                    |
| Precinct VBM                  | 0   | 0                    |
| MB Precinct                   | 58 27.36%                                 | 58 27.36%            |
| Provisional                   | 0 0.00%                                   | 1 100.00%            |
| Total                         | 58 27.23%                                 | 59 27.70%            |
| 95U599.10-MB 95U599.10        |   |                      |
| Precinct TSx                  | 0   | 0                    |
| Precinct OS                   | 0   | 0                    |
| Precinct VBM                  | 0   | 0                    |
| MB Precinct                   | 5 14.29%                                  | 13 37.14%            |
| Provisional                   | 0   | 0                    |
| Total                         | 5 14.29%                                  | 13 37.14%            |
| 96U101.11-MB 96U101.11        |   |                      |
| Precinct TSx                  | 0   | 0                    |
| Precinct OS                   | 0   | 0                    |
| Precinct VBM                  | 0   | 0                    |
| MB Precinct                   | 31 7.71%                                  | 280 69.65%           |
| Provisional                   | 5 83.33%                                  | 1 16.67%             |
| Total                         | 36 8.82%                                  | 281 68.87%           |
| 96U401.14-MB 96U401.14        |   |                      |
| Precinct TSx                  | 0   | 0                    |
| Precinct OS                   | 0   | 0                    |
| Precinct VBM                  | 0   | 0                    |
| MB Precinct                   | 47 14.11%                                 | 223 66.97%           |
| Provisional                   | 4 66.67%                                  | 1 16.67%             |
| Total                         | 51 15.04%                                 | 224 66.08%           |
| Total                         |   |                      |
| Precinct TSx                  | 1 12.50%                                  | 5 62.50%             |
| Precinct OS                   | 1447 12.98%                               | 6638 59.55%          |
| Precinct VBM                  | 3896 9.08%                                | 26330 61.35%         |
| MB Precinct                   | 476 9.79%                                 | 3047 62.66%          |
| Provisional                   | 307 22.05%                                | 743 53.38%           |
| Total                         | 6127 10.16%                               | 36763 60.94%         |
| STATE BOARD OF EQUALIZATION   |   |                      |
| STATE BOARD OF EQUALIZATION 1 |   |                      |
| Precinct TSx                  | 1 12.50%                                  | 5 62.50%             |
| Precinct OS                   | 1447 12.98%                               | 6638 59.55%          |
| Precinct VBM                  | 3896 9.08%                                | 26330 61.35%         |
| MB Precinct                   | 476 9.79%                                 | 3047 62.66%          |
| Provisional                   | 307 22.05%                                | 743 53.38%           |
| Total                         | 6127 10.16%                               | 36763 60.94%         |
| Total                         |   |                      |

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|-------------------------------------|---|---------------|----------------------|---------------|
|                                     | SEAN WHITE (DEM)                          |               | TOM MCCLINTOCK (REP) |               |
| Precinct TSx                        | 1   | 12.50%        | 5                    | 62.50%        |
| Precinct OS                         | 1447                                      | 12.98%        | 6638                 | 59.55%        |
| Precinct VBM                        | 3896                                      | 9.08%         | 26330                | 61.35%        |
| MB Precinct                         | 476                                       | 9.79%         | 3047                 | 62.66%        |
| Provisional                         | 307                                       | 22.05%        | 743                  | 53.38%        |
| <b>Total</b>                        | <b>6127</b>                               | <b>10.16%</b> | <b>36763</b>         | <b>60.94%</b> |
| <b>UNITED STATES SENATE</b>         |   |               |                      |               |
| <b>UNITED STATES SENATE</b>         |   |               |                      |               |
| Precinct TSx                        | 1   | 12.50%        | 5                    | 62.50%        |
| Precinct OS                         | 1447                                      | 12.98%        | 6638                 | 59.55%        |
| Precinct VBM                        | 3896                                      | 9.08%         | 26330                | 61.35%        |
| MB Precinct                         | 476                                       | 9.79%         | 3047                 | 62.66%        |
| Provisional                         | 307                                       | 22.05%        | 743                  | 53.38%        |
| <b>Total</b>                        | <b>6127</b>                               | <b>10.16%</b> | <b>36763</b>         | <b>60.94%</b> |
| <b>Total</b>                        |   |               |                      |               |
| Precinct TSx                        | 1   | 12.50%        | 5                    | 62.50%        |
| Precinct OS                         | 1447                                      | 12.98%        | 6638                 | 59.55%        |
| Precinct VBM                        | 3896                                      | 9.08%         | 26330                | 61.35%        |
| MB Precinct                         | 476                                       | 9.79%         | 3047                 | 62.66%        |
| Provisional                         | 307                                       | 22.05%        | 743                  | 53.38%        |
| <b>Total</b>                        | <b>6127</b>                               | <b>10.16%</b> | <b>36763</b>         | <b>60.94%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |   |               |                      |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |   |               |                      |               |
| Precinct TSx                        | 1   | 12.50%        | 5                    | 62.50%        |
| Precinct OS                         | 1447                                      | 12.98%        | 6638                 | 59.55%        |
| Precinct VBM                        | 3896                                      | 9.08%         | 26330                | 61.35%        |
| MB Precinct                         | 476                                       | 9.79%         | 3047                 | 62.66%        |
| Provisional                         | 307                                       | 22.05%        | 743                  | 53.38%        |
| <b>Total</b>                        | <b>6127</b>                               | <b>10.16%</b> | <b>36763</b>         | <b>60.94%</b> |
| <b>Total</b>                        |   |               |                      |               |
| Precinct TSx                        | 1   | 12.50%        | 5                    | 62.50%        |
| Precinct OS                         | 1447                                      | 12.98%        | 6638                 | 59.55%        |
| Precinct VBM                        | 3896                                      | 9.08%         | 26330                | 61.35%        |
| MB Precinct                         | 476                                       | 9.79%         | 3047                 | 62.66%        |
| Provisional                         | 307                                       | 22.05%        | 743                  | 53.38%        |
| <b>Total</b>                        | <b>6127</b>                               | <b>10.16%</b> | <b>36763</b>         | <b>60.94%</b> |
| <b>STATE SENATE</b>                 |   |               |                      |               |
| <b>1ST STATE SENATE DISTRICT</b>    |   |               |                      |               |
| Precinct TSx                        | 1   | 12.50%        | 5                    | 62.50%        |
| Precinct OS                         | 1447                                      | 12.98%        | 6638                 | 59.55%        |
| Precinct VBM                        | 3896                                      | 9.08%         | 26330                | 61.35%        |
| MB Precinct                         | 476                                       | 9.79%         | 3047                 | 62.66%        |
| Provisional                         | 307                                       | 22.05%        | 743                  | 53.38%        |
| <b>Total</b>                        | <b>6127</b>                               | <b>10.16%</b> | <b>36763</b>         | <b>60.94%</b> |
| <b>Total</b>                        |   |               |                      |               |
| Precinct TSx                        | 1   | 12.50%        | 5                    | 62.50%        |
| Precinct OS                         | 1447                                      | 12.98%        | 6638                 | 59.55%        |
| Precinct VBM                        | 3896                                      | 9.08%         | 26330                | 61.35%        |
| MB Precinct                         | 476                                       | 9.79%         | 3047                 | 62.66%        |
| Provisional                         | 307                                       | 22.05%        | 743                  | 53.38%        |
| <b>Total</b>                        | <b>6127</b>                               | <b>10.16%</b> | <b>36763</b>         | <b>60.94%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |   |               |                      |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |   |               |                      |               |
| Precinct TSx                        | 1   | 25.00%        | 3                    | 75.00%        |
| Precinct OS                         | 985                                       | 13.90%        | 4087                 | 57.69%        |
| Precinct VBM                        | 2608                                      | 9.81%         | 15573                | 58.60%        |
| MB Precinct                         | 398                                       | 9.64%         | 2544                 | 61.63%        |

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|                                   | SEAN WHITE (DEM)                          |        | TOM MCCLINTOCK (REP) |         |
| Provisional                       | 223                                       | 24.51% | 448                  | 49.23%  |
| Total                             | 4215                                      | 10.89% | 22655                | 58.54%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |   |        |                      |         |
| Precinct TSx                      | 0   | 0.00%  | 2                    | 50.00%  |
| Precinct OS                       | 462                                       | 11.37% | 2551                 | 62.80%  |
| Precinct VBM                      | 1288                                      | 7.88%  | 10757                | 65.82%  |
| MB Precinct                       | 78  | 10.61% | 503                  | 68.44%  |
| Provisional                       | 84  | 17.43% | 295                  | 61.20%  |
| Total                             | 1912                                      | 8.84%  | 14108                | 65.23%  |
| <b>Total</b>                      |   |        |                      |         |
| Precinct TSx                      | 1   | 12.50% | 5                    | 62.50%  |
| Precinct OS                       | 1447                                      | 12.98% | 6638                 | 59.55%  |
| Precinct VBM                      | 3896                                      | 9.08%  | 26330                | 61.35%  |
| MB Precinct                       | 476                                       | 9.79%  | 3047                 | 62.66%  |
| Provisional                       | 307                                       | 22.05% | 743                  | 53.38%  |
| Total                             | 6127                                      | 10.16% | 36763                | 60.94%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |   |        |                      |         |
| <b>1ST SUPERVISORIAL DISTRICT</b> |   |        |                      |         |
| Precinct TSx                      | 0   | -      | 0                    | -       |
| Precinct OS                       | 229                                       | 9.79%  | 1469                 | 62.80%  |
| Precinct VBM                      | 691                                       | 7.02%  | 6497                 | 65.97%  |
| MB Precinct                       | 31  | 7.71%  | 280                  | 69.65%  |
| Provisional                       | 49  | 18.70% | 158                  | 60.31%  |
| Total                             | 1000                                      | 7.78%  | 8404                 | 65.40%  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |   |        |                      |         |
| Precinct TSx                      | 0   | 0.00%  | 2                    | 50.00%  |
| Precinct OS                       | 275                                       | 11.67% | 1526                 | 64.74%  |
| Precinct VBM                      | 789                                       | 8.68%  | 5863                 | 64.49%  |
| MB Precinct                       | 113                                       | 7.44%  | 999                  | 65.77%  |
| Provisional                       | 40  | 13.79% | 186                  | 64.14%  |
| Total                             | 1217                                      | 9.18%  | 8576                 | 64.67%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |   |        |                      |         |
| Precinct TSx                      | 1   | 33.33% | 2                    | 66.67%  |
| Precinct OS                       | 260                                       | 11.73% | 1365                 | 61.60%  |
| Precinct VBM                      | 831                                       | 8.78%  | 5767                 | 60.96%  |
| MB Precinct                       | 35  | 8.37%  | 272                  | 65.07%  |
| Provisional                       | 60  | 24.29% | 134                  | 54.25%  |
| Total                             | 1187                                      | 9.62%  | 7540                 | 61.08%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |   |        |                      |         |
| Precinct TSx                      | 0   | -      | 0                    | -       |
| Precinct OS                       | 249                                       | 10.35% | 1610                 | 66.94%  |
| Precinct VBM                      | 706                                       | 7.92%  | 5757                 | 64.61%  |
| MB Precinct                       | 214                                       | 10.14% | 1328                 | 62.94%  |
| Provisional                       | 54  | 18.69% | 172                  | 59.52%  |
| Total                             | 1223                                      | 8.92%  | 8867                 | 64.65%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |   |        |                      |         |
| Precinct TSx                      | 0   | 0.00%  | 1                    | 100.00% |
| Precinct OS                       | 434                                       | 23.72% | 668                  | 36.50%  |
| Precinct VBM                      | 879                                       | 15.67% | 2446                 | 43.61%  |
| MB Precinct                       | 83  | 20.05% | 168                  | 40.58%  |
| Provisional                       | 104                                       | 34.21% | 93                   | 30.59%  |
| Total                             | 1500                                      | 18.39% | 3376                 | 41.38%  |
| <b>Total</b>                      |   |        |                      |         |
| Precinct TSx                      | 1   | 12.50% | 5                    | 62.50%  |
| Precinct OS                       | 1447                                      | 12.98% | 6638                 | 59.55%  |
| Precinct VBM                      | 3896                                      | 9.08%  | 26330                | 61.35%  |
| MB Precinct                       | 476                                       | 9.79%  | 3047                 | 62.66%  |
| Provisional                       | 307                                       | 22.05% | 743                  | 53.38%  |
| Total                             | 6127                                      | 10.16% | 36763                | 60.94%  |

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|                                 | SEAN WHITE (DEM)                          |        | TOM MCCLINTOCK (REP) |        |
| <b>CITIES</b>                   |   |        |                      |        |
| <b>CITY OF PLACERVILLE</b>      |   |        |                      |        |
| Precinct TSx                    | 0   | -      | 0                    | -      |
| Precinct OS                     | 89  | 15.53% | 299                  | 52.18% |
| Precinct VBM                    | 255                                       | 10.62% | 1275                 | 53.08% |
| MB Precinct                     | 0   | -      | 0                    | -      |
| Provisional                     | 23  | 32.86% | 29                   | 41.43% |
| Total                           | 367                                       | 12.05% | 1603                 | 52.64% |
| <b>CITY OF SOUTH LAKE TAHOE</b> |   |        |                      |        |
| Precinct TSx                    | 0   | -      | 0                    | -      |
| Precinct OS                     | 270                                       | 28.54% | 295                  | 31.18% |
| Precinct VBM                    | 500                                       | 19.58% | 967                  | 37.86% |
| MB Precinct                     | 0   | -      | 0                    | -      |
| Provisional                     | 68  | 35.98% | 49                   | 25.93% |
| Total                           | 838                                       | 22.72% | 1311                 | 35.54% |
| <b>UNINCORPORATED AREA</b>      |   |        |                      |        |
| Precinct TSx                    | 1   | 12.50% | 5                    | 62.50% |
| Precinct OS                     | 1088                                      | 11.30% | 6044                 | 62.78% |
| Precinct VBM                    | 3141                                      | 8.27%  | 24088                | 63.45% |
| MB Precinct                     | 476                                       | 9.79%  | 3047                 | 62.66% |
| Provisional                     | 216                                       | 19.06% | 665                  | 58.69% |
| Total                           | 4922                                      | 9.18%  | 33849                | 63.16% |
| <b>Total</b>                    |   |        |                      |        |
| Precinct TSx                    | 1   | 12.50% | 5                    | 62.50% |
| Precinct OS                     | 1447                                      | 12.98% | 6638                 | 59.55% |
| Precinct VBM                    | 3896                                      | 9.08%  | 26330                | 61.35% |
| MB Precinct                     | 476                                       | 9.79%  | 3047                 | 62.66% |
| Provisional                     | 307                                       | 22.05% | 743                  | 53.38% |
| Total                           | 6127                                      | 10.16% | 36763                | 60.94% |

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|                          | State Senator 1st State Senate |          |               |             |                  |        |
|--------------------------|--------------------------------|----------|---------------|-------------|------------------|--------|
|                          | Reg. Voters                    | Vote For | Times Counted | Total Votes | TED GAINES (REP) |        |
| <b>Jurisdiction Wide</b> |                                |          |               |             |                  |        |
| 100001.11-CP 100001.11   |                                |          |               |             |                  |        |
| Precinct TSx             | 1615                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS              | 1615                           | 1        | 190           | 168         | 76               | 45.24% |
| Precinct VBM             | 1615                           | 1        | 781           | 728         | 394              | 54.12% |
| MB Precinct              | 1615                           | 1        | 0             | 0           | 0                | -      |
| Provisional              | 1615                           | 1        | 17            | 15          | 10               | 66.67% |
| Total                    | 1615                           | 5        | 988           | 911         | 480              | 52.69% |
| 100002.11-CP 100002.11   |                                |          |               |             |                  |        |
| Precinct TSx             | 1000                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS              | 1000                           | 1        | 89            | 79          | 37               | 46.84% |
| Precinct VBM             | 1000                           | 1        | 476           | 431         | 237              | 54.99% |
| MB Precinct              | 1000                           | 1        | 0             | 0           | 0                | -      |
| Provisional              | 1000                           | 1        | 20            | 19          | 10               | 52.63% |
| Total                    | 1000                           | 5        | 585           | 529         | 284              | 53.69% |
| 100003.11-CP 100003.11   |                                |          |               |             |                  |        |
| Precinct TSx             | 1269                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS              | 1269                           | 1        | 123           | 112         | 66               | 58.93% |
| Precinct VBM             | 1269                           | 1        | 773           | 719         | 335              | 46.59% |
| MB Precinct              | 1269                           | 1        | 0             | 0           | 0                | -      |
| Provisional              | 1269                           | 1        | 24            | 21          | 11               | 52.38% |
| Total                    | 1269                           | 5        | 920           | 852         | 412              | 48.36% |
| 100004.11-CP 100004.11   |                                |          |               |             |                  |        |
| Precinct TSx             | 1633                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS              | 1633                           | 1        | 254           | 227         | 108              | 47.58% |
| Precinct VBM             | 1633                           | 1        | 738           | 685         | 341              | 49.78% |
| MB Precinct              | 1633                           | 1        | 0             | 0           | 0                | -      |
| Provisional              | 1633                           | 1        | 4             | 3           | 1                | 33.33% |
| Total                    | 1633                           | 5        | 996           | 915         | 450              | 49.18% |
| 100005.11-CP 100005.11   |                                |          |               |             |                  |        |
| Precinct TSx             | 1586                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS              | 1586                           | 1        | 146           | 134         | 62               | 46.27% |
| Precinct VBM             | 1586                           | 1        | 895           | 835         | 460              | 55.09% |
| MB Precinct              | 1586                           | 1        | 0             | 0           | 0                | -      |
| Provisional              | 1586                           | 1        | 23            | 22          | 10               | 45.45% |
| Total                    | 1586                           | 5        | 1064          | 991         | 532              | 53.68% |
| 100006.11-CP 100006.11   |                                |          |               |             |                  |        |
| Precinct TSx             | 1928                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS              | 1928                           | 1        | 252           | 222         | 126              | 56.76% |
| Precinct VBM             | 1928                           | 1        | 944           | 869         | 420              | 48.33% |
| MB Precinct              | 1928                           | 1        | 0             | 0           | 0                | -      |
| Provisional              | 1928                           | 1        | 31            | 25          | 13               | 52.00% |
| Total                    | 1928                           | 5        | 1227          | 1116        | 559              | 50.09% |
| 100008.11-CP 100008.11   |                                |          |               |             |                  |        |
| Precinct TSx             | 1671                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS              | 1671                           | 1        | 197           | 176         | 83               | 47.16% |
| Precinct VBM             | 1671                           | 1        | 740           | 684         | 340              | 49.71% |
| MB Precinct              | 1671                           | 1        | 0             | 0           | 0                | -      |
| Provisional              | 1671                           | 1        | 17            | 15          | 9                | 60.00% |
| Total                    | 1671                           | 5        | 954           | 875         | 432              | 49.37% |
| 100009.11-CP 100009.11   |                                |          |               |             |                  |        |
| Precinct TSx             | 1838                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS              | 1838                           | 1        | 169           | 155         | 70               | 45.16% |
| Precinct VBM             | 1838                           | 1        | 751           | 691         | 365              | 52.82% |
| MB Precinct              | 1838                           | 1        | 0             | 0           | 0                | -      |
| Provisional              | 1838                           | 1        | 26            | 21          | 10               | 47.62% |
| Total                    | 1838                           | 5        | 946           | 867         | 445              | 51.33% |
| 100012.11-CP 100012.11   |                                |          |               |             |                  |        |
| Precinct TSx             | 2108                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS              | 2108                           | 1        | 255           | 233         | 119              | 51.07% |
| Precinct VBM             | 2108                           | 1        | 968           | 870         | 446              | 51.26% |



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|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | TED GAINES (REP) |        |
| MB Precinct            | 2108                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 2108                           | 1        | 29            | 29          | 23               | 79.31% |
| Total                  | 2108                           | 5        | 1252          | 1132        | 588              | 51.94% |
| 100013.11-CP 100013.11 |                                |          |               |             |                  |        |
| Precinct TSx           | 2087                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 2087                           | 1        | 250           | 236         | 121              | 51.27% |
| Precinct VBM           | 2087                           | 1        | 908           | 837         | 429              | 51.25% |
| MB Precinct            | 2087                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 2087                           | 1        | 28            | 25          | 11               | 44.00% |
| Total                  | 2087                           | 5        | 1186          | 1098        | 561              | 51.09% |
| 100014.11-CP 100014.11 |                                |          |               |             |                  |        |
| Precinct TSx           | 1659                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1659                           | 1        | 211           | 189         | 91               | 48.15% |
| Precinct VBM           | 1659                           | 1        | 706           | 660         | 354              | 53.64% |
| MB Precinct            | 1659                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1659                           | 1        | 30            | 25          | 17               | 68.00% |
| Total                  | 1659                           | 5        | 947           | 874         | 462              | 52.86% |
| 100022.11-CP 100022.11 |                                |          |               |             |                  |        |
| Precinct TSx           | 1432                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1432                           | 1        | 177           | 164         | 87               | 53.05% |
| Precinct VBM           | 1432                           | 1        | 678           | 619         | 330              | 53.31% |
| MB Precinct            | 1432                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1432                           | 1        | 13            | 11          | 5                | 45.45% |
| Total                  | 1432                           | 5        | 868           | 794         | 422              | 53.15% |
| 100023.11-CP 100023.11 |                                |          |               |             |                  |        |
| Precinct TSx           | 2135                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 2135                           | 1        | 217           | 208         | 126              | 60.58% |
| Precinct VBM           | 2135                           | 1        | 1053          | 978         | 524              | 53.58% |
| MB Precinct            | 2135                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 2135                           | 1        | 33            | 29          | 12               | 41.38% |
| Total                  | 2135                           | 5        | 1303          | 1215        | 662              | 54.49% |
| 200003.12-CP 200003.12 |                                |          |               |             |                  |        |
| Precinct TSx           | 1362                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1362                           | 1        | 166           | 153         | 72               | 47.06% |
| Precinct VBM           | 1362                           | 1        | 558           | 510         | 245              | 48.04% |
| MB Precinct            | 1362                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1362                           | 1        | 31            | 26          | 13               | 50.00% |
| Total                  | 1362                           | 5        | 755           | 689         | 330              | 47.90% |
| 200004.12-CP 200004.12 |                                |          |               |             |                  |        |
| Precinct TSx           | 1507                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1507                           | 1        | 182           | 169         | 81               | 47.93% |
| Precinct VBM           | 1507                           | 1        | 662           | 613         | 289              | 47.15% |
| MB Precinct            | 1507                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1507                           | 1        | 14            | 13          | 8                | 61.54% |
| Total                  | 1507                           | 5        | 858           | 795         | 378              | 47.55% |
| 200005.02-CP 200005.02 |                                |          |               |             |                  |        |
| Precinct TSx           | 867                            | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 867                            | 1        | 94            | 90          | 45               | 50.00% |
| Precinct VBM           | 867                            | 1        | 452           | 422         | 198              | 46.92% |
| MB Precinct            | 867                            | 1        | 0             | 0           | 0                | -      |
| Provisional            | 867                            | 1        | 21            | 18          | 11               | 61.11% |
| Total                  | 867                            | 5        | 567           | 530         | 254              | 47.92% |
| 200006.12-CP 200006.12 |                                |          |               |             |                  |        |
| Precinct TSx           | 1086                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1086                           | 1        | 117           | 114         | 70               | 61.40% |
| Precinct VBM           | 1086                           | 1        | 532           | 493         | 294              | 59.63% |
| MB Precinct            | 1086                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1086                           | 1        | 29            | 26          | 10               | 38.46% |
| Total                  | 1086                           | 5        | 678           | 633         | 374              | 59.08% |
| 200008.12-CP 200008.12 |                                |          |               |             |                  |        |
| Precinct TSx           | 999                            | 1        | 0             | 0           | 0                | -      |

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|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | TED GAINES (REP) |        |
| Precinct OS            | 999                            | 1        | 155           | 143         | 69               | 48.25% |
| Precinct VBM           | 999                            | 1        | 379           | 338         | 171              | 50.59% |
| MB Precinct            | 999                            | 1        | 0             | 0           | 0                | -      |
| Provisional            | 999                            | 1        | 10            | 10          | 6                | 60.00% |
| Total                  | 999                            | 5        | 544           | 491         | 246              | 50.10% |
| 200010.12-CP 200010.12 |                                |          |               |             |                  |        |
| Precinct TSx           | 1976                           | 1        | 4             | 4           | 3                | 75.00% |
| Precinct OS            | 1976                           | 1        | 197           | 179         | 90               | 50.28% |
| Precinct VBM           | 1976                           | 1        | 859           | 786         | 399              | 50.76% |
| MB Precinct            | 1976                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1976                           | 1        | 34            | 27          | 20               | 74.07% |
| Total                  | 1976                           | 5        | 1094          | 996         | 512              | 51.41% |
| 200012.12-CP 200012.12 |                                |          |               |             |                  |        |
| Precinct TSx           | 1064                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1064                           | 1        | 145           | 133         | 62               | 46.62% |
| Precinct VBM           | 1064                           | 1        | 464           | 425         | 214              | 50.35% |
| MB Precinct            | 1064                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1064                           | 1        | 14            | 12          | 4                | 33.33% |
| Total                  | 1064                           | 5        | 623           | 570         | 280              | 49.12% |
| 200013.12-CP 200013.12 |                                |          |               |             |                  |        |
| Precinct TSx           | 1574                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1574                           | 1        | 211           | 198         | 94               | 47.47% |
| Precinct VBM           | 1574                           | 1        | 701           | 652         | 340              | 52.15% |
| MB Precinct            | 1574                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1574                           | 1        | 23            | 20          | 10               | 50.00% |
| Total                  | 1574                           | 5        | 935           | 870         | 444              | 51.03% |
| 200014.12-CP 200014.12 |                                |          |               |             |                  |        |
| Precinct TSx           | 1438                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1438                           | 1        | 201           | 184         | 102              | 55.43% |
| Precinct VBM           | 1438                           | 1        | 613           | 560         | 253              | 45.18% |
| MB Precinct            | 1438                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1438                           | 1        | 30            | 27          | 9                | 33.33% |
| Total                  | 1438                           | 5        | 844           | 771         | 364              | 47.21% |
| 200015.02-CP 200015.02 |                                |          |               |             |                  |        |
| Precinct TSx           | 1832                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1832                           | 1        | 218           | 204         | 102              | 50.00% |
| Precinct VBM           | 1832                           | 1        | 886           | 823         | 372              | 45.20% |
| MB Precinct            | 1832                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1832                           | 1        | 22            | 22          | 13               | 59.09% |
| Total                  | 1832                           | 5        | 1126          | 1049        | 487              | 46.43% |
| 200016.12-CP 200016.12 |                                |          |               |             |                  |        |
| Precinct TSx           | 301                            | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 301                            | 1        | 26            | 23          | 17               | 73.91% |
| Precinct VBM           | 301                            | 1        | 139           | 131         | 80               | 61.07% |
| MB Precinct            | 301                            | 1        | 0             | 0           | 0                | -      |
| Provisional            | 301                            | 1        | 6             | 5           | 4                | 80.00% |
| Total                  | 301                            | 5        | 171           | 159         | 101              | 63.52% |
| 200016.13-CP 200016.13 |                                |          |               |             |                  |        |
| Precinct TSx           | 565                            | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 565                            | 1        | 80            | 76          | 40               | 52.63% |
| Precinct VBM           | 565                            | 1        | 338           | 314         | 172              | 54.78% |
| MB Precinct            | 565                            | 1        | 0             | 0           | 0                | -      |
| Provisional            | 565                            | 1        | 6             | 6           | 3                | 50.00% |
| Total                  | 565                            | 5        | 424           | 396         | 215              | 54.29% |
| 200017.02-CP 200017.02 |                                |          |               |             |                  |        |
| Precinct TSx           | 1286                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1286                           | 1        | 146           | 138         | 67               | 48.55% |
| Precinct VBM           | 1286                           | 1        | 594           | 558         | 241              | 43.19% |
| MB Precinct            | 1286                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1286                           | 1        | 12            | 11          | 6                | 54.55% |
| Total                  | 1286                           | 5        | 752           | 707         | 314              | 44.41% |

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|                               | Reg. Voters                    | Vote For | Times Counted | Total Votes | TED GAINES (REP) |               |
| <b>200018.02-CP 200018.02</b> |                                |          |               |             |                  |               |
| Precinct TSx                  | 1454                           | 1        | 0             | 0           | 0                | -             |
| Precinct OS                   | 1454                           | 1        | 169           | 158         | 91               | 57.59%        |
| Precinct VBM                  | 1454                           | 1        | 704           | 649         | 341              | 52.54%        |
| MB Precinct                   | 1454                           | 1        | 0             | 0           | 0                | -             |
| Provisional                   | 1454                           | 1        | 13            | 12          | 9                | 75.00%        |
| <b>Total</b>                  | <b>1454</b>                    | <b>5</b> | <b>886</b>    | <b>819</b>  | <b>441</b>       | <b>53.85%</b> |
| <b>200020.02-CP 200020.02</b> |                                |          |               |             |                  |               |
| Precinct TSx                  | 1213                           | 1        | 0             | 0           | 0                | -             |
| Precinct OS                   | 1213                           | 1        | 139           | 127         | 83               | 65.35%        |
| Precinct VBM                  | 1213                           | 1        | 526           | 491         | 246              | 50.10%        |
| MB Precinct                   | 1213                           | 1        | 0             | 0           | 0                | -             |
| Provisional                   | 1213                           | 1        | 19            | 16          | 6                | 37.50%        |
| <b>Total</b>                  | <b>1213</b>                    | <b>5</b> | <b>684</b>    | <b>634</b>  | <b>335</b>       | <b>52.84%</b> |
| <b>200021.01-CP 200021.01</b> |                                |          |               |             |                  |               |
| Precinct TSx                  | 436                            | 1        | 0             | 0           | 0                | -             |
| Precinct OS                   | 436                            | 1        | 38            | 33          | 23               | 69.70%        |
| Precinct VBM                  | 436                            | 1        | 206           | 193         | 95               | 49.22%        |
| MB Precinct                   | 436                            | 1        | 0             | 0           | 0                | -             |
| Provisional                   | 436                            | 1        | 7             | 5           | 4                | 80.00%        |
| <b>Total</b>                  | <b>436</b>                     | <b>5</b> | <b>251</b>    | <b>231</b>  | <b>122</b>       | <b>52.81%</b> |
| <b>200021.02-CP 200021.02</b> |                                |          |               |             |                  |               |
| Precinct TSx                  | 1296                           | 1        | 0             | 0           | 0                | -             |
| Precinct OS                   | 1296                           | 1        | 170           | 159         | 95               | 59.75%        |
| Precinct VBM                  | 1296                           | 1        | 752           | 677         | 364              | 53.77%        |
| MB Precinct                   | 1296                           | 1        | 0             | 0           | 0                | -             |
| Provisional                   | 1296                           | 1        | 20            | 18          | 11               | 61.11%        |
| <b>Total</b>                  | <b>1296</b>                    | <b>5</b> | <b>942</b>    | <b>854</b>  | <b>470</b>       | <b>55.04%</b> |
| <b>200024.02-CP 200024.02</b> |                                |          |               |             |                  |               |
| Precinct TSx                  | 555                            | 1        | 0             | 0           | 0                | -             |
| Precinct OS                   | 555                            | 1        | 55            | 54          | 30               | 55.56%        |
| Precinct VBM                  | 555                            | 1        | 282           | 269         | 161              | 59.85%        |
| MB Precinct                   | 555                            | 1        | 0             | 0           | 0                | -             |
| Provisional                   | 555                            | 1        | 9             | 8           | 5                | 62.50%        |
| <b>Total</b>                  | <b>555</b>                     | <b>5</b> | <b>346</b>    | <b>331</b>  | <b>196</b>       | <b>59.21%</b> |
| <b>300001.04-CP 300001.04</b> |                                |          |               |             |                  |               |
| Precinct TSx                  | 259                            | 1        | 0             | 0           | 0                | -             |
| Precinct OS                   | 259                            | 1        | 12            | 12          | 6                | 50.00%        |
| Precinct VBM                  | 259                            | 1        | 100           | 95          | 43               | 45.26%        |
| MB Precinct                   | 259                            | 1        | 0             | 0           | 0                | -             |
| Provisional                   | 259                            | 1        | 0             | 0           | 0                | -             |
| <b>Total</b>                  | <b>259</b>                     | <b>5</b> | <b>112</b>    | <b>107</b>  | <b>49</b>        | <b>45.79%</b> |
| <b>300001.05-CP 300001.05</b> |                                |          |               |             |                  |               |
| Precinct TSx                  | 731                            | 1        | 0             | 0           | 0                | -             |
| Precinct OS                   | 731                            | 1        | 76            | 70          | 34               | 48.57%        |
| Precinct VBM                  | 731                            | 1        | 330           | 298         | 128              | 42.95%        |
| MB Precinct                   | 731                            | 1        | 0             | 0           | 0                | -             |
| Provisional                   | 731                            | 1        | 10            | 10          | 2                | 20.00%        |
| <b>Total</b>                  | <b>731</b>                     | <b>5</b> | <b>416</b>    | <b>378</b>  | <b>164</b>       | <b>43.39%</b> |
| <b>300003.05-CP 300003.05</b> |                                |          |               |             |                  |               |
| Precinct TSx                  | 1932                           | 1        | 0             | 0           | 0                | -             |
| Precinct OS                   | 1932                           | 1        | 205           | 180         | 76               | 42.22%        |
| Precinct VBM                  | 1932                           | 1        | 924           | 849         | 332              | 39.10%        |
| MB Precinct                   | 1932                           | 1        | 0             | 0           | 0                | -             |
| Provisional                   | 1932                           | 1        | 27            | 24          | 7                | 29.17%        |
| <b>Total</b>                  | <b>1932</b>                    | <b>5</b> | <b>1156</b>   | <b>1053</b> | <b>415</b>       | <b>39.41%</b> |
| <b>300004.05-CP 300004.05</b> |                                |          |               |             |                  |               |
| Precinct TSx                  | 1273                           | 1        | 0             | 0           | 0                | -             |
| Precinct OS                   | 1273                           | 1        | 175           | 151         | 58               | 38.41%        |
| Precinct VBM                  | 1273                           | 1        | 576           | 519         | 224              | 43.16%        |
| MB Precinct                   | 1273                           | 1        | 0             | 0           | 0                | -             |

**COUNTY OF EL DORADO**  
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|                        | State Senator 1st State Senate |          |               |             |                  |        |
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|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | TED GAINES (REP) |        |
| Provisional            | 1273                           | 1        | 17            | 14          | 9                | 64.29% |
| Total                  | 1273                           | 5        | 768           | 684         | 291              | 42.54% |
| 300005.05-CP 300005.05 |                                |          |               |             |                  |        |
| Precinct TSx           | 1374                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1374                           | 1        | 163           | 144         | 65               | 45.14% |
| Precinct VBM           | 1374                           | 1        | 660           | 609         | 267              | 43.84% |
| MB Precinct            | 1374                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1374                           | 1        | 26            | 21          | 7                | 33.33% |
| Total                  | 1374                           | 5        | 849           | 774         | 339              | 43.80% |
| 300008.05-CP 300008.05 |                                |          |               |             |                  |        |
| Precinct TSx           | 1366                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1366                           | 1        | 139           | 134         | 71               | 52.99% |
| Precinct VBM           | 1366                           | 1        | 740           | 697         | 359              | 51.51% |
| MB Precinct            | 1366                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1366                           | 1        | 14            | 13          | 9                | 69.23% |
| Total                  | 1366                           | 5        | 893           | 844         | 439              | 52.01% |
| 300009.04-CP 300009.04 |                                |          |               |             |                  |        |
| Precinct TSx           | 873                            | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 873                            | 1        | 121           | 110         | 60               | 54.55% |
| Precinct VBM           | 873                            | 1        | 395           | 364         | 190              | 52.20% |
| MB Precinct            | 873                            | 1        | 0             | 0           | 0                | -      |
| Provisional            | 873                            | 1        | 13            | 11          | 5                | 45.45% |
| Total                  | 873                            | 5        | 529           | 485         | 255              | 52.58% |
| 300010.04-CP 300010.04 |                                |          |               |             |                  |        |
| Precinct TSx           | 1340                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1340                           | 1        | 171           | 160         | 84               | 52.50% |
| Precinct VBM           | 1340                           | 1        | 656           | 621         | 299              | 48.15% |
| MB Precinct            | 1340                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1340                           | 1        | 11            | 11          | 6                | 54.55% |
| Total                  | 1340                           | 5        | 838           | 792         | 389              | 49.12% |
| 300011.04-CP 300011.04 |                                |          |               |             |                  |        |
| Precinct TSx           | 1491                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1491                           | 1        | 142           | 136         | 80               | 58.82% |
| Precinct VBM           | 1491                           | 1        | 711           | 665         | 330              | 49.62% |
| MB Precinct            | 1491                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1491                           | 1        | 22            | 20          | 6                | 30.00% |
| Total                  | 1491                           | 5        | 875           | 821         | 416              | 50.67% |
| 300013.05-CP 300013.05 |                                |          |               |             |                  |        |
| Precinct TSx           | 1490                           | 1        | 3             | 3           | 2                | 66.67% |
| Precinct OS            | 1490                           | 1        | 128           | 120         | 59               | 49.17% |
| Precinct VBM           | 1490                           | 1        | 721           | 682         | 351              | 51.47% |
| MB Precinct            | 1490                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1490                           | 1        | 15            | 13          | 7                | 53.85% |
| Total                  | 1490                           | 5        | 867           | 818         | 419              | 51.22% |
| 300015.04-CP 300015.04 |                                |          |               |             |                  |        |
| Precinct TSx           | 119                            | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 119                            | 1        | 10            | 10          | 6                | 60.00% |
| Precinct VBM           | 119                            | 1        | 63            | 53          | 29               | 54.72% |
| MB Precinct            | 119                            | 1        | 0             | 0           | 0                | -      |
| Provisional            | 119                            | 1        | 4             | 4           | 0                | 0.00%  |
| Total                  | 119                            | 5        | 77            | 67          | 35               | 52.24% |
| 300015.05-CP 300015.05 |                                |          |               |             |                  |        |
| Precinct TSx           | 503                            | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 503                            | 1        | 66            | 62          | 24               | 38.71% |
| Precinct VBM           | 503                            | 1        | 266           | 241         | 96               | 39.83% |
| MB Precinct            | 503                            | 1        | 0             | 0           | 0                | -      |
| Provisional            | 503                            | 1        | 2             | 2           | 1                | 50.00% |
| Total                  | 503                            | 5        | 334           | 305         | 121              | 39.67% |
| 300017.05-CP 300017.05 |                                |          |               |             |                  |        |
| Precinct TSx           | 1240                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1240                           | 1        | 117           | 110         | 56               | 50.91% |

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|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | TED GAINES (REP) |        |
| Precinct VBM           | 1240                           | 1        | 655           | 614         | 318              | 51.79% |
| MB Precinct            | 1240                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1240                           | 1        | 9             | 8           | 4                | 50.00% |
| Total                  | 1240                           | 5        | 781           | 732         | 378              | 51.64% |
| 300018.04-CP 300018.04 |                                |          |               |             |                  |        |
| Precinct TSx           | 1306                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1306                           | 1        | 148           | 138         | 90               | 65.22% |
| Precinct VBM           | 1306                           | 1        | 505           | 475         | 232              | 48.84% |
| MB Precinct            | 1306                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1306                           | 1        | 31            | 25          | 16               | 64.00% |
| Total                  | 1306                           | 5        | 684           | 638         | 338              | 52.98% |
| 300020.04-CP 300020.04 |                                |          |               |             |                  |        |
| Precinct TSx           | 1401                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1401                           | 1        | 168           | 153         | 74               | 48.37% |
| Precinct VBM           | 1401                           | 1        | 632           | 587         | 300              | 51.11% |
| MB Precinct            | 1401                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1401                           | 1        | 14            | 13          | 10               | 76.92% |
| Total                  | 1401                           | 5        | 814           | 753         | 384              | 51.00% |
| 300021.03-CP 300021.03 |                                |          |               |             |                  |        |
| Precinct TSx           | 1234                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1234                           | 1        | 153           | 137         | 64               | 46.72% |
| Precinct VBM           | 1234                           | 1        | 614           | 573         | 288              | 50.26% |
| MB Precinct            | 1234                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1234                           | 1        | 13            | 12          | 7                | 58.33% |
| Total                  | 1234                           | 5        | 780           | 722         | 359              | 49.72% |
| 300022.05-CP 300022.05 |                                |          |               |             |                  |        |
| Precinct TSx           | 872                            | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 872                            | 1        | 101           | 93          | 57               | 61.29% |
| Precinct VBM           | 872                            | 1        | 356           | 329         | 145              | 44.07% |
| MB Precinct            | 872                            | 1        | 0             | 0           | 0                | -      |
| Provisional            | 872                            | 1        | 12            | 12          | 5                | 41.67% |
| Total                  | 872                            | 5        | 469           | 434         | 207              | 47.70% |
| 300023.03-CP 300023.03 |                                |          |               |             |                  |        |
| Precinct TSx           | 1819                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1819                           | 1        | 228           | 217         | 113              | 52.07% |
| Precinct VBM           | 1819                           | 1        | 924           | 856         | 437              | 51.05% |
| MB Precinct            | 1819                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1819                           | 1        | 26            | 22          | 11               | 50.00% |
| Total                  | 1819                           | 5        | 1178          | 1095        | 561              | 51.23% |
| 300023.05-CP 300023.05 |                                |          |               |             |                  |        |
| Precinct TSx           | 386                            | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 386                            | 1        | 45            | 44          | 23               | 52.27% |
| Precinct VBM           | 386                            | 1        | 193           | 177         | 87               | 49.15% |
| MB Precinct            | 386                            | 1        | 0             | 0           | 0                | -      |
| Provisional            | 386                            | 1        | 8             | 8           | 2                | 25.00% |
| Total                  | 386                            | 5        | 246           | 229         | 112              | 48.91% |
| 400001.14-CP 400001.14 |                                |          |               |             |                  |        |
| Precinct TSx           | 1957                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1957                           | 1        | 178           | 161         | 88               | 54.66% |
| Precinct VBM           | 1957                           | 1        | 992           | 928         | 450              | 48.49% |
| MB Precinct            | 1957                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1957                           | 1        | 23            | 22          | 11               | 50.00% |
| Total                  | 1957                           | 5        | 1193          | 1111        | 549              | 49.41% |
| 400002.08-CP 400002.08 |                                |          |               |             |                  |        |
| Precinct TSx           | 1508                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1508                           | 1        | 200           | 186         | 107              | 57.53% |
| Precinct VBM           | 1508                           | 1        | 730           | 689         | 389              | 56.46% |
| MB Precinct            | 1508                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1508                           | 1        | 15            | 15          | 8                | 53.33% |
| Total                  | 1508                           | 5        | 945           | 890         | 504              | 56.63% |
| 400003.08-CP 400003.08 |                                |          |               |             |                  |        |

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|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | TED GAINES (REP) |
| Precinct TSx           | 1800                           | 1        | 0             | 0           | 0 -              |
| Precinct OS            | 1800                           | 1        | 245           | 226         | 136 60.18%       |
| Precinct VBM           | 1800                           | 1        | 773           | 711         | 338 47.54%       |
| MB Precinct            | 1800                           | 1        | 0             | 0           | 0 -              |
| Provisional            | 1800                           | 1        | 35            | 32          | 17 53.13%        |
| Total                  | 1800                           | 5        | 1053          | 969         | 491 50.67%       |
| 400004.14-CP 400004.14 |                                |          |               |             |                  |
| Precinct TSx           | 1536                           | 1        | 0             | 0           | 0 -              |
| Precinct OS            | 1536                           | 1        | 194           | 178         | 91 51.12%        |
| Precinct VBM           | 1536                           | 1        | 662           | 608         | 298 49.01%       |
| MB Precinct            | 1536                           | 1        | 0             | 0           | 0 -              |
| Provisional            | 1536                           | 1        | 19            | 18          | 11 61.11%        |
| Total                  | 1536                           | 5        | 875           | 804         | 400 49.75%       |
| 400005.08-CP 400005.08 |                                |          |               |             |                  |
| Precinct TSx           | 655                            | 1        | 0             | 0           | 0 -              |
| Precinct OS            | 655                            | 1        | 79            | 73          | 41 56.16%        |
| Precinct VBM           | 655                            | 1        | 262           | 239         | 106 44.35%       |
| MB Precinct            | 655                            | 1        | 0             | 0           | 0 -              |
| Provisional            | 655                            | 1        | 11            | 9           | 4 44.44%         |
| Total                  | 655                            | 5        | 352           | 321         | 151 47.04%       |
| 400006.08-CP 400006.08 |                                |          |               |             |                  |
| Precinct TSx           | 1537                           | 1        | 0             | 0           | 0 -              |
| Precinct OS            | 1537                           | 1        | 228           | 214         | 110 51.40%       |
| Precinct VBM           | 1537                           | 1        | 722           | 660         | 305 46.21%       |
| MB Precinct            | 1537                           | 1        | 0             | 0           | 0 -              |
| Provisional            | 1537                           | 1        | 19            | 15          | 9 60.00%         |
| Total                  | 1537                           | 5        | 969           | 889         | 424 47.69%       |
| 400007.08-CP 400007.08 |                                |          |               |             |                  |
| Precinct TSx           | 1766                           | 1        | 0             | 0           | 0 -              |
| Precinct OS            | 1766                           | 1        | 228           | 214         | 127 59.35%       |
| Precinct VBM           | 1766                           | 1        | 765           | 722         | 401 55.54%       |
| MB Precinct            | 1766                           | 1        | 0             | 0           | 0 -              |
| Provisional            | 1766                           | 1        | 22            | 17          | 5 29.41%         |
| Total                  | 1766                           | 5        | 1015          | 953         | 533 55.93%       |
| 400010.08-CP 400010.08 |                                |          |               |             |                  |
| Precinct TSx           | 1875                           | 1        | 0             | 0           | 0 -              |
| Precinct OS            | 1875                           | 1        | 244           | 225         | 102 45.33%       |
| Precinct VBM           | 1875                           | 1        | 869           | 825         | 375 45.45%       |
| MB Precinct            | 1875                           | 1        | 0             | 0           | 0 -              |
| Provisional            | 1875                           | 1        | 31            | 26          | 9 34.62%         |
| Total                  | 1875                           | 5        | 1144          | 1076        | 486 45.17%       |
| 400012.08-CP 400012.08 |                                |          |               |             |                  |
| Precinct TSx           | 877                            | 1        | 0             | 0           | 0 -              |
| Precinct OS            | 877                            | 1        | 113           | 108         | 40 37.04%        |
| Precinct VBM           | 877                            | 1        | 434           | 402         | 196 48.76%       |
| MB Precinct            | 877                            | 1        | 0             | 0           | 0 -              |
| Provisional            | 877                            | 1        | 22            | 17          | 11 64.71%        |
| Total                  | 877                            | 5        | 569           | 527         | 247 46.87%       |
| 400013.08-CP 400013.08 |                                |          |               |             |                  |
| Precinct TSx           | 758                            | 1        | 0             | 0           | 0 -              |
| Precinct OS            | 758                            | 1        | 127           | 117         | 57 48.72%        |
| Precinct VBM           | 758                            | 1        | 315           | 287         | 133 46.34%       |
| MB Precinct            | 758                            | 1        | 0             | 0           | 0 -              |
| Provisional            | 758                            | 1        | 4             | 3           | 2 66.67%         |
| Total                  | 758                            | 5        | 446           | 407         | 192 47.17%       |
| 400014.08-CP 400014.08 |                                |          |               |             |                  |
| Precinct TSx           | 1526                           | 1        | 0             | 0           | 0 -              |
| Precinct OS            | 1526                           | 1        | 230           | 212         | 117 55.19%       |
| Precinct VBM           | 1526                           | 1        | 736           | 691         | 345 49.93%       |
| MB Precinct            | 1526                           | 1        | 0             | 0           | 0 -              |
| Provisional            | 1526                           | 1        | 36            | 34          | 16 47.06%        |

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|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | TED GAINES (REP) |        |
| Total                  | 1526                           | 5        | 1002          | 937         | 478              | 51.01% |
| 400017.08-CP 400017.08 |                                |          |               |             |                  |        |
| Precinct TSx           | 1495                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1495                           | 1        | 175           | 162         | 98               | 60.49% |
| Precinct VBM           | 1495                           | 1        | 752           | 708         | 383              | 54.10% |
| MB Precinct            | 1495                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1495                           | 1        | 28            | 27          | 15               | 55.56% |
| Total                  | 1495                           | 5        | 955           | 897         | 496              | 55.30% |
| 400018.08-CP 400018.08 |                                |          |               |             |                  |        |
| Precinct TSx           | 597                            | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 597                            | 1        | 86            | 79          | 51               | 64.56% |
| Precinct VBM           | 597                            | 1        | 276           | 245         | 162              | 66.12% |
| MB Precinct            | 597                            | 1        | 0             | 0           | 0                | -      |
| Provisional            | 597                            | 1        | 7             | 5           | 3                | 60.00% |
| Total                  | 597                            | 5        | 369           | 329         | 216              | 65.65% |
| 400020.08-CP 400020.08 |                                |          |               |             |                  |        |
| Precinct TSx           | 724                            | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 724                            | 1        | 114           | 105         | 66               | 62.86% |
| Precinct VBM           | 724                            | 1        | 343           | 326         | 155              | 47.55% |
| MB Precinct            | 724                            | 1        | 0             | 0           | 0                | -      |
| Provisional            | 724                            | 1        | 2             | 2           | 0                | 0.00%  |
| Total                  | 724                            | 5        | 459           | 433         | 221              | 51.04% |
| 400021.07-CP 400021.07 |                                |          |               |             |                  |        |
| Precinct TSx           | 1332                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1332                           | 1        | 127           | 118         | 76               | 64.41% |
| Precinct VBM           | 1332                           | 1        | 767           | 721         | 393              | 54.51% |
| MB Precinct            | 1332                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1332                           | 1        | 18            | 18          | 8                | 44.44% |
| Total                  | 1332                           | 5        | 912           | 857         | 477              | 55.66% |
| 500001.10-CP 500001.10 |                                |          |               |             |                  |        |
| Precinct TSx           | 1717                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1717                           | 1        | 210           | 184         | 59               | 32.07% |
| Precinct VBM           | 1717                           | 1        | 617           | 553         | 201              | 36.35% |
| MB Precinct            | 1717                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1717                           | 1        | 19            | 13          | 1                | 7.69%  |
| Total                  | 1717                           | 5        | 846           | 750         | 261              | 34.80% |
| 500002.10-CP 500002.10 |                                |          |               |             |                  |        |
| Precinct TSx           | 1381                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1381                           | 1        | 137           | 119         | 37               | 31.09% |
| Precinct VBM           | 1381                           | 1        | 412           | 373         | 92               | 24.66% |
| MB Precinct            | 1381                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1381                           | 1        | 31            | 27          | 5                | 18.52% |
| Total                  | 1381                           | 5        | 580           | 519         | 134              | 25.82% |
| 500003.10-CP 500003.10 |                                |          |               |             |                  |        |
| Precinct TSx           | 1065                           | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1065                           | 1        | 131           | 113         | 34               | 30.09% |
| Precinct VBM           | 1065                           | 1        | 346           | 308         | 83               | 26.95% |
| MB Precinct            | 1065                           | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1065                           | 1        | 11            | 5           | 2                | 40.00% |
| Total                  | 1065                           | 5        | 488           | 426         | 119              | 27.93% |
| 500004.10-CP 500004.10 |                                |          |               |             |                  |        |
| Precinct TSx           | 981                            | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 981                            | 1        | 122           | 108         | 31               | 28.70% |
| Precinct VBM           | 981                            | 1        | 264           | 230         | 71               | 30.87% |
| MB Precinct            | 981                            | 1        | 0             | 0           | 0                | -      |
| Provisional            | 981                            | 1        | 21            | 18          | 5                | 27.78% |
| Total                  | 981                            | 5        | 407           | 356         | 107              | 30.06% |
| 500005.10-CP 500005.10 |                                |          |               |             |                  |        |
| Precinct TSx           | 946                            | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 946                            | 1        | 107           | 97          | 21               | 21.65% |
| Precinct VBM           | 946                            | 1        | 250           | 224         | 54               | 24.11% |

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|------------------------|--------------------------------|----------|---------------|-------------|------------------|---------|--|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | TED GAINES (REP) |         |  |
| MB Precinct            | 946                            | 1        | 0             | 0           | 0                | -       |  |
| Provisional            | 946                            | 1        | 16            | 12          | 2                | 16.67%  |  |
| Total                  | 946                            | 5        | 373           | 333         | 77               | 23.12%  |  |
| 500006.10-CP 500006.10 |                                |          |               |             |                  |         |  |
| Precinct TSx           | 1661                           | 1        | 0             | 0           | 0                | -       |  |
| Precinct OS            | 1661                           | 1        | 190           | 165         | 43               | 26.06%  |  |
| Precinct VBM           | 1661                           | 1        | 498           | 439         | 112              | 25.51%  |  |
| MB Precinct            | 1661                           | 1        | 0             | 0           | 0                | -       |  |
| Provisional            | 1661                           | 1        | 58            | 48          | 13               | 27.08%  |  |
| Total                  | 1661                           | 5        | 746           | 652         | 168              | 25.77%  |  |
| 500007.10-CP 500007.10 |                                |          |               |             |                  |         |  |
| Precinct TSx           | 1788                           | 1        | 0             | 0           | 0                | -       |  |
| Precinct OS            | 1788                           | 1        | 208           | 186         | 73               | 39.25%  |  |
| Precinct VBM           | 1788                           | 1        | 688           | 621         | 242              | 38.97%  |  |
| MB Precinct            | 1788                           | 1        | 0             | 0           | 0                | -       |  |
| Provisional            | 1788                           | 1        | 20            | 18          | 6                | 33.33%  |  |
| Total                  | 1788                           | 5        | 916           | 825         | 321              | 38.91%  |  |
| 500009.10-CP 500009.10 |                                |          |               |             |                  |         |  |
| Precinct TSx           | 1073                           | 1        | 0             | 0           | 0                | -       |  |
| Precinct OS            | 1073                           | 1        | 121           | 105         | 18               | 17.14%  |  |
| Precinct VBM           | 1073                           | 1        | 304           | 259         | 73               | 28.19%  |  |
| MB Precinct            | 1073                           | 1        | 0             | 0           | 0                | -       |  |
| Provisional            | 1073                           | 1        | 38            | 30          | 6                | 20.00%  |  |
| Total                  | 1073                           | 5        | 463           | 394         | 97               | 24.62%  |  |
| 500011.10-CP 500011.10 |                                |          |               |             |                  |         |  |
| Precinct TSx           | 1635                           | 1        | 0             | 0           | 0                | -       |  |
| Precinct OS            | 1635                           | 1        | 178           | 161         | 41               | 25.47%  |  |
| Precinct VBM           | 1635                           | 1        | 380           | 350         | 90               | 25.71%  |  |
| MB Precinct            | 1635                           | 1        | 0             | 0           | 0                | -       |  |
| Provisional            | 1635                           | 1        | 40            | 30          | 5                | 16.67%  |  |
| Total                  | 1635                           | 5        | 598           | 541         | 136              | 25.14%  |  |
| 500012.10-CP 500012.10 |                                |          |               |             |                  |         |  |
| Precinct TSx           | 1574                           | 1        | 1             | 1           | 1                | 100.00% |  |
| Precinct OS            | 1574                           | 1        | 195           | 167         | 42               | 25.15%  |  |
| Precinct VBM           | 1574                           | 1        | 601           | 554         | 195              | 35.20%  |  |
| MB Precinct            | 1574                           | 1        | 0             | 0           | 0                | -       |  |
| Provisional            | 1574                           | 1        | 37            | 28          | 4                | 14.29%  |  |
| Total                  | 1574                           | 5        | 834           | 750         | 242              | 32.27%  |  |
| 500015.10-CP 500015.10 |                                |          |               |             |                  |         |  |
| Precinct TSx           | 978                            | 1        | 0             | 0           | 0                | -       |  |
| Precinct OS            | 978                            | 1        | 105           | 97          | 24               | 24.74%  |  |
| Precinct VBM           | 978                            | 1        | 407           | 374         | 120              | 32.09%  |  |
| MB Precinct            | 978                            | 1        | 0             | 0           | 0                | -       |  |
| Provisional            | 978                            | 1        | 13            | 9           | 2                | 22.22%  |  |
| Total                  | 978                            | 5        | 525           | 480         | 146              | 30.42%  |  |
| 500020.10-CP 500020.10 |                                |          |               |             |                  |         |  |
| Precinct TSx           | 1530                           | 1        | 0             | 0           | 0                | -       |  |
| Precinct OS            | 1530                           | 1        | 161           | 146         | 70               | 47.95%  |  |
| Precinct VBM           | 1530                           | 1        | 659           | 625         | 295              | 47.20%  |  |
| MB Precinct            | 1530                           | 1        | 0             | 0           | 0                | -       |  |
| Provisional            | 1530                           | 1        | 27            | 26          | 12               | 46.15%  |  |
| Total                  | 1530                           | 5        | 847           | 797         | 377              | 47.30%  |  |
| 500021.10-CP 500021.10 |                                |          |               |             |                  |         |  |
| Precinct TSx           | 1404                           | 1        | 0             | 0           | 0                | -       |  |
| Precinct OS            | 1404                           | 1        | 169           | 154         | 87               | 56.49%  |  |
| Precinct VBM           | 1404                           | 1        | 632           | 598         | 312              | 52.17%  |  |
| MB Precinct            | 1404                           | 1        | 0             | 0           | 0                | -       |  |
| Provisional            | 1404                           | 1        | 28            | 22          | 8                | 36.36%  |  |
| Total                  | 1404                           | 5        | 829           | 774         | 407              | 52.58%  |  |
| 95U201.01-MB 95U201.01 |                                |          |               |             |                  |         |  |
| Precinct TSx           | 8                              | 1        | 0             | 0           | 0                | -       |  |



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|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | TED GAINES (REP) |         |
| Precinct OS            | 8                              | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 8                              | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 8                              | 1        | 7             | 7           | 5                | 71.43%  |
| Provisional            | 8                              | 1        | 0             | 0           | 0                | -       |
| Total                  | 8                              | 5        | 7             | 7           | 5                | 71.43%  |
| 95U201.02-MB 95U201.02 |                                |          |               |             |                  |         |
| Precinct TSx           | 541                            | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 541                            | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 541                            | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 541                            | 1        | 358           | 323         | 179              | 55.42%  |
| Provisional            | 541                            | 1        | 1             | 1           | 1                | 100.00% |
| Total                  | 541                            | 5        | 359           | 324         | 180              | 55.56%  |
| 95U202.02-MB 95U202.02 |                                |          |               |             |                  |         |
| Precinct TSx           | 859                            | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 859                            | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 859                            | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 859                            | 1        | 574           | 537         | 253              | 47.11%  |
| Provisional            | 859                            | 1        | 4             | 3           | 1                | 33.33%  |
| Total                  | 859                            | 5        | 578           | 540         | 254              | 47.04%  |
| 95U203.02-MB 95U203.02 |                                |          |               |             |                  |         |
| Precinct TSx           | 0                              | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 0                              | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 0                              | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 0                              | 1        | 0             | 0           | 0                | -       |
| Provisional            | 0                              | 1        | 0             | 0           | 0                | -       |
| Total                  | 0                              | 5        | 0             | 0           | 0                | -       |
| 95U204.01-MB 95U204.01 |                                |          |               |             |                  |         |
| Precinct TSx           | 895                            | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 895                            | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 895                            | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 895                            | 1        | 651           | 613         | 322              | 52.53%  |
| Provisional            | 895                            | 1        | 2             | 2           | 2                | 100.00% |
| Total                  | 895                            | 5        | 653           | 615         | 324              | 52.68%  |
| 95U301.05-MB 95U301.05 |                                |          |               |             |                  |         |
| Precinct TSx           | 244                            | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 244                            | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 244                            | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 244                            | 1        | 151           | 141         | 78               | 55.32%  |
| Provisional            | 244                            | 1        | 2             | 1           | 1                | 100.00% |
| Total                  | 244                            | 5        | 153           | 142         | 79               | 55.63%  |
| 95U302.04-MB 95U302.04 |                                |          |               |             |                  |         |
| Precinct TSx           | 0                              | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 0                              | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 0                              | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 0                              | 1        | 1             | 1           | 0                | 0.00%   |
| Provisional            | 0                              | 1        | 0             | 0           | 0                | -       |
| Total                  | 0                              | 5        | 1             | 1           | 0                | 0.00%   |
| 95U302.05-MB 95U302.05 |                                |          |               |             |                  |         |
| Precinct TSx           | 128                            | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 128                            | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 128                            | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 128                            | 1        | 86            | 81          | 42               | 51.85%  |
| Provisional            | 128                            | 1        | 0             | 0           | 0                | -       |
| Total                  | 128                            | 5        | 86            | 81          | 42               | 51.85%  |
| 95U303.03-MB 95U303.03 |                                |          |               |             |                  |         |
| Precinct TSx           | 144                            | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 144                            | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 144                            | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 144                            | 1        | 84            | 81          | 42               | 51.85%  |
| Provisional            | 144                            | 1        | 0             | 0           | 0                | -       |
| Total                  | 144                            | 5        | 84            | 81          | 42               | 51.85%  |

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|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | TED GAINES (REP) |         |
| 95U303.05-MB 95U303.05 |                                |          |               |             |                  |         |
| Precinct TSx           | 159                            | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 159                            | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 159                            | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 159                            | 1        | 111           | 100         | 60               | 60.00%  |
| Provisional            | 159                            | 1        | 1             | 1           | 1                | 100.00% |
| Total                  | 159                            | 5        | 112           | 101         | 61               | 60.40%  |
| 95U304.05-MB 95U304.05 |                                |          |               |             |                  |         |
| Precinct TSx           | 0                              | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 0                              | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 0                              | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 0                              | 1        | 0             | 0           | 0                | -       |
| Provisional            | 0                              | 1        | 0             | 0           | 0                | -       |
| Total                  | 0                              | 5        | 0             | 0           | 0                | -       |
| 95U305.05-MB 95U305.05 |                                |          |               |             |                  |         |
| Precinct TSx           | 7                              | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 7                              | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 7                              | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 7                              | 1        | 7             | 7           | 3                | 42.86%  |
| Provisional            | 7                              | 1        | 0             | 0           | 0                | -       |
| Total                  | 7                              | 5        | 7             | 7           | 3                | 42.86%  |
| 95U401.08-MB 95U401.08 |                                |          |               |             |                  |         |
| Precinct TSx           | 39                             | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 39                             | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 39                             | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 39                             | 1        | 16            | 14          | 9                | 64.29%  |
| Provisional            | 39                             | 1        | 0             | 0           | 0                | -       |
| Total                  | 39                             | 5        | 16            | 14          | 9                | 64.29%  |
| 95U402.07-MB 95U402.07 |                                |          |               |             |                  |         |
| Precinct TSx           | 413                            | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 413                            | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 413                            | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 413                            | 1        | 256           | 241         | 133              | 55.19%  |
| Provisional            | 413                            | 1        | 1             | 1           | 0                | 0.00%   |
| Total                  | 413                            | 5        | 257           | 242         | 133              | 54.96%  |
| 95U402.08-MB 95U402.08 |                                |          |               |             |                  |         |
| Precinct TSx           | 873                            | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 873                            | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 873                            | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 873                            | 1        | 566           | 525         | 277              | 52.76%  |
| Provisional            | 873                            | 1        | 3             | 3           | 1                | 33.33%  |
| Total                  | 873                            | 5        | 569           | 528         | 278              | 52.65%  |
| 95U403.08-MB 95U403.08 |                                |          |               |             |                  |         |
| Precinct TSx           | 1591                           | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 1591                           | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 1591                           | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 1591                           | 1        | 1042          | 981         | 432              | 44.04%  |
| Provisional            | 1591                           | 1        | 13            | 13          | 6                | 46.15%  |
| Total                  | 1591                           | 5        | 1055          | 994         | 438              | 44.06%  |
| 95U404.06-MB 95U404.06 |                                |          |               |             |                  |         |
| Precinct TSx           | 0                              | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 0                              | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 0                              | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 0                              | 1        | 0             | 0           | 0                | -       |
| Provisional            | 0                              | 1        | 0             | 0           | 0                | -       |
| Total                  | 0                              | 5        | 0             | 0           | 0                | -       |
| 95U404.08-MB 95U404.08 |                                |          |               |             |                  |         |
| Precinct TSx           | 0                              | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 0                              | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 0                              | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 0                              | 1        | 0             | 0           | 0                | -       |

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|                               | Reg. Voters                    | Vote For | Times Counted | Total Votes | TED GAINES (REP) |         |
| Provisional                   | 0                              | 1        | 0             | 0           | 0                | -       |
| Total                         | 0                              | 5        | 0             | 0           | 0                | -       |
| 95U501.09-MB 95U501.09        |                                |          |               |             |                  |         |
| Precinct TSx                  | 0                              | 1        | 0             | 0           | 0                | -       |
| Precinct OS                   | 0                              | 1        | 0             | 0           | 0                | -       |
| Precinct VBM                  | 0                              | 1        | 0             | 0           | 0                | -       |
| MB Precinct                   | 0                              | 1        | 2             | 2           | 0                | 0.00%   |
| Provisional                   | 0                              | 1        | 0             | 0           | 0                | -       |
| Total                         | 0                              | 5        | 2             | 2           | 0                | 0.00%   |
| 95U501.10-MB 95U501.10        |                                |          |               |             |                  |         |
| Precinct TSx                  | 286                            | 1        | 0             | 0           | 0                | -       |
| Precinct OS                   | 286                            | 1        | 0             | 0           | 0                | -       |
| Precinct VBM                  | 286                            | 1        | 0             | 0           | 0                | -       |
| MB Precinct                   | 286                            | 1        | 172           | 161         | 83               | 51.55%  |
| Provisional                   | 286                            | 1        | 2             | 2           | 1                | 50.00%  |
| Total                         | 286                            | 5        | 174           | 163         | 84               | 51.53%  |
| 95U598.10-MB 95U598.10        |                                |          |               |             |                  |         |
| Precinct TSx                  | 568                            | 1        | 0             | 0           | 0                | -       |
| Precinct OS                   | 568                            | 1        | 0             | 0           | 0                | -       |
| Precinct VBM                  | 568                            | 1        | 0             | 0           | 0                | -       |
| MB Precinct                   | 568                            | 1        | 239           | 205         | 52               | 25.37%  |
| Provisional                   | 568                            | 1        | 3             | 1           | 1                | 100.00% |
| Total                         | 568                            | 5        | 242           | 206         | 53               | 25.73%  |
| 95U599.10-MB 95U599.10        |                                |          |               |             |                  |         |
| Precinct TSx                  | 75                             | 1        | 0             | 0           | 0                | -       |
| Precinct OS                   | 75                             | 1        | 0             | 0           | 0                | -       |
| Precinct VBM                  | 75                             | 1        | 0             | 0           | 0                | -       |
| MB Precinct                   | 75                             | 1        | 38            | 33          | 11               | 33.33%  |
| Provisional                   | 75                             | 1        | 0             | 0           | 0                | -       |
| Total                         | 75                             | 5        | 38            | 33          | 11               | 33.33%  |
| 96U101.11-MB 96U101.11        |                                |          |               |             |                  |         |
| Precinct TSx                  | 626                            | 1        | 0             | 0           | 0                | -       |
| Precinct OS                   | 626                            | 1        | 0             | 0           | 0                | -       |
| Precinct VBM                  | 626                            | 1        | 0             | 0           | 0                | -       |
| MB Precinct                   | 626                            | 1        | 423           | 394         | 207              | 52.54%  |
| Provisional                   | 626                            | 1        | 6             | 6           | 1                | 16.67%  |
| Total                         | 626                            | 5        | 429           | 400         | 208              | 52.00%  |
| 96U401.14-MB 96U401.14        |                                |          |               |             |                  |         |
| Precinct TSx                  | 566                            | 1        | 0             | 0           | 0                | -       |
| Precinct OS                   | 566                            | 1        | 0             | 0           | 0                | -       |
| Precinct VBM                  | 566                            | 1        | 0             | 0           | 0                | -       |
| MB Precinct                   | 566                            | 1        | 359           | 325         | 162              | 49.85%  |
| Provisional                   | 566                            | 1        | 6             | 6           | 1                | 16.67%  |
| Total                         | 566                            | 5        | 365           | 331         | 163              | 49.24%  |
| Total                         |                                |          |               |             |                  |         |
| Precinct TSx                  | 109479                         | 102      | 8             | 8           | 6                | 75.00%  |
| Precinct OS                   | 109479                         | 102      | 12009         | 10999       | 5392             | 49.02%  |
| Precinct VBM                  | 109479                         | 102      | 45535         | 42084       | 20274            | 48.18%  |
| MB Precinct                   | 109479                         | 102      | 5143          | 4772        | 2350             | 49.25%  |
| Provisional                   | 109479                         | 102      | 1584          | 1371        | 625              | 45.59%  |
| Total                         | 109479                         | 510      | 64279         | 59234       | 28647            | 48.36%  |
| STATE BOARD OF EQUALIZATION   |                                |          |               |             |                  |         |
| STATE BOARD OF EQUALIZATION 1 |                                |          |               |             |                  |         |
| Precinct TSx                  | 109479                         | 102      | 8             | 8           | 6                | 75.00%  |
| Precinct OS                   | 109479                         | 102      | 12009         | 10999       | 5392             | 49.02%  |
| Precinct VBM                  | 109479                         | 102      | 45535         | 42084       | 20274            | 48.18%  |
| MB Precinct                   | 109479                         | 102      | 5143          | 4772        | 2350             | 49.25%  |
| Provisional                   | 109479                         | 102      | 1584          | 1371        | 625              | 45.59%  |
| Total                         | 109479                         | 510      | 64279         | 59234       | 28647            | 48.36%  |
| Total                         |                                |          |               |             |                  |         |

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|                                     | Reg. Voters                    | Vote For   | Times Counted | Total Votes  | TED GAINES (REP)    |
| Precinct TSx                        | 109479                         | 102        | 8             | 8            | 6 75.00%            |
| Precinct OS                         | 109479                         | 102        | 12009         | 10999        | 5392 49.02%         |
| Precinct VBM                        | 109479                         | 102        | 45535         | 42084        | 20274 48.18%        |
| MB Precinct                         | 109479                         | 102        | 5143          | 4772         | 2350 49.25%         |
| Provisional                         | 109479                         | 102        | 1584          | 1371         | 625 45.59%          |
| <b>Total</b>                        | <b>109479</b>                  | <b>510</b> | <b>64279</b>  | <b>59234</b> | <b>28647 48.36%</b> |
| <b>UNITED STATES SENATE</b>         |                                |            |               |              |                     |
| <b>UNITED STATES SENATE</b>         |                                |            |               |              |                     |
| Precinct TSx                        | 109479                         | 102        | 8             | 8            | 6 75.00%            |
| Precinct OS                         | 109479                         | 102        | 12009         | 10999        | 5392 49.02%         |
| Precinct VBM                        | 109479                         | 102        | 45535         | 42084        | 20274 48.18%        |
| MB Precinct                         | 109479                         | 102        | 5143          | 4772         | 2350 49.25%         |
| Provisional                         | 109479                         | 102        | 1584          | 1371         | 625 45.59%          |
| <b>Total</b>                        | <b>109479</b>                  | <b>510</b> | <b>64279</b>  | <b>59234</b> | <b>28647 48.36%</b> |
| <b>Total</b>                        |                                |            |               |              |                     |
| Precinct TSx                        | 109479                         | 102        | 8             | 8            | 6 75.00%            |
| Precinct OS                         | 109479                         | 102        | 12009         | 10999        | 5392 49.02%         |
| Precinct VBM                        | 109479                         | 102        | 45535         | 42084        | 20274 48.18%        |
| MB Precinct                         | 109479                         | 102        | 5143          | 4772         | 2350 49.25%         |
| Provisional                         | 109479                         | 102        | 1584          | 1371         | 625 45.59%          |
| <b>Total</b>                        | <b>109479</b>                  | <b>510</b> | <b>64279</b>  | <b>59234</b> | <b>28647 48.36%</b> |
| <b>Total</b>                        |                                |            |               |              |                     |
| <b>UNITED STATES REPRESENTATIVE</b> |                                |            |               |              |                     |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                                |            |               |              |                     |
| Precinct TSx                        | 109479                         | 102        | 8             | 8            | 6 75.00%            |
| Precinct OS                         | 109479                         | 102        | 12009         | 10999        | 5392 49.02%         |
| Precinct VBM                        | 109479                         | 102        | 45535         | 42084        | 20274 48.18%        |
| MB Precinct                         | 109479                         | 102        | 5143          | 4772         | 2350 49.25%         |
| Provisional                         | 109479                         | 102        | 1584          | 1371         | 625 45.59%          |
| <b>Total</b>                        | <b>109479</b>                  | <b>510</b> | <b>64279</b>  | <b>59234</b> | <b>28647 48.36%</b> |
| <b>Total</b>                        |                                |            |               |              |                     |
| Precinct TSx                        | 109479                         | 102        | 8             | 8            | 6 75.00%            |
| Precinct OS                         | 109479                         | 102        | 12009         | 10999        | 5392 49.02%         |
| Precinct VBM                        | 109479                         | 102        | 45535         | 42084        | 20274 48.18%        |
| MB Precinct                         | 109479                         | 102        | 5143          | 4772         | 2350 49.25%         |
| Provisional                         | 109479                         | 102        | 1584          | 1371         | 625 45.59%          |
| <b>Total</b>                        | <b>109479</b>                  | <b>510</b> | <b>64279</b>  | <b>59234</b> | <b>28647 48.36%</b> |
| <b>Total</b>                        |                                |            |               |              |                     |
| <b>STATE SENATE</b>                 |                                |            |               |              |                     |
| <b>1ST STATE SENATE DISTRICT</b>    |                                |            |               |              |                     |
| Precinct TSx                        | 109479                         | 102        | 8             | 8            | 6 75.00%            |
| Precinct OS                         | 109479                         | 102        | 12009         | 10999        | 5392 49.02%         |
| Precinct VBM                        | 109479                         | 102        | 45535         | 42084        | 20274 48.18%        |
| MB Precinct                         | 109479                         | 102        | 5143          | 4772         | 2350 49.25%         |
| Provisional                         | 109479                         | 102        | 1584          | 1371         | 625 45.59%          |
| <b>Total</b>                        | <b>109479</b>                  | <b>510</b> | <b>64279</b>  | <b>59234</b> | <b>28647 48.36%</b> |
| <b>Total</b>                        |                                |            |               |              |                     |
| Precinct TSx                        | 109479                         | 102        | 8             | 8            | 6 75.00%            |
| Precinct OS                         | 109479                         | 102        | 12009         | 10999        | 5392 49.02%         |
| Precinct VBM                        | 109479                         | 102        | 45535         | 42084        | 20274 48.18%        |
| MB Precinct                         | 109479                         | 102        | 5143          | 4772         | 2350 49.25%         |
| Provisional                         | 109479                         | 102        | 1584          | 1371         | 625 45.59%          |
| <b>Total</b>                        | <b>109479</b>                  | <b>510</b> | <b>64279</b>  | <b>59234</b> | <b>28647 48.36%</b> |
| <b>Total</b>                        |                                |            |               |              |                     |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                                |            |               |              |                     |
| <b>5TH STATE ASSEMBLY DIST</b>      |                                |            |               |              |                     |
| Precinct TSx                        | 70961                          | 75         | 4             | 4            | 3 75.00%            |
| Precinct OS                         | 70961                          | 75         | 7627          | 6985         | 3344 47.87%         |
| Precinct VBM                        | 70961                          | 75         | 28225         | 26120        | 12094 46.30%        |
| MB Precinct                         | 70961                          | 75         | 4361          | 4053         | 1981 48.88%         |

**COUNTY OF EL DORADO**  
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|                                   | State Senator 1st State Senate |          |               |             |                  |         |
|-----------------------------------|--------------------------------|----------|---------------|-------------|------------------|---------|
|                                   | Reg. Voters                    | Vote For | Times Counted | Total Votes | TED GAINES (REP) |         |
| Provisional                       | 70961                          | 75       | 1038          | 887         | 372              | 41.94%  |
| Total                             | 70961                          | 375      | 41255         | 38049       | 17794            | 46.77%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |                                |          |               |             |                  |         |
| Precinct TSx                      | 38518                          | 27       | 4             | 4           | 3                | 75.00%  |
| Precinct OS                       | 38518                          | 27       | 4382          | 4014        | 2048             | 51.02%  |
| Precinct VBM                      | 38518                          | 27       | 17310         | 15964       | 8180             | 51.24%  |
| MB Precinct                       | 38518                          | 27       | 782           | 719         | 369              | 51.32%  |
| Provisional                       | 38518                          | 27       | 546           | 484         | 253              | 52.27%  |
| Total                             | 38518                          | 135      | 23024         | 21185       | 10853            | 51.23%  |
| <b>Total</b>                      |                                |          |               |             |                  |         |
| Precinct TSx                      | 109479                         | 102      | 8             | 8           | 6                | 75.00%  |
| Precinct OS                       | 109479                         | 102      | 12009         | 10999       | 5392             | 49.02%  |
| Precinct VBM                      | 109479                         | 102      | 45535         | 42084       | 20274            | 48.18%  |
| MB Precinct                       | 109479                         | 102      | 5143          | 4772        | 2350             | 49.25%  |
| Provisional                       | 109479                         | 102      | 1584          | 1371        | 625              | 45.59%  |
| Total                             | 109479                         | 510      | 64279         | 59234       | 28647            | 48.36%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |                                |          |               |             |                  |         |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                                |          |               |             |                  |         |
| Precinct TSx                      | 22587                          | 14       | 0             | 0           | 0                | -       |
| Precinct OS                       | 22587                          | 14       | 2530          | 2303        | 1172             | 50.89%  |
| Precinct VBM                      | 22587                          | 14       | 10411         | 9606        | 4975             | 51.79%  |
| MB Precinct                       | 22587                          | 14       | 423           | 394         | 207              | 52.54%  |
| Provisional                       | 22587                          | 14       | 301           | 266         | 143              | 53.76%  |
| Total                             | 22587                          | 70       | 13665         | 12569       | 6497             | 51.69%  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                                |          |               |             |                  |         |
| Precinct TSx                      | 23114                          | 23       | 4             | 4           | 3                | 75.00%  |
| Precinct OS                       | 23114                          | 23       | 2509          | 2335        | 1233             | 52.81%  |
| Precinct VBM                      | 23114                          | 23       | 9647          | 8904        | 4475             | 50.26%  |
| MB Precinct                       | 23114                          | 23       | 1590          | 1480        | 759              | 51.28%  |
| Provisional                       | 23114                          | 23       | 327           | 288         | 156              | 54.17%  |
| Total                             | 23114                          | 115      | 14077         | 13011       | 6626             | 50.93%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                                |          |               |             |                  |         |
| Precinct TSx                      | 21691                          | 26       | 3             | 3           | 2                | 66.67%  |
| Precinct OS                       | 21691                          | 26       | 2368          | 2181        | 1100             | 50.44%  |
| Precinct VBM                      | 21691                          | 26       | 10021         | 9304        | 4455             | 47.88%  |
| MB Precinct                       | 21691                          | 26       | 440           | 411         | 225              | 54.74%  |
| Provisional                       | 21691                          | 26       | 277           | 245         | 116              | 47.35%  |
| Total                             | 21691                          | 130      | 13109         | 12144       | 5898             | 48.57%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                                |          |               |             |                  |         |
| Precinct TSx                      | 23425                          | 22       | 0             | 0           | 0                | -       |
| Precinct OS                       | 23425                          | 22       | 2568          | 2378        | 1307             | 54.96%  |
| Precinct VBM                      | 23425                          | 22       | 9398          | 8762        | 4429             | 50.55%  |
| MB Precinct                       | 23425                          | 22       | 2239          | 2086        | 1013             | 48.56%  |
| Provisional                       | 23425                          | 22       | 315           | 283         | 137              | 48.41%  |
| Total                             | 23425                          | 110      | 14520         | 13509       | 6886             | 50.97%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                                |          |               |             |                  |         |
| Precinct TSx                      | 18662                          | 17       | 1             | 1           | 1                | 100.00% |
| Precinct OS                       | 18662                          | 17       | 2034          | 1802        | 580              | 32.19%  |
| Precinct VBM                      | 18662                          | 17       | 6058          | 5508        | 1940             | 35.22%  |
| MB Precinct                       | 18662                          | 17       | 451           | 401         | 146              | 36.41%  |
| Provisional                       | 18662                          | 17       | 364           | 289         | 73               | 25.26%  |
| Total                             | 18662                          | 85       | 8908          | 8001        | 2740             | 34.25%  |
| <b>Total</b>                      |                                |          |               |             |                  |         |
| Precinct TSx                      | 109479                         | 102      | 8             | 8           | 6                | 75.00%  |
| Precinct OS                       | 109479                         | 102      | 12009         | 10999       | 5392             | 49.02%  |
| Precinct VBM                      | 109479                         | 102      | 45535         | 42084       | 20274            | 48.18%  |
| MB Precinct                       | 109479                         | 102      | 5143          | 4772        | 2350             | 49.25%  |
| Provisional                       | 109479                         | 102      | 1584          | 1371        | 625              | 45.59%  |
| Total                             | 109479                         | 510      | 64279         | 59234       | 28647            | 48.36%  |

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|---------------------------------|--------------------------------|------------|---------------|--------------|---------------------|
|                                 | Reg. Voters                    | Vote For   | Times Counted | Total Votes  | TED GAINES (REP)    |
| <b>CITIES</b>                   |                                |            |               |              |                     |
| <b>CITY OF PLACERVILLE</b>      |                                |            |               |              |                     |
| Precinct TSx                    | 5569                           | 5          | 0             | 0            | 0 -                 |
| Precinct OS                     | 5569                           | 5          | 631           | 557          | 239 42.91%          |
| Precinct VBM                    | 5569                           | 5          | 2590          | 2370         | 994 41.94%          |
| MB Precinct                     | 5569                           | 5          | 0             | 0            | 0 -                 |
| Provisional                     | 5569                           | 5          | 80            | 69           | 25 36.23%           |
| <b>Total</b>                    | <b>5569</b>                    | <b>25</b>  | <b>3301</b>   | <b>2996</b>  | <b>1258 41.99%</b>  |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                                |            |               |              |                     |
| Precinct TSx                    | 9465                           | 7          | 0             | 0            | 0 -                 |
| Precinct OS                     | 9465                           | 7          | 1063          | 941          | 264 28.06%          |
| Precinct VBM                    | 9465                           | 7          | 2796          | 2496         | 734 29.41%          |
| MB Precinct                     | 9465                           | 7          | 0             | 0            | 0 -                 |
| Provisional                     | 9465                           | 7          | 224           | 183          | 42 22.95%           |
| <b>Total</b>                    | <b>9465</b>                    | <b>35</b>  | <b>4083</b>   | <b>3620</b>  | <b>1040 28.73%</b>  |
| <b>UNINCORPORATED AREA</b>      |                                |            |               |              |                     |
| Precinct TSx                    | 94445                          | 90         | 8             | 8            | 6 75.00%            |
| Precinct OS                     | 94445                          | 90         | 10315         | 9501         | 4889 51.46%         |
| Precinct VBM                    | 94445                          | 90         | 40149         | 37218        | 18546 49.83%        |
| MB Precinct                     | 94445                          | 90         | 5143          | 4772         | 2350 49.25%         |
| Provisional                     | 94445                          | 90         | 1280          | 1119         | 558 49.87%          |
| <b>Total</b>                    | <b>94445</b>                   | <b>450</b> | <b>56895</b>  | <b>52618</b> | <b>26349 50.08%</b> |
| <b>Total</b>                    |                                |            |               |              |                     |
| Precinct TSx                    | 109479                         | 102        | 8             | 8            | 6 75.00%            |
| Precinct OS                     | 109479                         | 102        | 12009         | 10999        | 5392 49.02%         |
| Precinct VBM                    | 109479                         | 102        | 45535         | 42084        | 20274 48.18%        |
| MB Precinct                     | 109479                         | 102        | 5143          | 4772         | 2350 49.25%         |
| Provisional                     | 109479                         | 102        | 1584          | 1371         | 625 45.59%          |
| <b>Total</b>                    | <b>109479</b>                  | <b>510</b> | <b>64279</b>  | <b>59234</b> | <b>28647 48.36%</b> |

**COUNTY OF EL DORADO**  
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|                        | State Senator 1st State Senate |                 |
|------------------------|--------------------------------|-----------------|
|                        | STEVEN BAIRD (REP)             | ROB ROWEN (DEM) |
| Jurisdiction Wide      |                                |                 |
| 100001.11-CP 100001.11 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 37 22.02%                      | 55 32.74%       |
| Precinct VBM           | 139 19.09%                     | 194 26.65%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 0 0.00%                        | 5 33.33%        |
| Total                  | 176 19.32%                     | 254 27.88%      |
| 100002.11-CP 100002.11 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 18 22.78%                      | 24 30.38%       |
| Precinct VBM           | 70 16.24%                      | 123 28.54%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 2 10.53%                       | 7 36.84%        |
| Total                  | 90 17.01%                      | 154 29.11%      |
| 100003.11-CP 100003.11 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 19 16.96%                      | 27 24.11%       |
| Precinct VBM           | 136 18.92%                     | 248 34.49%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 4 19.05%                       | 5 23.81%        |
| Total                  | 159 18.66%                     | 280 32.86%      |
| 100004.11-CP 100004.11 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 29 12.78%                      | 88 38.77%       |
| Precinct VBM           | 117 17.08%                     | 225 32.85%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 0 0.00%                        | 2 66.67%        |
| Total                  | 146 15.96%                     | 315 34.43%      |
| 100005.11-CP 100005.11 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 34 25.37%                      | 38 28.36%       |
| Precinct VBM           | 187 22.40%                     | 188 22.51%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 5 22.73%                       | 7 31.82%        |
| Total                  | 226 22.81%                     | 233 23.51%      |
| 100006.11-CP 100006.11 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 30 13.51%                      | 66 29.73%       |
| Precinct VBM           | 137 15.77%                     | 309 35.56%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 2 8.00%                        | 10 40.00%       |
| Total                  | 169 15.14%                     | 385 34.50%      |
| 100008.11-CP 100008.11 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 27 15.34%                      | 66 37.50%       |
| Precinct VBM           | 157 22.95%                     | 186 27.19%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 2 13.33%                       | 4 26.67%        |
| Total                  | 186 21.26%                     | 256 29.26%      |
| 100009.11-CP 100009.11 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 26 16.77%                      | 59 38.06%       |
| Precinct VBM           | 110 15.92%                     | 215 31.11%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 3 14.29%                       | 8 38.10%        |
| Total                  | 139 16.03%                     | 282 32.53%      |
| 100012.11-CP 100012.11 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 36 15.45%                      | 78 33.48%       |
| Precinct VBM           | 150 17.24%                     | 271 31.15%      |

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|                        | State Senator 1st State Senate |        |                 |        |
|------------------------|--------------------------------|--------|-----------------|--------|
|                        | STEVEN BAIRD (REP)             |        | ROB ROWEN (DEM) |        |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 1                              | 3.45%  | 5               | 17.24% |
| Total                  | 187                            | 16.52% | 354             | 31.27% |
| 100013.11-CP 100013.11 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 33                             | 13.98% | 81              | 34.32% |
| Precinct VBM           | 160                            | 19.12% | 245             | 29.27% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 6                              | 24.00% | 8               | 32.00% |
| Total                  | 199                            | 18.12% | 334             | 30.42% |
| 100014.11-CP 100014.11 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 37                             | 19.58% | 61              | 32.28% |
| Precinct VBM           | 127                            | 19.24% | 176             | 26.67% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 1                              | 4.00%  | 7               | 28.00% |
| Total                  | 165                            | 18.88% | 244             | 27.92% |
| 100022.11-CP 100022.11 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 25                             | 15.24% | 52              | 31.71% |
| Precinct VBM           | 125                            | 20.19% | 163             | 26.33% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 1                              | 9.09%  | 5               | 45.45% |
| Total                  | 151                            | 19.02% | 220             | 27.71% |
| 100023.11-CP 100023.11 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 29                             | 13.94% | 52              | 25.00% |
| Precinct VBM           | 196                            | 20.04% | 257             | 26.28% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 3                              | 10.34% | 14              | 48.28% |
| Total                  | 228                            | 18.77% | 323             | 26.58% |
| 200003.12-CP 200003.12 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 25                             | 16.34% | 56              | 36.60% |
| Precinct VBM           | 95                             | 18.63% | 170             | 33.33% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 8                              | 30.77% | 5               | 19.23% |
| Total                  | 128                            | 18.58% | 231             | 33.53% |
| 200004.12-CP 200004.12 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 37                             | 21.89% | 51              | 30.18% |
| Precinct VBM           | 104                            | 16.97% | 218             | 35.56% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 2                              | 15.38% | 3               | 23.08% |
| Total                  | 143                            | 17.99% | 272             | 34.21% |
| 200005.02-CP 200005.02 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 12                             | 13.33% | 33              | 36.67% |
| Precinct VBM           | 78                             | 18.48% | 144             | 34.12% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 2                              | 11.11% | 5               | 27.78% |
| Total                  | 92                             | 17.36% | 182             | 34.34% |
| 200006.12-CP 200006.12 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 17                             | 14.91% | 27              | 23.68% |
| Precinct VBM           | 93                             | 18.86% | 105             | 21.30% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 5                              | 19.23% | 11              | 42.31% |
| Total                  | 115                            | 18.17% | 143             | 22.59% |
| 200008.12-CP 200008.12 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |



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|------------------------|--------------------------------|--------|-----------------|--------|
|                        | STEVEN BAIRD (REP)             |        | ROB ROWEN (DEM) |        |
| Precinct OS            | 27                             | 18.88% | 46              | 32.17% |
| Precinct VBM           | 49                             | 14.50% | 118             | 34.91% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 1                              | 10.00% | 3               | 30.00% |
| Total                  | 77                             | 15.68% | 167             | 34.01% |
| 200010.12-CP 200010.12 |                                |        |                 |        |
| Precinct TSx           | 0                              | 0.00%  | 1               | 25.00% |
| Precinct OS            | 18                             | 10.06% | 71              | 39.66% |
| Precinct VBM           | 132                            | 16.79% | 255             | 32.44% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 2                              | 7.41%  | 5               | 18.52% |
| Total                  | 152                            | 15.26% | 332             | 33.33% |
| 200012.12-CP 200012.12 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 24                             | 18.05% | 47              | 35.34% |
| Precinct VBM           | 82                             | 19.29% | 129             | 30.35% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 2                              | 16.67% | 6               | 50.00% |
| Total                  | 108                            | 18.95% | 182             | 31.93% |
| 200013.12-CP 200013.12 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 32                             | 16.16% | 72              | 36.36% |
| Precinct VBM           | 131                            | 20.09% | 179             | 27.45% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 3                              | 15.00% | 6               | 30.00% |
| Total                  | 166                            | 19.08% | 257             | 29.54% |
| 200014.12-CP 200014.12 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 23                             | 12.50% | 58              | 31.52% |
| Precinct VBM           | 121                            | 21.61% | 186             | 33.21% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 5                              | 18.52% | 13              | 48.15% |
| Total                  | 149                            | 19.33% | 257             | 33.33% |
| 200015.02-CP 200015.02 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 31                             | 15.20% | 71              | 34.80% |
| Precinct VBM           | 144                            | 17.50% | 305             | 37.06% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 3                              | 13.64% | 6               | 27.27% |
| Total                  | 178                            | 16.97% | 382             | 36.42% |
| 200016.12-CP 200016.12 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 3                              | 13.04% | 3               | 13.04% |
| Precinct VBM           | 24                             | 18.32% | 27              | 20.61% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 0                              | 0.00%  | 1               | 20.00% |
| Total                  | 27                             | 16.98% | 31              | 19.50% |
| 200016.13-CP 200016.13 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 18                             | 23.68% | 17              | 22.37% |
| Precinct VBM           | 71                             | 22.61% | 71              | 22.61% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 2                              | 33.33% | 1               | 16.67% |
| Total                  | 91                             | 22.98% | 89              | 22.47% |
| 200017.02-CP 200017.02 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 23                             | 16.67% | 48              | 34.78% |
| Precinct VBM           | 107                            | 19.18% | 209             | 37.46% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 1                              | 9.09%  | 4               | 36.36% |
| Total                  | 131                            | 18.53% | 261             | 36.92% |

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|------------------------|--------------------------------|-----------------|
|                        | STEVEN BAIRD (REP)             | ROB ROWEN (DEM) |
| 200018.02-CP 200018.02 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 34 21.52%                      | 33 20.89%       |
| Precinct VBM           | 124 19.11%                     | 184 28.35%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 0 0.00%                        | 3 25.00%        |
| Total                  | 158 19.29%                     | 220 26.86%      |
| 200020.02-CP 200020.02 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 15 11.81%                      | 29 22.83%       |
| Precinct VBM           | 103 20.98%                     | 140 28.51%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 4 25.00%                       | 6 37.50%        |
| Total                  | 122 19.24%                     | 175 27.60%      |
| 200021.01-CP 200021.01 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 2 6.06%                        | 8 24.24%        |
| Precinct VBM           | 52 26.94%                      | 46 23.83%       |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 0 0.00%                        | 1 20.00%        |
| Total                  | 54 23.38%                      | 55 23.81%       |
| 200021.02-CP 200021.02 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 22 13.84%                      | 42 26.42%       |
| Precinct VBM           | 121 17.87%                     | 192 28.36%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 2 11.11%                       | 5 27.78%        |
| Total                  | 145 16.98%                     | 239 27.99%      |
| 200024.02-CP 200024.02 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 17 31.48%                      | 7 12.96%        |
| Precinct VBM           | 53 19.70%                      | 54 20.07%       |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 3 37.50%                       | 0 0.00%         |
| Total                  | 73 22.05%                      | 61 18.43%       |
| 300001.04-CP 300001.04 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 2 16.67%                       | 4 33.33%        |
| Precinct VBM           | 12 12.63%                      | 40 42.11%       |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 0 -                            | 0 -             |
| Total                  | 14 13.08%                      | 44 41.12%       |
| 300001.05-CP 300001.05 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 6 8.57%                        | 30 42.86%       |
| Precinct VBM           | 41 13.76%                      | 128 42.95%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 2 20.00%                       | 6 60.00%        |
| Total                  | 49 12.96%                      | 164 43.39%      |
| 300003.05-CP 300003.05 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 20 11.11%                      | 84 46.67%       |
| Precinct VBM           | 148 17.43%                     | 368 43.35%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 4 16.67%                       | 13 54.17%       |
| Total                  | 172 16.33%                     | 465 44.16%      |
| 300004.05-CP 300004.05 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 23 15.23%                      | 70 46.36%       |
| Precinct VBM           | 78 15.03%                      | 217 41.81%      |
| MB Precinct            | 0 -                            | 0 -             |

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|                        | STEVEN BAIRD (REP)             |        | ROB ROWEN (DEM) |        |
| Provisional            | 0                              | 0.00%  | 5               | 35.71% |
| Total                  | 101                            | 14.77% | 292             | 42.69% |
| 300005.05-CP 300005.05 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 23                             | 15.97% | 56              | 38.89% |
| Precinct VBM           | 107                            | 17.57% | 233             | 38.26% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 5                              | 23.81% | 9               | 42.86% |
| Total                  | 135                            | 17.44% | 298             | 38.50% |
| 300008.05-CP 300008.05 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 27                             | 20.15% | 36              | 26.87% |
| Precinct VBM           | 135                            | 19.37% | 203             | 29.12% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 0                              | 0.00%  | 3               | 23.08% |
| Total                  | 162                            | 19.19% | 242             | 28.67% |
| 300009.04-CP 300009.04 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 17                             | 15.45% | 33              | 30.00% |
| Precinct VBM           | 72                             | 19.78% | 102             | 28.02% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 1                              | 9.09%  | 5               | 45.45% |
| Total                  | 90                             | 18.56% | 140             | 28.87% |
| 300010.04-CP 300010.04 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 39                             | 24.38% | 37              | 23.13% |
| Precinct VBM           | 116                            | 18.68% | 205             | 33.01% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 2                              | 18.18% | 3               | 27.27% |
| Total                  | 157                            | 19.82% | 245             | 30.93% |
| 300011.04-CP 300011.04 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 21                             | 15.44% | 35              | 25.74% |
| Precinct VBM           | 132                            | 19.85% | 199             | 29.92% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 6                              | 30.00% | 8               | 40.00% |
| Total                  | 159                            | 19.37% | 242             | 29.48% |
| 300013.05-CP 300013.05 |                                |        |                 |        |
| Precinct TSx           | 1                              | 33.33% | 0               | 0.00%  |
| Precinct OS            | 17                             | 14.17% | 44              | 36.67% |
| Precinct VBM           | 130                            | 19.06% | 201             | 29.47% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 2                              | 15.38% | 4               | 30.77% |
| Total                  | 150                            | 18.34% | 249             | 30.44% |
| 300015.04-CP 300015.04 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 2                              | 20.00% | 2               | 20.00% |
| Precinct VBM           | 8                              | 15.09% | 16              | 30.19% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 2                              | 50.00% | 2               | 50.00% |
| Total                  | 12                             | 17.91% | 20              | 29.85% |
| 300015.05-CP 300015.05 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 15                             | 24.19% | 22              | 35.48% |
| Precinct VBM           | 48                             | 19.92% | 97              | 40.25% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 0                              | 0.00%  | 1               | 50.00% |
| Total                  | 63                             | 20.66% | 120             | 39.34% |
| 300017.05-CP 300017.05 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 19                             | 17.27% | 34              | 30.91% |

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|                        | STEVEN BAIRD (REP)             |        | ROB ROWEN (DEM) |        |
| Precinct VBM           | 105                            | 17.10% | 190             | 30.94% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 3                              | 37.50% | 1               | 12.50% |
| Total                  | 127                            | 17.35% | 225             | 30.74% |
| 300018.04-CP 300018.04 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 10                             | 7.25%  | 37              | 26.81% |
| Precinct VBM           | 91                             | 19.16% | 150             | 31.58% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 1                              | 4.00%  | 8               | 32.00% |
| Total                  | 102                            | 15.99% | 195             | 30.56% |
| 300020.04-CP 300020.04 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 33                             | 21.57% | 44              | 28.76% |
| Precinct VBM           | 99                             | 16.87% | 186             | 31.69% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 2                              | 15.38% | 1               | 7.69%  |
| Total                  | 134                            | 17.80% | 231             | 30.68% |
| 300021.03-CP 300021.03 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 24                             | 17.52% | 49              | 35.77% |
| Precinct VBM           | 84                             | 14.66% | 201             | 35.08% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 1                              | 8.33%  | 4               | 33.33% |
| Total                  | 109                            | 15.10% | 254             | 35.18% |
| 300022.05-CP 300022.05 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 12                             | 12.90% | 24              | 25.81% |
| Precinct VBM           | 51                             | 15.50% | 133             | 40.43% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 2                              | 16.67% | 5               | 41.67% |
| Total                  | 65                             | 14.98% | 162             | 37.33% |
| 300023.03-CP 300023.03 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 26                             | 11.98% | 78              | 35.94% |
| Precinct VBM           | 136                            | 15.89% | 282             | 32.94% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 4                              | 18.18% | 7               | 31.82% |
| Total                  | 166                            | 15.16% | 367             | 33.52% |
| 300023.05-CP 300023.05 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 9                              | 20.45% | 12              | 27.27% |
| Precinct VBM           | 30                             | 16.95% | 59              | 33.33% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 2                              | 25.00% | 4               | 50.00% |
| Total                  | 41                             | 17.90% | 75              | 32.75% |
| 400001.14-CP 400001.14 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 22                             | 13.66% | 51              | 31.68% |
| Precinct VBM           | 206                            | 22.20% | 268             | 28.88% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 3                              | 13.64% | 8               | 36.36% |
| Total                  | 231                            | 20.79% | 327             | 29.43% |
| 400002.08-CP 400002.08 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 34                             | 18.28% | 45              | 24.19% |
| Precinct VBM           | 130                            | 18.87% | 170             | 24.67% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 1                              | 6.67%  | 6               | 40.00% |
| Total                  | 165                            | 18.54% | 221             | 24.83% |
| 400003.08-CP 400003.08 |                                |        |                 |        |

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|                        | STEVEN BAIRD (REP)             | ROB ROWEN (DEM) |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 32 14.16%                      | 57 25.22%       |
| Precinct VBM           | 139 19.55%                     | 233 32.77%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 2 6.25%                        | 13 40.63%       |
| Total                  | 173 17.85%                     | 303 31.27%      |
| 400004.14-CP 400004.14 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 35 19.66%                      | 52 29.21%       |
| Precinct VBM           | 123 20.23%                     | 186 30.59%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 3 16.67%                       | 4 22.22%        |
| Total                  | 161 20.02%                     | 242 30.10%      |
| 400005.08-CP 400005.08 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 9 12.33%                       | 23 31.51%       |
| Precinct VBM           | 48 20.08%                      | 84 35.15%       |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 3 33.33%                       | 2 22.22%        |
| Total                  | 60 18.69%                      | 109 33.96%      |
| 400006.08-CP 400006.08 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 27 12.62%                      | 77 35.98%       |
| Precinct VBM           | 106 16.06%                     | 249 37.73%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 2 13.33%                       | 4 26.67%        |
| Total                  | 135 15.19%                     | 330 37.12%      |
| 400007.08-CP 400007.08 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 24 11.21%                      | 63 29.44%       |
| Precinct VBM           | 121 16.76%                     | 198 27.42%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 8 47.06%                       | 4 23.53%        |
| Total                  | 153 16.05%                     | 265 27.81%      |
| 400010.08-CP 400010.08 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 39 17.33%                      | 84 37.33%       |
| Precinct VBM           | 130 15.76%                     | 319 38.67%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 7 26.92%                       | 10 38.46%       |
| Total                  | 176 16.36%                     | 413 38.38%      |
| 400012.08-CP 400012.08 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 21 19.44%                      | 47 43.52%       |
| Precinct VBM           | 71 17.66%                      | 134 33.33%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 1 5.88%                        | 5 29.41%        |
| Total                  | 93 17.65%                      | 186 35.29%      |
| 400013.08-CP 400013.08 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 39 33.33%                      | 21 17.95%       |
| Precinct VBM           | 64 22.30%                      | 90 31.36%       |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 0 0.00%                        | 1 33.33%        |
| Total                  | 103 25.31%                     | 112 27.52%      |
| 400014.08-CP 400014.08 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 40 18.87%                      | 55 25.94%       |
| Precinct VBM           | 119 17.22%                     | 226 32.71%      |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 5 14.71%                       | 13 38.24%       |

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|                        | STEVEN BAIRD (REP)             |        | ROB ROWEN (DEM) |         |
| Total                  | 164                            | 17.50% | 294             | 31.38%  |
| 400017.08-CP 400017.08 |                                |        |                 |         |
| Precinct TSx           | 0                              | -      | 0               | -       |
| Precinct OS            | 33                             | 20.37% | 31              | 19.14%  |
| Precinct VBM           | 136                            | 19.21% | 189             | 26.69%  |
| MB Precinct            | 0                              | -      | 0               | -       |
| Provisional            | 4                              | 14.81% | 8               | 29.63%  |
| Total                  | 173                            | 19.29% | 228             | 25.42%  |
| 400018.08-CP 400018.08 |                                |        |                 |         |
| Precinct TSx           | 0                              | -      | 0               | -       |
| Precinct OS            | 13                             | 16.46% | 14              | 17.72%  |
| Precinct VBM           | 37                             | 15.10% | 46              | 18.78%  |
| MB Precinct            | 0                              | -      | 0               | -       |
| Provisional            | 0                              | 0.00%  | 2               | 40.00%  |
| Total                  | 50                             | 15.20% | 62              | 18.84%  |
| 400020.08-CP 400020.08 |                                |        |                 |         |
| Precinct TSx           | 0                              | -      | 0               | -       |
| Precinct OS            | 18                             | 17.14% | 21              | 20.00%  |
| Precinct VBM           | 72                             | 22.09% | 97              | 29.75%  |
| MB Precinct            | 0                              | -      | 0               | -       |
| Provisional            | 0                              | 0.00%  | 2               | 100.00% |
| Total                  | 90                             | 20.79% | 120             | 27.71%  |
| 400021.07-CP 400021.07 |                                |        |                 |         |
| Precinct TSx           | 0                              | -      | 0               | -       |
| Precinct OS            | 12                             | 10.17% | 30              | 25.42%  |
| Precinct VBM           | 142                            | 19.69% | 185             | 25.66%  |
| MB Precinct            | 0                              | -      | 0               | -       |
| Provisional            | 5                              | 27.78% | 5               | 27.78%  |
| Total                  | 159                            | 18.55% | 220             | 25.67%  |
| 500001.10-CP 500001.10 |                                |        |                 |         |
| Precinct TSx           | 0                              | -      | 0               | -       |
| Precinct OS            | 23                             | 12.50% | 102             | 55.43%  |
| Precinct VBM           | 55                             | 9.95%  | 294             | 53.16%  |
| MB Precinct            | 0                              | -      | 0               | -       |
| Provisional            | 1                              | 7.69%  | 11              | 84.62%  |
| Total                  | 79                             | 10.53% | 407             | 54.27%  |
| 500002.10-CP 500002.10 |                                |        |                 |         |
| Precinct TSx           | 0                              | -      | 0               | -       |
| Precinct OS            | 15                             | 12.61% | 67              | 56.30%  |
| Precinct VBM           | 49                             | 13.14% | 231             | 61.93%  |
| MB Precinct            | 0                              | -      | 0               | -       |
| Provisional            | 4                              | 14.81% | 18              | 66.67%  |
| Total                  | 68                             | 13.10% | 316             | 60.89%  |
| 500003.10-CP 500003.10 |                                |        |                 |         |
| Precinct TSx           | 0                              | -      | 0               | -       |
| Precinct OS            | 9                              | 7.96%  | 70              | 61.95%  |
| Precinct VBM           | 54                             | 17.53% | 170             | 55.19%  |
| MB Precinct            | 0                              | -      | 0               | -       |
| Provisional            | 1                              | 20.00% | 2               | 40.00%  |
| Total                  | 64                             | 15.02% | 242             | 56.81%  |
| 500004.10-CP 500004.10 |                                |        |                 |         |
| Precinct TSx           | 0                              | -      | 0               | -       |
| Precinct OS            | 14                             | 12.96% | 63              | 58.33%  |
| Precinct VBM           | 25                             | 10.87% | 134             | 58.26%  |
| MB Precinct            | 0                              | -      | 0               | -       |
| Provisional            | 4                              | 22.22% | 9               | 50.00%  |
| Total                  | 43                             | 12.08% | 206             | 57.87%  |
| 500005.10-CP 500005.10 |                                |        |                 |         |
| Precinct TSx           | 0                              | -      | 0               | -       |
| Precinct OS            | 10                             | 10.31% | 65              | 67.01%  |
| Precinct VBM           | 31                             | 13.84% | 138             | 61.61%  |

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|                        | State Senator 1st State Senate |        |                 |        |
|------------------------|--------------------------------|--------|-----------------|--------|
|                        | STEVEN BAIRD (REP)             |        | ROB ROWEN (DEM) |        |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 1                              | 8.33%  | 9               | 75.00% |
| Total                  | 42                             | 12.61% | 212             | 63.66% |
| 500006.10-CP 500006.10 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 12                             | 7.27%  | 109             | 66.06% |
| Precinct VBM           | 42                             | 9.57%  | 283             | 64.46% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 4                              | 8.33%  | 30              | 62.50% |
| Total                  | 58                             | 8.90%  | 422             | 64.72% |
| 500007.10-CP 500007.10 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 20                             | 10.75% | 93              | 50.00% |
| Precinct VBM           | 99                             | 15.94% | 279             | 44.93% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 0                              | 0.00%  | 12              | 66.67% |
| Total                  | 119                            | 14.42% | 384             | 46.55% |
| 500009.10-CP 500009.10 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 10                             | 9.52%  | 76              | 72.38% |
| Precinct VBM           | 31                             | 11.97% | 155             | 59.85% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 3                              | 10.00% | 20              | 66.67% |
| Total                  | 44                             | 11.17% | 251             | 63.71% |
| 500011.10-CP 500011.10 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 19                             | 11.80% | 100             | 62.11% |
| Precinct VBM           | 43                             | 12.29% | 214             | 61.14% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 4                              | 13.33% | 20              | 66.67% |
| Total                  | 66                             | 12.20% | 334             | 61.74% |
| 500012.10-CP 500012.10 |                                |        |                 |        |
| Precinct TSx           | 0                              | 0.00%  | 0               | 0.00%  |
| Precinct OS            | 17                             | 10.18% | 108             | 64.67% |
| Precinct VBM           | 62                             | 11.19% | 296             | 53.43% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 2                              | 7.14%  | 22              | 78.57% |
| Total                  | 81                             | 10.80% | 426             | 56.80% |
| 500015.10-CP 500015.10 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 6                              | 6.19%  | 67              | 69.07% |
| Precinct VBM           | 37                             | 9.89%  | 217             | 58.02% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 2                              | 22.22% | 5               | 55.56% |
| Total                  | 45                             | 9.38%  | 289             | 60.21% |
| 500020.10-CP 500020.10 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 27                             | 18.49% | 49              | 33.56% |
| Precinct VBM           | 104                            | 16.64% | 225             | 36.00% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 4                              | 15.38% | 10              | 38.46% |
| Total                  | 135                            | 16.94% | 284             | 35.63% |
| 500021.10-CP 500021.10 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |
| Precinct OS            | 24                             | 15.58% | 43              | 27.92% |
| Precinct VBM           | 92                             | 15.38% | 190             | 31.77% |
| MB Precinct            | 0                              | -      | 0               | -      |
| Provisional            | 3                              | 13.64% | 11              | 50.00% |
| Total                  | 119                            | 15.37% | 244             | 31.52% |
| 95U201.01-MB 95U201.01 |                                |        |                 |        |
| Precinct TSx           | 0                              | -      | 0               | -      |

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|                        | State Senator 1st State Senate |                 |
|------------------------|--------------------------------|-----------------|
|                        | STEVEN BAIRD (REP)             | ROB ROWEN (DEM) |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 1 14.29%                       | 1 14.29%        |
| Provisional            | 0 -                            | 0 -             |
| Total                  | 1 14.29%                       | 1 14.29%        |
| 95U201.02-MB 95U201.02 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 58 17.96%                      | 86 26.63%       |
| Provisional            | 0 0.00%                        | 0 0.00%         |
| Total                  | 58 17.90%                      | 86 26.54%       |
| 95U202.02-MB 95U202.02 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 112 20.86%                     | 172 32.03%      |
| Provisional            | 0 0.00%                        | 2 66.67%        |
| Total                  | 112 20.74%                     | 174 32.22%      |
| 95U203.02-MB 95U203.02 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 0 -                            | 0 -             |
| Total                  | 0 -                            | 0 -             |
| 95U204.01-MB 95U204.01 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 114 18.60%                     | 177 28.87%      |
| Provisional            | 0 0.00%                        | 0 0.00%         |
| Total                  | 114 18.54%                     | 177 28.78%      |
| 95U301.05-MB 95U301.05 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 21 14.89%                      | 42 29.79%       |
| Provisional            | 0 0.00%                        | 0 0.00%         |
| Total                  | 21 14.79%                      | 42 29.58%       |
| 95U302.04-MB 95U302.04 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 0 0.00%                        | 1 100.00%       |
| Provisional            | 0 -                            | 0 -             |
| Total                  | 0 0.00%                        | 1 100.00%       |
| 95U302.05-MB 95U302.05 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 12 14.81%                      | 27 33.33%       |
| Provisional            | 0 -                            | 0 -             |
| Total                  | 12 14.81%                      | 27 33.33%       |
| 95U303.03-MB 95U303.03 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 16 19.75%                      | 23 28.40%       |
| Provisional            | 0 -                            | 0 -             |
| Total                  | 16 19.75%                      | 23 28.40%       |



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|                        | State Senator 1st State Senate |                 |
|------------------------|--------------------------------|-----------------|
|                        | STEVEN BAIRD (REP)             | ROB ROWEN (DEM) |
| 95U303.05-MB 95U303.05 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 14 14.00%                      | 26 26.00%       |
| Provisional            | 0 0.00%                        | 0 0.00%         |
| Total                  | 14 13.86%                      | 26 25.74%       |
| 95U304.05-MB 95U304.05 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 0 -                            | 0 -             |
| Total                  | 0 -                            | 0 -             |
| 95U305.05-MB 95U305.05 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 3 42.86%                       | 1 14.29%        |
| Provisional            | 0 -                            | 0 -             |
| Total                  | 3 42.86%                       | 1 14.29%        |
| 95U401.08-MB 95U401.08 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 0 0.00%                        | 5 35.71%        |
| Provisional            | 0 -                            | 0 -             |
| Total                  | 0 0.00%                        | 5 35.71%        |
| 95U402.07-MB 95U402.07 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 42 17.43%                      | 66 27.39%       |
| Provisional            | 0 0.00%                        | 1 100.00%       |
| Total                  | 42 17.36%                      | 67 27.69%       |
| 95U402.08-MB 95U402.08 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 124 23.62%                     | 124 23.62%      |
| Provisional            | 2 66.67%                       | 0 0.00%         |
| Total                  | 126 23.86%                     | 124 23.48%      |
| 95U403.08-MB 95U403.08 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 149 15.19%                     | 396 40.37%      |
| Provisional            | 1 7.69%                        | 6 46.15%        |
| Total                  | 150 15.09%                     | 402 40.44%      |
| 95U404.06-MB 95U404.06 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 0 -                            | 0 -             |
| Provisional            | 0 -                            | 0 -             |
| Total                  | 0 -                            | 0 -             |
| 95U404.08-MB 95U404.08 |                                |                 |
| Precinct TSx           | 0 -                            | 0 -             |
| Precinct OS            | 0 -                            | 0 -             |
| Precinct VBM           | 0 -                            | 0 -             |
| MB Precinct            | 0 -                            | 0 -             |

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|                               | State Senator 1st State Senate |                 |
|-------------------------------|--------------------------------|-----------------|
|                               | STEVEN BAIRD (REP)             | ROB ROWEN (DEM) |
| Provisional                   | 0                              | 0               |
| Total                         | 0                              | 0               |
| 95U501.09-MB 95U501.09        |                                |                 |
| Precinct TSx                  | 0                              | 0               |
| Precinct OS                   | 0                              | 0               |
| Precinct VBM                  | 0                              | 0               |
| MB Precinct                   | 0 0.00%                        | 2 100.00%       |
| Provisional                   | 0                              | 0               |
| Total                         | 0 0.00%                        | 2 100.00%       |
| 95U501.10-MB 95U501.10        |                                |                 |
| Precinct TSx                  | 0                              | 0               |
| Precinct OS                   | 0                              | 0               |
| Precinct VBM                  | 0                              | 0               |
| MB Precinct                   | 20 12.42%                      | 58 36.02%       |
| Provisional                   | 0 0.00%                        | 1 50.00%        |
| Total                         | 20 12.27%                      | 59 36.20%       |
| 95U598.10-MB 95U598.10        |                                |                 |
| Precinct TSx                  | 0                              | 0               |
| Precinct OS                   | 0                              | 0               |
| Precinct VBM                  | 0                              | 0               |
| MB Precinct                   | 19 9.27%                       | 133 64.88%      |
| Provisional                   | 0 0.00%                        | 0 0.00%         |
| Total                         | 19 9.22%                       | 133 64.56%      |
| 95U599.10-MB 95U599.10        |                                |                 |
| Precinct TSx                  | 0                              | 0               |
| Precinct OS                   | 0                              | 0               |
| Precinct VBM                  | 0                              | 0               |
| MB Precinct                   | 3 9.09%                        | 19 57.58%       |
| Provisional                   | 0                              | 0               |
| Total                         | 3 9.09%                        | 19 57.58%       |
| 96U101.11-MB 96U101.11        |                                |                 |
| Precinct TSx                  | 0                              | 0               |
| Precinct OS                   | 0                              | 0               |
| Precinct VBM                  | 0                              | 0               |
| MB Precinct                   | 89 22.59%                      | 97 24.62%       |
| Provisional                   | 0 0.00%                        | 5 83.33%        |
| Total                         | 89 22.25%                      | 102 25.50%      |
| 96U401.14-MB 96U401.14        |                                |                 |
| Precinct TSx                  | 0                              | 0               |
| Precinct OS                   | 0                              | 0               |
| Precinct VBM                  | 0                              | 0               |
| MB Precinct                   | 59 18.15%                      | 103 31.69%      |
| Provisional                   | 2 33.33%                       | 3 50.00%        |
| Total                         | 61 18.43%                      | 106 32.02%      |
| Total                         |                                |                 |
| Precinct TSx                  | 1 12.50%                       | 1 12.50%        |
| Precinct OS                   | 1709 15.54%                    | 3880 35.28%     |
| Precinct VBM                  | 7486 17.79%                    | 14242 33.84%    |
| MB Precinct                   | 856 17.94%                     | 1559 32.67%     |
| Provisional                   | 196 14.30%                     | 544 39.68%      |
| Total                         | 10248 17.30%                   | 20226 34.15%    |
| STATE BOARD OF EQUALIZATION   |                                |                 |
| STATE BOARD OF EQUALIZATION 1 |                                |                 |
| Precinct TSx                  | 1 12.50%                       | 1 12.50%        |
| Precinct OS                   | 1709 15.54%                    | 3880 35.28%     |
| Precinct VBM                  | 7486 17.79%                    | 14242 33.84%    |
| MB Precinct                   | 856 17.94%                     | 1559 32.67%     |
| Provisional                   | 196 14.30%                     | 544 39.68%      |
| Total                         | 10248 17.30%                   | 20226 34.15%    |
| Total                         |                                |                 |

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|                                     | State Senator 1st State Senate |               |                 |               |
|-------------------------------------|--------------------------------|---------------|-----------------|---------------|
|                                     | STEVEN BAIRD (REP)             |               | ROB ROWEN (DEM) |               |
| Precinct TSx                        | 1                              | 12.50%        | 1               | 12.50%        |
| Precinct OS                         | 1709                           | 15.54%        | 3880            | 35.28%        |
| Precinct VBM                        | 7486                           | 17.79%        | 14242           | 33.84%        |
| MB Precinct                         | 856                            | 17.94%        | 1559            | 32.67%        |
| Provisional                         | 196                            | 14.30%        | 544             | 39.68%        |
| <b>Total</b>                        | <b>10248</b>                   | <b>17.30%</b> | <b>20226</b>    | <b>34.15%</b> |
| <b>UNITED STATES SENATE</b>         |                                |               |                 |               |
| <b>UNITED STATES SENATE</b>         |                                |               |                 |               |
| Precinct TSx                        | 1                              | 12.50%        | 1               | 12.50%        |
| Precinct OS                         | 1709                           | 15.54%        | 3880            | 35.28%        |
| Precinct VBM                        | 7486                           | 17.79%        | 14242           | 33.84%        |
| MB Precinct                         | 856                            | 17.94%        | 1559            | 32.67%        |
| Provisional                         | 196                            | 14.30%        | 544             | 39.68%        |
| <b>Total</b>                        | <b>10248</b>                   | <b>17.30%</b> | <b>20226</b>    | <b>34.15%</b> |
| <b>Total</b>                        |                                |               |                 |               |
| Precinct TSx                        | 1                              | 12.50%        | 1               | 12.50%        |
| Precinct OS                         | 1709                           | 15.54%        | 3880            | 35.28%        |
| Precinct VBM                        | 7486                           | 17.79%        | 14242           | 33.84%        |
| MB Precinct                         | 856                            | 17.94%        | 1559            | 32.67%        |
| Provisional                         | 196                            | 14.30%        | 544             | 39.68%        |
| <b>Total</b>                        | <b>10248</b>                   | <b>17.30%</b> | <b>20226</b>    | <b>34.15%</b> |
| <b>Total</b>                        |                                |               |                 |               |
| <b>UNITED STATES REPRESENTATIVE</b> |                                |               |                 |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                                |               |                 |               |
| Precinct TSx                        | 1                              | 12.50%        | 1               | 12.50%        |
| Precinct OS                         | 1709                           | 15.54%        | 3880            | 35.28%        |
| Precinct VBM                        | 7486                           | 17.79%        | 14242           | 33.84%        |
| MB Precinct                         | 856                            | 17.94%        | 1559            | 32.67%        |
| Provisional                         | 196                            | 14.30%        | 544             | 39.68%        |
| <b>Total</b>                        | <b>10248</b>                   | <b>17.30%</b> | <b>20226</b>    | <b>34.15%</b> |
| <b>Total</b>                        |                                |               |                 |               |
| Precinct TSx                        | 1                              | 12.50%        | 1               | 12.50%        |
| Precinct OS                         | 1709                           | 15.54%        | 3880            | 35.28%        |
| Precinct VBM                        | 7486                           | 17.79%        | 14242           | 33.84%        |
| MB Precinct                         | 856                            | 17.94%        | 1559            | 32.67%        |
| Provisional                         | 196                            | 14.30%        | 544             | 39.68%        |
| <b>Total</b>                        | <b>10248</b>                   | <b>17.30%</b> | <b>20226</b>    | <b>34.15%</b> |
| <b>Total</b>                        |                                |               |                 |               |
| <b>STATE SENATE</b>                 |                                |               |                 |               |
| <b>1ST STATE SENATE DISTRICT</b>    |                                |               |                 |               |
| Precinct TSx                        | 1                              | 12.50%        | 1               | 12.50%        |
| Precinct OS                         | 1709                           | 15.54%        | 3880            | 35.28%        |
| Precinct VBM                        | 7486                           | 17.79%        | 14242           | 33.84%        |
| MB Precinct                         | 856                            | 17.94%        | 1559            | 32.67%        |
| Provisional                         | 196                            | 14.30%        | 544             | 39.68%        |
| <b>Total</b>                        | <b>10248</b>                   | <b>17.30%</b> | <b>20226</b>    | <b>34.15%</b> |
| <b>Total</b>                        |                                |               |                 |               |
| Precinct TSx                        | 1                              | 12.50%        | 1               | 12.50%        |
| Precinct OS                         | 1709                           | 15.54%        | 3880            | 35.28%        |
| Precinct VBM                        | 7486                           | 17.79%        | 14242           | 33.84%        |
| MB Precinct                         | 856                            | 17.94%        | 1559            | 32.67%        |
| Provisional                         | 196                            | 14.30%        | 544             | 39.68%        |
| <b>Total</b>                        | <b>10248</b>                   | <b>17.30%</b> | <b>20226</b>    | <b>34.15%</b> |
| <b>Total</b>                        |                                |               |                 |               |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                                |               |                 |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |                                |               |                 |               |
| Precinct TSx                        | 1                              | 25.00%        | 0               | 0.00%         |
| Precinct OS                         | 1048                           | 15.00%        | 2582            | 36.96%        |
| Precinct VBM                        | 4444                           | 17.01%        | 9530            | 36.49%        |
| MB Precinct                         | 708                            | 17.47%        | 1359            | 33.53%        |

**COUNTY OF EL DORADO**  
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|                                   | State Senator 1st State Senate |        |                 |        |
|-----------------------------------|--------------------------------|--------|-----------------|--------|
|                                   | STEVEN BAIRD (REP)             |        | ROB ROWEN (DEM) |        |
| Provisional                       | 128                            | 14.43% | 383             | 43.18% |
| Total                             | 6329                           | 16.63% | 13854           | 36.41% |
| <b>6TH STATE ASSEMBLY DIST</b>    |                                |        |                 |        |
| Precinct TSx                      | 0                              | 0.00%  | 1               | 25.00% |
| Precinct OS                       | 661                            | 16.47% | 1298            | 32.34% |
| Precinct VBM                      | 3042                           | 19.06% | 4712            | 29.52% |
| MB Precinct                       | 148                            | 20.58% | 200             | 27.82% |
| Provisional                       | 68                             | 14.05% | 161             | 33.26% |
| Total                             | 3919                           | 18.50% | 6372            | 30.08% |
| <b>Total</b>                      |                                |        |                 |        |
| Precinct TSx                      | 1                              | 12.50% | 1               | 12.50% |
| Precinct OS                       | 1709                           | 15.54% | 3880            | 35.28% |
| Precinct VBM                      | 7486                           | 17.79% | 14242           | 33.84% |
| MB Precinct                       | 856                            | 17.94% | 1559            | 32.67% |
| Provisional                       | 196                            | 14.30% | 544             | 39.68% |
| Total                             | 10248                          | 17.30% | 20226           | 34.15% |
| <b>SUPERVISORIAL DISTRICTS</b>    |                                |        |                 |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                                |        |                 |        |
| Precinct TSx                      | 0                              | -      | 0               | -      |
| Precinct OS                       | 380                            | 16.50% | 747             | 32.44% |
| Precinct VBM                      | 1811                           | 18.85% | 2800            | 29.15% |
| MB Precinct                       | 89                             | 22.59% | 97              | 24.62% |
| Provisional                       | 30                             | 11.28% | 92              | 34.59% |
| Total                             | 2310                           | 18.38% | 3736            | 29.72% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                                |        |                 |        |
| Precinct TSx                      | 0                              | 0.00%  | 1               | 25.00% |
| Precinct OS                       | 380                            | 16.27% | 719             | 30.79% |
| Precinct VBM                      | 1684                           | 18.91% | 2732            | 30.68% |
| MB Precinct                       | 285                            | 19.26% | 436             | 29.46% |
| Provisional                       | 45                             | 15.63% | 86              | 29.86% |
| Total                             | 2394                           | 18.40% | 3974            | 30.54% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                                |        |                 |        |
| Precinct TSx                      | 1                              | 33.33% | 0               | 0.00%  |
| Precinct OS                       | 345                            | 15.82% | 731             | 33.52% |
| Precinct VBM                      | 1623                           | 17.44% | 3210            | 34.50% |
| MB Precinct                       | 66                             | 16.06% | 120             | 29.20% |
| Provisional                       | 39                             | 15.92% | 89              | 36.33% |
| Total                             | 2074                           | 17.08% | 4150            | 34.17% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                                |        |                 |        |
| Precinct TSx                      | 0                              | -      | 0               | -      |
| Precinct OS                       | 398                            | 16.74% | 671             | 28.22% |
| Precinct VBM                      | 1644                           | 18.76% | 2674            | 30.52% |
| MB Precinct                       | 374                            | 17.93% | 694             | 33.27% |
| Provisional                       | 49                             | 17.31% | 97              | 34.28% |
| Total                             | 2465                           | 18.25% | 4136            | 30.62% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                                |        |                 |        |
| Precinct TSx                      | 0                              | 0.00%  | 0               | 0.00%  |
| Precinct OS                       | 206                            | 11.43% | 1012            | 56.16% |
| Precinct VBM                      | 724                            | 13.14% | 2826            | 51.31% |
| MB Precinct                       | 42                             | 10.47% | 212             | 52.87% |
| Provisional                       | 33                             | 11.42% | 180             | 62.28% |
| Total                             | 1005                           | 12.56% | 4230            | 52.87% |
| <b>Total</b>                      |                                |        |                 |        |
| Precinct TSx                      | 1                              | 12.50% | 1               | 12.50% |
| Precinct OS                       | 1709                           | 15.54% | 3880            | 35.28% |
| Precinct VBM                      | 7486                           | 17.79% | 14242           | 33.84% |
| MB Precinct                       | 856                            | 17.94% | 1559            | 32.67% |
| Provisional                       | 196                            | 14.30% | 544             | 39.68% |
| Total                             | 10248                          | 17.30% | 20226           | 34.15% |

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|                                 | State Senator 1st State Senate |               |                 |               |
|---------------------------------|--------------------------------|---------------|-----------------|---------------|
|                                 | STEVEN BAIRD (REP)             |               | ROB ROWEN (DEM) |               |
| <b>CITIES</b>                   |                                |               |                 |               |
| <b>CITY OF PLACERVILLE</b>      |                                |               |                 |               |
| Precinct TSx                    | 0                              | -             | 0               | -             |
| Precinct OS                     | 74                             | 13.29%        | 244             | 43.81%        |
| Precinct VBM                    | 386                            | 16.29%        | 986             | 41.60%        |
| MB Precinct                     | 0                              | -             | 0               | -             |
| Provisional                     | 11                             | 15.94%        | 33              | 47.83%        |
| <b>Total</b>                    | <b>471</b>                     | <b>15.72%</b> | <b>1263</b>     | <b>42.16%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                                |               |                 |               |
| Precinct TSx                    | 0                              | -             | 0               | -             |
| Precinct OS                     | 100                            | 10.63%        | 573             | 60.89%        |
| Precinct VBM                    | 320                            | 12.82%        | 1434            | 57.45%        |
| MB Precinct                     | 0                              | -             | 0               | -             |
| Provisional                     | 20                             | 10.93%        | 118             | 64.48%        |
| <b>Total</b>                    | <b>440</b>                     | <b>12.15%</b> | <b>2125</b>     | <b>58.70%</b> |
| <b>UNINCORPORATED AREA</b>      |                                |               |                 |               |
| Precinct TSx                    | 1                              | 12.50%        | 1               | 12.50%        |
| Precinct OS                     | 1535                           | 16.16%        | 3063            | 32.24%        |
| Precinct VBM                    | 6780                           | 18.22%        | 11822           | 31.76%        |
| MB Precinct                     | 856                            | 17.94%        | 1559            | 32.67%        |
| Provisional                     | 165                            | 14.75%        | 393             | 35.12%        |
| <b>Total</b>                    | <b>9337</b>                    | <b>17.74%</b> | <b>16838</b>    | <b>32.00%</b> |
| <b>Total</b>                    |                                |               |                 |               |
| Precinct TSx                    | 1                              | 12.50%        | 1               | 12.50%        |
| Precinct OS                     | 1709                           | 15.54%        | 3880            | 35.28%        |
| Precinct VBM                    | 7486                           | 17.79%        | 14242           | 33.84%        |
| MB Precinct                     | 856                            | 17.94%        | 1559            | 32.67%        |
| Provisional                     | 196                            | 14.30%        | 544             | 39.68%        |
| <b>Total</b>                    | <b>10248</b>                   | <b>17.30%</b> | <b>20226</b>    | <b>34.15%</b> |

**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 5th District |          |               |             |                        |
|------------------------|---|----------|---------------|-------------|------------------------|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | KAI ELLSWORTH<br>(DEM) |
| Jurisdiction Wide      |   |          |               |             |                        |
| 100001.11-CP 100001.11 |   |          |               |             |                        |
| Precinct TSx           | -   | -        | -             | -           | -                      |
| Precinct OS            | -   | -        | -             | -           | -                      |
| Precinct VBM           | -   | -        | -             | -           | -                      |
| MB Precinct            | -   | -        | -             | -           | -                      |
| Provisional            | -   | -        | -             | -           | -                      |
| Total                  | -   | -        | -             | -           | -                      |
| 100002.11-CP 100002.11 |   |          |               |             |                        |
| Precinct TSx           | -   | -        | -             | -           | -                      |
| Precinct OS            | -   | -        | -             | -           | -                      |
| Precinct VBM           | -   | -        | -             | -           | -                      |
| MB Precinct            | -   | -        | -             | -           | -                      |
| Provisional            | -   | -        | -             | -           | -                      |
| Total                  | -   | -        | -             | -           | -                      |
| 100003.11-CP 100003.11 |   |          |               |             |                        |
| Precinct TSx           | -   | -        | -             | -           | -                      |
| Precinct OS            | -   | -        | -             | -           | -                      |
| Precinct VBM           | -   | -        | -             | -           | -                      |
| MB Precinct            | -   | -        | -             | -           | -                      |
| Provisional            | -   | -        | -             | -           | -                      |
| Total                  | -   | -        | -             | -           | -                      |
| 100004.11-CP 100004.11 |   |          |               |             |                        |
| Precinct TSx           | -   | -        | -             | -           | -                      |
| Precinct OS            | -   | -        | -             | -           | -                      |
| Precinct VBM           | -   | -        | -             | -           | -                      |
| MB Precinct            | -   | -        | -             | -           | -                      |
| Provisional            | -   | -        | -             | -           | -                      |
| Total                  | -   | -        | -             | -           | -                      |
| 100005.11-CP 100005.11 |   |          |               |             |                        |
| Precinct TSx           | -   | -        | -             | -           | -                      |
| Precinct OS            | -   | -        | -             | -           | -                      |
| Precinct VBM           | -   | -        | -             | -           | -                      |
| MB Precinct            | -   | -        | -             | -           | -                      |
| Provisional            | -   | -        | -             | -           | -                      |
| Total                  | -   | -        | -             | -           | -                      |
| 100006.11-CP 100006.11 |   |          |               |             |                        |
| Precinct TSx           | -   | -        | -             | -           | -                      |
| Precinct OS            | -   | -        | -             | -           | -                      |
| Precinct VBM           | -   | -        | -             | -           | -                      |
| MB Precinct            | -   | -        | -             | -           | -                      |
| Provisional            | -   | -        | -             | -           | -                      |
| Total                  | -   | -        | -             | -           | -                      |
| 100008.11-CP 100008.11 |   |          |               |             |                        |
| Precinct TSx           | -   | -        | -             | -           | -                      |
| Precinct OS            | -   | -        | -             | -           | -                      |
| Precinct VBM           | -   | -        | -             | -           | -                      |
| MB Precinct            | -   | -        | -             | -           | -                      |
| Provisional            | -   | -        | -             | -           | -                      |
| Total                  | -   | -        | -             | -           | -                      |
| 100009.11-CP 100009.11 |   |          |               |             |                        |
| Precinct TSx           | -   | -        | -             | -           | -                      |
| Precinct OS            | -   | -        | -             | -           | -                      |
| Precinct VBM           | -   | -        | -             | -           | -                      |
| MB Precinct            | -   | -        | -             | -           | -                      |
| Provisional            | -   | -        | -             | -           | -                      |
| Total                  | -   | -        | -             | -           | -                      |
| 100012.11-CP 100012.11 |   |          |               |             |                        |
| Precinct TSx           | -   | -        | -             | -           | -                      |
| Precinct OS            | -   | -        | -             | -           | -                      |
| Precinct VBM           | -   | -        | -             | -           | -                      |



**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 5th District |          |               |             |    | KAI ELLSWORTH<br>(DEM) |   |
|------------------------|---|----------|---------------|-------------|----|------------------------|---|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes |    |                        |   |
| Precinct OS            | -   | -        | -             | -           | -  | -                      | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                      | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                      | - |
| Provisional            | -   | -        | -             | -           | -  | -                      | - |
| Total                  | -   | -        | -             | -           | -  | -                      | - |
| 200010.12-CP 200010.12 |   |          |               |             |    |                        |   |
| Precinct TSx           | -   | -        | -             | -           | -  | -                      | - |
| Precinct OS            | -   | -        | -             | -           | -  | -                      | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                      | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                      | - |
| Provisional            | -   | -        | -             | -           | -  | -                      | - |
| Total                  | -   | -        | -             | -           | -  | -                      | - |
| 200012.12-CP 200012.12 |   |          |               |             |    |                        |   |
| Precinct TSx           | -   | -        | -             | -           | -  | -                      | - |
| Precinct OS            | -   | -        | -             | -           | -  | -                      | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                      | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                      | - |
| Provisional            | -   | -        | -             | -           | -  | -                      | - |
| Total                  | -   | -        | -             | -           | -  | -                      | - |
| 200013.12-CP 200013.12 |   |          |               |             |    |                        |   |
| Precinct TSx           | -   | -        | -             | -           | -  | -                      | - |
| Precinct OS            | -   | -        | -             | -           | -  | -                      | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                      | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                      | - |
| Provisional            | -   | -        | -             | -           | -  | -                      | - |
| Total                  | -   | -        | -             | -           | -  | -                      | - |
| 200014.12-CP 200014.12 |   |          |               |             |    |                        |   |
| Precinct TSx           | -   | -        | -             | -           | -  | -                      | - |
| Precinct OS            | -   | -        | -             | -           | -  | -                      | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                      | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                      | - |
| Provisional            | -   | -        | -             | -           | -  | -                      | - |
| Total                  | -   | -        | -             | -           | -  | -                      | - |
| 200015.02-CP 200015.02 |   |          |               |             |    |                        |   |
| Precinct TSx           | 1832                                      | 1        | 0             | 0           | 0  | -                      | - |
| Precinct OS            | 1832                                      | 1        | 218           | 191         | 16 | 8.38%                  |   |
| Precinct VBM           | 1832                                      | 1        | 886           | 801         | 75 | 9.36%                  |   |
| MB Precinct            | 1832                                      | 1        | 0             | 0           | 0  | -                      |   |
| Provisional            | 1832                                      | 1        | 22            | 20          | 1  | 5.00%                  |   |
| Total                  | 1832                                      | 5        | 1126          | 1012        | 92 | 9.09%                  |   |
| 200016.12-CP 200016.12 |   |          |               |             |    |                        |   |
| Precinct TSx           | -   | -        | -             | -           | -  | -                      | - |
| Precinct OS            | -   | -        | -             | -           | -  | -                      | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                      | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                      | - |
| Provisional            | -   | -        | -             | -           | -  | -                      | - |
| Total                  | -   | -        | -             | -           | -  | -                      | - |
| 200016.13-CP 200016.13 |   |          |               |             |    |                        |   |
| Precinct TSx           | -   | -        | -             | -           | -  | -                      | - |
| Precinct OS            | -   | -        | -             | -           | -  | -                      | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                      | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                      | - |
| Provisional            | -   | -        | -             | -           | -  | -                      | - |
| Total                  | -   | -        | -             | -           | -  | -                      | - |
| 200017.02-CP 200017.02 |   |          |               |             |    |                        |   |
| Precinct TSx           | 1286                                      | 1        | 0             | 0           | 0  | -                      |   |
| Precinct OS            | 1286                                      | 1        | 146           | 135         | 11 | 8.15%                  |   |
| Precinct VBM           | 1286                                      | 1        | 594           | 546         | 46 | 8.42%                  |   |
| MB Precinct            | 1286                                      | 1        | 0             | 0           | 0  | -                      |   |
| Provisional            | 1286                                      | 1        | 12            | 10          | 1  | 10.00%                 |   |
| Total                  | 1286                                      | 5        | 752           | 691         | 58 | 8.39%                  |   |



**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 5th District |          |               |             |                        |        |
|------------------------|---|----------|---------------|-------------|------------------------|--------|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | KAI ELLSWORTH<br>(DEM) |        |
| 200018.02-CP 200018.02 |   |          |               |             |                        |        |
| Precinct TSx           | 1454                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1454                                      | 1        | 169           | 158         | 10                     | 6.33%  |
| Precinct VBM           | 1454                                      | 1        | 704           | 652         | 47                     | 7.21%  |
| MB Precinct            | 1454                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1454                                      | 1        | 13            | 10          | 0                      | 0.00%  |
| Total                  | 1454                                      | 5        | 886           | 820         | 57                     | 6.95%  |
| 200020.02-CP 200020.02 |   |          |               |             |                        |        |
| Precinct TSx           | 1213                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1213                                      | 1        | 139           | 125         | 9                      | 7.20%  |
| Precinct VBM           | 1213                                      | 1        | 526           | 476         | 35                     | 7.35%  |
| MB Precinct            | 1213                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1213                                      | 1        | 19            | 14          | 5                      | 35.71% |
| Total                  | 1213                                      | 5        | 684           | 615         | 49                     | 7.97%  |
| 200021.01-CP 200021.01 |   |          |               |             |                        |        |
| Precinct TSx           | 436                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 436                                       | 1        | 38            | 32          | 5                      | 15.63% |
| Precinct VBM           | 436                                       | 1        | 206           | 190         | 7                      | 3.68%  |
| MB Precinct            | 436                                       | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 436                                       | 1        | 7             | 4           | 0                      | 0.00%  |
| Total                  | 436                                       | 5        | 251           | 226         | 12                     | 5.31%  |
| 200021.02-CP 200021.02 |   |          |               |             |                        |        |
| Precinct TSx           | 1296                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1296                                      | 1        | 170           | 153         | 16                     | 10.46% |
| Precinct VBM           | 1296                                      | 1        | 752           | 661         | 27                     | 4.08%  |
| MB Precinct            | 1296                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1296                                      | 1        | 20            | 16          | 1                      | 6.25%  |
| Total                  | 1296                                      | 5        | 942           | 830         | 44                     | 5.30%  |
| 200024.02-CP 200024.02 |   |          |               |             |                        |        |
| Precinct TSx           | 555                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 555                                       | 1        | 55            | 53          | 1                      | 1.89%  |
| Precinct VBM           | 555                                       | 1        | 282           | 267         | 16                     | 5.99%  |
| MB Precinct            | 555                                       | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 555                                       | 1        | 9             | 7           | 0                      | 0.00%  |
| Total                  | 555                                       | 5        | 346           | 327         | 17                     | 5.20%  |
| 300001.04-CP 300001.04 |   |          |               |             |                        |        |
| Precinct TSx           | 259                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 259                                       | 1        | 12            | 11          | 1                      | 9.09%  |
| Precinct VBM           | 259                                       | 1        | 100           | 95          | 13                     | 13.68% |
| MB Precinct            | 259                                       | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 259                                       | 1        | 0             | 0           | 0                      | -      |
| Total                  | 259                                       | 5        | 112           | 106         | 14                     | 13.21% |
| 300001.05-CP 300001.05 |   |          |               |             |                        |        |
| Precinct TSx           | 731                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 731                                       | 1        | 76            | 69          | 12                     | 17.39% |
| Precinct VBM           | 731                                       | 1        | 330           | 300         | 24                     | 8.00%  |
| MB Precinct            | 731                                       | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 731                                       | 1        | 10            | 9           | 2                      | 22.22% |
| Total                  | 731                                       | 5        | 416           | 378         | 38                     | 10.05% |
| 300003.05-CP 300003.05 |   |          |               |             |                        |        |
| Precinct TSx           | 1932                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1932                                      | 1        | 205           | 172         | 17                     | 9.88%  |
| Precinct VBM           | 1932                                      | 1        | 924           | 837         | 93                     | 11.11% |
| MB Precinct            | 1932                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1932                                      | 1        | 27            | 23          | 4                      | 17.39% |
| Total                  | 1932                                      | 5        | 1156          | 1032        | 114                    | 11.05% |
| 300004.05-CP 300004.05 |   |          |               |             |                        |        |
| Precinct TSx           | 1273                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1273                                      | 1        | 175           | 151         | 26                     | 17.22% |
| Precinct VBM           | 1273                                      | 1        | 576           | 519         | 69                     | 13.29% |
| MB Precinct            | 1273                                      | 1        | 0             | 0           | 0                      | -      |

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|------------------------|---|----------|---------------|-------------|------------------------|--------|--|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | KAI ELLSWORTH<br>(DEM) |        |  |
| Provisional            | 1273                                      | 1        | 17            | 13          | 3                      | 23.08% |  |
| Total                  | 1273                                      | 5        | 768           | 683         | 98                     | 14.35% |  |
| 300005.05-CP 300005.05 |   |          |               |             |                        |        |  |
| Precinct TSx           | 1374                                      | 1        | 0             | 0           | 0                      | -      |  |
| Precinct OS            | 1374                                      | 1        | 163           | 145         | 23                     | 15.86% |  |
| Precinct VBM           | 1374                                      | 1        | 660           | 598         | 51                     | 8.53%  |  |
| MB Precinct            | 1374                                      | 1        | 0             | 0           | 0                      | -      |  |
| Provisional            | 1374                                      | 1        | 26            | 21          | 9                      | 42.86% |  |
| Total                  | 1374                                      | 5        | 849           | 764         | 83                     | 10.86% |  |
| 300008.05-CP 300008.05 |   |          |               |             |                        |        |  |
| Precinct TSx           | 1366                                      | 1        | 0             | 0           | 0                      | -      |  |
| Precinct OS            | 1366                                      | 1        | 139           | 129         | 7                      | 5.43%  |  |
| Precinct VBM           | 1366                                      | 1        | 740           | 706         | 55                     | 7.79%  |  |
| MB Precinct            | 1366                                      | 1        | 0             | 0           | 0                      | -      |  |
| Provisional            | 1366                                      | 1        | 14            | 12          | 3                      | 25.00% |  |
| Total                  | 1366                                      | 5        | 893           | 847         | 65                     | 7.67%  |  |
| 300009.04-CP 300009.04 |   |          |               |             |                        |        |  |
| Precinct TSx           | 873                                       | 1        | 0             | 0           | 0                      | -      |  |
| Precinct OS            | 873                                       | 1        | 121           | 107         | 8                      | 7.48%  |  |
| Precinct VBM           | 873                                       | 1        | 395           | 355         | 30                     | 8.45%  |  |
| MB Precinct            | 873                                       | 1        | 0             | 0           | 0                      | -      |  |
| Provisional            | 873                                       | 1        | 13            | 11          | 0                      | 0.00%  |  |
| Total                  | 873                                       | 5        | 529           | 473         | 38                     | 8.03%  |  |
| 300010.04-CP 300010.04 |   |          |               |             |                        |        |  |
| Precinct TSx           | 1340                                      | 1        | 0             | 0           | 0                      | -      |  |
| Precinct OS            | 1340                                      | 1        | 171           | 153         | 17                     | 11.11% |  |
| Precinct VBM           | 1340                                      | 1        | 656           | 612         | 53                     | 8.66%  |  |
| MB Precinct            | 1340                                      | 1        | 0             | 0           | 0                      | -      |  |
| Provisional            | 1340                                      | 1        | 11            | 11          | 1                      | 9.09%  |  |
| Total                  | 1340                                      | 5        | 838           | 776         | 71                     | 9.15%  |  |
| 300011.04-CP 300011.04 |   |          |               |             |                        |        |  |
| Precinct TSx           | 1491                                      | 1        | 0             | 0           | 0                      | -      |  |
| Precinct OS            | 1491                                      | 1        | 142           | 131         | 12                     | 9.16%  |  |
| Precinct VBM           | 1491                                      | 1        | 711           | 657         | 38                     | 5.78%  |  |
| MB Precinct            | 1491                                      | 1        | 0             | 0           | 0                      | -      |  |
| Provisional            | 1491                                      | 1        | 22            | 18          | 1                      | 5.56%  |  |
| Total                  | 1491                                      | 5        | 875           | 806         | 51                     | 6.33%  |  |
| 300013.05-CP 300013.05 |   |          |               |             |                        |        |  |
| Precinct TSx           | 1490                                      | 1        | 3             | 3           | 0                      | 0.00%  |  |
| Precinct OS            | 1490                                      | 1        | 128           | 119         | 13                     | 10.92% |  |
| Precinct VBM           | 1490                                      | 1        | 721           | 683         | 55                     | 8.05%  |  |
| MB Precinct            | 1490                                      | 1        | 0             | 0           | 0                      | -      |  |
| Provisional            | 1490                                      | 1        | 15            | 13          | 3                      | 23.08% |  |
| Total                  | 1490                                      | 5        | 867           | 818         | 71                     | 8.68%  |  |
| 300015.04-CP 300015.04 |   |          |               |             |                        |        |  |
| Precinct TSx           | 119                                       | 1        | 0             | 0           | 0                      | -      |  |
| Precinct OS            | 119                                       | 1        | 10            | 9           | 1                      | 11.11% |  |
| Precinct VBM           | 119                                       | 1        | 63            | 54          | 0                      | 0.00%  |  |
| MB Precinct            | 119                                       | 1        | 0             | 0           | 0                      | -      |  |
| Provisional            | 119                                       | 1        | 4             | 4           | 0                      | 0.00%  |  |
| Total                  | 119                                       | 5        | 77            | 67          | 1                      | 1.49%  |  |
| 300015.05-CP 300015.05 |   |          |               |             |                        |        |  |
| Precinct TSx           | 503                                       | 1        | 0             | 0           | 0                      | -      |  |
| Precinct OS            | 503                                       | 1        | 66            | 60          | 4                      | 6.67%  |  |
| Precinct VBM           | 503                                       | 1        | 266           | 239         | 23                     | 9.62%  |  |
| MB Precinct            | 503                                       | 1        | 0             | 0           | 0                      | -      |  |
| Provisional            | 503                                       | 1        | 2             | 1           | 0                      | 0.00%  |  |
| Total                  | 503                                       | 5        | 334           | 300         | 27                     | 9.00%  |  |
| 300017.05-CP 300017.05 |   |          |               |             |                        |        |  |
| Precinct TSx           | 1240                                      | 1        | 0             | 0           | 0                      | -      |  |
| Precinct OS            | 1240                                      | 1        | 117           | 108         | 8                      | 7.41%  |  |

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|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | KAI ELLSWORTH<br>(DEM) |        |
| Precinct VBM           | 1240                                      | 1        | 655           | 607         | 46                     | 7.58%  |
| MB Precinct            | 1240                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1240                                      | 1        | 9             | 8           | 2                      | 25.00% |
| Total                  | 1240                                      | 5        | 781           | 723         | 56                     | 7.75%  |
| 300018.04-CP 300018.04 |   |          |               |             |                        |        |
| Precinct TSx           | 1306                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1306                                      | 1        | 148           | 134         | 18                     | 13.43% |
| Precinct VBM           | 1306                                      | 1        | 505           | 464         | 43                     | 9.27%  |
| MB Precinct            | 1306                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1306                                      | 1        | 31            | 24          | 4                      | 16.67% |
| Total                  | 1306                                      | 5        | 684           | 622         | 65                     | 10.45% |
| 300020.04-CP 300020.04 |   |          |               |             |                        |        |
| Precinct TSx           | 1401                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1401                                      | 1        | 168           | 148         | 10                     | 6.76%  |
| Precinct VBM           | 1401                                      | 1        | 632           | 579         | 59                     | 10.19% |
| MB Precinct            | 1401                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1401                                      | 1        | 14            | 13          | 0                      | 0.00%  |
| Total                  | 1401                                      | 5        | 814           | 740         | 69                     | 9.32%  |
| 300021.03-CP 300021.03 |   |          |               |             |                        |        |
| Precinct TSx           | 1234                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1234                                      | 1        | 153           | 134         | 12                     | 8.96%  |
| Precinct VBM           | 1234                                      | 1        | 614           | 569         | 50                     | 8.79%  |
| MB Precinct            | 1234                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1234                                      | 1        | 13            | 13          | 0                      | 0.00%  |
| Total                  | 1234                                      | 5        | 780           | 716         | 62                     | 8.66%  |
| 300022.05-CP 300022.05 |   |          |               |             |                        |        |
| Precinct TSx           | 872                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 872                                       | 1        | 101           | 91          | 9                      | 9.89%  |
| Precinct VBM           | 872                                       | 1        | 356           | 322         | 39                     | 12.11% |
| MB Precinct            | 872                                       | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 872                                       | 1        | 12            | 11          | 2                      | 18.18% |
| Total                  | 872                                       | 5        | 469           | 424         | 50                     | 11.79% |
| 300023.03-CP 300023.03 |   |          |               |             |                        |        |
| Precinct TSx           | 1819                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1819                                      | 1        | 228           | 213         | 14                     | 6.57%  |
| Precinct VBM           | 1819                                      | 1        | 924           | 845         | 62                     | 7.34%  |
| MB Precinct            | 1819                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1819                                      | 1        | 26            | 21          | 6                      | 28.57% |
| Total                  | 1819                                      | 5        | 1178          | 1079        | 82                     | 7.60%  |
| 300023.05-CP 300023.05 |   |          |               |             |                        |        |
| Precinct TSx           | 386                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 386                                       | 1        | 45            | 40          | 4                      | 10.00% |
| Precinct VBM           | 386                                       | 1        | 193           | 178         | 10                     | 5.62%  |
| MB Precinct            | 386                                       | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 386                                       | 1        | 8             | 6           | 1                      | 16.67% |
| Total                  | 386                                       | 5        | 246           | 224         | 15                     | 6.70%  |
| 400001.14-CP 400001.14 |   |          |               |             |                        |        |
| Precinct TSx           | -   | -        | -             | -           | -                      | -      |
| Precinct OS            | -   | -        | -             | -           | -                      | -      |
| Precinct VBM           | -   | -        | -             | -           | -                      | -      |
| MB Precinct            | -   | -        | -             | -           | -                      | -      |
| Provisional            | -   | -        | -             | -           | -                      | -      |
| Total                  | -   | -        | -             | -           | -                      | -      |
| 400002.08-CP 400002.08 |   |          |               |             |                        |        |
| Precinct TSx           | 1508                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1508                                      | 1        | 200           | 179         | 12                     | 6.70%  |
| Precinct VBM           | 1508                                      | 1        | 730           | 675         | 27                     | 4.00%  |
| MB Precinct            | 1508                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1508                                      | 1        | 15            | 12          | 0                      | 0.00%  |
| Total                  | 1508                                      | 5        | 945           | 866         | 39                     | 4.50%  |
| 400003.08-CP 400003.08 |   |          |               |             |                        |        |

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|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | KAI ELLSWORTH<br>(DEM) |        |
| Precinct TSx           | 1800                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1800                                      | 1        | 245           | 222         | 18                     | 8.11%  |
| Precinct VBM           | 1800                                      | 1        | 773           | 696         | 49                     | 7.04%  |
| MB Precinct            | 1800                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1800                                      | 1        | 35            | 29          | 6                      | 20.69% |
| Total                  | 1800                                      | 5        | 1053          | 947         | 73                     | 7.71%  |
| 400004.14-CP 400004.14 |   |          |               |             |                        |        |
| Precinct TSx           | -   | -        | -             | -           | -                      | -      |
| Precinct OS            | -   | -        | -             | -           | -                      | -      |
| Precinct VBM           | -   | -        | -             | -           | -                      | -      |
| MB Precinct            | -   | -        | -             | -           | -                      | -      |
| Provisional            | -   | -        | -             | -           | -                      | -      |
| Total                  | -   | -        | -             | -           | -                      | -      |
| 400005.08-CP 400005.08 |   |          |               |             |                        |        |
| Precinct TSx           | 655                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 655                                       | 1        | 79            | 73          | 2                      | 2.74%  |
| Precinct VBM           | 655                                       | 1        | 262           | 235         | 26                     | 11.06% |
| MB Precinct            | 655                                       | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 655                                       | 1        | 11            | 10          | 0                      | 0.00%  |
| Total                  | 655                                       | 5        | 352           | 318         | 28                     | 8.81%  |
| 400006.08-CP 400006.08 |   |          |               |             |                        |        |
| Precinct TSx           | 1537                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1537                                      | 1        | 228           | 205         | 21                     | 10.24% |
| Precinct VBM           | 1537                                      | 1        | 722           | 655         | 64                     | 9.77%  |
| MB Precinct            | 1537                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1537                                      | 1        | 19            | 13          | 2                      | 15.38% |
| Total                  | 1537                                      | 5        | 969           | 873         | 87                     | 9.97%  |
| 400007.08-CP 400007.08 |   |          |               |             |                        |        |
| Precinct TSx           | 1766                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1766                                      | 1        | 228           | 209         | 23                     | 11.00% |
| Precinct VBM           | 1766                                      | 1        | 765           | 708         | 76                     | 10.73% |
| MB Precinct            | 1766                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1766                                      | 1        | 22            | 19          | 4                      | 21.05% |
| Total                  | 1766                                      | 5        | 1015          | 936         | 103                    | 11.00% |
| 400010.08-CP 400010.08 |   |          |               |             |                        |        |
| Precinct TSx           | 1875                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1875                                      | 1        | 244           | 224         | 35                     | 15.63% |
| Precinct VBM           | 1875                                      | 1        | 869           | 816         | 96                     | 11.76% |
| MB Precinct            | 1875                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1875                                      | 1        | 31            | 20          | 2                      | 10.00% |
| Total                  | 1875                                      | 5        | 1144          | 1060        | 133                    | 12.55% |
| 400012.08-CP 400012.08 |   |          |               |             |                        |        |
| Precinct TSx           | 877                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 877                                       | 1        | 113           | 103         | 8                      | 7.77%  |
| Precinct VBM           | 877                                       | 1        | 434           | 402         | 34                     | 8.46%  |
| MB Precinct            | 877                                       | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 877                                       | 1        | 22            | 17          | 2                      | 11.76% |
| Total                  | 877                                       | 5        | 569           | 522         | 44                     | 8.43%  |
| 400013.08-CP 400013.08 |   |          |               |             |                        |        |
| Precinct TSx           | 758                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 758                                       | 1        | 127           | 109         | 5                      | 4.59%  |
| Precinct VBM           | 758                                       | 1        | 315           | 289         | 17                     | 5.88%  |
| MB Precinct            | 758                                       | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 758                                       | 1        | 4             | 3           | 1                      | 33.33% |
| Total                  | 758                                       | 5        | 446           | 401         | 23                     | 5.74%  |
| 400014.08-CP 400014.08 |   |          |               |             |                        |        |
| Precinct TSx           | 1526                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1526                                      | 1        | 230           | 201         | 13                     | 6.47%  |
| Precinct VBM           | 1526                                      | 1        | 736           | 674         | 36                     | 5.34%  |
| MB Precinct            | 1526                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1526                                      | 1        | 36            | 34          | 5                      | 14.71% |

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|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | KAI ELLSWORTH<br>(DEM) |        |
| Total                  | 1526                                      | 5        | 1002          | 909         | 54                     | 5.94%  |
| 400017.08-CP 400017.08 |   |          |               |             |                        |        |
| Precinct TSx           | 1495                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1495                                      | 1        | 175           | 157         | 7                      | 4.46%  |
| Precinct VBM           | 1495                                      | 1        | 752           | 694         | 44                     | 6.34%  |
| MB Precinct            | 1495                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1495                                      | 1        | 28            | 21          | 2                      | 9.52%  |
| Total                  | 1495                                      | 5        | 955           | 872         | 53                     | 6.08%  |
| 400018.08-CP 400018.08 |   |          |               |             |                        |        |
| Precinct TSx           | 597                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 597                                       | 1        | 86            | 75          | 3                      | 4.00%  |
| Precinct VBM           | 597                                       | 1        | 276           | 240         | 10                     | 4.17%  |
| MB Precinct            | 597                                       | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 597                                       | 1        | 7             | 3           | 0                      | 0.00%  |
| Total                  | 597                                       | 5        | 369           | 318         | 13                     | 4.09%  |
| 400020.08-CP 400020.08 |   |          |               |             |                        |        |
| Precinct TSx           | 724                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 724                                       | 1        | 114           | 103         | 7                      | 6.80%  |
| Precinct VBM           | 724                                       | 1        | 343           | 316         | 32                     | 10.13% |
| MB Precinct            | 724                                       | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 724                                       | 1        | 2             | 2           | 0                      | 0.00%  |
| Total                  | 724                                       | 5        | 459           | 421         | 39                     | 9.26%  |
| 400021.07-CP 400021.07 |   |          |               |             |                        |        |
| Precinct TSx           | 1332                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1332                                      | 1        | 127           | 115         | 10                     | 8.70%  |
| Precinct VBM           | 1332                                      | 1        | 767           | 709         | 37                     | 5.22%  |
| MB Precinct            | 1332                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1332                                      | 1        | 18            | 12          | 2                      | 16.67% |
| Total                  | 1332                                      | 5        | 912           | 836         | 49                     | 5.86%  |
| 500001.10-CP 500001.10 |   |          |               |             |                        |        |
| Precinct TSx           | 1717                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1717                                      | 1        | 210           | 182         | 22                     | 12.09% |
| Precinct VBM           | 1717                                      | 1        | 617           | 545         | 68                     | 12.48% |
| MB Precinct            | 1717                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1717                                      | 1        | 19            | 12          | 6                      | 50.00% |
| Total                  | 1717                                      | 5        | 846           | 739         | 96                     | 12.99% |
| 500002.10-CP 500002.10 |   |          |               |             |                        |        |
| Precinct TSx           | 1381                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1381                                      | 1        | 137           | 117         | 27                     | 23.08% |
| Precinct VBM           | 1381                                      | 1        | 412           | 372         | 68                     | 18.28% |
| MB Precinct            | 1381                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1381                                      | 1        | 31            | 23          | 9                      | 39.13% |
| Total                  | 1381                                      | 5        | 580           | 512         | 104                    | 20.31% |
| 500003.10-CP 500003.10 |   |          |               |             |                        |        |
| Precinct TSx           | 1065                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1065                                      | 1        | 131           | 110         | 19                     | 17.27% |
| Precinct VBM           | 1065                                      | 1        | 346           | 302         | 50                     | 16.56% |
| MB Precinct            | 1065                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1065                                      | 1        | 11            | 6           | 3                      | 50.00% |
| Total                  | 1065                                      | 5        | 488           | 418         | 72                     | 17.22% |
| 500004.10-CP 500004.10 |   |          |               |             |                        |        |
| Precinct TSx           | 981                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 981                                       | 1        | 122           | 103         | 26                     | 25.24% |
| Precinct VBM           | 981                                       | 1        | 264           | 219         | 42                     | 19.18% |
| MB Precinct            | 981                                       | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 981                                       | 1        | 21            | 18          | 5                      | 27.78% |
| Total                  | 981                                       | 5        | 407           | 340         | 73                     | 21.47% |
| 500005.10-CP 500005.10 |   |          |               |             |                        |        |
| Precinct TSx           | 946                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 946                                       | 1        | 107           | 94          | 19                     | 20.21% |
| Precinct VBM           | 946                                       | 1        | 250           | 224         | 41                     | 18.30% |

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|                        | Member of the State Assembly 5th District |          |               |             |                        |        |
|------------------------|---|----------|---------------|-------------|------------------------|--------|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | KAI ELLSWORTH<br>(DEM) |        |
| MB Precinct            | 946                                       | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 946                                       | 1        | 16            | 12          | 5                      | 41.67% |
| Total                  | 946                                       | 5        | 373           | 330         | 65                     | 19.70% |
| 500006.10-CP 500006.10 |   |          |               |             |                        |        |
| Precinct TSx           | 1661                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1661                                      | 1        | 190           | 156         | 41                     | 26.28% |
| Precinct VBM           | 1661                                      | 1        | 498           | 431         | 87                     | 20.19% |
| MB Precinct            | 1661                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1661                                      | 1        | 58            | 42          | 19                     | 45.24% |
| Total                  | 1661                                      | 5        | 746           | 629         | 147                    | 23.37% |
| 500007.10-CP 500007.10 |   |          |               |             |                        |        |
| Precinct TSx           | 1788                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1788                                      | 1        | 208           | 184         | 44                     | 23.91% |
| Precinct VBM           | 1788                                      | 1        | 688           | 606         | 68                     | 11.22% |
| MB Precinct            | 1788                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1788                                      | 1        | 20            | 17          | 3                      | 17.65% |
| Total                  | 1788                                      | 5        | 916           | 807         | 115                    | 14.25% |
| 500009.10-CP 500009.10 |   |          |               |             |                        |        |
| Precinct TSx           | 1073                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1073                                      | 1        | 121           | 107         | 28                     | 26.17% |
| Precinct VBM           | 1073                                      | 1        | 304           | 251         | 36                     | 14.34% |
| MB Precinct            | 1073                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1073                                      | 1        | 38            | 27          | 7                      | 25.93% |
| Total                  | 1073                                      | 5        | 463           | 385         | 71                     | 18.44% |
| 500011.10-CP 500011.10 |   |          |               |             |                        |        |
| Precinct TSx           | 1635                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1635                                      | 1        | 178           | 155         | 40                     | 25.81% |
| Precinct VBM           | 1635                                      | 1        | 380           | 334         | 49                     | 14.67% |
| MB Precinct            | 1635                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1635                                      | 1        | 40            | 33          | 10                     | 30.30% |
| Total                  | 1635                                      | 5        | 598           | 522         | 99                     | 18.97% |
| 500012.10-CP 500012.10 |   |          |               |             |                        |        |
| Precinct TSx           | 1574                                      | 1        | 1             | 1           | 0                      | 0.00%  |
| Precinct OS            | 1574                                      | 1        | 195           | 161         | 36                     | 22.36% |
| Precinct VBM           | 1574                                      | 1        | 601           | 542         | 69                     | 12.73% |
| MB Precinct            | 1574                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1574                                      | 1        | 37            | 27          | 11                     | 40.74% |
| Total                  | 1574                                      | 5        | 834           | 731         | 116                    | 15.87% |
| 500015.10-CP 500015.10 |   |          |               |             |                        |        |
| Precinct TSx           | 978                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 978                                       | 1        | 105           | 90          | 19                     | 21.11% |
| Precinct VBM           | 978                                       | 1        | 407           | 355         | 29                     | 8.17%  |
| MB Precinct            | 978                                       | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 978                                       | 1        | 13            | 10          | 2                      | 20.00% |
| Total                  | 978                                       | 5        | 525           | 455         | 50                     | 10.99% |
| 500020.10-CP 500020.10 |   |          |               |             |                        |        |
| Precinct TSx           | 1530                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1530                                      | 1        | 161           | 145         | 10                     | 6.90%  |
| Precinct VBM           | 1530                                      | 1        | 659           | 622         | 60                     | 9.65%  |
| MB Precinct            | 1530                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1530                                      | 1        | 27            | 23          | 3                      | 13.04% |
| Total                  | 1530                                      | 5        | 847           | 790         | 73                     | 9.24%  |
| 500021.10-CP 500021.10 |   |          |               |             |                        |        |
| Precinct TSx           | 1404                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1404                                      | 1        | 169           | 154         | 21                     | 13.64% |
| Precinct VBM           | 1404                                      | 1        | 632           | 578         | 48                     | 8.30%  |
| MB Precinct            | 1404                                      | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 1404                                      | 1        | 28            | 23          | 5                      | 21.74% |
| Total                  | 1404                                      | 5        | 829           | 755         | 74                     | 9.80%  |
| 95U201.01-MB 95U201.01 |   |          |               |             |                        |        |
| Precinct TSx           | 8   | 1        | 0             | 0           | 0                      | -      |

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|------------------------|---|----------|---------------|-------------|------------------------|--------|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | KAI ELLSWORTH<br>(DEM) |        |
| Precinct OS            | 8   | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 8   | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 8   | 1        | 7             | 6           | 1                      | 16.67% |
| Provisional            | 8   | 1        | 0             | 0           | 0                      | -      |
| Total                  | 8   | 5        | 7             | 6           | 1                      | 16.67% |
| 95U201.02-MB 95U201.02 |   |          |               |             |                        |        |
| Precinct TSx           | 541                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 541                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 541                                       | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 541                                       | 1        | 358           | 318         | 21                     | 6.60%  |
| Provisional            | 541                                       | 1        | 1             | 1           | 0                      | 0.00%  |
| Total                  | 541                                       | 5        | 359           | 319         | 21                     | 6.58%  |
| 95U202.02-MB 95U202.02 |   |          |               |             |                        |        |
| Precinct TSx           | 859                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 859                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 859                                       | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 859                                       | 1        | 574           | 525         | 33                     | 6.29%  |
| Provisional            | 859                                       | 1        | 4             | 3           | 0                      | 0.00%  |
| Total                  | 859                                       | 5        | 578           | 528         | 33                     | 6.25%  |
| 95U203.02-MB 95U203.02 |   |          |               |             |                        |        |
| Precinct TSx           | 0   | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 0   | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 0   | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 0   | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 0   | 1        | 0             | 0           | 0                      | -      |
| Total                  | 0   | 5        | 0             | 0           | 0                      | -      |
| 95U204.01-MB 95U204.01 |   |          |               |             |                        |        |
| Precinct TSx           | 895                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 895                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 895                                       | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 895                                       | 1        | 651           | 612         | 44                     | 7.19%  |
| Provisional            | 895                                       | 1        | 2             | 2           | 0                      | 0.00%  |
| Total                  | 895                                       | 5        | 653           | 614         | 44                     | 7.17%  |
| 95U301.05-MB 95U301.05 |   |          |               |             |                        |        |
| Precinct TSx           | 244                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 244                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 244                                       | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 244                                       | 1        | 151           | 141         | 14                     | 9.93%  |
| Provisional            | 244                                       | 1        | 2             | 1           | 0                      | 0.00%  |
| Total                  | 244                                       | 5        | 153           | 142         | 14                     | 9.86%  |
| 95U302.04-MB 95U302.04 |   |          |               |             |                        |        |
| Precinct TSx           | 0   | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 0   | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 0   | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 0   | 1        | 1             | 1           | 0                      | 0.00%  |
| Provisional            | 0   | 1        | 0             | 0           | 0                      | -      |
| Total                  | 0   | 5        | 1             | 1           | 0                      | 0.00%  |
| 95U302.05-MB 95U302.05 |   |          |               |             |                        |        |
| Precinct TSx           | 128                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 128                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 128                                       | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 128                                       | 1        | 86            | 84          | 10                     | 11.90% |
| Provisional            | 128                                       | 1        | 0             | 0           | 0                      | -      |
| Total                  | 128                                       | 5        | 86            | 84          | 10                     | 11.90% |
| 95U303.03-MB 95U303.03 |   |          |               |             |                        |        |
| Precinct TSx           | 144                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 144                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 144                                       | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 144                                       | 1        | 84            | 82          | 11                     | 13.41% |
| Provisional            | 144                                       | 1        | 0             | 0           | 0                      | -      |
| Total                  | 144                                       | 5        | 84            | 82          | 11                     | 13.41% |

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|------------------------|---|----------|---------------|-------------|------------------------|--------|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | KAI ELLSWORTH<br>(DEM) |        |
| 95U303.05-MB 95U303.05 |   |          |               |             |                        |        |
| Precinct TSx           | 159                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 159                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 159                                       | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 159                                       | 1        | 111           | 99          | 2                      | 2.02%  |
| Provisional            | 159                                       | 1        | 1             | 1           | 0                      | 0.00%  |
| Total                  | 159                                       | 5        | 112           | 100         | 2                      | 2.00%  |
| 95U304.05-MB 95U304.05 |   |          |               |             |                        |        |
| Precinct TSx           | 0   | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 0   | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 0   | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 0   | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 0   | 1        | 0             | 0           | 0                      | -      |
| Total                  | 0   | 5        | 0             | 0           | 0                      | -      |
| 95U305.05-MB 95U305.05 |   |          |               |             |                        |        |
| Precinct TSx           | 7   | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 7   | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 7   | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 7   | 1        | 7             | 7           | 0                      | 0.00%  |
| Provisional            | 7   | 1        | 0             | 0           | 0                      | -      |
| Total                  | 7   | 5        | 7             | 7           | 0                      | 0.00%  |
| 95U401.08-MB 95U401.08 |   |          |               |             |                        |        |
| Precinct TSx           | 39  | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 39  | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 39  | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 39  | 1        | 16            | 13          | 0                      | 0.00%  |
| Provisional            | 39  | 1        | 0             | 0           | 0                      | -      |
| Total                  | 39  | 5        | 16            | 13          | 0                      | 0.00%  |
| 95U402.07-MB 95U402.07 |   |          |               |             |                        |        |
| Precinct TSx           | 413                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 413                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 413                                       | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 413                                       | 1        | 256           | 236         | 20                     | 8.47%  |
| Provisional            | 413                                       | 1        | 1             | 1           | 0                      | 0.00%  |
| Total                  | 413                                       | 5        | 257           | 237         | 20                     | 8.44%  |
| 95U402.08-MB 95U402.08 |   |          |               |             |                        |        |
| Precinct TSx           | 873                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 873                                       | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 873                                       | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 873                                       | 1        | 566           | 514         | 20                     | 3.89%  |
| Provisional            | 873                                       | 1        | 3             | 2           | 0                      | 0.00%  |
| Total                  | 873                                       | 5        | 569           | 516         | 20                     | 3.88%  |
| 95U403.08-MB 95U403.08 |   |          |               |             |                        |        |
| Precinct TSx           | 1591                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 1591                                      | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 1591                                      | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 1591                                      | 1        | 1042          | 965         | 103                    | 10.67% |
| Provisional            | 1591                                      | 1        | 13            | 10          | 3                      | 30.00% |
| Total                  | 1591                                      | 5        | 1055          | 975         | 106                    | 10.87% |
| 95U404.06-MB 95U404.06 |   |          |               |             |                        |        |
| Precinct TSx           | 0   | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 0   | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 0   | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 0   | 1        | 0             | 0           | 0                      | -      |
| Provisional            | 0   | 1        | 0             | 0           | 0                      | -      |
| Total                  | 0   | 5        | 0             | 0           | 0                      | -      |
| 95U404.08-MB 95U404.08 |   |          |               |             |                        |        |
| Precinct TSx           | 0   | 1        | 0             | 0           | 0                      | -      |
| Precinct OS            | 0   | 1        | 0             | 0           | 0                      | -      |
| Precinct VBM           | 0   | 1        | 0             | 0           | 0                      | -      |
| MB Precinct            | 0   | 1        | 0             | 0           | 0                      | -      |



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|-------------------------------|---|----------|---------------|-------------|------------------------|---------|
|                               | Reg. Voters                               | Vote For | Times Counted | Total Votes | KAI ELLSWORTH<br>(DEM) |         |
| Provisional                   | 0   | 1        | 0             | 0           | 0                      | -       |
| Total                         | 0   | 5        | 0             | 0           | 0                      | -       |
| 95U501.09-MB 95U501.09        |   |          |               |             |                        |         |
| Precinct TSx                  | 0   | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                   | 0   | 1        | 0             | 0           | 0                      | -       |
| Precinct VBM                  | 0   | 1        | 0             | 0           | 0                      | -       |
| MB Precinct                   | 0   | 1        | 2             | 2           | 2                      | 100.00% |
| Provisional                   | 0   | 1        | 0             | 0           | 0                      | -       |
| Total                         | 0   | 5        | 2             | 2           | 2                      | 100.00% |
| 95U501.10-MB 95U501.10        |   |          |               |             |                        |         |
| Precinct TSx                  | 286                                       | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                   | 286                                       | 1        | 0             | 0           | 0                      | -       |
| Precinct VBM                  | 286                                       | 1        | 0             | 0           | 0                      | -       |
| MB Precinct                   | 286                                       | 1        | 172           | 157         | 14                     | 8.92%   |
| Provisional                   | 286                                       | 1        | 2             | 2           | 0                      | 0.00%   |
| Total                         | 286                                       | 5        | 174           | 159         | 14                     | 8.81%   |
| 95U598.10-MB 95U598.10        |   |          |               |             |                        |         |
| Precinct TSx                  | 568                                       | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                   | 568                                       | 1        | 0             | 0           | 0                      | -       |
| Precinct VBM                  | 568                                       | 1        | 0             | 0           | 0                      | -       |
| MB Precinct                   | 568                                       | 1        | 239           | 192         | 48                     | 25.00%  |
| Provisional                   | 568                                       | 1        | 3             | 1           | 0                      | 0.00%   |
| Total                         | 568                                       | 5        | 242           | 193         | 48                     | 24.87%  |
| 95U599.10-MB 95U599.10        |   |          |               |             |                        |         |
| Precinct TSx                  | 75  | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                   | 75  | 1        | 0             | 0           | 0                      | -       |
| Precinct VBM                  | 75  | 1        | 0             | 0           | 0                      | -       |
| MB Precinct                   | 75  | 1        | 38            | 33          | 4                      | 12.12%  |
| Provisional                   | 75  | 1        | 0             | 0           | 0                      | -       |
| Total                         | 75  | 5        | 38            | 33          | 4                      | 12.12%  |
| 96U101.11-MB 96U101.11        |   |          |               |             |                        |         |
| Precinct TSx                  | -   | -        | -             | -           | -                      | -       |
| Precinct OS                   | -   | -        | -             | -           | -                      | -       |
| Precinct VBM                  | -   | -        | -             | -           | -                      | -       |
| MB Precinct                   | -   | -        | -             | -           | -                      | -       |
| Provisional                   | -   | -        | -             | -           | -                      | -       |
| Total                         | -   | -        | -             | -           | -                      | -       |
| 96U401.14-MB 96U401.14        |   |          |               |             |                        |         |
| Precinct TSx                  | -   | -        | -             | -           | -                      | -       |
| Precinct OS                   | -   | -        | -             | -           | -                      | -       |
| Precinct VBM                  | -   | -        | -             | -           | -                      | -       |
| MB Precinct                   | -   | -        | -             | -           | -                      | -       |
| Provisional                   | -   | -        | -             | -           | -                      | -       |
| Total                         | -   | -        | -             | -           | -                      | -       |
| Total                         |   |          |               |             |                        |         |
| Precinct TSx                  | 70961                                     | 75       | 4             | 4           | 0                      | 0.00%   |
| Precinct OS                   | 70961                                     | 75       | 7627          | 6791        | 807                    | 11.88%  |
| Precinct VBM                  | 70961                                     | 75       | 28225         | 25719       | 2360                   | 9.18%   |
| MB Precinct                   | 70961                                     | 75       | 4361          | 3987        | 347                    | 8.70%   |
| Provisional                   | 70961                                     | 75       | 1038          | 821         | 169                    | 20.58%  |
| Total                         | 70961                                     | 375      | 41255         | 37322       | 3683                   | 9.87%   |
| STATE BOARD OF EQUALIZATION   |   |          |               |             |                        |         |
| STATE BOARD OF EQUALIZATION 1 |   |          |               |             |                        |         |
| Precinct TSx                  | 70961                                     | 75       | 4             | 4           | 0                      | 0.00%   |
| Precinct OS                   | 70961                                     | 75       | 7627          | 6791        | 807                    | 11.88%  |
| Precinct VBM                  | 70961                                     | 75       | 28225         | 25719       | 2360                   | 9.18%   |
| MB Precinct                   | 70961                                     | 75       | 4361          | 3987        | 347                    | 8.70%   |
| Provisional                   | 70961                                     | 75       | 1038          | 821         | 169                    | 20.58%  |
| Total                         | 70961                                     | 375      | 41255         | 37322       | 3683                   | 9.87%   |
| Total                         |   |          |               |             |                        |         |

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|-------------------------------------|---|------------|---------------|--------------|------------------------|--------------|--|
|                                     | Reg. Voters                               | Vote For   | Times Counted | Total Votes  | KAI ELLSWORTH<br>(DEM) |              |  |
| Precinct TSx                        | 70961                                     | 75         | 4             | 4            | 0                      | 0.00%        |  |
| Precinct OS                         | 70961                                     | 75         | 7627          | 6791         | 807                    | 11.88%       |  |
| Precinct VBM                        | 70961                                     | 75         | 28225         | 25719        | 2360                   | 9.18%        |  |
| MB Precinct                         | 70961                                     | 75         | 4361          | 3987         | 347                    | 8.70%        |  |
| Provisional                         | 70961                                     | 75         | 1038          | 821          | 169                    | 20.58%       |  |
| <b>Total</b>                        | <b>70961</b>                              | <b>375</b> | <b>41255</b>  | <b>37322</b> | <b>3683</b>            | <b>9.87%</b> |  |
| <b>UNITED STATES SENATE</b>         |   |            |               |              |                        |              |  |
| <b>UNITED STATES SENATE</b>         |   |            |               |              |                        |              |  |
| Precinct TSx                        | 70961                                     | 75         | 4             | 4            | 0                      | 0.00%        |  |
| Precinct OS                         | 70961                                     | 75         | 7627          | 6791         | 807                    | 11.88%       |  |
| Precinct VBM                        | 70961                                     | 75         | 28225         | 25719        | 2360                   | 9.18%        |  |
| MB Precinct                         | 70961                                     | 75         | 4361          | 3987         | 347                    | 8.70%        |  |
| Provisional                         | 70961                                     | 75         | 1038          | 821          | 169                    | 20.58%       |  |
| <b>Total</b>                        | <b>70961</b>                              | <b>375</b> | <b>41255</b>  | <b>37322</b> | <b>3683</b>            | <b>9.87%</b> |  |
| <b>Total</b>                        |   |            |               |              |                        |              |  |
| Precinct TSx                        | 70961                                     | 75         | 4             | 4            | 0                      | 0.00%        |  |
| Precinct OS                         | 70961                                     | 75         | 7627          | 6791         | 807                    | 11.88%       |  |
| Precinct VBM                        | 70961                                     | 75         | 28225         | 25719        | 2360                   | 9.18%        |  |
| MB Precinct                         | 70961                                     | 75         | 4361          | 3987         | 347                    | 8.70%        |  |
| Provisional                         | 70961                                     | 75         | 1038          | 821          | 169                    | 20.58%       |  |
| <b>Total</b>                        | <b>70961</b>                              | <b>375</b> | <b>41255</b>  | <b>37322</b> | <b>3683</b>            | <b>9.87%</b> |  |
| <b>UNITED STATES REPRESENTATIVE</b> |   |            |               |              |                        |              |  |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |   |            |               |              |                        |              |  |
| Precinct TSx                        | 70961                                     | 75         | 4             | 4            | 0                      | 0.00%        |  |
| Precinct OS                         | 70961                                     | 75         | 7627          | 6791         | 807                    | 11.88%       |  |
| Precinct VBM                        | 70961                                     | 75         | 28225         | 25719        | 2360                   | 9.18%        |  |
| MB Precinct                         | 70961                                     | 75         | 4361          | 3987         | 347                    | 8.70%        |  |
| Provisional                         | 70961                                     | 75         | 1038          | 821          | 169                    | 20.58%       |  |
| <b>Total</b>                        | <b>70961</b>                              | <b>375</b> | <b>41255</b>  | <b>37322</b> | <b>3683</b>            | <b>9.87%</b> |  |
| <b>Total</b>                        |   |            |               |              |                        |              |  |
| Precinct TSx                        | 70961                                     | 75         | 4             | 4            | 0                      | 0.00%        |  |
| Precinct OS                         | 70961                                     | 75         | 7627          | 6791         | 807                    | 11.88%       |  |
| Precinct VBM                        | 70961                                     | 75         | 28225         | 25719        | 2360                   | 9.18%        |  |
| MB Precinct                         | 70961                                     | 75         | 4361          | 3987         | 347                    | 8.70%        |  |
| Provisional                         | 70961                                     | 75         | 1038          | 821          | 169                    | 20.58%       |  |
| <b>Total</b>                        | <b>70961</b>                              | <b>375</b> | <b>41255</b>  | <b>37322</b> | <b>3683</b>            | <b>9.87%</b> |  |
| <b>STATE SENATE</b>                 |   |            |               |              |                        |              |  |
| <b>1ST STATE SENATE DISTRICT</b>    |   |            |               |              |                        |              |  |
| Precinct TSx                        | 70961                                     | 75         | 4             | 4            | 0                      | 0.00%        |  |
| Precinct OS                         | 70961                                     | 75         | 7627          | 6791         | 807                    | 11.88%       |  |
| Precinct VBM                        | 70961                                     | 75         | 28225         | 25719        | 2360                   | 9.18%        |  |
| MB Precinct                         | 70961                                     | 75         | 4361          | 3987         | 347                    | 8.70%        |  |
| Provisional                         | 70961                                     | 75         | 1038          | 821          | 169                    | 20.58%       |  |
| <b>Total</b>                        | <b>70961</b>                              | <b>375</b> | <b>41255</b>  | <b>37322</b> | <b>3683</b>            | <b>9.87%</b> |  |
| <b>Total</b>                        |   |            |               |              |                        |              |  |
| Precinct TSx                        | 70961                                     | 75         | 4             | 4            | 0                      | 0.00%        |  |
| Precinct OS                         | 70961                                     | 75         | 7627          | 6791         | 807                    | 11.88%       |  |
| Precinct VBM                        | 70961                                     | 75         | 28225         | 25719        | 2360                   | 9.18%        |  |
| MB Precinct                         | 70961                                     | 75         | 4361          | 3987         | 347                    | 8.70%        |  |
| Provisional                         | 70961                                     | 75         | 1038          | 821          | 169                    | 20.58%       |  |
| <b>Total</b>                        | <b>70961</b>                              | <b>375</b> | <b>41255</b>  | <b>37322</b> | <b>3683</b>            | <b>9.87%</b> |  |
| <b>STATE ASSEMBLY DISTRICTS</b>     |   |            |               |              |                        |              |  |
| <b>5TH STATE ASSEMBLY DIST</b>      |   |            |               |              |                        |              |  |
| Precinct TSx                        | 70961                                     | 75         | 4             | 4            | 0                      | 0.00%        |  |
| Precinct OS                         | 70961                                     | 75         | 7627          | 6791         | 807                    | 11.88%       |  |
| Precinct VBM                        | 70961                                     | 75         | 28225         | 25719        | 2360                   | 9.18%        |  |
| MB Precinct                         | 70961                                     | 75         | 4361          | 3987         | 347                    | 8.70%        |  |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                   | Member of the State Assembly 5th District |          |               |             |                        |        |
|-----------------------------------|---|----------|---------------|-------------|------------------------|--------|
|                                   | Reg. Voters                               | Vote For | Times Counted | Total Votes | KAI ELLSWORTH<br>(DEM) |        |
| Provisional                       | 70961                                     | 75       | 1038          | 821         | 169                    | 20.58% |
| Total                             | 70961                                     | 375      | 41255         | 37322       | 3683                   | 9.87%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |   |          |               |             |                        |        |
| Precinct TSx                      | 0   | 0        | 0             | 0           | 0                      | -      |
| Precinct OS                       | 0   | 0        | 0             | 0           | 0                      | -      |
| Precinct VBM                      | 0   | 0        | 0             | 0           | 0                      | -      |
| MB Precinct                       | 0   | 0        | 0             | 0           | 0                      | -      |
| Provisional                       | 0   | 0        | 0             | 0           | 0                      | -      |
| Total                             | 0   | 0        | 0             | 0           | 0                      | -      |
| Total                             | 70961                                     | 75       | 4             | 4           | 0                      | 0.00%  |
| Precinct OS                       | 70961                                     | 75       | 7627          | 6791        | 807                    | 11.88% |
| Precinct VBM                      | 70961                                     | 75       | 28225         | 25719       | 2360                   | 9.18%  |
| MB Precinct                       | 70961                                     | 75       | 4361          | 3987        | 347                    | 8.70%  |
| Provisional                       | 70961                                     | 75       | 1038          | 821         | 169                    | 20.58% |
| Total                             | 70961                                     | 375      | 41255         | 37322       | 3683                   | 9.87%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |   |          |               |             |                        |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |   |          |               |             |                        |        |
| Precinct TSx                      | 0   | 0        | 0             | 0           | 0                      | -      |
| Precinct OS                       | 0   | 0        | 0             | 0           | 0                      | -      |
| Precinct VBM                      | 0   | 0        | 0             | 0           | 0                      | -      |
| MB Precinct                       | 0   | 0        | 0             | 0           | 0                      | -      |
| Provisional                       | 0   | 0        | 0             | 0           | 0                      | -      |
| Total                             | 0   | 0        | 0             | 0           | 0                      | -      |
| <b>2ND SUPERVISORIAL DISTRICT</b> |   |          |               |             |                        |        |
| Precinct TSx                      | 11242                                     | 13       | 0             | 0           | 0                      | -      |
| Precinct OS                       | 11242                                     | 13       | 1029          | 934         | 75                     | 8.03%  |
| Precinct VBM                      | 11242                                     | 13       | 4402          | 4010        | 284                    | 7.08%  |
| MB Precinct                       | 11242                                     | 13       | 1590          | 1461        | 99                     | 6.78%  |
| Provisional                       | 11242                                     | 13       | 130           | 103         | 11                     | 10.68% |
| Total                             | 11242                                     | 65       | 7151          | 6508        | 469                    | 7.21%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |   |          |               |             |                        |        |
| Precinct TSx                      | 21691                                     | 26       | 3             | 3           | 0                      | 0.00%  |
| Precinct OS                       | 21691                                     | 26       | 2368          | 2124        | 216                    | 10.17% |
| Precinct VBM                      | 21691                                     | 26       | 10021         | 9219        | 813                    | 8.82%  |
| MB Precinct                       | 21691                                     | 26       | 440           | 414         | 37                     | 8.94%  |
| Provisional                       | 21691                                     | 26       | 277           | 234         | 41                     | 17.52% |
| Total                             | 21691                                     | 130      | 13109         | 11994       | 1107                   | 9.23%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |   |          |               |             |                        |        |
| Precinct TSx                      | 19366                                     | 19       | 0             | 0           | 0                      | -      |
| Precinct OS                       | 19366                                     | 19       | 2196          | 1975        | 164                    | 8.30%  |
| Precinct VBM                      | 19366                                     | 19       | 7744          | 7109        | 548                    | 7.71%  |
| MB Precinct                       | 19366                                     | 19       | 1880          | 1728        | 143                    | 8.28%  |
| Provisional                       | 19366                                     | 19       | 267           | 208         | 29                     | 13.94% |
| Total                             | 19366                                     | 95       | 12087         | 11020       | 884                    | 8.02%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |   |          |               |             |                        |        |
| Precinct TSx                      | 18662                                     | 17       | 1             | 1           | 0                      | 0.00%  |
| Precinct OS                       | 18662                                     | 17       | 2034          | 1758        | 352                    | 20.02% |
| Precinct VBM                      | 18662                                     | 17       | 6058          | 5381        | 715                    | 13.29% |
| MB Precinct                       | 18662                                     | 17       | 451           | 384         | 68                     | 17.71% |
| Provisional                       | 18662                                     | 17       | 364           | 276         | 88                     | 31.88% |
| Total                             | 18662                                     | 85       | 8908          | 7800        | 1223                   | 15.68% |
| Total                             | 70961                                     | 75       | 4             | 4           | 0                      | 0.00%  |
| Precinct OS                       | 70961                                     | 75       | 7627          | 6791        | 807                    | 11.88% |
| Precinct VBM                      | 70961                                     | 75       | 28225         | 25719       | 2360                   | 9.18%  |
| MB Precinct                       | 70961                                     | 75       | 4361          | 3987        | 347                    | 8.70%  |
| Provisional                       | 70961                                     | 75       | 1038          | 821         | 169                    | 20.58% |
| Total                             | 70961                                     | 375      | 41255         | 37322       | 3683                   | 9.87%  |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                 | Member of the State Assembly 5th District |            |               |              |                        |
|---------------------------------|---|------------|---------------|--------------|------------------------|
|                                 | Reg. Voters                               | Vote For   | Times Counted | Total Votes  | KAI ELLSWORTH<br>(DEM) |
| <b>CITIES</b>                   |   |            |               |              |                        |
| <b>CITY OF PLACERVILLE</b>      |   |            |               |              |                        |
| Precinct TSx                    | 5569                                      | 5          | 0             | 0            | 0 -                    |
| Precinct OS                     | 5569                                      | 5          | 631           | 548          | 79 14.42%              |
| Precinct VBM                    | 5569                                      | 5          | 2590          | 2349         | 250 10.64%             |
| MB Precinct                     | 5569                                      | 5          | 0             | 0            | 0 -                    |
| Provisional                     | 5569                                      | 5          | 80            | 66           | 18 27.27%              |
| <b>Total</b>                    | <b>5569</b>                               | <b>25</b>  | <b>3301</b>   | <b>2963</b>  | <b>347 11.71%</b>      |
| <b>CITY OF SOUTH LAKE TAHOE</b> |   |            |               |              |                        |
| Precinct TSx                    | 9465                                      | 7          | 0             | 0            | 0 -                    |
| Precinct OS                     | 9465                                      | 7          | 1063          | 916          | 225 24.56%             |
| Precinct VBM                    | 9465                                      | 7          | 2796          | 2437         | 391 16.04%             |
| MB Precinct                     | 9465                                      | 7          | 0             | 0            | 0 -                    |
| Provisional                     | 9465                                      | 7          | 224           | 172          | 58 33.72%              |
| <b>Total</b>                    | <b>9465</b>                               | <b>35</b>  | <b>4083</b>   | <b>3525</b>  | <b>674 19.12%</b>      |
| <b>UNINCORPORATED AREA</b>      |   |            |               |              |                        |
| Precinct TSx                    | 55927                                     | 63         | 4             | 4            | 0 0.00%                |
| Precinct OS                     | 55927                                     | 63         | 5933          | 5327         | 503 9.44%              |
| Precinct VBM                    | 55927                                     | 63         | 22839         | 20933        | 1719 8.21%             |
| MB Precinct                     | 55927                                     | 63         | 4361          | 3987         | 347 8.70%              |
| Provisional                     | 55927                                     | 63         | 734           | 583          | 93 15.95%              |
| <b>Total</b>                    | <b>55927</b>                              | <b>315</b> | <b>33871</b>  | <b>30834</b> | <b>2662 8.63%</b>      |
| <b>Total</b>                    |   |            |               |              |                        |
| Precinct TSx                    | 70961                                     | 75         | 4             | 4            | 0 0.00%                |
| Precinct OS                     | 70961                                     | 75         | 7627          | 6791         | 807 11.88%             |
| Precinct VBM                    | 70961                                     | 75         | 28225         | 25719        | 2360 9.18%             |
| MB Precinct                     | 70961                                     | 75         | 4361          | 3987         | 347 8.70%              |
| Provisional                     | 70961                                     | 75         | 1038          | 821          | 169 20.58%             |
| <b>Total</b>                    | <b>70961</b>                              | <b>375</b> | <b>41255</b>  | <b>37322</b> | <b>3683 9.87%</b>      |

**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 5th District |                        |                         |
|------------------------|---|------------------------|-------------------------|
|                        | MARK BELDEN<br>(NPP)                      | FRANK BIGELOW<br>(REP) | ROBERT CARABAS<br>(DEM) |
| Jurisdiction Wide      |   |                        |                         |
| 100001.11-CP 100001.11 |   |                        |                         |
| Precinct TSx           | -   | -                      | -                       |
| Precinct OS            | -   | -                      | -                       |
| Precinct VBM           | -   | -                      | -                       |
| MB Precinct            | -   | -                      | -                       |
| Provisional            | -   | -                      | -                       |
| Total                  | -   | -                      | -                       |
| 100002.11-CP 100002.11 |   |                        |                         |
| Precinct TSx           | -   | -                      | -                       |
| Precinct OS            | -   | -                      | -                       |
| Precinct VBM           | -   | -                      | -                       |
| MB Precinct            | -   | -                      | -                       |
| Provisional            | -   | -                      | -                       |
| Total                  | -   | -                      | -                       |
| 100003.11-CP 100003.11 |   |                        |                         |
| Precinct TSx           | -   | -                      | -                       |
| Precinct OS            | -   | -                      | -                       |
| Precinct VBM           | -   | -                      | -                       |
| MB Precinct            | -   | -                      | -                       |
| Provisional            | -   | -                      | -                       |
| Total                  | -   | -                      | -                       |
| 100004.11-CP 100004.11 |   |                        |                         |
| Precinct TSx           | -   | -                      | -                       |
| Precinct OS            | -   | -                      | -                       |
| Precinct VBM           | -   | -                      | -                       |
| MB Precinct            | -   | -                      | -                       |
| Provisional            | -   | -                      | -                       |
| Total                  | -   | -                      | -                       |
| 100005.11-CP 100005.11 |   |                        |                         |
| Precinct TSx           | -   | -                      | -                       |
| Precinct OS            | -   | -                      | -                       |
| Precinct VBM           | -   | -                      | -                       |
| MB Precinct            | -   | -                      | -                       |
| Provisional            | -   | -                      | -                       |
| Total                  | -   | -                      | -                       |
| 100006.11-CP 100006.11 |   |                        |                         |
| Precinct TSx           | -   | -                      | -                       |
| Precinct OS            | -   | -                      | -                       |
| Precinct VBM           | -   | -                      | -                       |
| MB Precinct            | -   | -                      | -                       |
| Provisional            | -   | -                      | -                       |
| Total                  | -   | -                      | -                       |
| 100008.11-CP 100008.11 |   |                        |                         |
| Precinct TSx           | -   | -                      | -                       |
| Precinct OS            | -   | -                      | -                       |
| Precinct VBM           | -   | -                      | -                       |
| MB Precinct            | -   | -                      | -                       |
| Provisional            | -   | -                      | -                       |
| Total                  | -   | -                      | -                       |
| 100009.11-CP 100009.11 |   |                        |                         |
| Precinct TSx           | -   | -                      | -                       |
| Precinct OS            | -   | -                      | -                       |
| Precinct VBM           | -   | -                      | -                       |
| MB Precinct            | -   | -                      | -                       |
| Provisional            | -   | -                      | -                       |
| Total                  | -   | -                      | -                       |
| 100012.11-CP 100012.11 |   |                        |                         |
| Precinct TSx           | -   | -                      | -                       |
| Precinct OS            | -   | -                      | -                       |
| Precinct VBM           | -   | -                      | -                       |

**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 5th District |       |                        |        |                         |        |
|------------------------|---|-------|------------------------|--------|-------------------------|--------|
|                        | MARK BELDEN<br>(NPP)                      |       | FRANK BIGELOW<br>(REP) |        | ROBERT CARABAS<br>(DEM) |        |
| MB Precinct            | -   | -     | -                      | -      | -                       | -      |
| Provisional            | -   | -     | -                      | -      | -                       | -      |
| Total                  | -   | -     | -                      | -      | -                       | -      |
| 100013.11-CP 100013.11 |   |       |                        |        |                         |        |
| Precinct TSx           | -   | -     | -                      | -      | -                       | -      |
| Precinct OS            | -   | -     | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -     | -                      | -      | -                       | -      |
| MB Precinct            | -   | -     | -                      | -      | -                       | -      |
| Provisional            | -   | -     | -                      | -      | -                       | -      |
| Total                  | -   | -     | -                      | -      | -                       | -      |
| 100014.11-CP 100014.11 |   |       |                        |        |                         |        |
| Precinct TSx           | -   | -     | -                      | -      | -                       | -      |
| Precinct OS            | -   | -     | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -     | -                      | -      | -                       | -      |
| MB Precinct            | -   | -     | -                      | -      | -                       | -      |
| Provisional            | -   | -     | -                      | -      | -                       | -      |
| Total                  | -   | -     | -                      | -      | -                       | -      |
| 100022.11-CP 100022.11 |   |       |                        |        |                         |        |
| Precinct TSx           | -   | -     | -                      | -      | -                       | -      |
| Precinct OS            | -   | -     | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -     | -                      | -      | -                       | -      |
| MB Precinct            | -   | -     | -                      | -      | -                       | -      |
| Provisional            | -   | -     | -                      | -      | -                       | -      |
| Total                  | -   | -     | -                      | -      | -                       | -      |
| 100023.11-CP 100023.11 |   |       |                        |        |                         |        |
| Precinct TSx           | -   | -     | -                      | -      | -                       | -      |
| Precinct OS            | -   | -     | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -     | -                      | -      | -                       | -      |
| MB Precinct            | -   | -     | -                      | -      | -                       | -      |
| Provisional            | -   | -     | -                      | -      | -                       | -      |
| Total                  | -   | -     | -                      | -      | -                       | -      |
| 200003.12-CP 200003.12 |   |       |                        |        |                         |        |
| Precinct TSx           | -   | -     | -                      | -      | -                       | -      |
| Precinct OS            | -   | -     | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -     | -                      | -      | -                       | -      |
| MB Precinct            | -   | -     | -                      | -      | -                       | -      |
| Provisional            | -   | -     | -                      | -      | -                       | -      |
| Total                  | -   | -     | -                      | -      | -                       | -      |
| 200004.12-CP 200004.12 |   |       |                        |        |                         |        |
| Precinct TSx           | -   | -     | -                      | -      | -                       | -      |
| Precinct OS            | -   | -     | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -     | -                      | -      | -                       | -      |
| MB Precinct            | -   | -     | -                      | -      | -                       | -      |
| Provisional            | -   | -     | -                      | -      | -                       | -      |
| Total                  | -   | -     | -                      | -      | -                       | -      |
| 200005.02-CP 200005.02 |   |       |                        |        |                         |        |
| Precinct TSx           | 0   | -     | 0                      | -      | 0                       | -      |
| Precinct OS            | 7   | 8.05% | 53                     | 60.92% | 20                      | 22.99% |
| Precinct VBM           | 31  | 7.43% | 264                    | 63.31% | 90                      | 21.58% |
| MB Precinct            | 0   | -     | 0                      | -      | 0                       | -      |
| Provisional            | 1   | 6.25% | 11                     | 68.75% | 1                       | 6.25%  |
| Total                  | 39  | 7.50% | 328                    | 63.08% | 111                     | 21.35% |
| 200006.12-CP 200006.12 |   |       |                        |        |                         |        |
| Precinct TSx           | -   | -     | -                      | -      | -                       | -      |
| Precinct OS            | -   | -     | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -     | -                      | -      | -                       | -      |
| MB Precinct            | -   | -     | -                      | -      | -                       | -      |
| Provisional            | -   | -     | -                      | -      | -                       | -      |
| Total                  | -   | -     | -                      | -      | -                       | -      |
| 200008.12-CP 200008.12 |   |       |                        |        |                         |        |
| Precinct TSx           | -   | -     | -                      | -      | -                       | -      |

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|                        | MARK BELDEN<br>(NPP)                      |        | FRANK BIGELOW<br>(REP) |        | ROBERT CARABAS<br>(DEM) |        |
| Precinct OS            | -   | -      | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -      | -                      | -      | -                       | -      |
| MB Precinct            | -   | -      | -                      | -      | -                       | -      |
| Provisional            | -   | -      | -                      | -      | -                       | -      |
| Total                  | -   | -      | -                      | -      | -                       | -      |
| 200010.12-CP 200010.12 |   |        |                        |        |                         |        |
| Precinct TSx           | -   | -      | -                      | -      | -                       | -      |
| Precinct OS            | -   | -      | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -      | -                      | -      | -                       | -      |
| MB Precinct            | -   | -      | -                      | -      | -                       | -      |
| Provisional            | -   | -      | -                      | -      | -                       | -      |
| Total                  | -   | -      | -                      | -      | -                       | -      |
| 200012.12-CP 200012.12 |   |        |                        |        |                         |        |
| Precinct TSx           | -   | -      | -                      | -      | -                       | -      |
| Precinct OS            | -   | -      | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -      | -                      | -      | -                       | -      |
| MB Precinct            | -   | -      | -                      | -      | -                       | -      |
| Provisional            | -   | -      | -                      | -      | -                       | -      |
| Total                  | -   | -      | -                      | -      | -                       | -      |
| 200013.12-CP 200013.12 |   |        |                        |        |                         |        |
| Precinct TSx           | -   | -      | -                      | -      | -                       | -      |
| Precinct OS            | -   | -      | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -      | -                      | -      | -                       | -      |
| MB Precinct            | -   | -      | -                      | -      | -                       | -      |
| Provisional            | -   | -      | -                      | -      | -                       | -      |
| Total                  | -   | -      | -                      | -      | -                       | -      |
| 200014.12-CP 200014.12 |   |        |                        |        |                         |        |
| Precinct TSx           | -   | -      | -                      | -      | -                       | -      |
| Precinct OS            | -   | -      | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -      | -                      | -      | -                       | -      |
| MB Precinct            | -   | -      | -                      | -      | -                       | -      |
| Provisional            | -   | -      | -                      | -      | -                       | -      |
| Total                  | -   | -      | -                      | -      | -                       | -      |
| 200015.02-CP 200015.02 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 15  | 7.85%  | 113                    | 59.16% | 47                      | 24.61% |
| Precinct VBM           | 107                                       | 13.36% | 454                    | 56.68% | 161                     | 20.10% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 5   | 25.00% | 11                     | 55.00% | 3                       | 15.00% |
| Total                  | 127                                       | 12.55% | 578                    | 57.11% | 211                     | 20.85% |
| 200016.12-CP 200016.12 |   |        |                        |        |                         |        |
| Precinct TSx           | -   | -      | -                      | -      | -                       | -      |
| Precinct OS            | -   | -      | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -      | -                      | -      | -                       | -      |
| MB Precinct            | -   | -      | -                      | -      | -                       | -      |
| Provisional            | -   | -      | -                      | -      | -                       | -      |
| Total                  | -   | -      | -                      | -      | -                       | -      |
| 200016.13-CP 200016.13 |   |        |                        |        |                         |        |
| Precinct TSx           | -   | -      | -                      | -      | -                       | -      |
| Precinct OS            | -   | -      | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -      | -                      | -      | -                       | -      |
| MB Precinct            | -   | -      | -                      | -      | -                       | -      |
| Provisional            | -   | -      | -                      | -      | -                       | -      |
| Total                  | -   | -      | -                      | -      | -                       | -      |
| 200017.02-CP 200017.02 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 11  | 8.15%  | 76                     | 56.30% | 37                      | 27.41% |
| Precinct VBM           | 59  | 10.81% | 304                    | 55.68% | 136                     | 24.91% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 0   | 0.00%  | 5                      | 50.00% | 4                       | 40.00% |
| Total                  | 70  | 10.13% | 385                    | 55.72% | 177                     | 25.62% |

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|                        | MARK BELDEN<br>(NPP)                      |        | FRANK BIGELOW<br>(REP) |        | ROBERT CARABAS<br>(DEM) |        |
| 200018.02-CP 200018.02 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 13  | 8.23%  | 116                    | 73.42% | 19                      | 12.03% |
| Precinct VBM           | 55  | 8.44%  | 425                    | 65.18% | 125                     | 19.17% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 1   | 10.00% | 8                      | 80.00% | 1                       | 10.00% |
| Total                  | 69  | 8.41%  | 549                    | 66.95% | 145                     | 17.68% |
| 200020.02-CP 200020.02 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 9   | 7.20%  | 92                     | 73.60% | 15                      | 12.00% |
| Precinct VBM           | 52  | 10.92% | 304                    | 63.87% | 82                      | 17.23% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 2   | 14.29% | 7                      | 50.00% | 0                       | 0.00%  |
| Total                  | 63  | 10.24% | 403                    | 65.53% | 97                      | 15.77% |
| 200021.01-CP 200021.01 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 2   | 6.25%  | 19                     | 59.38% | 6                       | 18.75% |
| Precinct VBM           | 25  | 13.16% | 124                    | 65.26% | 33                      | 17.37% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 1   | 25.00% | 3                      | 75.00% | 0                       | 0.00%  |
| Total                  | 28  | 12.39% | 146                    | 64.60% | 39                      | 17.26% |
| 200021.02-CP 200021.02 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 11  | 7.19%  | 100                    | 65.36% | 26                      | 16.99% |
| Precinct VBM           | 59  | 8.93%  | 433                    | 65.51% | 141                     | 21.33% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 3   | 18.75% | 11                     | 68.75% | 1                       | 6.25%  |
| Total                  | 73  | 8.80%  | 544                    | 65.54% | 168                     | 20.24% |
| 200024.02-CP 200024.02 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 7   | 13.21% | 42                     | 79.25% | 3                       | 5.66%  |
| Precinct VBM           | 27  | 10.11% | 194                    | 72.66% | 28                      | 10.49% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 1   | 14.29% | 5                      | 71.43% | 1                       | 14.29% |
| Total                  | 35  | 10.70% | 241                    | 73.70% | 32                      | 9.79%  |
| 300001.04-CP 300001.04 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 2   | 18.18% | 7                      | 63.64% | 1                       | 9.09%  |
| Precinct VBM           | 12  | 12.63% | 48                     | 50.53% | 22                      | 23.16% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 0   | -      | 0                      | -      | 0                       | -      |
| Total                  | 14  | 13.21% | 55                     | 51.89% | 23                      | 21.70% |
| 300001.05-CP 300001.05 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 5   | 7.25%  | 39                     | 56.52% | 13                      | 18.84% |
| Precinct VBM           | 42  | 14.00% | 147                    | 49.00% | 86                      | 28.67% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 3   | 33.33% | 2                      | 22.22% | 2                       | 22.22% |
| Total                  | 50  | 13.23% | 188                    | 49.74% | 101                     | 26.72% |
| 300003.05-CP 300003.05 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 17  | 9.88%  | 87                     | 50.58% | 50                      | 29.07% |
| Precinct VBM           | 85  | 10.16% | 414                    | 49.46% | 243                     | 29.03% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 3   | 13.04% | 10                     | 43.48% | 6                       | 26.09% |
| Total                  | 105                                       | 10.17% | 511                    | 49.52% | 299                     | 28.97% |
| 300004.05-CP 300004.05 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 13  | 8.61%  | 69                     | 45.70% | 43                      | 28.48% |
| Precinct VBM           | 41  | 7.90%  | 272                    | 52.41% | 136                     | 26.20% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |



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|                        | MARK BELDEN<br>(NPP)                      |        | FRANK BIGELOW<br>(REP) |         | ROBERT CARABAS<br>(DEM) |        |
| Provisional            | 2   | 15.38% | 6                      | 46.15%  | 2                       | 15.38% |
| Total                  | 56  | 8.20%  | 347                    | 50.81%  | 181                     | 26.50% |
| 300005.05-CP 300005.05 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 6   | 4.14%  | 81                     | 55.86%  | 35                      | 24.14% |
| Precinct VBM           | 61  | 10.20% | 339                    | 56.69%  | 145                     | 24.25% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 2   | 9.52%  | 5                      | 23.81%  | 5                       | 23.81% |
| Total                  | 69  | 9.03%  | 425                    | 55.63%  | 185                     | 24.21% |
| 300008.05-CP 300008.05 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 8   | 6.20%  | 92                     | 71.32%  | 22                      | 17.05% |
| Precinct VBM           | 59  | 8.36%  | 468                    | 66.29%  | 123                     | 17.42% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 0   | 0.00%  | 8                      | 66.67%  | 0                       | 0.00%  |
| Total                  | 67  | 7.91%  | 568                    | 67.06%  | 145                     | 17.12% |
| 300009.04-CP 300009.04 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 4   | 3.74%  | 74                     | 69.16%  | 21                      | 19.63% |
| Precinct VBM           | 34  | 9.58%  | 231                    | 65.07%  | 60                      | 16.90% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 2   | 18.18% | 5                      | 45.45%  | 4                       | 36.36% |
| Total                  | 40  | 8.46%  | 310                    | 65.54%  | 85                      | 17.97% |
| 300010.04-CP 300010.04 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 12  | 7.84%  | 102                    | 66.67%  | 21                      | 13.73% |
| Precinct VBM           | 54  | 8.82%  | 366                    | 59.80%  | 138                     | 22.55% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 2   | 18.18% | 6                      | 54.55%  | 2                       | 18.18% |
| Total                  | 68  | 8.76%  | 474                    | 61.08%  | 161                     | 20.75% |
| 300011.04-CP 300011.04 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 10  | 7.63%  | 90                     | 68.70%  | 19                      | 14.50% |
| Precinct VBM           | 65  | 9.89%  | 418                    | 63.62%  | 133                     | 20.24% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 6   | 33.33% | 7                      | 38.89%  | 4                       | 22.22% |
| Total                  | 81  | 10.05% | 515                    | 63.90%  | 156                     | 19.35% |
| 300013.05-CP 300013.05 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | 0.00%  | 3                      | 100.00% | 0                       | 0.00%  |
| Precinct OS            | 13  | 10.92% | 68                     | 57.14%  | 25                      | 21.01% |
| Precinct VBM           | 49  | 7.17%  | 450                    | 65.89%  | 127                     | 18.59% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 1   | 7.69%  | 8                      | 61.54%  | 1                       | 7.69%  |
| Total                  | 63  | 7.70%  | 529                    | 64.67%  | 153                     | 18.70% |
| 300015.04-CP 300015.04 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 1   | 11.11% | 7                      | 77.78%  | 0                       | 0.00%  |
| Precinct VBM           | 6   | 11.11% | 38                     | 70.37%  | 10                      | 18.52% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 1   | 25.00% | 2                      | 50.00%  | 1                       | 25.00% |
| Total                  | 8   | 11.94% | 47                     | 70.15%  | 11                      | 16.42% |
| 300015.05-CP 300015.05 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 5   | 8.33%  | 33                     | 55.00%  | 18                      | 30.00% |
| Precinct VBM           | 27  | 11.30% | 127                    | 53.14%  | 62                      | 25.94% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 0   | 0.00%  | 1                      | 100.00% | 0                       | 0.00%  |
| Total                  | 32  | 10.67% | 161                    | 53.67%  | 80                      | 26.67% |
| 300017.05-CP 300017.05 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 11  | 10.19% | 70                     | 64.81%  | 18                      | 16.67% |

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|                        | MARK BELDEN<br>(NPP)                      |        | FRANK BIGELOW<br>(REP) |        | ROBERT CARABAS<br>(DEM) |        |
| Precinct VBM           | 51  | 8.40%  | 395                    | 65.07% | 115                     | 18.95% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 2   | 25.00% | 4                      | 50.00% | 0                       | 0.00%  |
| Total                  | 64  | 8.85%  | 469                    | 64.87% | 133                     | 18.40% |
| 300018.04-CP 300018.04 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 7   | 5.22%  | 92                     | 68.66% | 16                      | 11.94% |
| Precinct VBM           | 43  | 9.27%  | 281                    | 60.56% | 96                      | 20.69% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 2   | 8.33%  | 14                     | 58.33% | 4                       | 16.67% |
| Total                  | 52  | 8.36%  | 387                    | 62.22% | 116                     | 18.65% |
| 300020.04-CP 300020.04 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 17  | 11.49% | 86                     | 58.11% | 34                      | 22.97% |
| Precinct VBM           | 54  | 9.33%  | 356                    | 61.49% | 110                     | 19.00% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 1   | 7.69%  | 12                     | 92.31% | 0                       | 0.00%  |
| Total                  | 72  | 9.73%  | 454                    | 61.35% | 144                     | 19.46% |
| 300021.03-CP 300021.03 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 11  | 8.21%  | 73                     | 54.48% | 38                      | 28.36% |
| Precinct VBM           | 42  | 7.38%  | 350                    | 61.51% | 126                     | 22.14% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 2   | 15.38% | 7                      | 53.85% | 4                       | 30.77% |
| Total                  | 55  | 7.68%  | 430                    | 60.06% | 168                     | 23.46% |
| 300022.05-CP 300022.05 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 3   | 3.30%  | 65                     | 71.43% | 14                      | 15.38% |
| Precinct VBM           | 29  | 9.01%  | 172                    | 53.42% | 82                      | 25.47% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 0   | 0.00%  | 7                      | 63.64% | 2                       | 18.18% |
| Total                  | 32  | 7.55%  | 244                    | 57.55% | 98                      | 23.11% |
| 300023.03-CP 300023.03 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 29  | 13.62% | 120                    | 56.34% | 50                      | 23.47% |
| Precinct VBM           | 57  | 6.75%  | 540                    | 63.91% | 185                     | 21.89% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 1   | 4.76%  | 11                     | 52.38% | 3                       | 14.29% |
| Total                  | 87  | 8.06%  | 671                    | 62.19% | 238                     | 22.06% |
| 300023.05-CP 300023.05 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 5   | 12.50% | 23                     | 57.50% | 8                       | 20.00% |
| Precinct VBM           | 15  | 8.43%  | 116                    | 65.17% | 36                      | 20.22% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 0   | 0.00%  | 2                      | 33.33% | 3                       | 50.00% |
| Total                  | 20  | 8.93%  | 141                    | 62.95% | 47                      | 20.98% |
| 400001.14-CP 400001.14 |   |        |                        |        |                         |        |
| Precinct TSx           | -   | -      | -                      | -      | -                       | -      |
| Precinct OS            | -   | -      | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -      | -                      | -      | -                       | -      |
| MB Precinct            | -   | -      | -                      | -      | -                       | -      |
| Provisional            | -   | -      | -                      | -      | -                       | -      |
| Total                  | -   | -      | -                      | -      | -                       | -      |
| 400002.08-CP 400002.08 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 7   | 3.91%  | 131                    | 73.18% | 29                      | 16.20% |
| Precinct VBM           | 50  | 7.41%  | 480                    | 71.11% | 114                     | 16.89% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 0   | 0.00%  | 6                      | 50.00% | 6                       | 50.00% |
| Total                  | 57  | 6.58%  | 617                    | 71.25% | 149                     | 17.21% |
| 400003.08-CP 400003.08 |   |        |                        |        |                         |        |

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|------------------------|---|--------|------------------------|--------|-------------------------|--------|
|                        | MARK BELDEN<br>(NPP)                      |        | FRANK BIGELOW<br>(REP) |        | ROBERT CARABAS<br>(DEM) |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 18  | 8.11%  | 148                    | 66.67% | 37                      | 16.67% |
| Precinct VBM           | 64  | 9.20%  | 432                    | 62.07% | 150                     | 21.55% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 3   | 10.34% | 17                     | 58.62% | 3                       | 10.34% |
| Total                  | 85  | 8.98%  | 597                    | 63.04% | 190                     | 20.06% |
| 400004.14-CP 400004.14 |   |        |                        |        |                         |        |
| Precinct TSx           | -   | -      | -                      | -      | -                       | -      |
| Precinct OS            | -   | -      | -                      | -      | -                       | -      |
| Precinct VBM           | -   | -      | -                      | -      | -                       | -      |
| MB Precinct            | -   | -      | -                      | -      | -                       | -      |
| Provisional            | -   | -      | -                      | -      | -                       | -      |
| Total                  | -   | -      | -                      | -      | -                       | -      |
| 400005.08-CP 400005.08 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 7   | 9.59%  | 43                     | 58.90% | 21                      | 28.77% |
| Precinct VBM           | 18  | 7.66%  | 132                    | 56.17% | 57                      | 24.26% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 0   | 0.00%  | 8                      | 80.00% | 2                       | 20.00% |
| Total                  | 25  | 7.86%  | 183                    | 57.55% | 80                      | 25.16% |
| 400006.08-CP 400006.08 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 16  | 7.80%  | 124                    | 60.49% | 44                      | 21.46% |
| Precinct VBM           | 63  | 9.62%  | 372                    | 56.79% | 155                     | 23.66% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 2   | 15.38% | 9                      | 69.23% | 0                       | 0.00%  |
| Total                  | 81  | 9.28%  | 505                    | 57.85% | 199                     | 22.79% |
| 400007.08-CP 400007.08 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 19  | 9.09%  | 132                    | 63.16% | 35                      | 16.75% |
| Precinct VBM           | 73  | 10.31% | 453                    | 63.98% | 102                     | 14.41% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 3   | 15.79% | 11                     | 57.89% | 1                       | 5.26%  |
| Total                  | 95  | 10.15% | 596                    | 63.68% | 138                     | 14.74% |
| 400010.08-CP 400010.08 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 16  | 7.14%  | 120                    | 53.57% | 53                      | 23.66% |
| Precinct VBM           | 74  | 9.07%  | 438                    | 53.68% | 208                     | 25.49% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 2   | 10.00% | 11                     | 55.00% | 5                       | 25.00% |
| Total                  | 92  | 8.68%  | 569                    | 53.68% | 266                     | 25.09% |
| 400012.08-CP 400012.08 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 9   | 8.74%  | 56                     | 54.37% | 30                      | 29.13% |
| Precinct VBM           | 36  | 8.96%  | 237                    | 58.96% | 95                      | 23.63% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 2   | 11.76% | 11                     | 64.71% | 2                       | 11.76% |
| Total                  | 47  | 9.00%  | 304                    | 58.24% | 127                     | 24.33% |
| 400013.08-CP 400013.08 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 3   | 2.75%  | 84                     | 77.06% | 17                      | 15.60% |
| Precinct VBM           | 28  | 9.69%  | 182                    | 62.98% | 62                      | 21.45% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 0   | 0.00%  | 1                      | 33.33% | 1                       | 33.33% |
| Total                  | 31  | 7.73%  | 267                    | 66.58% | 80                      | 19.95% |
| 400014.08-CP 400014.08 |   |        |                        |        |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -      | 0                       | -      |
| Precinct OS            | 19  | 9.45%  | 135                    | 67.16% | 34                      | 16.92% |
| Precinct VBM           | 64  | 9.50%  | 432                    | 64.09% | 142                     | 21.07% |
| MB Precinct            | 0   | -      | 0                      | -      | 0                       | -      |
| Provisional            | 2   | 5.88%  | 20                     | 58.82% | 7                       | 20.59% |

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|------------------------|---|--------|------------------------|---------|-------------------------|---------|
|                        | MARK BELDEN<br>(NPP)                      |        | FRANK BIGELOW<br>(REP) |         | ROBERT CARABAS<br>(DEM) |         |
| Total                  | 85  | 9.35%  | 587                    | 64.58%  | 183                     | 20.13%  |
| 400017.08-CP 400017.08 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 15  | 9.55%  | 114                    | 72.61%  | 21                      | 13.38%  |
| Precinct VBM           | 73  | 10.52% | 456                    | 65.71%  | 120                     | 17.29%  |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -       |
| Provisional            | 2   | 9.52%  | 15                     | 71.43%  | 2                       | 9.52%   |
| Total                  | 90  | 10.32% | 585                    | 67.09%  | 143                     | 16.40%  |
| 400018.08-CP 400018.08 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 9   | 12.00% | 52                     | 69.33%  | 10                      | 13.33%  |
| Precinct VBM           | 19  | 7.92%  | 181                    | 75.42%  | 30                      | 12.50%  |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -       |
| Provisional            | 0   | 0.00%  | 3                      | 100.00% | 0                       | 0.00%   |
| Total                  | 28  | 8.81%  | 236                    | 74.21%  | 40                      | 12.58%  |
| 400020.08-CP 400020.08 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 11  | 10.68% | 78                     | 75.73%  | 7                       | 6.80%   |
| Precinct VBM           | 27  | 8.54%  | 195                    | 61.71%  | 61                      | 19.30%  |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -       |
| Provisional            | 0   | 0.00%  | 0                      | 0.00%   | 2                       | 100.00% |
| Total                  | 38  | 9.03%  | 273                    | 64.85%  | 70                      | 16.63%  |
| 400021.07-CP 400021.07 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 4   | 3.48%  | 80                     | 69.57%  | 21                      | 18.26%  |
| Precinct VBM           | 72  | 10.16% | 475                    | 67.00%  | 124                     | 17.49%  |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -       |
| Provisional            | 1   | 8.33%  | 7                      | 58.33%  | 2                       | 16.67%  |
| Total                  | 77  | 9.21%  | 562                    | 67.22%  | 147                     | 17.58%  |
| 500001.10-CP 500001.10 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 16  | 8.79%  | 73                     | 40.11%  | 71                      | 39.01%  |
| Precinct VBM           | 54  | 9.91%  | 222                    | 40.73%  | 201                     | 36.88%  |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -       |
| Provisional            | 0   | 0.00%  | 2                      | 16.67%  | 4                       | 33.33%  |
| Total                  | 70  | 9.47%  | 297                    | 40.19%  | 276                     | 37.35%  |
| 500002.10-CP 500002.10 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 8   | 6.84%  | 43                     | 36.75%  | 39                      | 33.33%  |
| Precinct VBM           | 46  | 12.37% | 117                    | 31.45%  | 141                     | 37.90%  |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -       |
| Provisional            | 3   | 13.04% | 4                      | 17.39%  | 7                       | 30.43%  |
| Total                  | 57  | 11.13% | 164                    | 32.03%  | 187                     | 36.52%  |
| 500003.10-CP 500003.10 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 11  | 10.00% | 34                     | 30.91%  | 46                      | 41.82%  |
| Precinct VBM           | 35  | 11.59% | 117                    | 38.74%  | 100                     | 33.11%  |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -       |
| Provisional            | 2   | 33.33% | 1                      | 16.67%  | 0                       | 0.00%   |
| Total                  | 48  | 11.48% | 152                    | 36.36%  | 146                     | 34.93%  |
| 500004.10-CP 500004.10 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 8   | 7.77%  | 29                     | 28.16%  | 40                      | 38.83%  |
| Precinct VBM           | 27  | 12.33% | 80                     | 36.53%  | 69                      | 31.51%  |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -       |
| Provisional            | 3   | 16.67% | 3                      | 16.67%  | 7                       | 38.89%  |
| Total                  | 38  | 11.18% | 112                    | 32.94%  | 116                     | 34.12%  |
| 500005.10-CP 500005.10 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 14  | 14.89% | 25                     | 26.60%  | 35                      | 37.23%  |
| Precinct VBM           | 37  | 16.52% | 58                     | 25.89%  | 87                      | 38.84%  |

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|------------------------|---|--------|------------------------|---------|-------------------------|--------|
|                        | MARK BELDEN<br>(NPP)                      |        | FRANK BIGELOW<br>(REP) |         | ROBERT CARABAS<br>(DEM) |        |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 3   | 25.00% | 1                      | 8.33%   | 3                       | 25.00% |
| Total                  | 54  | 16.36% | 84                     | 25.45%  | 125                     | 37.88% |
| 500006.10-CP 500006.10 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 16  | 10.26% | 37                     | 23.72%  | 61                      | 39.10% |
| Precinct VBM           | 39  | 9.05%  | 132                    | 30.63%  | 173                     | 40.14% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 6   | 14.29% | 10                     | 23.81%  | 7                       | 16.67% |
| Total                  | 61  | 9.70%  | 179                    | 28.46%  | 241                     | 38.31% |
| 500007.10-CP 500007.10 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 15  | 8.15%  | 82                     | 44.57%  | 43                      | 23.37% |
| Precinct VBM           | 68  | 11.22% | 296                    | 48.84%  | 174                     | 28.71% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 2   | 11.76% | 4                      | 23.53%  | 8                       | 47.06% |
| Total                  | 85  | 10.53% | 382                    | 47.34%  | 225                     | 27.88% |
| 500009.10-CP 500009.10 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 13  | 12.15% | 22                     | 20.56%  | 42                      | 39.25% |
| Precinct VBM           | 27  | 10.76% | 99                     | 39.44%  | 89                      | 35.46% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 4   | 14.81% | 5                      | 18.52%  | 11                      | 40.74% |
| Total                  | 44  | 11.43% | 126                    | 32.73%  | 142                     | 36.88% |
| 500011.10-CP 500011.10 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 18  | 11.61% | 44                     | 28.39%  | 52                      | 33.55% |
| Precinct VBM           | 45  | 13.47% | 110                    | 32.93%  | 129                     | 38.62% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 5   | 15.15% | 7                      | 21.21%  | 10                      | 30.30% |
| Total                  | 68  | 13.03% | 161                    | 30.84%  | 191                     | 36.59% |
| 500012.10-CP 500012.10 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | 0.00%  | 1                      | 100.00% | 0                       | 0.00%  |
| Precinct OS            | 20  | 12.42% | 42                     | 26.09%  | 63                      | 39.13% |
| Precinct VBM           | 62  | 11.44% | 207                    | 38.19%  | 202                     | 37.27% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 1   | 3.70%  | 6                      | 22.22%  | 8                       | 29.63% |
| Total                  | 83  | 11.35% | 256                    | 35.02%  | 273                     | 37.35% |
| 500015.10-CP 500015.10 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 12  | 13.33% | 25                     | 27.78%  | 34                      | 37.78% |
| Precinct VBM           | 38  | 10.70% | 128                    | 36.06%  | 160                     | 45.07% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 1   | 10.00% | 4                      | 40.00%  | 3                       | 30.00% |
| Total                  | 51  | 11.21% | 157                    | 34.51%  | 197                     | 43.30% |
| 500020.10-CP 500020.10 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 14  | 9.66%  | 82                     | 56.55%  | 39                      | 26.90% |
| Precinct VBM           | 66  | 10.61% | 354                    | 56.91%  | 142                     | 22.83% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 3   | 13.04% | 12                     | 52.17%  | 5                       | 21.74% |
| Total                  | 83  | 10.51% | 448                    | 56.71%  | 186                     | 23.54% |
| 500021.10-CP 500021.10 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS            | 15  | 9.74%  | 94                     | 61.04%  | 24                      | 15.58% |
| Precinct VBM           | 39  | 6.75%  | 357                    | 61.76%  | 132                     | 22.84% |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional            | 1   | 4.35%  | 12                     | 52.17%  | 5                       | 21.74% |
| Total                  | 55  | 7.28%  | 463                    | 61.32%  | 161                     | 21.32% |
| 95U201.01-MB 95U201.01 |   |        |                        |         |                         |        |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -      |

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|                        | MARK BELDEN<br>(NPP)                      |        | FRANK BIGELOW<br>(REP) |         | ROBERT CARABAS<br>(DEM) |         |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 0   | 0.00%  | 5                      | 83.33%  | 0                       | 0.00%   |
| Provisional            | 0   | -      | 0                      | -       | 0                       | -       |
| Total                  | 0   | 0.00%  | 5                      | 83.33%  | 0                       | 0.00%   |
| 95U201.02-MB 95U201.02 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 23  | 7.23%  | 210                    | 66.04%  | 64                      | 20.13%  |
| Provisional            | 0   | 0.00%  | 1                      | 100.00% | 0                       | 0.00%   |
| Total                  | 23  | 7.21%  | 211                    | 66.14%  | 64                      | 20.06%  |
| 95U202.02-MB 95U202.02 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 51  | 9.71%  | 309                    | 58.86%  | 131                     | 24.95%  |
| Provisional            | 1   | 33.33% | 1                      | 33.33%  | 1                       | 33.33%  |
| Total                  | 52  | 9.85%  | 310                    | 58.71%  | 132                     | 25.00%  |
| 95U203.02-MB 95U203.02 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -       |
| Provisional            | 0   | -      | 0                      | -       | 0                       | -       |
| Total                  | 0   | -      | 0                      | -       | 0                       | -       |
| 95U204.01-MB 95U204.01 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 54  | 8.82%  | 407                    | 66.50%  | 107                     | 17.48%  |
| Provisional            | 0   | 0.00%  | 2                      | 100.00% | 0                       | 0.00%   |
| Total                  | 54  | 8.79%  | 409                    | 66.61%  | 107                     | 17.43%  |
| 95U301.05-MB 95U301.05 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 17  | 12.06% | 88                     | 62.41%  | 22                      | 15.60%  |
| Provisional            | 0   | 0.00%  | 1                      | 100.00% | 0                       | 0.00%   |
| Total                  | 17  | 11.97% | 89                     | 62.68%  | 22                      | 15.49%  |
| 95U302.04-MB 95U302.04 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 0   | 0.00%  | 0                      | 0.00%   | 1                       | 100.00% |
| Provisional            | 0   | -      | 0                      | -       | 0                       | -       |
| Total                  | 0   | 0.00%  | 0                      | 0.00%   | 1                       | 100.00% |
| 95U302.05-MB 95U302.05 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 13  | 15.48% | 48                     | 57.14%  | 13                      | 15.48%  |
| Provisional            | 0   | -      | 0                      | -       | 0                       | -       |
| Total                  | 13  | 15.48% | 48                     | 57.14%  | 13                      | 15.48%  |
| 95U303.03-MB 95U303.03 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 7   | 8.54%  | 47                     | 57.32%  | 17                      | 20.73%  |
| Provisional            | 0   | -      | 0                      | -       | 0                       | -       |
| Total                  | 7   | 8.54%  | 47                     | 57.32%  | 17                      | 20.73%  |

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|                        | MARK BELDEN<br>(NPP)                      |        | FRANK BIGELOW<br>(REP) |         | ROBERT CARABAS<br>(DEM) |         |
| 95U303.05-MB 95U303.05 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 9   | 9.09%  | 69                     | 69.70%  | 19                      | 19.19%  |
| Provisional            | 0   | 0.00%  | 1                      | 100.00% | 0                       | 0.00%   |
| Total                  | 9   | 9.00%  | 70                     | 70.00%  | 19                      | 19.00%  |
| 95U304.05-MB 95U304.05 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -       |
| Provisional            | 0   | -      | 0                      | -       | 0                       | -       |
| Total                  | 0   | -      | 0                      | -       | 0                       | -       |
| 95U305.05-MB 95U305.05 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 1   | 14.29% | 5                      | 71.43%  | 1                       | 14.29%  |
| Provisional            | 0   | -      | 0                      | -       | 0                       | -       |
| Total                  | 1   | 14.29% | 5                      | 71.43%  | 1                       | 14.29%  |
| 95U401.08-MB 95U401.08 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 2   | 15.38% | 8                      | 61.54%  | 3                       | 23.08%  |
| Provisional            | 0   | -      | 0                      | -       | 0                       | -       |
| Total                  | 2   | 15.38% | 8                      | 61.54%  | 3                       | 23.08%  |
| 95U402.07-MB 95U402.07 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 22  | 9.32%  | 142                    | 60.17%  | 51                      | 21.61%  |
| Provisional            | 0   | 0.00%  | 0                      | 0.00%   | 1                       | 100.00% |
| Total                  | 22  | 9.28%  | 142                    | 59.92%  | 52                      | 21.94%  |
| 95U402.08-MB 95U402.08 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 40  | 7.78%  | 368                    | 71.60%  | 86                      | 16.73%  |
| Provisional            | 0   | 0.00%  | 1                      | 50.00%  | 1                       | 50.00%  |
| Total                  | 40  | 7.75%  | 369                    | 71.51%  | 87                      | 16.86%  |
| 95U403.08-MB 95U403.08 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 76  | 7.88%  | 530                    | 54.92%  | 256                     | 26.53%  |
| Provisional            | 1   | 10.00% | 5                      | 50.00%  | 0                       | 0.00%   |
| Total                  | 77  | 7.90%  | 535                    | 54.87%  | 256                     | 26.26%  |
| 95U404.06-MB 95U404.06 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -       |
| Provisional            | 0   | -      | 0                      | -       | 0                       | -       |
| Total                  | 0   | -      | 0                      | -       | 0                       | -       |
| 95U404.08-MB 95U404.08 |   |        |                        |         |                         |         |
| Precinct TSx           | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct OS            | 0   | -      | 0                      | -       | 0                       | -       |
| Precinct VBM           | 0   | -      | 0                      | -       | 0                       | -       |
| MB Precinct            | 0   | -      | 0                      | -       | 0                       | -       |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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|                               | Member of the State Assembly 5th District |        |                        |         |                         |        |
|-------------------------------|---|--------|------------------------|---------|-------------------------|--------|
|                               | MARK BELDEN<br>(NPP)                      |        | FRANK BIGELOW<br>(REP) |         | ROBERT CARABAS<br>(DEM) |        |
| Provisional                   | 0   | -      | 0                      | -       | 0                       | -      |
| Total                         | 0   | -      | 0                      | -       | 0                       | -      |
| 95U501.09-MB 95U501.09        |   |        |                        |         |                         |        |
| Precinct TSx                  | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS                   | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct VBM                  | 0   | -      | 0                      | -       | 0                       | -      |
| MB Precinct                   | 0   | 0.00%  | 0                      | 0.00%   | 0                       | 0.00%  |
| Provisional                   | 0   | -      | 0                      | -       | 0                       | -      |
| Total                         | 0   | 0.00%  | 0                      | 0.00%   | 0                       | 0.00%  |
| 95U501.10-MB 95U501.10        |   |        |                        |         |                         |        |
| Precinct TSx                  | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS                   | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct VBM                  | 0   | -      | 0                      | -       | 0                       | -      |
| MB Precinct                   | 15  | 9.55%  | 88                     | 56.05%  | 40                      | 25.48% |
| Provisional                   | 0   | 0.00%  | 1                      | 50.00%  | 1                       | 50.00% |
| Total                         | 15  | 9.43%  | 89                     | 55.97%  | 41                      | 25.79% |
| 95U598.10-MB 95U598.10        |   |        |                        |         |                         |        |
| Precinct TSx                  | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS                   | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct VBM                  | 0   | -      | 0                      | -       | 0                       | -      |
| MB Precinct                   | 18  | 9.38%  | 56                     | 29.17%  | 67                      | 34.90% |
| Provisional                   | 0   | 0.00%  | 1                      | 100.00% | 0                       | 0.00%  |
| Total                         | 18  | 9.33%  | 57                     | 29.53%  | 67                      | 34.72% |
| 95U599.10-MB 95U599.10        |   |        |                        |         |                         |        |
| Precinct TSx                  | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS                   | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct VBM                  | 0   | -      | 0                      | -       | 0                       | -      |
| MB Precinct                   | 5   | 15.15% | 14                     | 42.42%  | 10                      | 30.30% |
| Provisional                   | 0   | -      | 0                      | -       | 0                       | -      |
| Total                         | 5   | 15.15% | 14                     | 42.42%  | 10                      | 30.30% |
| 96U101.11-MB 96U101.11        |   |        |                        |         |                         |        |
| Precinct TSx                  | -   | -      | -                      | -       | -                       | -      |
| Precinct OS                   | -   | -      | -                      | -       | -                       | -      |
| Precinct VBM                  | -   | -      | -                      | -       | -                       | -      |
| MB Precinct                   | -   | -      | -                      | -       | -                       | -      |
| Provisional                   | -   | -      | -                      | -       | -                       | -      |
| Total                         | -   | -      | -                      | -       | -                       | -      |
| 96U401.14-MB 96U401.14        |   |        |                        |         |                         |        |
| Precinct TSx                  | -   | -      | -                      | -       | -                       | -      |
| Precinct OS                   | -   | -      | -                      | -       | -                       | -      |
| Precinct VBM                  | -   | -      | -                      | -       | -                       | -      |
| MB Precinct                   | -   | -      | -                      | -       | -                       | -      |
| Provisional                   | -   | -      | -                      | -       | -                       | -      |
| Total                         | -   | -      | -                      | -       | -                       | -      |
| Total                         |   |        |                        |         |                         |        |
| Precinct TSx                  | 0   | 0.00%  | 4                      | 100.00% | 0                       | 0.00%  |
| Precinct OS                   | 587                                       | 8.64%  | 3818                   | 56.22%  | 1567                    | 23.07% |
| Precinct VBM                  | 2485                                      | 9.66%  | 14772                  | 57.44%  | 6050                    | 23.52% |
| MB Precinct                   | 353                                       | 8.85%  | 2394                   | 60.05%  | 888                     | 22.27% |
| Provisional                   | 97  | 11.81% | 382                    | 46.53%  | 169                     | 20.58% |
| Total                         | 3522                                      | 9.44%  | 21370                  | 57.26%  | 8674                    | 23.24% |
| STATE BOARD OF EQUALIZATION   |   |        |                        |         |                         |        |
| STATE BOARD OF EQUALIZATION 1 |   |        |                        |         |                         |        |
| Precinct TSx                  | 0   | 0.00%  | 4                      | 100.00% | 0                       | 0.00%  |
| Precinct OS                   | 587                                       | 8.64%  | 3818                   | 56.22%  | 1567                    | 23.07% |
| Precinct VBM                  | 2485                                      | 9.66%  | 14772                  | 57.44%  | 6050                    | 23.52% |
| MB Precinct                   | 353                                       | 8.85%  | 2394                   | 60.05%  | 888                     | 22.27% |
| Provisional                   | 97  | 11.81% | 382                    | 46.53%  | 169                     | 20.58% |
| Total                         | 3522                                      | 9.44%  | 21370                  | 57.26%  | 8674                    | 23.24% |
| Total                         |   |        |                        |         |                         |        |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                     | Member of the State Assembly 5th District |              |                        |               |                         |               |
|-------------------------------------|---|--------------|------------------------|---------------|-------------------------|---------------|
|                                     | MARK BELDEN<br>(NPP)                      |              | FRANK BIGELOW<br>(REP) |               | ROBERT CARABAS<br>(DEM) |               |
| Precinct TSx                        | 0   | 0.00%        | 4                      | 100.00%       | 0                       | 0.00%         |
| Precinct OS                         | 587                                       | 8.64%        | 3818                   | 56.22%        | 1567                    | 23.07%        |
| Precinct VBM                        | 2485                                      | 9.66%        | 14772                  | 57.44%        | 6050                    | 23.52%        |
| MB Precinct                         | 353                                       | 8.85%        | 2394                   | 60.05%        | 888                     | 22.27%        |
| Provisional                         | 97  | 11.81%       | 382                    | 46.53%        | 169                     | 20.58%        |
| <b>Total</b>                        | <b>3522</b>                               | <b>9.44%</b> | <b>21370</b>           | <b>57.26%</b> | <b>8674</b>             | <b>23.24%</b> |
| <b>UNITED STATES SENATE</b>         |   |              |                        |               |                         |               |
| <b>UNITED STATES SENATE</b>         |   |              |                        |               |                         |               |
| Precinct TSx                        | 0   | 0.00%        | 4                      | 100.00%       | 0                       | 0.00%         |
| Precinct OS                         | 587                                       | 8.64%        | 3818                   | 56.22%        | 1567                    | 23.07%        |
| Precinct VBM                        | 2485                                      | 9.66%        | 14772                  | 57.44%        | 6050                    | 23.52%        |
| MB Precinct                         | 353                                       | 8.85%        | 2394                   | 60.05%        | 888                     | 22.27%        |
| Provisional                         | 97  | 11.81%       | 382                    | 46.53%        | 169                     | 20.58%        |
| <b>Total</b>                        | <b>3522</b>                               | <b>9.44%</b> | <b>21370</b>           | <b>57.26%</b> | <b>8674</b>             | <b>23.24%</b> |
| <b>Total</b>                        |   |              |                        |               |                         |               |
| Precinct TSx                        | 0   | 0.00%        | 4                      | 100.00%       | 0                       | 0.00%         |
| Precinct OS                         | 587                                       | 8.64%        | 3818                   | 56.22%        | 1567                    | 23.07%        |
| Precinct VBM                        | 2485                                      | 9.66%        | 14772                  | 57.44%        | 6050                    | 23.52%        |
| MB Precinct                         | 353                                       | 8.85%        | 2394                   | 60.05%        | 888                     | 22.27%        |
| Provisional                         | 97  | 11.81%       | 382                    | 46.53%        | 169                     | 20.58%        |
| <b>Total</b>                        | <b>3522</b>                               | <b>9.44%</b> | <b>21370</b>           | <b>57.26%</b> | <b>8674</b>             | <b>23.24%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |   |              |                        |               |                         |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |   |              |                        |               |                         |               |
| Precinct TSx                        | 0   | 0.00%        | 4                      | 100.00%       | 0                       | 0.00%         |
| Precinct OS                         | 587                                       | 8.64%        | 3818                   | 56.22%        | 1567                    | 23.07%        |
| Precinct VBM                        | 2485                                      | 9.66%        | 14772                  | 57.44%        | 6050                    | 23.52%        |
| MB Precinct                         | 353                                       | 8.85%        | 2394                   | 60.05%        | 888                     | 22.27%        |
| Provisional                         | 97  | 11.81%       | 382                    | 46.53%        | 169                     | 20.58%        |
| <b>Total</b>                        | <b>3522</b>                               | <b>9.44%</b> | <b>21370</b>           | <b>57.26%</b> | <b>8674</b>             | <b>23.24%</b> |
| <b>Total</b>                        |   |              |                        |               |                         |               |
| Precinct TSx                        | 0   | 0.00%        | 4                      | 100.00%       | 0                       | 0.00%         |
| Precinct OS                         | 587                                       | 8.64%        | 3818                   | 56.22%        | 1567                    | 23.07%        |
| Precinct VBM                        | 2485                                      | 9.66%        | 14772                  | 57.44%        | 6050                    | 23.52%        |
| MB Precinct                         | 353                                       | 8.85%        | 2394                   | 60.05%        | 888                     | 22.27%        |
| Provisional                         | 97  | 11.81%       | 382                    | 46.53%        | 169                     | 20.58%        |
| <b>Total</b>                        | <b>3522</b>                               | <b>9.44%</b> | <b>21370</b>           | <b>57.26%</b> | <b>8674</b>             | <b>23.24%</b> |
| <b>STATE SENATE</b>                 |   |              |                        |               |                         |               |
| <b>1ST STATE SENATE DISTRICT</b>    |   |              |                        |               |                         |               |
| Precinct TSx                        | 0   | 0.00%        | 4                      | 100.00%       | 0                       | 0.00%         |
| Precinct OS                         | 587                                       | 8.64%        | 3818                   | 56.22%        | 1567                    | 23.07%        |
| Precinct VBM                        | 2485                                      | 9.66%        | 14772                  | 57.44%        | 6050                    | 23.52%        |
| MB Precinct                         | 353                                       | 8.85%        | 2394                   | 60.05%        | 888                     | 22.27%        |
| Provisional                         | 97  | 11.81%       | 382                    | 46.53%        | 169                     | 20.58%        |
| <b>Total</b>                        | <b>3522</b>                               | <b>9.44%</b> | <b>21370</b>           | <b>57.26%</b> | <b>8674</b>             | <b>23.24%</b> |
| <b>Total</b>                        |   |              |                        |               |                         |               |
| Precinct TSx                        | 0   | 0.00%        | 4                      | 100.00%       | 0                       | 0.00%         |
| Precinct OS                         | 587                                       | 8.64%        | 3818                   | 56.22%        | 1567                    | 23.07%        |
| Precinct VBM                        | 2485                                      | 9.66%        | 14772                  | 57.44%        | 6050                    | 23.52%        |
| MB Precinct                         | 353                                       | 8.85%        | 2394                   | 60.05%        | 888                     | 22.27%        |
| Provisional                         | 97  | 11.81%       | 382                    | 46.53%        | 169                     | 20.58%        |
| <b>Total</b>                        | <b>3522</b>                               | <b>9.44%</b> | <b>21370</b>           | <b>57.26%</b> | <b>8674</b>             | <b>23.24%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |   |              |                        |               |                         |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |   |              |                        |               |                         |               |
| Precinct TSx                        | 0   | 0.00%        | 4                      | 100.00%       | 0                       | 0.00%         |
| Precinct OS                         | 587                                       | 8.64%        | 3818                   | 56.22%        | 1567                    | 23.07%        |
| Precinct VBM                        | 2485                                      | 9.66%        | 14772                  | 57.44%        | 6050                    | 23.52%        |
| MB Precinct                         | 353                                       | 8.85%        | 2394                   | 60.05%        | 888                     | 22.27%        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                   | Member of the State Assembly 5th District |        |                        |         |                         |        |
|-----------------------------------|---|--------|------------------------|---------|-------------------------|--------|
|                                   | MARK BELDEN<br>(NPP)                      |        | FRANK BIGELOW<br>(REP) |         | ROBERT CARABAS<br>(DEM) |        |
| Provisional                       | 97  | 11.81% | 382                    | 46.53%  | 169                     | 20.58% |
| Total                             | 3522                                      | 9.44%  | 21370                  | 57.26%  | 8674                    | 23.24% |
| <b>6TH STATE ASSEMBLY DIST</b>    |   |        |                        |         |                         |        |
| Precinct TSx                      | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS                       | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct VBM                      | 0   | -      | 0                      | -       | 0                       | -      |
| MB Precinct                       | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional                       | 0   | -      | 0                      | -       | 0                       | -      |
| Total                             | 0   | -      | 0                      | -       | 0                       | -      |
| <b>Total</b>                      |   |        |                        |         |                         |        |
| Precinct TSx                      | 0   | 0.00%  | 4                      | 100.00% | 0                       | 0.00%  |
| Precinct OS                       | 587                                       | 8.64%  | 3818                   | 56.22%  | 1567                    | 23.07% |
| Precinct VBM                      | 2485                                      | 9.66%  | 14772                  | 57.44%  | 6050                    | 23.52% |
| MB Precinct                       | 353                                       | 8.85%  | 2394                   | 60.05%  | 888                     | 22.27% |
| Provisional                       | 97  | 11.81% | 382                    | 46.53%  | 169                     | 20.58% |
| Total                             | 3522                                      | 9.44%  | 21370                  | 57.26%  | 8674                    | 23.24% |
| <b>SUPERVISORIAL DISTRICTS</b>    |   |        |                        |         |                         |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |   |        |                        |         |                         |        |
| Precinct TSx                      | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS                       | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct VBM                      | 0   | -      | 0                      | -       | 0                       | -      |
| MB Precinct                       | 0   | -      | 0                      | -       | 0                       | -      |
| Provisional                       | 0   | -      | 0                      | -       | 0                       | -      |
| Total                             | 0   | -      | 0                      | -       | 0                       | -      |
| <b>2ND SUPERVISORIAL DISTRICT</b> |   |        |                        |         |                         |        |
| Precinct TSx                      | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS                       | 75  | 8.03%  | 611                    | 65.42%  | 173                     | 18.52% |
| Precinct VBM                      | 415                                       | 10.35% | 2502                   | 62.39%  | 796                     | 19.85% |
| MB Precinct                       | 128                                       | 8.76%  | 931                    | 63.72%  | 302                     | 20.67% |
| Provisional                       | 15  | 14.56% | 65                     | 63.11%  | 12                      | 11.65% |
| Total                             | 633                                       | 9.73%  | 4109                   | 63.14%  | 1283                    | 19.71% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |   |        |                        |         |                         |        |
| Precinct TSx                      | 0   | 0.00%  | 3                      | 100.00% | 0                       | 0.00%  |
| Precinct OS                       | 179                                       | 8.43%  | 1278                   | 60.17%  | 446                     | 21.00% |
| Precinct VBM                      | 826                                       | 8.96%  | 5528                   | 59.96%  | 2035                    | 22.07% |
| MB Precinct                       | 47  | 11.35% | 257                    | 62.08%  | 73                      | 17.63% |
| Provisional                       | 30  | 12.82% | 119                    | 50.85%  | 43                      | 18.38% |
| Total                             | 1082                                      | 9.02%  | 7185                   | 59.90%  | 2597                    | 21.65% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |   |        |                        |         |                         |        |
| Precinct TSx                      | 0   | -      | 0                      | -       | 0                       | -      |
| Precinct OS                       | 153                                       | 7.75%  | 1297                   | 65.67%  | 359                     | 18.18% |
| Precinct VBM                      | 661                                       | 9.30%  | 4465                   | 62.81%  | 1420                    | 19.97% |
| MB Precinct                       | 140                                       | 8.10%  | 1048                   | 60.65%  | 396                     | 22.92% |
| Provisional                       | 18  | 8.65%  | 125                    | 60.10%  | 35                      | 16.83% |
| Total                             | 972                                       | 8.82%  | 6935                   | 62.93%  | 2210                    | 20.05% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |   |        |                        |         |                         |        |
| Precinct TSx                      | 0   | 0.00%  | 1                      | 100.00% | 0                       | 0.00%  |
| Precinct OS                       | 180                                       | 10.24% | 632                    | 35.95%  | 589                     | 33.50% |
| Precinct VBM                      | 583                                       | 10.83% | 2277                   | 42.32%  | 1799                    | 33.43% |
| MB Precinct                       | 38  | 9.90%  | 158                    | 41.15%  | 117                     | 30.47% |
| Provisional                       | 34  | 12.32% | 73                     | 26.45%  | 79                      | 28.62% |
| Total                             | 835                                       | 10.71% | 3141                   | 40.27%  | 2584                    | 33.13% |
| <b>Total</b>                      |   |        |                        |         |                         |        |
| Precinct TSx                      | 0   | 0.00%  | 4                      | 100.00% | 0                       | 0.00%  |
| Precinct OS                       | 587                                       | 8.64%  | 3818                   | 56.22%  | 1567                    | 23.07% |
| Precinct VBM                      | 2485                                      | 9.66%  | 14772                  | 57.44%  | 6050                    | 23.52% |
| MB Precinct                       | 353                                       | 8.85%  | 2394                   | 60.05%  | 888                     | 22.27% |
| Provisional                       | 97  | 11.81% | 382                    | 46.53%  | 169                     | 20.58% |
| Total                             | 3522                                      | 9.44%  | 21370                  | 57.26%  | 8674                    | 23.24% |

**COUNTY OF EL DORADO**  
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|                                 | Member of the State Assembly 5th District |               |                        |               |                         |               |
|---------------------------------|---|---------------|------------------------|---------------|-------------------------|---------------|
|                                 | MARK BELDEN<br>(NPP)                      |               | FRANK BIGELOW<br>(REP) |               | ROBERT CARABAS<br>(DEM) |               |
| <b>CITIES</b>                   |   |               |                        |               |                         |               |
| <b>CITY OF PLACERVILLE</b>      |   |               |                        |               |                         |               |
| Precinct TSx                    | 0   | -             | 0                      | -             | 0                       | -             |
| Precinct OS                     | 43  | 7.85%         | 283                    | 51.64%        | 142                     | 25.91%        |
| Precinct VBM                    | 241                                       | 10.26%        | 1220                   | 51.94%        | 632                     | 26.91%        |
| MB Precinct                     | 0   | -             | 0                      | -             | 0                       | -             |
| Provisional                     | 10  | 15.15%        | 23                     | 34.85%        | 15                      | 22.73%        |
| <b>Total</b>                    | <b>294</b>                                | <b>9.92%</b>  | <b>1526</b>            | <b>51.50%</b> | <b>789</b>              | <b>26.63%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |   |               |                        |               |                         |               |
| Precinct TSx                    | 0   | -             | 0                      | -             | 0                       | -             |
| Precinct OS                     | 92  | 10.04%        | 282                    | 30.79%        | 312                     | 34.06%        |
| Precinct VBM                    | 289                                       | 11.86%        | 892                    | 36.60%        | 862                     | 35.37%        |
| MB Precinct                     | 0   | -             | 0                      | -             | 0                       | -             |
| Provisional                     | 26  | 15.12%        | 34                     | 19.77%        | 53                      | 30.81%        |
| <b>Total</b>                    | <b>407</b>                                | <b>11.55%</b> | <b>1208</b>            | <b>34.27%</b> | <b>1227</b>             | <b>34.81%</b> |
| <b>UNINCORPORATED AREA</b>      |   |               |                        |               |                         |               |
| Precinct TSx                    | 0   | 0.00%         | 4                      | 100.00%       | 0                       | 0.00%         |
| Precinct OS                     | 452                                       | 8.49%         | 3253                   | 61.07%        | 1113                    | 20.89%        |
| Precinct VBM                    | 1955                                      | 9.34%         | 12660                  | 60.48%        | 4556                    | 21.76%        |
| MB Precinct                     | 353                                       | 8.85%         | 2394                   | 60.05%        | 888                     | 22.27%        |
| Provisional                     | 61  | 10.46%        | 325                    | 55.75%        | 101                     | 17.32%        |
| <b>Total</b>                    | <b>2821</b>                               | <b>9.15%</b>  | <b>18636</b>           | <b>60.44%</b> | <b>6658</b>             | <b>21.59%</b> |
| <b>Total</b>                    |   |               |                        |               |                         |               |
| Precinct TSx                    | 0   | 0.00%         | 4                      | 100.00%       | 0                       | 0.00%         |
| Precinct OS                     | 587                                       | 8.64%         | 3818                   | 56.22%        | 1567                    | 23.07%        |
| Precinct VBM                    | 2485                                      | 9.66%         | 14772                  | 57.44%        | 6050                    | 23.52%        |
| MB Precinct                     | 353                                       | 8.85%         | 2394                   | 60.05%        | 888                     | 22.27%        |
| Provisional                     | 97  | 11.81%        | 382                    | 46.53%        | 169                     | 20.58%        |
| <b>Total</b>                    | <b>3522</b>                               | <b>9.44%</b>  | <b>21370</b>           | <b>57.26%</b> | <b>8674</b>             | <b>23.24%</b> |

**COUNTY OF EL DORADO**  
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|                          | Member of the State Assembly 6th District |          |               |             |                             |        |  |
|--------------------------|---|----------|---------------|-------------|-----------------------------|--------|--|
|                          | Reg. Voters                               | Vote For | Times Counted | Total Votes | RON "MIK"<br>MIKULACO (REP) |        |  |
| <b>Jurisdiction Wide</b> |   |          |               |             |                             |        |  |
| 100001.11-CP 100001.11   |   |          |               |             |                             |        |  |
| Precinct TSx             | 1615                                      | 1        | 0             | 0           | 0                           | -      |  |
| Precinct OS              | 1615                                      | 1        | 190           | 162         | 35                          | 21.60% |  |
| Precinct VBM             | 1615                                      | 1        | 781           | 707         | 96                          | 13.58% |  |
| MB Precinct              | 1615                                      | 1        | 0             | 0           | 0                           | -      |  |
| Provisional              | 1615                                      | 1        | 17            | 13          | 5                           | 38.46% |  |
| Total                    | 1615                                      | 5        | 988           | 882         | 136                         | 15.42% |  |
| 100002.11-CP 100002.11   |   |          |               |             |                             |        |  |
| Precinct TSx             | 1000                                      | 1        | 0             | 0           | 0                           | -      |  |
| Precinct OS              | 1000                                      | 1        | 89            | 74          | 12                          | 16.22% |  |
| Precinct VBM             | 1000                                      | 1        | 476           | 420         | 70                          | 16.67% |  |
| MB Precinct              | 1000                                      | 1        | 0             | 0           | 0                           | -      |  |
| Provisional              | 1000                                      | 1        | 20            | 18          | 2                           | 11.11% |  |
| Total                    | 1000                                      | 5        | 585           | 512         | 84                          | 16.41% |  |
| 100003.11-CP 100003.11   |   |          |               |             |                             |        |  |
| Precinct TSx             | 1269                                      | 1        | 0             | 0           | 0                           | -      |  |
| Precinct OS              | 1269                                      | 1        | 123           | 111         | 20                          | 18.02% |  |
| Precinct VBM             | 1269                                      | 1        | 773           | 696         | 81                          | 11.64% |  |
| MB Precinct              | 1269                                      | 1        | 0             | 0           | 0                           | -      |  |
| Provisional              | 1269                                      | 1        | 24            | 21          | 0                           | 0.00%  |  |
| Total                    | 1269                                      | 5        | 920           | 828         | 101                         | 12.20% |  |
| 100004.11-CP 100004.11   |   |          |               |             |                             |        |  |
| Precinct TSx             | 1633                                      | 1        | 0             | 0           | 0                           | -      |  |
| Precinct OS              | 1633                                      | 1        | 254           | 223         | 30                          | 13.45% |  |
| Precinct VBM             | 1633                                      | 1        | 738           | 651         | 89                          | 13.67% |  |
| MB Precinct              | 1633                                      | 1        | 0             | 0           | 0                           | -      |  |
| Provisional              | 1633                                      | 1        | 4             | 3           | 0                           | 0.00%  |  |
| Total                    | 1633                                      | 5        | 996           | 877         | 119                         | 13.57% |  |
| 100005.11-CP 100005.11   |   |          |               |             |                             |        |  |
| Precinct TSx             | 1586                                      | 1        | 0             | 0           | 0                           | -      |  |
| Precinct OS              | 1586                                      | 1        | 146           | 131         | 23                          | 17.56% |  |
| Precinct VBM             | 1586                                      | 1        | 895           | 815         | 142                         | 17.42% |  |
| MB Precinct              | 1586                                      | 1        | 0             | 0           | 0                           | -      |  |
| Provisional              | 1586                                      | 1        | 23            | 20          | 5                           | 25.00% |  |
| Total                    | 1586                                      | 5        | 1064          | 966         | 170                         | 17.60% |  |
| 100006.11-CP 100006.11   |   |          |               |             |                             |        |  |
| Precinct TSx             | 1928                                      | 1        | 0             | 0           | 0                           | -      |  |
| Precinct OS              | 1928                                      | 1        | 252           | 215         | 45                          | 20.93% |  |
| Precinct VBM             | 1928                                      | 1        | 944           | 840         | 118                         | 14.05% |  |
| MB Precinct              | 1928                                      | 1        | 0             | 0           | 0                           | -      |  |
| Provisional              | 1928                                      | 1        | 31            | 21          | 5                           | 23.81% |  |
| Total                    | 1928                                      | 5        | 1227          | 1076        | 168                         | 15.61% |  |
| 100008.11-CP 100008.11   |   |          |               |             |                             |        |  |
| Precinct TSx             | 1671                                      | 1        | 0             | 0           | 0                           | -      |  |
| Precinct OS              | 1671                                      | 1        | 197           | 167         | 26                          | 15.57% |  |
| Precinct VBM             | 1671                                      | 1        | 740           | 655         | 85                          | 12.98% |  |
| MB Precinct              | 1671                                      | 1        | 0             | 0           | 0                           | -      |  |
| Provisional              | 1671                                      | 1        | 17            | 13          | 3                           | 23.08% |  |
| Total                    | 1671                                      | 5        | 954           | 835         | 114                         | 13.65% |  |
| 100009.11-CP 100009.11   |   |          |               |             |                             |        |  |
| Precinct TSx             | 1838                                      | 1        | 0             | 0           | 0                           | -      |  |
| Precinct OS              | 1838                                      | 1        | 169           | 151         | 24                          | 15.89% |  |
| Precinct VBM             | 1838                                      | 1        | 751           | 683         | 112                         | 16.40% |  |
| MB Precinct              | 1838                                      | 1        | 0             | 0           | 0                           | -      |  |
| Provisional              | 1838                                      | 1        | 26            | 19          | 3                           | 15.79% |  |
| Total                    | 1838                                      | 5        | 946           | 853         | 139                         | 16.30% |  |
| 100012.11-CP 100012.11   |   |          |               |             |                             |        |  |
| Precinct TSx             | 2108                                      | 1        | 0             | 0           | 0                           | -      |  |
| Precinct OS              | 2108                                      | 1        | 255           | 226         | 37                          | 16.37% |  |
| Precinct VBM             | 2108                                      | 1        | 968           | 844         | 125                         | 14.81% |  |

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|------------------------|---|----------|---------------|-------------|-----------------------------|--------|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | RON "MIK"<br>MIKULACO (REP) |        |
| MB Precinct            | 2108                                      | 1        | 0             | 0           | 0                           | -      |
| Provisional            | 2108                                      | 1        | 29            | 24          | 3                           | 12.50% |
| Total                  | 2108                                      | 5        | 1252          | 1094        | 165                         | 15.08% |
| 100013.11-CP 100013.11 |   |          |               |             |                             |        |
| Precinct TSx           | 2087                                      | 1        | 0             | 0           | 0                           | -      |
| Precinct OS            | 2087                                      | 1        | 250           | 230         | 42                          | 18.26% |
| Precinct VBM           | 2087                                      | 1        | 908           | 819         | 139                         | 16.97% |
| MB Precinct            | 2087                                      | 1        | 0             | 0           | 0                           | -      |
| Provisional            | 2087                                      | 1        | 28            | 21          | 3                           | 14.29% |
| Total                  | 2087                                      | 5        | 1186          | 1070        | 184                         | 17.20% |
| 100014.11-CP 100014.11 |   |          |               |             |                             |        |
| Precinct TSx           | 1659                                      | 1        | 0             | 0           | 0                           | -      |
| Precinct OS            | 1659                                      | 1        | 211           | 183         | 15                          | 8.20%  |
| Precinct VBM           | 1659                                      | 1        | 706           | 631         | 104                         | 16.48% |
| MB Precinct            | 1659                                      | 1        | 0             | 0           | 0                           | -      |
| Provisional            | 1659                                      | 1        | 30            | 24          | 3                           | 12.50% |
| Total                  | 1659                                      | 5        | 947           | 838         | 122                         | 14.56% |
| 100022.11-CP 100022.11 |   |          |               |             |                             |        |
| Precinct TSx           | 1432                                      | 1        | 0             | 0           | 0                           | -      |
| Precinct OS            | 1432                                      | 1        | 177           | 160         | 17                          | 10.63% |
| Precinct VBM           | 1432                                      | 1        | 678           | 600         | 129                         | 21.50% |
| MB Precinct            | 1432                                      | 1        | 0             | 0           | 0                           | -      |
| Provisional            | 1432                                      | 1        | 13            | 11          | 0                           | 0.00%  |
| Total                  | 1432                                      | 5        | 868           | 771         | 146                         | 18.94% |
| 100023.11-CP 100023.11 |   |          |               |             |                             |        |
| Precinct TSx           | 2135                                      | 1        | 0             | 0           | 0                           | -      |
| Precinct OS            | 2135                                      | 1        | 217           | 200         | 35                          | 17.50% |
| Precinct VBM           | 2135                                      | 1        | 1053          | 963         | 187                         | 19.42% |
| MB Precinct            | 2135                                      | 1        | 0             | 0           | 0                           | -      |
| Provisional            | 2135                                      | 1        | 33            | 28          | 9                           | 32.14% |
| Total                  | 2135                                      | 5        | 1303          | 1191        | 231                         | 19.40% |
| 200003.12-CP 200003.12 |   |          |               |             |                             |        |
| Precinct TSx           | 1362                                      | 1        | 0             | 0           | 0                           | -      |
| Precinct OS            | 1362                                      | 1        | 166           | 150         | 28                          | 18.67% |
| Precinct VBM           | 1362                                      | 1        | 558           | 506         | 59                          | 11.66% |
| MB Precinct            | 1362                                      | 1        | 0             | 0           | 0                           | -      |
| Provisional            | 1362                                      | 1        | 31            | 26          | 6                           | 23.08% |
| Total                  | 1362                                      | 5        | 755           | 682         | 93                          | 13.64% |
| 200004.12-CP 200004.12 |   |          |               |             |                             |        |
| Precinct TSx           | 1507                                      | 1        | 0             | 0           | 0                           | -      |
| Precinct OS            | 1507                                      | 1        | 182           | 161         | 24                          | 14.91% |
| Precinct VBM           | 1507                                      | 1        | 662           | 604         | 76                          | 12.58% |
| MB Precinct            | 1507                                      | 1        | 0             | 0           | 0                           | -      |
| Provisional            | 1507                                      | 1        | 14            | 13          | 1                           | 7.69%  |
| Total                  | 1507                                      | 5        | 858           | 778         | 101                         | 12.98% |
| 200005.02-CP 200005.02 |   |          |               |             |                             |        |
| Precinct TSx           | -   | -        | -             | -           | -                           | -      |
| Precinct OS            | -   | -        | -             | -           | -                           | -      |
| Precinct VBM           | -   | -        | -             | -           | -                           | -      |
| MB Precinct            | -   | -        | -             | -           | -                           | -      |
| Provisional            | -   | -        | -             | -           | -                           | -      |
| Total                  | -   | -        | -             | -           | -                           | -      |
| 200006.12-CP 200006.12 |   |          |               |             |                             |        |
| Precinct TSx           | 1086                                      | 1        | 0             | 0           | 0                           | -      |
| Precinct OS            | 1086                                      | 1        | 117           | 111         | 11                          | 9.91%  |
| Precinct VBM           | 1086                                      | 1        | 532           | 474         | 56                          | 11.81% |
| MB Precinct            | 1086                                      | 1        | 0             | 0           | 0                           | -      |
| Provisional            | 1086                                      | 1        | 29            | 27          | 5                           | 18.52% |
| Total                  | 1086                                      | 5        | 678           | 612         | 72                          | 11.76% |
| 200008.12-CP 200008.12 |   |          |               |             |                             |        |
| Precinct TSx           | 999                                       | 1        | 0             | 0           | 0                           | -      |

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|------------------------|---|----------|---------------|-------------|-----------------------------|--------|--|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | RON "MIK"<br>MIKULACO (REP) |        |  |
| Precinct OS            | 999                                       | 1        | 155           | 134         | 13                          | 9.70%  |  |
| Precinct VBM           | 999                                       | 1        | 379           | 318         | 45                          | 14.15% |  |
| MB Precinct            | 999                                       | 1        | 0             | 0           | 0                           | -      |  |
| Provisional            | 999                                       | 1        | 10            | 10          | 3                           | 30.00% |  |
| Total                  | 999                                       | 5        | 544           | 462         | 61                          | 13.20% |  |
| 200010.12-CP 200010.12 |   |          |               |             |                             |        |  |
| Precinct TSx           | 1976                                      | 1        | 4             | 4           | 1                           | 25.00% |  |
| Precinct OS            | 1976                                      | 1        | 197           | 174         | 21                          | 12.07% |  |
| Precinct VBM           | 1976                                      | 1        | 859           | 759         | 113                         | 14.89% |  |
| MB Precinct            | 1976                                      | 1        | 0             | 0           | 0                           | -      |  |
| Provisional            | 1976                                      | 1        | 34            | 24          | 7                           | 29.17% |  |
| Total                  | 1976                                      | 5        | 1094          | 961         | 142                         | 14.78% |  |
| 200012.12-CP 200012.12 |   |          |               |             |                             |        |  |
| Precinct TSx           | 1064                                      | 1        | 0             | 0           | 0                           | -      |  |
| Precinct OS            | 1064                                      | 1        | 145           | 126         | 16                          | 12.70% |  |
| Precinct VBM           | 1064                                      | 1        | 464           | 413         | 51                          | 12.35% |  |
| MB Precinct            | 1064                                      | 1        | 0             | 0           | 0                           | -      |  |
| Provisional            | 1064                                      | 1        | 14            | 12          | 1                           | 8.33%  |  |
| Total                  | 1064                                      | 5        | 623           | 551         | 68                          | 12.34% |  |
| 200013.12-CP 200013.12 |   |          |               |             |                             |        |  |
| Precinct TSx           | 1574                                      | 1        | 0             | 0           | 0                           | -      |  |
| Precinct OS            | 1574                                      | 1        | 211           | 188         | 30                          | 15.96% |  |
| Precinct VBM           | 1574                                      | 1        | 701           | 650         | 67                          | 10.31% |  |
| MB Precinct            | 1574                                      | 1        | 0             | 0           | 0                           | -      |  |
| Provisional            | 1574                                      | 1        | 23            | 18          | 4                           | 22.22% |  |
| Total                  | 1574                                      | 5        | 935           | 856         | 101                         | 11.80% |  |
| 200014.12-CP 200014.12 |   |          |               |             |                             |        |  |
| Precinct TSx           | 1438                                      | 1        | 0             | 0           | 0                           | -      |  |
| Precinct OS            | 1438                                      | 1        | 201           | 180         | 30                          | 16.67% |  |
| Precinct VBM           | 1438                                      | 1        | 613           | 540         | 64                          | 11.85% |  |
| MB Precinct            | 1438                                      | 1        | 0             | 0           | 0                           | -      |  |
| Provisional            | 1438                                      | 1        | 30            | 25          | 1                           | 4.00%  |  |
| Total                  | 1438                                      | 5        | 844           | 745         | 95                          | 12.75% |  |
| 200015.02-CP 200015.02 |   |          |               |             |                             |        |  |
| Precinct TSx           | -   | -        | -             | -           | -                           | -      |  |
| Precinct OS            | -   | -        | -             | -           | -                           | -      |  |
| Precinct VBM           | -   | -        | -             | -           | -                           | -      |  |
| MB Precinct            | -   | -        | -             | -           | -                           | -      |  |
| Provisional            | -   | -        | -             | -           | -                           | -      |  |
| Total                  | -   | -        | -             | -           | -                           | -      |  |
| 200016.12-CP 200016.12 |   |          |               |             |                             |        |  |
| Precinct TSx           | 301                                       | 1        | 0             | 0           | 0                           | -      |  |
| Precinct OS            | 301                                       | 1        | 26            | 21          | 6                           | 28.57% |  |
| Precinct VBM           | 301                                       | 1        | 139           | 124         | 19                          | 15.32% |  |
| MB Precinct            | 301                                       | 1        | 0             | 0           | 0                           | -      |  |
| Provisional            | 301                                       | 1        | 6             | 4           | 1                           | 25.00% |  |
| Total                  | 301                                       | 5        | 171           | 149         | 26                          | 17.45% |  |
| 200016.13-CP 200016.13 |   |          |               |             |                             |        |  |
| Precinct TSx           | 565                                       | 1        | 0             | 0           | 0                           | -      |  |
| Precinct OS            | 565                                       | 1        | 80            | 72          | 12                          | 16.67% |  |
| Precinct VBM           | 565                                       | 1        | 338           | 297         | 24                          | 8.08%  |  |
| MB Precinct            | 565                                       | 1        | 0             | 0           | 0                           | -      |  |
| Provisional            | 565                                       | 1        | 6             | 3           | 1                           | 33.33% |  |
| Total                  | 565                                       | 5        | 424           | 372         | 37                          | 9.95%  |  |
| 200017.02-CP 200017.02 |   |          |               |             |                             |        |  |
| Precinct TSx           | -   | -        | -             | -           | -                           | -      |  |
| Precinct OS            | -   | -        | -             | -           | -                           | -      |  |
| Precinct VBM           | -   | -        | -             | -           | -                           | -      |  |
| MB Precinct            | -   | -        | -             | -           | -                           | -      |  |
| Provisional            | -   | -        | -             | -           | -                           | -      |  |
| Total                  | -   | -        | -             | -           | -                           | -      |  |

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|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | RON "MIK"<br>MIKULACO (REP) |
| 200018.02-CP 200018.02 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 200020.02-CP 200020.02 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 200021.01-CP 200021.01 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 200021.02-CP 200021.02 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 200024.02-CP 200024.02 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 300001.04-CP 300001.04 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 300001.05-CP 300001.05 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 300003.05-CP 300003.05 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 300004.05-CP 300004.05 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | Member of the State Assembly 6th District |          |               |             |                             |
|------------------------|---|----------|---------------|-------------|-----------------------------|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | RON "MIK"<br>MIKULACO (REP) |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 300005.05-CP 300005.05 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 300008.05-CP 300008.05 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 300009.04-CP 300009.04 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 300010.04-CP 300010.04 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 300011.04-CP 300011.04 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 300013.05-CP 300013.05 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 300015.04-CP 300015.04 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 300015.05-CP 300015.05 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 300017.05-CP 300017.05 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |



**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 6th District |          |               |             |     | RON "MIK"<br>MIKULACO (REP) |   |
|------------------------|---|----------|---------------|-------------|-----|-----------------------------|---|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes |     |                             |   |
| Precinct VBM           | -   | -        | -             | -           | -   | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -   | -                           | - |
| Provisional            | -   | -        | -             | -           | -   | -                           | - |
| Total                  | -   | -        | -             | -           | -   | -                           | - |
| 300018.04-CP 300018.04 |   |          |               |             |     |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -   | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -   | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -   | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -   | -                           | - |
| Provisional            | -   | -        | -             | -           | -   | -                           | - |
| Total                  | -   | -        | -             | -           | -   | -                           | - |
| 300020.04-CP 300020.04 |   |          |               |             |     |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -   | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -   | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -   | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -   | -                           | - |
| Provisional            | -   | -        | -             | -           | -   | -                           | - |
| Total                  | -   | -        | -             | -           | -   | -                           | - |
| 300021.03-CP 300021.03 |   |          |               |             |     |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -   | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -   | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -   | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -   | -                           | - |
| Provisional            | -   | -        | -             | -           | -   | -                           | - |
| Total                  | -   | -        | -             | -           | -   | -                           | - |
| 300022.05-CP 300022.05 |   |          |               |             |     |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -   | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -   | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -   | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -   | -                           | - |
| Provisional            | -   | -        | -             | -           | -   | -                           | - |
| Total                  | -   | -        | -             | -           | -   | -                           | - |
| 300023.03-CP 300023.03 |   |          |               |             |     |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -   | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -   | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -   | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -   | -                           | - |
| Provisional            | -   | -        | -             | -           | -   | -                           | - |
| Total                  | -   | -        | -             | -           | -   | -                           | - |
| 300023.05-CP 300023.05 |   |          |               |             |     |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -   | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -   | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -   | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -   | -                           | - |
| Provisional            | -   | -        | -             | -           | -   | -                           | - |
| Total                  | -   | -        | -             | -           | -   | -                           | - |
| 400001.14-CP 400001.14 |   |          |               |             |     |                             |   |
| Precinct TSx           | 1957                                      | 1        | 0             | 0           | 0   | -                           | - |
| Precinct OS            | 1957                                      | 1        | 178           | 151         | 11  | 7.28%                       | - |
| Precinct VBM           | 1957                                      | 1        | 992           | 893         | 103 | 11.53%                      | - |
| MB Precinct            | 1957                                      | 1        | 0             | 0           | 0   | -                           | - |
| Provisional            | 1957                                      | 1        | 23            | 21          | 0   | 0.00%                       | - |
| Total                  | 1957                                      | 5        | 1193          | 1065        | 114 | 10.70%                      | - |
| 400002.08-CP 400002.08 |   |          |               |             |     |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -   | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -   | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -   | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -   | -                           | - |
| Provisional            | -   | -        | -             | -           | -   | -                           | - |
| Total                  | -   | -        | -             | -           | -   | -                           | - |
| 400003.08-CP 400003.08 |   |          |               |             |     |                             |   |

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|------------------------|---|----------|---------------|-------------|----|-----------------------------|---|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes |    |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -  | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -  | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                           | - |
| Provisional            | -   | -        | -             | -           | -  | -                           | - |
| Total                  | -   | -        | -             | -           | -  | -                           | - |
| 400004.14-CP 400004.14 |   |          |               |             |    |                             |   |
| Precinct TSx           | 1536                                      | 1        | 0             | 0           | 0  | 0                           | - |
| Precinct OS            | 1536                                      | 1        | 194           | 168         | 31 | 18.45%                      | - |
| Precinct VBM           | 1536                                      | 1        | 662           | 590         | 54 | 9.15%                       | - |
| MB Precinct            | 1536                                      | 1        | 0             | 0           | 0  | 0                           | - |
| Provisional            | 1536                                      | 1        | 19            | 16          | 4  | 25.00%                      | - |
| Total                  | 1536                                      | 5        | 875           | 774         | 89 | 11.50%                      | - |
| 400005.08-CP 400005.08 |   |          |               |             |    |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -  | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -  | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                           | - |
| Provisional            | -   | -        | -             | -           | -  | -                           | - |
| Total                  | -   | -        | -             | -           | -  | -                           | - |
| 400006.08-CP 400006.08 |   |          |               |             |    |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -  | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -  | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                           | - |
| Provisional            | -   | -        | -             | -           | -  | -                           | - |
| Total                  | -   | -        | -             | -           | -  | -                           | - |
| 400007.08-CP 400007.08 |   |          |               |             |    |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -  | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -  | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                           | - |
| Provisional            | -   | -        | -             | -           | -  | -                           | - |
| Total                  | -   | -        | -             | -           | -  | -                           | - |
| 400010.08-CP 400010.08 |   |          |               |             |    |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -  | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -  | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                           | - |
| Provisional            | -   | -        | -             | -           | -  | -                           | - |
| Total                  | -   | -        | -             | -           | -  | -                           | - |
| 400012.08-CP 400012.08 |   |          |               |             |    |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -  | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -  | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                           | - |
| Provisional            | -   | -        | -             | -           | -  | -                           | - |
| Total                  | -   | -        | -             | -           | -  | -                           | - |
| 400013.08-CP 400013.08 |   |          |               |             |    |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -  | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -  | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                           | - |
| Provisional            | -   | -        | -             | -           | -  | -                           | - |
| Total                  | -   | -        | -             | -           | -  | -                           | - |
| 400014.08-CP 400014.08 |   |          |               |             |    |                             |   |
| Precinct TSx           | -   | -        | -             | -           | -  | -                           | - |
| Precinct OS            | -   | -        | -             | -           | -  | -                           | - |
| Precinct VBM           | -   | -        | -             | -           | -  | -                           | - |
| MB Precinct            | -   | -        | -             | -           | -  | -                           | - |
| Provisional            | -   | -        | -             | -           | -  | -                           | - |

**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 6th District |          |               |             |                             |
|------------------------|---|----------|---------------|-------------|-----------------------------|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | RON "MIK"<br>MIKULACO (REP) |
| Total                  | -   | -        | -             | -           | -                           |
| 400017.08-CP 400017.08 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 400018.08-CP 400018.08 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 400020.08-CP 400020.08 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 400021.07-CP 400021.07 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 500001.10-CP 500001.10 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 500002.10-CP 500002.10 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 500003.10-CP 500003.10 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 500004.10-CP 500004.10 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 500005.10-CP 500005.10 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |

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|                        | Member of the State Assembly 6th District |          |               |             |                             |
|------------------------|---|----------|---------------|-------------|-----------------------------|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | RON "MIK"<br>MIKULACO (REP) |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 500006.10-CP 500006.10 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 500007.10-CP 500007.10 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 500009.10-CP 500009.10 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 500011.10-CP 500011.10 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 500012.10-CP 500012.10 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 500015.10-CP 500015.10 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 500020.10-CP 500020.10 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 500021.10-CP 500021.10 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U201.01-MB 95U201.01 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |

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|                        | Member of the State Assembly 6th District |          |               |             |                             |
|------------------------|---|----------|---------------|-------------|-----------------------------|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | RON "MIK"<br>MIKULACO (REP) |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U201.02-MB 95U201.02 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U202.02-MB 95U202.02 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U203.02-MB 95U203.02 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U204.01-MB 95U204.01 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U301.05-MB 95U301.05 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U302.04-MB 95U302.04 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U302.05-MB 95U302.05 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U303.03-MB 95U303.03 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |

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|                        | Member of the State Assembly 6th District |          |               |             |                             |
|------------------------|---|----------|---------------|-------------|-----------------------------|
|                        | Reg. Voters                               | Vote For | Times Counted | Total Votes | RON "MIK"<br>MIKULACO (REP) |
| 95U303.05-MB 95U303.05 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U304.05-MB 95U304.05 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U305.05-MB 95U305.05 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U401.08-MB 95U401.08 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U402.07-MB 95U402.07 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U402.08-MB 95U402.08 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U403.08-MB 95U403.08 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U404.06-MB 95U404.06 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |
| Provisional            | -   | -        | -             | -           | -                           |
| Total                  | -   | -        | -             | -           | -                           |
| 95U404.08-MB 95U404.08 |   |          |               |             |                             |
| Precinct TSx           | -   | -        | -             | -           | -                           |
| Precinct OS            | -   | -        | -             | -           | -                           |
| Precinct VBM           | -   | -        | -             | -           | -                           |
| MB Precinct            | -   | -        | -             | -           | -                           |

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|                               | Member of the State Assembly 6th District |          |               |             |                             |        |   |
|-------------------------------|---|----------|---------------|-------------|-----------------------------|--------|---|
|                               | Reg. Voters                               | Vote For | Times Counted | Total Votes | RON "MIK"<br>MIKULACO (REP) |        |   |
| Provisional                   | -   | -        | -             | -           | -                           | -      | - |
| Total                         | -   | -        | -             | -           | -                           | -      | - |
| 95U501.09-MB 95U501.09        |   |          |               |             |                             |        |   |
| Precinct TSx                  | -   | -        | -             | -           | -                           | -      | - |
| Precinct OS                   | -   | -        | -             | -           | -                           | -      | - |
| Precinct VBM                  | -   | -        | -             | -           | -                           | -      | - |
| MB Precinct                   | -   | -        | -             | -           | -                           | -      | - |
| Provisional                   | -   | -        | -             | -           | -                           | -      | - |
| Total                         | -   | -        | -             | -           | -                           | -      | - |
| 95U501.10-MB 95U501.10        |   |          |               |             |                             |        |   |
| Precinct TSx                  | -   | -        | -             | -           | -                           | -      | - |
| Precinct OS                   | -   | -        | -             | -           | -                           | -      | - |
| Precinct VBM                  | -   | -        | -             | -           | -                           | -      | - |
| MB Precinct                   | -   | -        | -             | -           | -                           | -      | - |
| Provisional                   | -   | -        | -             | -           | -                           | -      | - |
| Total                         | -   | -        | -             | -           | -                           | -      | - |
| 95U598.10-MB 95U598.10        |   |          |               |             |                             |        |   |
| Precinct TSx                  | -   | -        | -             | -           | -                           | -      | - |
| Precinct OS                   | -   | -        | -             | -           | -                           | -      | - |
| Precinct VBM                  | -   | -        | -             | -           | -                           | -      | - |
| MB Precinct                   | -   | -        | -             | -           | -                           | -      | - |
| Provisional                   | -   | -        | -             | -           | -                           | -      | - |
| Total                         | -   | -        | -             | -           | -                           | -      | - |
| 95U599.10-MB 95U599.10        |   |          |               |             |                             |        |   |
| Precinct TSx                  | -   | -        | -             | -           | -                           | -      | - |
| Precinct OS                   | -   | -        | -             | -           | -                           | -      | - |
| Precinct VBM                  | -   | -        | -             | -           | -                           | -      | - |
| MB Precinct                   | -   | -        | -             | -           | -                           | -      | - |
| Provisional                   | -   | -        | -             | -           | -                           | -      | - |
| Total                         | -   | -        | -             | -           | -                           | -      | - |
| 96U101.11-MB 96U101.11        |   |          |               |             |                             |        |   |
| Precinct TSx                  | 626                                       | 1        | 0             | 0           | 0                           | -      | - |
| Precinct OS                   | 626                                       | 1        | 0             | 0           | 0                           | -      | - |
| Precinct VBM                  | 626                                       | 1        | 0             | 0           | 0                           | -      | - |
| MB Precinct                   | 626                                       | 1        | 423           | 381         | 39                          | 10.24% |   |
| Provisional                   | 626                                       | 1        | 6             | 6           | 0                           | 0.00%  |   |
| Total                         | 626                                       | 5        | 429           | 387         | 39                          | 10.08% |   |
| 96U401.14-MB 96U401.14        |   |          |               |             |                             |        |   |
| Precinct TSx                  | 566                                       | 1        | 0             | 0           | 0                           | -      |   |
| Precinct OS                   | 566                                       | 1        | 0             | 0           | 0                           | -      |   |
| Precinct VBM                  | 566                                       | 1        | 0             | 0           | 0                           | -      |   |
| MB Precinct                   | 566                                       | 1        | 359           | 316         | 35                          | 11.08% |   |
| Provisional                   | 566                                       | 1        | 6             | 1           | 0                           | 0.00%  |   |
| Total                         | 566                                       | 5        | 365           | 317         | 35                          | 11.04% |   |
| Total                         |   |          |               |             |                             |        |   |
| Precinct TSx                  | 38518                                     | 27       | 4             | 4           | 1                           | 25.00% |   |
| Precinct OS                   | 38518                                     | 27       | 4382          | 3869        | 594                         | 15.35% |   |
| Precinct VBM                  | 38518                                     | 27       | 17310         | 15492       | 2208                        | 14.25% |   |
| MB Precinct                   | 38518                                     | 27       | 782           | 697         | 74                          | 10.62% |   |
| Provisional                   | 38518                                     | 27       | 546           | 442         | 75                          | 16.97% |   |
| Total                         | 38518                                     | 135      | 23024         | 20504       | 2952                        | 14.40% |   |
| STATE BOARD OF EQUALIZATION   |   |          |               |             |                             |        |   |
| STATE BOARD OF EQUALIZATION 1 |   |          |               |             |                             |        |   |
| Precinct TSx                  | 38518                                     | 27       | 4             | 4           | 1                           | 25.00% |   |
| Precinct OS                   | 38518                                     | 27       | 4382          | 3869        | 594                         | 15.35% |   |
| Precinct VBM                  | 38518                                     | 27       | 17310         | 15492       | 2208                        | 14.25% |   |
| MB Precinct                   | 38518                                     | 27       | 782           | 697         | 74                          | 10.62% |   |
| Provisional                   | 38518                                     | 27       | 546           | 442         | 75                          | 16.97% |   |
| Total                         | 38518                                     | 135      | 23024         | 20504       | 2952                        | 14.40% |   |
| Total                         |   |          |               |             |                             |        |   |

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|                                     | Member of the State Assembly 6th District |            |               |              |                             |               |
|-------------------------------------|---|------------|---------------|--------------|-----------------------------|---------------|
|                                     | Reg. Voters                               | Vote For   | Times Counted | Total Votes  | RON "MIK"<br>MIKULACO (REP) |               |
| Precinct TSx                        | 38518                                     | 27         | 4             | 4            | 1                           | 25.00%        |
| Precinct OS                         | 38518                                     | 27         | 4382          | 3869         | 594                         | 15.35%        |
| Precinct VBM                        | 38518                                     | 27         | 17310         | 15492        | 2208                        | 14.25%        |
| MB Precinct                         | 38518                                     | 27         | 782           | 697          | 74                          | 10.62%        |
| Provisional                         | 38518                                     | 27         | 546           | 442          | 75                          | 16.97%        |
| <b>Total</b>                        | <b>38518</b>                              | <b>135</b> | <b>23024</b>  | <b>20504</b> | <b>2952</b>                 | <b>14.40%</b> |
| <b>UNITED STATES SENATE</b>         |   |            |               |              |                             |               |
| <b>UNITED STATES SENATE</b>         |   |            |               |              |                             |               |
| Precinct TSx                        | 38518                                     | 27         | 4             | 4            | 1                           | 25.00%        |
| Precinct OS                         | 38518                                     | 27         | 4382          | 3869         | 594                         | 15.35%        |
| Precinct VBM                        | 38518                                     | 27         | 17310         | 15492        | 2208                        | 14.25%        |
| MB Precinct                         | 38518                                     | 27         | 782           | 697          | 74                          | 10.62%        |
| Provisional                         | 38518                                     | 27         | 546           | 442          | 75                          | 16.97%        |
| <b>Total</b>                        | <b>38518</b>                              | <b>135</b> | <b>23024</b>  | <b>20504</b> | <b>2952</b>                 | <b>14.40%</b> |
| <b>Total</b>                        |   |            |               |              |                             |               |
| Precinct TSx                        | 38518                                     | 27         | 4             | 4            | 1                           | 25.00%        |
| Precinct OS                         | 38518                                     | 27         | 4382          | 3869         | 594                         | 15.35%        |
| Precinct VBM                        | 38518                                     | 27         | 17310         | 15492        | 2208                        | 14.25%        |
| MB Precinct                         | 38518                                     | 27         | 782           | 697          | 74                          | 10.62%        |
| Provisional                         | 38518                                     | 27         | 546           | 442          | 75                          | 16.97%        |
| <b>Total</b>                        | <b>38518</b>                              | <b>135</b> | <b>23024</b>  | <b>20504</b> | <b>2952</b>                 | <b>14.40%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |   |            |               |              |                             |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |   |            |               |              |                             |               |
| Precinct TSx                        | 38518                                     | 27         | 4             | 4            | 1                           | 25.00%        |
| Precinct OS                         | 38518                                     | 27         | 4382          | 3869         | 594                         | 15.35%        |
| Precinct VBM                        | 38518                                     | 27         | 17310         | 15492        | 2208                        | 14.25%        |
| MB Precinct                         | 38518                                     | 27         | 782           | 697          | 74                          | 10.62%        |
| Provisional                         | 38518                                     | 27         | 546           | 442          | 75                          | 16.97%        |
| <b>Total</b>                        | <b>38518</b>                              | <b>135</b> | <b>23024</b>  | <b>20504</b> | <b>2952</b>                 | <b>14.40%</b> |
| <b>Total</b>                        |   |            |               |              |                             |               |
| Precinct TSx                        | 38518                                     | 27         | 4             | 4            | 1                           | 25.00%        |
| Precinct OS                         | 38518                                     | 27         | 4382          | 3869         | 594                         | 15.35%        |
| Precinct VBM                        | 38518                                     | 27         | 17310         | 15492        | 2208                        | 14.25%        |
| MB Precinct                         | 38518                                     | 27         | 782           | 697          | 74                          | 10.62%        |
| Provisional                         | 38518                                     | 27         | 546           | 442          | 75                          | 16.97%        |
| <b>Total</b>                        | <b>38518</b>                              | <b>135</b> | <b>23024</b>  | <b>20504</b> | <b>2952</b>                 | <b>14.40%</b> |
| <b>STATE SENATE</b>                 |   |            |               |              |                             |               |
| <b>1ST STATE SENATE DISTRICT</b>    |   |            |               |              |                             |               |
| Precinct TSx                        | 38518                                     | 27         | 4             | 4            | 1                           | 25.00%        |
| Precinct OS                         | 38518                                     | 27         | 4382          | 3869         | 594                         | 15.35%        |
| Precinct VBM                        | 38518                                     | 27         | 17310         | 15492        | 2208                        | 14.25%        |
| MB Precinct                         | 38518                                     | 27         | 782           | 697          | 74                          | 10.62%        |
| Provisional                         | 38518                                     | 27         | 546           | 442          | 75                          | 16.97%        |
| <b>Total</b>                        | <b>38518</b>                              | <b>135</b> | <b>23024</b>  | <b>20504</b> | <b>2952</b>                 | <b>14.40%</b> |
| <b>Total</b>                        |   |            |               |              |                             |               |
| Precinct TSx                        | 38518                                     | 27         | 4             | 4            | 1                           | 25.00%        |
| Precinct OS                         | 38518                                     | 27         | 4382          | 3869         | 594                         | 15.35%        |
| Precinct VBM                        | 38518                                     | 27         | 17310         | 15492        | 2208                        | 14.25%        |
| MB Precinct                         | 38518                                     | 27         | 782           | 697          | 74                          | 10.62%        |
| Provisional                         | 38518                                     | 27         | 546           | 442          | 75                          | 16.97%        |
| <b>Total</b>                        | <b>38518</b>                              | <b>135</b> | <b>23024</b>  | <b>20504</b> | <b>2952</b>                 | <b>14.40%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |   |            |               |              |                             |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |   |            |               |              |                             |               |
| Precinct TSx                        | 0   | 0          | 0             | 0            | 0                           | -             |
| Precinct OS                         | 0   | 0          | 0             | 0            | 0                           | -             |
| Precinct VBM                        | 0   | 0          | 0             | 0            | 0                           | -             |
| MB Precinct                         | 0   | 0          | 0             | 0            | 0                           | -             |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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| Member of the State Assembly 6th District |             |          |               |             |                             |        |
|---|-------------|----------|---------------|-------------|-----------------------------|--------|
|   | Reg. Voters | Vote For | Times Counted | Total Votes | RON "MIK"<br>MIKULACO (REP) |        |
| Provisional                               | 0           | 0        | 0             | 0           | 0                           | -      |
| Total                                     | 0           | 0        | 0             | 0           | 0                           | -      |
| <b>6TH STATE ASSEMBLY DIST</b>            |             |          |               |             |                             |        |
| Precinct TSx                              | 38518       | 27       | 4             | 4           | 1                           | 25.00% |
| Precinct OS                               | 38518       | 27       | 4382          | 3869        | 594                         | 15.35% |
| Precinct VBM                              | 38518       | 27       | 17310         | 15492       | 2208                        | 14.25% |
| MB Precinct                               | 38518       | 27       | 782           | 697         | 74                          | 10.62% |
| Provisional                               | 38518       | 27       | 546           | 442         | 75                          | 16.97% |
| Total                                     | 38518       | 135      | 23024         | 20504       | 2952                        | 14.40% |
| <b>Total</b>                              |             |          |               |             |                             |        |
| Precinct TSx                              | 38518       | 27       | 4             | 4           | 1                           | 25.00% |
| Precinct OS                               | 38518       | 27       | 4382          | 3869        | 594                         | 15.35% |
| Precinct VBM                              | 38518       | 27       | 17310         | 15492       | 2208                        | 14.25% |
| MB Precinct                               | 38518       | 27       | 782           | 697         | 74                          | 10.62% |
| Provisional                               | 38518       | 27       | 546           | 442         | 75                          | 16.97% |
| Total                                     | 38518       | 135      | 23024         | 20504       | 2952                        | 14.40% |
| <b>SUPERVISORIAL DISTRICTS</b>            |             |          |               |             |                             |        |
| <b>1ST SUPERVISORIAL DISTRICT</b>         |             |          |               |             |                             |        |
| Precinct TSx                              | 22587       | 14       | 0             | 0           | 0                           | -      |
| Precinct OS                               | 22587       | 14       | 2530          | 2233        | 361                         | 16.17% |
| Precinct VBM                              | 22587       | 14       | 10411         | 9324        | 1477                        | 15.84% |
| MB Precinct                               | 22587       | 14       | 423           | 381         | 39                          | 10.24% |
| Provisional                               | 22587       | 14       | 301           | 242         | 41                          | 16.94% |
| Total                                     | 22587       | 70       | 13665         | 12180       | 1918                        | 15.75% |
| <b>2ND SUPERVISORIAL DISTRICT</b>         |             |          |               |             |                             |        |
| Precinct TSx                              | 11872       | 10       | 4             | 4           | 1                           | 25.00% |
| Precinct OS                               | 11872       | 10       | 1480          | 1317        | 191                         | 14.50% |
| Precinct VBM                              | 11872       | 10       | 5245          | 4685        | 574                         | 12.25% |
| MB Precinct                               | 11872       | 10       | 0             | 0           | 0                           | -      |
| Provisional                               | 11872       | 10       | 197           | 162         | 30                          | 18.52% |
| Total                                     | 11872       | 50       | 6926          | 6168        | 796                         | 12.91% |
| <b>3RD SUPERVISORIAL DISTRICT</b>         |             |          |               |             |                             |        |
| Precinct TSx                              | 0           | 0        | 0             | 0           | 0                           | -      |
| Precinct OS                               | 0           | 0        | 0             | 0           | 0                           | -      |
| Precinct VBM                              | 0           | 0        | 0             | 0           | 0                           | -      |
| MB Precinct                               | 0           | 0        | 0             | 0           | 0                           | -      |
| Provisional                               | 0           | 0        | 0             | 0           | 0                           | -      |
| Total                                     | 0           | 0        | 0             | 0           | 0                           | -      |
| <b>4TH SUPERVISORIAL DISTRICT</b>         |             |          |               |             |                             |        |
| Precinct TSx                              | 4059        | 3        | 0             | 0           | 0                           | -      |
| Precinct OS                               | 4059        | 3        | 372           | 319         | 42                          | 13.17% |
| Precinct VBM                              | 4059        | 3        | 1654          | 1483        | 157                         | 10.59% |
| MB Precinct                               | 4059        | 3        | 359           | 316         | 35                          | 11.08% |
| Provisional                               | 4059        | 3        | 48            | 38          | 4                           | 10.53% |
| Total                                     | 4059        | 15       | 2433          | 2156        | 238                         | 11.04% |
| <b>5TH SUPERVISORIAL DISTRICT</b>         |             |          |               |             |                             |        |
| Precinct TSx                              | 0           | 0        | 0             | 0           | 0                           | -      |
| Precinct OS                               | 0           | 0        | 0             | 0           | 0                           | -      |
| Precinct VBM                              | 0           | 0        | 0             | 0           | 0                           | -      |
| MB Precinct                               | 0           | 0        | 0             | 0           | 0                           | -      |
| Provisional                               | 0           | 0        | 0             | 0           | 0                           | -      |
| Total                                     | 0           | 0        | 0             | 0           | 0                           | -      |
| <b>Total</b>                              |             |          |               |             |                             |        |
| Precinct TSx                              | 38518       | 27       | 4             | 4           | 1                           | 25.00% |
| Precinct OS                               | 38518       | 27       | 4382          | 3869        | 594                         | 15.35% |
| Precinct VBM                              | 38518       | 27       | 17310         | 15492       | 2208                        | 14.25% |
| MB Precinct                               | 38518       | 27       | 782           | 697         | 74                          | 10.62% |
| Provisional                               | 38518       | 27       | 546           | 442         | 75                          | 16.97% |
| Total                                     | 38518       | 135      | 23024         | 20504       | 2952                        | 14.40% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                 | Member of the State Assembly 6th District |          |               |             |      | RON "MIK"<br>MIKULACO (REP) |   |
|---------------------------------|---|----------|---------------|-------------|------|-----------------------------|---|
|                                 | Reg. Voters                               | Vote For | Times Counted | Total Votes |      |                             |   |
| <b>CITIES</b>                   |   |          |               |             |      |                             |   |
| <b>CITY OF PLACERVILLE</b>      |   |          |               |             |      |                             |   |
| Precinct TSx                    | 0   | 0        | 0             | 0           | 0    | 0                           | - |
| Precinct OS                     | 0   | 0        | 0             | 0           | 0    | 0                           | - |
| Precinct VBM                    | 0   | 0        | 0             | 0           | 0    | 0                           | - |
| MB Precinct                     | 0   | 0        | 0             | 0           | 0    | 0                           | - |
| Provisional                     | 0   | 0        | 0             | 0           | 0    | 0                           | - |
| Total                           | 0   | 0        | 0             | 0           | 0    | 0                           | - |
| <b>CITY OF SOUTH LAKE TAHOE</b> |   |          |               |             |      |                             |   |
| Precinct TSx                    | 0   | 0        | 0             | 0           | 0    | 0                           | - |
| Precinct OS                     | 0   | 0        | 0             | 0           | 0    | 0                           | - |
| Precinct VBM                    | 0   | 0        | 0             | 0           | 0    | 0                           | - |
| MB Precinct                     | 0   | 0        | 0             | 0           | 0    | 0                           | - |
| Provisional                     | 0   | 0        | 0             | 0           | 0    | 0                           | - |
| Total                           | 0   | 0        | 0             | 0           | 0    | 0                           | - |
| <b>UNINCORPORATED AREA</b>      |   |          |               |             |      |                             |   |
| Precinct TSx                    | 38518                                     | 27       | 4             | 4           | 1    | 25.00%                      |   |
| Precinct OS                     | 38518                                     | 27       | 4382          | 3869        | 594  | 15.35%                      |   |
| Precinct VBM                    | 38518                                     | 27       | 17310         | 15492       | 2208 | 14.25%                      |   |
| MB Precinct                     | 38518                                     | 27       | 782           | 697         | 74   | 10.62%                      |   |
| Provisional                     | 38518                                     | 27       | 546           | 442         | 75   | 16.97%                      |   |
| Total                           | 38518                                     | 135      | 23024         | 20504       | 2952 | 14.40%                      |   |
| <b>Total</b>                    |   |          |               |             |      |                             |   |
| Precinct TSx                    | 38518                                     | 27       | 4             | 4           | 1    | 25.00%                      |   |
| Precinct OS                     | 38518                                     | 27       | 4382          | 3869        | 594  | 15.35%                      |   |
| Precinct VBM                    | 38518                                     | 27       | 17310         | 15492       | 2208 | 14.25%                      |   |
| MB Precinct                     | 38518                                     | 27       | 782           | 697         | 74   | 10.62%                      |   |
| Provisional                     | 38518                                     | 27       | 546           | 442         | 75   | 16.97%                      |   |
| Total                           | 38518                                     | 135      | 23024         | 20504       | 2952 | 14.40%                      |   |

**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 6th District |        |                     |       |                  |        |                    |        |
|------------------------|---|--------|---------------------|-------|------------------|--------|--------------------|--------|
|                        | BILL HALLDIN (REP)                        |        | SUZANNE JONES (REP) |       | ANDY PUGNO (REP) |        | BRIAN CAPLES (DEM) |        |
| Jurisdiction Wide      |   |        |                     |       |                  |        |                    |        |
| 100001.11-CP 100001.11 |   |        |                     |       |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct OS            | 15  | 9.26%  | 6                   | 3.70% | 13               | 8.02%  | 22                 | 13.58% |
| Precinct VBM           | 80  | 11.32% | 20                  | 2.83% | 91               | 12.87% | 92                 | 13.01% |
| MB Precinct            | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Provisional            | 0   | 0.00%  | 0                   | 0.00% | 2                | 15.38% | 3                  | 23.08% |
| Total                  | 95  | 10.77% | 26                  | 2.95% | 106              | 12.02% | 117                | 13.27% |
| 100002.11-CP 100002.11 |   |        |                     |       |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct OS            | 8   | 10.81% | 2                   | 2.70% | 1                | 1.35%  | 13                 | 17.57% |
| Precinct VBM           | 37  | 8.81%  | 24                  | 5.71% | 48               | 11.43% | 66                 | 15.71% |
| MB Precinct            | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Provisional            | 1   | 5.56%  | 0                   | 0.00% | 1                | 5.56%  | 5                  | 27.78% |
| Total                  | 46  | 8.98%  | 26                  | 5.08% | 50               | 9.77%  | 84                 | 16.41% |
| 100003.11-CP 100003.11 |   |        |                     |       |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct OS            | 9   | 8.11%  | 3                   | 2.70% | 21               | 18.92% | 16                 | 14.41% |
| Precinct VBM           | 49  | 7.04%  | 32                  | 4.60% | 75               | 10.78% | 134                | 19.25% |
| MB Precinct            | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Provisional            | 5   | 23.81% | 0                   | 0.00% | 1                | 4.76%  | 4                  | 19.05% |
| Total                  | 63  | 7.61%  | 35                  | 4.23% | 97               | 11.71% | 154                | 18.60% |
| 100004.11-CP 100004.11 |   |        |                     |       |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct OS            | 16  | 7.17%  | 9                   | 4.04% | 27               | 12.11% | 43                 | 19.28% |
| Precinct VBM           | 58  | 8.91%  | 14                  | 2.15% | 74               | 11.37% | 132                | 20.28% |
| MB Precinct            | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Provisional            | 0   | 0.00%  | 0                   | 0.00% | 0                | 0.00%  | 0                  | 0.00%  |
| Total                  | 74  | 8.44%  | 23                  | 2.62% | 101              | 11.52% | 175                | 19.95% |
| 100005.11-CP 100005.11 |   |        |                     |       |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct OS            | 18  | 13.74% | 7                   | 5.34% | 18               | 13.74% | 24                 | 18.32% |
| Precinct VBM           | 78  | 9.57%  | 46                  | 5.64% | 70               | 8.59%  | 120                | 14.72% |
| MB Precinct            | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Provisional            | 3   | 15.00% | 1                   | 5.00% | 2                | 10.00% | 2                  | 10.00% |
| Total                  | 99  | 10.25% | 54                  | 5.59% | 90               | 9.32%  | 146                | 15.11% |
| 100006.11-CP 100006.11 |   |        |                     |       |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct OS            | 14  | 6.51%  | 3                   | 1.40% | 20               | 9.30%  | 41                 | 19.07% |
| Precinct VBM           | 56  | 6.67%  | 29                  | 3.45% | 71               | 8.45%  | 181                | 21.55% |
| MB Precinct            | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Provisional            | 0   | 0.00%  | 0                   | 0.00% | 2                | 9.52%  | 3                  | 14.29% |
| Total                  | 70  | 6.51%  | 32                  | 2.97% | 93               | 8.64%  | 225                | 20.91% |
| 100008.11-CP 100008.11 |   |        |                     |       |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct OS            | 16  | 9.58%  | 2                   | 1.20% | 27               | 16.17% | 37                 | 22.16% |
| Precinct VBM           | 50  | 7.63%  | 28                  | 4.27% | 83               | 12.67% | 118                | 18.02% |
| MB Precinct            | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Provisional            | 0   | 0.00%  | 0                   | 0.00% | 1                | 7.69%  | 2                  | 15.38% |
| Total                  | 66  | 7.90%  | 30                  | 3.59% | 111              | 13.29% | 157                | 18.80% |
| 100009.11-CP 100009.11 |   |        |                     |       |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct OS            | 6   | 3.97%  | 3                   | 1.99% | 11               | 7.28%  | 32                 | 21.19% |
| Precinct VBM           | 48  | 7.03%  | 27                  | 3.95% | 75               | 10.98% | 122                | 17.86% |
| MB Precinct            | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Provisional            | 0   | 0.00%  | 0                   | 0.00% | 1                | 5.26%  | 4                  | 21.05% |
| Total                  | 54  | 6.33%  | 30                  | 3.52% | 87               | 10.20% | 158                | 18.52% |
| 100012.11-CP 100012.11 |   |        |                     |       |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct OS            | 18  | 7.96%  | 8                   | 3.54% | 33               | 14.60% | 40                 | 17.70% |
| Precinct VBM           | 79  | 9.36%  | 17                  | 2.01% | 81               | 9.60%  | 131                | 15.52% |

**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 6th District |        |                     |        |                  |        |                    |        |
|------------------------|---|--------|---------------------|--------|------------------|--------|--------------------|--------|
|                        | BILL HALLDIN (REP)                        |        | SUZANNE JONES (REP) |        | ANDY PUGNO (REP) |        | BRIAN CAPLES (DEM) |        |
| MB Precinct            | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Provisional            | 2   | 8.33%  | 0                   | 0.00%  | 3                | 12.50% | 2                  | 8.33%  |
| Total                  | 99  | 9.05%  | 25                  | 2.29%  | 117              | 10.69% | 173                | 15.81% |
| 100013.11-CP 100013.11 |   |        |                     |        |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Precinct OS            | 13  | 5.65%  | 6                   | 2.61%  | 26               | 11.30% | 47                 | 20.43% |
| Precinct VBM           | 69  | 8.42%  | 26                  | 3.17%  | 83               | 10.13% | 158                | 19.29% |
| MB Precinct            | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Provisional            | 2   | 9.52%  | 1                   | 4.76%  | 0                | 0.00%  | 3                  | 14.29% |
| Total                  | 84  | 7.85%  | 33                  | 3.08%  | 109              | 10.19% | 208                | 19.44% |
| 100014.11-CP 100014.11 |   |        |                     |        |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Precinct OS            | 12  | 6.56%  | 7                   | 3.83%  | 29               | 15.85% | 26                 | 14.21% |
| Precinct VBM           | 39  | 6.18%  | 24                  | 3.80%  | 85               | 13.47% | 100                | 15.85% |
| MB Precinct            | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Provisional            | 2   | 8.33%  | 0                   | 0.00%  | 1                | 4.17%  | 5                  | 20.83% |
| Total                  | 53  | 6.32%  | 31                  | 3.70%  | 115              | 13.72% | 131                | 15.63% |
| 100022.11-CP 100022.11 |   |        |                     |        |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Precinct OS            | 21  | 13.13% | 7                   | 4.38%  | 12               | 7.50%  | 23                 | 14.38% |
| Precinct VBM           | 38  | 6.33%  | 17                  | 2.83%  | 70               | 11.67% | 89                 | 14.83% |
| MB Precinct            | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Provisional            | 1   | 9.09%  | 0                   | 0.00%  | 2                | 18.18% | 2                  | 18.18% |
| Total                  | 60  | 7.78%  | 24                  | 3.11%  | 84               | 10.89% | 114                | 14.79% |
| 100023.11-CP 100023.11 |   |        |                     |        |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Precinct OS            | 14  | 7.00%  | 6                   | 3.00%  | 29               | 14.50% | 24                 | 12.00% |
| Precinct VBM           | 81  | 8.41%  | 21                  | 2.18%  | 154              | 15.99% | 142                | 14.75% |
| MB Precinct            | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Provisional            | 0   | 0.00%  | 0                   | 0.00%  | 7                | 25.00% | 7                  | 25.00% |
| Total                  | 95  | 7.98%  | 27                  | 2.27%  | 190              | 15.95% | 173                | 14.53% |
| 200003.12-CP 200003.12 |   |        |                     |        |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Precinct OS            | 10  | 6.67%  | 4                   | 2.67%  | 14               | 9.33%  | 28                 | 18.67% |
| Precinct VBM           | 44  | 8.70%  | 19                  | 3.75%  | 40               | 7.91%  | 96                 | 18.97% |
| MB Precinct            | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Provisional            | 0   | 0.00%  | 3                   | 11.54% | 4                | 15.38% | 1                  | 3.85%  |
| Total                  | 54  | 7.92%  | 26                  | 3.81%  | 58               | 8.50%  | 125                | 18.33% |
| 200004.12-CP 200004.12 |   |        |                     |        |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Precinct OS            | 10  | 6.21%  | 5                   | 3.11%  | 24               | 14.91% | 27                 | 16.77% |
| Precinct VBM           | 52  | 8.61%  | 16                  | 2.65%  | 83               | 13.74% | 132                | 21.85% |
| MB Precinct            | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Provisional            | 1   | 7.69%  | 2                   | 15.38% | 1                | 7.69%  | 5                  | 38.46% |
| Total                  | 63  | 8.10%  | 23                  | 2.96%  | 108              | 13.88% | 164                | 21.08% |
| 200005.02-CP 200005.02 |   |        |                     |        |                  |        |                    |        |
| Precinct TSx           | -   | -      | -                   | -      | -                | -      | -                  | -      |
| Precinct OS            | -   | -      | -                   | -      | -                | -      | -                  | -      |
| Precinct VBM           | -   | -      | -                   | -      | -                | -      | -                  | -      |
| MB Precinct            | -   | -      | -                   | -      | -                | -      | -                  | -      |
| Provisional            | -   | -      | -                   | -      | -                | -      | -                  | -      |
| Total                  | -   | -      | -                   | -      | -                | -      | -                  | -      |
| 200006.12-CP 200006.12 |   |        |                     |        |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Precinct OS            | 17  | 15.32% | 0                   | 0.00%  | 19               | 17.12% | 20                 | 18.02% |
| Precinct VBM           | 45  | 9.49%  | 14                  | 2.95%  | 65               | 13.71% | 62                 | 13.08% |
| MB Precinct            | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |
| Provisional            | 3   | 11.11% | 1                   | 3.70%  | 6                | 22.22% | 8                  | 29.63% |
| Total                  | 65  | 10.62% | 15                  | 2.45%  | 90               | 14.71% | 90                 | 14.71% |
| 200008.12-CP 200008.12 |   |        |                     |        |                  |        |                    |        |
| Precinct TSx           | 0   | -      | 0                   | -      | 0                | -      | 0                  | -      |



**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 6th District |                        |                  |                       |
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|                        | BILL HALLDIN (REP)                        | SUZANNE JONES<br>(REP) | ANDY PUGNO (REP) | BRIAN CAPLES<br>(DEM) |
| 200018.02-CP 200018.02 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 200020.02-CP 200020.02 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 200021.01-CP 200021.01 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 200021.02-CP 200021.02 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 200024.02-CP 200024.02 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 300001.04-CP 300001.04 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 300001.05-CP 300001.05 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 300003.05-CP 300003.05 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 300004.05-CP 300004.05 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |

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|------------------------|---|------------------------|------------------|-----------------------|
|                        | BILL HALLDIN (REP)                        | SUZANNE JONES<br>(REP) | ANDY PUGNO (REP) | BRIAN CAPLES<br>(DEM) |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 300005.05-CP 300005.05 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 300008.05-CP 300008.05 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 300009.04-CP 300009.04 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 300010.04-CP 300010.04 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 300011.04-CP 300011.04 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 300013.05-CP 300013.05 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 300015.04-CP 300015.04 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 300015.05-CP 300015.05 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 300017.05-CP 300017.05 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |

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|------------------------|---|-------|---------------------|-------|------------------|--------|--------------------|--------|
|                        | BILL HALLDIN (REP)                        |       | SUZANNE JONES (REP) |       | ANDY PUGNO (REP) |        | BRIAN CAPLES (DEM) |        |
| Precinct VBM           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| MB Precinct            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Provisional            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Total                  | -   | -     | -                   | -     | -                | -      | -                  | -      |
| 300018.04-CP 300018.04 |   |       |                     |       |                  |        |                    |        |
| Precinct TSx           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Precinct OS            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Precinct VBM           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| MB Precinct            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Provisional            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Total                  | -   | -     | -                   | -     | -                | -      | -                  | -      |
| 300020.04-CP 300020.04 |   |       |                     |       |                  |        |                    |        |
| Precinct TSx           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Precinct OS            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Precinct VBM           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| MB Precinct            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Provisional            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Total                  | -   | -     | -                   | -     | -                | -      | -                  | -      |
| 300021.03-CP 300021.03 |   |       |                     |       |                  |        |                    |        |
| Precinct TSx           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Precinct OS            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Precinct VBM           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| MB Precinct            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Provisional            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Total                  | -   | -     | -                   | -     | -                | -      | -                  | -      |
| 300022.05-CP 300022.05 |   |       |                     |       |                  |        |                    |        |
| Precinct TSx           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Precinct OS            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Precinct VBM           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| MB Precinct            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Provisional            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Total                  | -   | -     | -                   | -     | -                | -      | -                  | -      |
| 300023.03-CP 300023.03 |   |       |                     |       |                  |        |                    |        |
| Precinct TSx           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Precinct OS            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Precinct VBM           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| MB Precinct            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Provisional            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Total                  | -   | -     | -                   | -     | -                | -      | -                  | -      |
| 300023.05-CP 300023.05 |   |       |                     |       |                  |        |                    |        |
| Precinct TSx           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Precinct OS            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Precinct VBM           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| MB Precinct            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Provisional            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Total                  | -   | -     | -                   | -     | -                | -      | -                  | -      |
| 400001.14-CP 400001.14 |   |       |                     |       |                  |        |                    |        |
| Precinct TSx           | 0   | -     | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct OS            | 15  | 9.93% | 5                   | 3.31% | 26               | 17.22% | 32                 | 21.19% |
| Precinct VBM           | 67  | 7.50% | 35                  | 3.92% | 121              | 13.55% | 152                | 17.02% |
| MB Precinct            | 0   | -     | 0                   | -     | 0                | -      | 0                  | -      |
| Provisional            | 1   | 4.76% | 0                   | 0.00% | 3                | 14.29% | 6                  | 28.57% |
| Total                  | 83  | 7.79% | 40                  | 3.76% | 150              | 14.08% | 190                | 17.84% |
| 400002.08-CP 400002.08 |   |       |                     |       |                  |        |                    |        |
| Precinct TSx           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Precinct OS            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Precinct VBM           | -   | -     | -                   | -     | -                | -      | -                  | -      |
| MB Precinct            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Provisional            | -   | -     | -                   | -     | -                | -      | -                  | -      |
| Total                  | -   | -     | -                   | -     | -                | -      | -                  | -      |
| 400003.08-CP 400003.08 |   |       |                     |       |                  |        |                    |        |





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|                        | BILL HALLDIN (REP)                        | SUZANNE JONES (REP) | ANDY PUGNO (REP) | BRIAN CAPLES (DEM) |
| Total                  | -   | -                   | -                | -                  |
| 400017.08-CP 400017.08 | -   | -                   | -                | -                  |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 400018.08-CP 400018.08 | -   | -                   | -                | -                  |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 400020.08-CP 400020.08 | -   | -                   | -                | -                  |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 400021.07-CP 400021.07 | -   | -                   | -                | -                  |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 500001.10-CP 500001.10 | -   | -                   | -                | -                  |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 500002.10-CP 500002.10 | -   | -                   | -                | -                  |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 500003.10-CP 500003.10 | -   | -                   | -                | -                  |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 500004.10-CP 500004.10 | -   | -                   | -                | -                  |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 500005.10-CP 500005.10 | -   | -                   | -                | -                  |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |

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|                        | BILL HALLDIN (REP)                        | SUZANNE JONES (REP) | ANDY PUGNO (REP) | BRIAN CAPLES (DEM) |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 500006.10-CP 500006.10 |   |                     |                  |                    |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 500007.10-CP 500007.10 |   |                     |                  |                    |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 500009.10-CP 500009.10 |   |                     |                  |                    |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 500011.10-CP 500011.10 |   |                     |                  |                    |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 500012.10-CP 500012.10 |   |                     |                  |                    |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 500015.10-CP 500015.10 |   |                     |                  |                    |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 500020.10-CP 500020.10 |   |                     |                  |                    |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 500021.10-CP 500021.10 |   |                     |                  |                    |
| Precinct TSx           | -   | -                   | -                | -                  |
| Precinct OS            | -   | -                   | -                | -                  |
| Precinct VBM           | -   | -                   | -                | -                  |
| MB Precinct            | -   | -                   | -                | -                  |
| Provisional            | -   | -                   | -                | -                  |
| Total                  | -   | -                   | -                | -                  |
| 95U201.01-MB 95U201.01 |   |                     |                  |                    |
| Precinct TSx           | -   | -                   | -                | -                  |

**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 6th District |                        |                  |                       |
|------------------------|---|------------------------|------------------|-----------------------|
|                        | BILL HALLDIN (REP)                        | SUZANNE JONES<br>(REP) | ANDY PUGNO (REP) | BRIAN CAPLES<br>(DEM) |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U201.02-MB 95U201.02 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U202.02-MB 95U202.02 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U203.02-MB 95U203.02 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U204.01-MB 95U204.01 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U301.05-MB 95U301.05 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U302.04-MB 95U302.04 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U302.05-MB 95U302.05 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U303.03-MB 95U303.03 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |

**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 6th District |                        |                  |                       |
|------------------------|---|------------------------|------------------|-----------------------|
|                        | BILL HALLDIN (REP)                        | SUZANNE JONES<br>(REP) | ANDY PUGNO (REP) | BRIAN CAPLES<br>(DEM) |
| 95U303.05-MB 95U303.05 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U304.05-MB 95U304.05 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U305.05-MB 95U305.05 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U401.08-MB 95U401.08 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U402.07-MB 95U402.07 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U402.08-MB 95U402.08 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U403.08-MB 95U403.08 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U404.06-MB 95U404.06 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |
| Provisional            | -   | -                      | -                | -                     |
| Total                  | -   | -                      | -                | -                     |
| 95U404.08-MB 95U404.08 |   |                        |                  |                       |
| Precinct TSx           | -   | -                      | -                | -                     |
| Precinct OS            | -   | -                      | -                | -                     |
| Precinct VBM           | -   | -                      | -                | -                     |
| MB Precinct            | -   | -                      | -                | -                     |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                               | Member of the State Assembly 6th District |        |                     |       |                  |        |                    |        |
|-------------------------------|---|--------|---------------------|-------|------------------|--------|--------------------|--------|
|                               | BILL HALLDIN (REP)                        |        | SUZANNE JONES (REP) |       | ANDY PUGNO (REP) |        | BRIAN CAPLES (DEM) |        |
| Provisional                   | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Total                         | -   | -      | -                   | -     | -                | -      | -                  | -      |
| 95U501.09-MB 95U501.09        |   |        |                     |       |                  |        |                    |        |
| Precinct TSx                  | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Precinct OS                   | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Precinct VBM                  | -   | -      | -                   | -     | -                | -      | -                  | -      |
| MB Precinct                   | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Provisional                   | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Total                         | -   | -      | -                   | -     | -                | -      | -                  | -      |
| 95U501.10-MB 95U501.10        |   |        |                     |       |                  |        |                    |        |
| Precinct TSx                  | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Precinct OS                   | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Precinct VBM                  | -   | -      | -                   | -     | -                | -      | -                  | -      |
| MB Precinct                   | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Provisional                   | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Total                         | -   | -      | -                   | -     | -                | -      | -                  | -      |
| 95U598.10-MB 95U598.10        |   |        |                     |       |                  |        |                    |        |
| Precinct TSx                  | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Precinct OS                   | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Precinct VBM                  | -   | -      | -                   | -     | -                | -      | -                  | -      |
| MB Precinct                   | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Provisional                   | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Total                         | -   | -      | -                   | -     | -                | -      | -                  | -      |
| 95U599.10-MB 95U599.10        |   |        |                     |       |                  |        |                    |        |
| Precinct TSx                  | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Precinct OS                   | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Precinct VBM                  | -   | -      | -                   | -     | -                | -      | -                  | -      |
| MB Precinct                   | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Provisional                   | -   | -      | -                   | -     | -                | -      | -                  | -      |
| Total                         | -   | -      | -                   | -     | -                | -      | -                  | -      |
| 96U101.11-MB 96U101.11        |   |        |                     |       |                  |        |                    |        |
| Precinct TSx                  | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct OS                   | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct VBM                  | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| MB Precinct                   | 29  | 7.61%  | 8                   | 2.10% | 71               | 18.64% | 62                 | 16.27% |
| Provisional                   | 0   | 0.00%  | 0                   | 0.00% | 1                | 16.67% | 3                  | 50.00% |
| Total                         | 29  | 7.49%  | 8                   | 2.07% | 72               | 18.60% | 65                 | 16.80% |
| 96U401.14-MB 96U401.14        |   |        |                     |       |                  |        |                    |        |
| Precinct TSx                  | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct OS                   | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct VBM                  | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| MB Precinct                   | 22  | 6.96%  | 3                   | 0.95% | 62               | 19.62% | 50                 | 15.82% |
| Provisional                   | 0   | 0.00%  | 0                   | 0.00% | 0                | 0.00%  | 0                  | 0.00%  |
| Total                         | 22  | 6.94%  | 3                   | 0.95% | 62               | 19.56% | 50                 | 15.77% |
| Total                         |   |        |                     |       |                  |        |                    |        |
| Precinct TSx                  | 1   | 25.00% | 0                   | 0.00% | 0                | 0.00%  | 0                  | 0.00%  |
| Precinct OS                   | 317                                       | 8.19%  | 105                 | 2.71% | 495              | 12.79% | 718                | 18.56% |
| Precinct VBM                  | 1307                                      | 8.44%  | 542                 | 3.50% | 1848             | 11.93% | 2685               | 17.33% |
| MB Precinct                   | 51  | 7.32%  | 11                  | 1.58% | 133              | 19.08% | 112                | 16.07% |
| Provisional                   | 31  | 7.01%  | 10                  | 2.26% | 50               | 11.31% | 88                 | 19.91% |
| Total                         | 1707                                      | 8.33%  | 668                 | 3.26% | 2526             | 12.32% | 3603               | 17.57% |
| STATE BOARD OF EQUALIZATION   |   |        |                     |       |                  |        |                    |        |
| STATE BOARD OF EQUALIZATION 1 |   |        |                     |       |                  |        |                    |        |
| Precinct TSx                  | 1   | 25.00% | 0                   | 0.00% | 0                | 0.00%  | 0                  | 0.00%  |
| Precinct OS                   | 317                                       | 8.19%  | 105                 | 2.71% | 495              | 12.79% | 718                | 18.56% |
| Precinct VBM                  | 1307                                      | 8.44%  | 542                 | 3.50% | 1848             | 11.93% | 2685               | 17.33% |
| MB Precinct                   | 51  | 7.32%  | 11                  | 1.58% | 133              | 19.08% | 112                | 16.07% |
| Provisional                   | 31  | 7.01%  | 10                  | 2.26% | 50               | 11.31% | 88                 | 19.91% |
| Total                         | 1707                                      | 8.33%  | 668                 | 3.26% | 2526             | 12.32% | 3603               | 17.57% |
| Total                         |   |        |                     |       |                  |        |                    |        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                     | Member of the State Assembly 6th District |              |                     |              |                  |               |                    |               |
|-------------------------------------|---|--------------|---------------------|--------------|------------------|---------------|--------------------|---------------|
|                                     | BILL HALLDIN (REP)                        |              | SUZANNE JONES (REP) |              | ANDY PUGNO (REP) |               | BRIAN CAPLES (DEM) |               |
| Precinct TSx                        | 1   | 25.00%       | 0                   | 0.00%        | 0                | 0.00%         | 0                  | 0.00%         |
| Precinct OS                         | 317                                       | 8.19%        | 105                 | 2.71%        | 495              | 12.79%        | 718                | 18.56%        |
| Precinct VBM                        | 1307                                      | 8.44%        | 542                 | 3.50%        | 1848             | 11.93%        | 2685               | 17.33%        |
| MB Precinct                         | 51  | 7.32%        | 11                  | 1.58%        | 133              | 19.08%        | 112                | 16.07%        |
| Provisional                         | 31  | 7.01%        | 10                  | 2.26%        | 50               | 11.31%        | 88                 | 19.91%        |
| <b>Total</b>                        | <b>1707</b>                               | <b>8.33%</b> | <b>668</b>          | <b>3.26%</b> | <b>2526</b>      | <b>12.32%</b> | <b>3603</b>        | <b>17.57%</b> |
| <b>UNITED STATES SENATE</b>         |   |              |                     |              |                  |               |                    |               |
| <b>UNITED STATES SENATE</b>         |   |              |                     |              |                  |               |                    |               |
| Precinct TSx                        | 1   | 25.00%       | 0                   | 0.00%        | 0                | 0.00%         | 0                  | 0.00%         |
| Precinct OS                         | 317                                       | 8.19%        | 105                 | 2.71%        | 495              | 12.79%        | 718                | 18.56%        |
| Precinct VBM                        | 1307                                      | 8.44%        | 542                 | 3.50%        | 1848             | 11.93%        | 2685               | 17.33%        |
| MB Precinct                         | 51  | 7.32%        | 11                  | 1.58%        | 133              | 19.08%        | 112                | 16.07%        |
| Provisional                         | 31  | 7.01%        | 10                  | 2.26%        | 50               | 11.31%        | 88                 | 19.91%        |
| <b>Total</b>                        | <b>1707</b>                               | <b>8.33%</b> | <b>668</b>          | <b>3.26%</b> | <b>2526</b>      | <b>12.32%</b> | <b>3603</b>        | <b>17.57%</b> |
| <b>Total</b>                        |   |              |                     |              |                  |               |                    |               |
| Precinct TSx                        | 1   | 25.00%       | 0                   | 0.00%        | 0                | 0.00%         | 0                  | 0.00%         |
| Precinct OS                         | 317                                       | 8.19%        | 105                 | 2.71%        | 495              | 12.79%        | 718                | 18.56%        |
| Precinct VBM                        | 1307                                      | 8.44%        | 542                 | 3.50%        | 1848             | 11.93%        | 2685               | 17.33%        |
| MB Precinct                         | 51  | 7.32%        | 11                  | 1.58%        | 133              | 19.08%        | 112                | 16.07%        |
| Provisional                         | 31  | 7.01%        | 10                  | 2.26%        | 50               | 11.31%        | 88                 | 19.91%        |
| <b>Total</b>                        | <b>1707</b>                               | <b>8.33%</b> | <b>668</b>          | <b>3.26%</b> | <b>2526</b>      | <b>12.32%</b> | <b>3603</b>        | <b>17.57%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |   |              |                     |              |                  |               |                    |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |   |              |                     |              |                  |               |                    |               |
| Precinct TSx                        | 1   | 25.00%       | 0                   | 0.00%        | 0                | 0.00%         | 0                  | 0.00%         |
| Precinct OS                         | 317                                       | 8.19%        | 105                 | 2.71%        | 495              | 12.79%        | 718                | 18.56%        |
| Precinct VBM                        | 1307                                      | 8.44%        | 542                 | 3.50%        | 1848             | 11.93%        | 2685               | 17.33%        |
| MB Precinct                         | 51  | 7.32%        | 11                  | 1.58%        | 133              | 19.08%        | 112                | 16.07%        |
| Provisional                         | 31  | 7.01%        | 10                  | 2.26%        | 50               | 11.31%        | 88                 | 19.91%        |
| <b>Total</b>                        | <b>1707</b>                               | <b>8.33%</b> | <b>668</b>          | <b>3.26%</b> | <b>2526</b>      | <b>12.32%</b> | <b>3603</b>        | <b>17.57%</b> |
| <b>Total</b>                        |   |              |                     |              |                  |               |                    |               |
| Precinct TSx                        | 1   | 25.00%       | 0                   | 0.00%        | 0                | 0.00%         | 0                  | 0.00%         |
| Precinct OS                         | 317                                       | 8.19%        | 105                 | 2.71%        | 495              | 12.79%        | 718                | 18.56%        |
| Precinct VBM                        | 1307                                      | 8.44%        | 542                 | 3.50%        | 1848             | 11.93%        | 2685               | 17.33%        |
| MB Precinct                         | 51  | 7.32%        | 11                  | 1.58%        | 133              | 19.08%        | 112                | 16.07%        |
| Provisional                         | 31  | 7.01%        | 10                  | 2.26%        | 50               | 11.31%        | 88                 | 19.91%        |
| <b>Total</b>                        | <b>1707</b>                               | <b>8.33%</b> | <b>668</b>          | <b>3.26%</b> | <b>2526</b>      | <b>12.32%</b> | <b>3603</b>        | <b>17.57%</b> |
| <b>STATE SENATE</b>                 |   |              |                     |              |                  |               |                    |               |
| <b>1ST STATE SENATE DISTRICT</b>    |   |              |                     |              |                  |               |                    |               |
| Precinct TSx                        | 1   | 25.00%       | 0                   | 0.00%        | 0                | 0.00%         | 0                  | 0.00%         |
| Precinct OS                         | 317                                       | 8.19%        | 105                 | 2.71%        | 495              | 12.79%        | 718                | 18.56%        |
| Precinct VBM                        | 1307                                      | 8.44%        | 542                 | 3.50%        | 1848             | 11.93%        | 2685               | 17.33%        |
| MB Precinct                         | 51  | 7.32%        | 11                  | 1.58%        | 133              | 19.08%        | 112                | 16.07%        |
| Provisional                         | 31  | 7.01%        | 10                  | 2.26%        | 50               | 11.31%        | 88                 | 19.91%        |
| <b>Total</b>                        | <b>1707</b>                               | <b>8.33%</b> | <b>668</b>          | <b>3.26%</b> | <b>2526</b>      | <b>12.32%</b> | <b>3603</b>        | <b>17.57%</b> |
| <b>Total</b>                        |   |              |                     |              |                  |               |                    |               |
| Precinct TSx                        | 1   | 25.00%       | 0                   | 0.00%        | 0                | 0.00%         | 0                  | 0.00%         |
| Precinct OS                         | 317                                       | 8.19%        | 105                 | 2.71%        | 495              | 12.79%        | 718                | 18.56%        |
| Precinct VBM                        | 1307                                      | 8.44%        | 542                 | 3.50%        | 1848             | 11.93%        | 2685               | 17.33%        |
| MB Precinct                         | 51  | 7.32%        | 11                  | 1.58%        | 133              | 19.08%        | 112                | 16.07%        |
| Provisional                         | 31  | 7.01%        | 10                  | 2.26%        | 50               | 11.31%        | 88                 | 19.91%        |
| <b>Total</b>                        | <b>1707</b>                               | <b>8.33%</b> | <b>668</b>          | <b>3.26%</b> | <b>2526</b>      | <b>12.32%</b> | <b>3603</b>        | <b>17.57%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |   |              |                     |              |                  |               |                    |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |   |              |                     |              |                  |               |                    |               |
| Precinct TSx                        | 0   | -            | 0                   | -            | 0                | -             | 0                  | -             |
| Precinct OS                         | 0   | -            | 0                   | -            | 0                | -             | 0                  | -             |
| Precinct VBM                        | 0   | -            | 0                   | -            | 0                | -             | 0                  | -             |
| MB Precinct                         | 0   | -            | 0                   | -            | 0                | -             | 0                  | -             |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                   |      | Member of the State Assembly 6th District |     |                     |      |                  |      |                    |      |
|-----------------------------------|------|---|-----|---------------------|------|------------------|------|--------------------|------|
|                                   |      | BILL HALLDIN (REP)                        |     | SUZANNE JONES (REP) |      | ANDY PUGNO (REP) |      | BRIAN CAPLES (DEM) |      |
| Provisional                       |      | 0   |     | 0                   |      | 0                |      | 0                  |      |
| Total                             |      | 0   |     | 0                   |      | 0                |      | 0                  |      |
| <b>6TH STATE ASSEMBLY DIST</b>    |      |   |     |                     |      |                  |      |                    |      |
| Precinct TSx                      | 1    | 25.00%                                    | 0   | 0.00%               | 0    | 0.00%            | 0    | 0.00%              | 0    |
| Precinct OS                       | 317  | 8.19%                                     | 105 | 2.71%               | 495  | 12.79%           | 718  | 18.56%             | 718  |
| Precinct VBM                      | 1307 | 8.44%                                     | 542 | 3.50%               | 1848 | 11.93%           | 2685 | 17.33%             | 2685 |
| MB Precinct                       | 51   | 7.32%                                     | 11  | 1.58%               | 133  | 19.08%           | 112  | 16.07%             | 112  |
| Provisional                       | 31   | 7.01%                                     | 10  | 2.26%               | 50   | 11.31%           | 88   | 19.91%             | 88   |
| Total                             | 1707 | 8.33%                                     | 668 | 3.26%               | 2526 | 12.32%           | 3603 | 17.57%             | 3603 |
| <b>Total</b>                      |      |   |     |                     |      |                  |      |                    |      |
| Precinct TSx                      | 1    | 25.00%                                    | 0   | 0.00%               | 0    | 0.00%            | 0    | 0.00%              | 0    |
| Precinct OS                       | 317  | 8.19%                                     | 105 | 2.71%               | 495  | 12.79%           | 718  | 18.56%             | 718  |
| Precinct VBM                      | 1307 | 8.44%                                     | 542 | 3.50%               | 1848 | 11.93%           | 2685 | 17.33%             | 2685 |
| MB Precinct                       | 51   | 7.32%                                     | 11  | 1.58%               | 133  | 19.08%           | 112  | 16.07%             | 112  |
| Provisional                       | 31   | 7.01%                                     | 10  | 2.26%               | 50   | 11.31%           | 88   | 19.91%             | 88   |
| Total                             | 1707 | 8.33%                                     | 668 | 3.26%               | 2526 | 12.32%           | 3603 | 17.57%             | 3603 |
| <b>SUPERVISORIAL DISTRICTS</b>    |      |   |     |                     |      |                  |      |                    |      |
| <b>1ST SUPERVISORIAL DISTRICT</b> |      |   |     |                     |      |                  |      |                    |      |
| Precinct TSx                      | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| Precinct OS                       | 180  | 8.06%                                     | 69  | 3.09%               | 267  | 11.96%           | 388  | 17.38%             | 388  |
| Precinct VBM                      | 762  | 8.17%                                     | 325 | 3.49%               | 1060 | 11.37%           | 1585 | 17.00%             | 1585 |
| MB Precinct                       | 29   | 7.61%                                     | 8   | 2.10%               | 71   | 18.64%           | 62   | 16.27%             | 62   |
| Provisional                       | 16   | 6.61%                                     | 2   | 0.83%               | 24   | 9.92%            | 45   | 18.60%             | 45   |
| Total                             | 987  | 8.10%                                     | 404 | 3.32%               | 1422 | 11.67%           | 2080 | 17.08%             | 2080 |
| <b>2ND SUPERVISORIAL DISTRICT</b> |      |   |     |                     |      |                  |      |                    |      |
| Precinct TSx                      | 1    | 25.00%                                    | 0   | 0.00%               | 0    | 0.00%            | 0    | 0.00%              | 0    |
| Precinct OS                       | 114  | 8.66%                                     | 29  | 2.20%               | 175  | 13.29%           | 266  | 20.20%             | 266  |
| Precinct VBM                      | 409  | 8.73%                                     | 150 | 3.20%               | 601  | 12.83%           | 847  | 18.08%             | 847  |
| MB Precinct                       | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| Provisional                       | 12   | 7.41%                                     | 7   | 4.32%               | 22   | 13.58%           | 36   | 22.22%             | 36   |
| Total                             | 536  | 8.69%                                     | 186 | 3.02%               | 798  | 12.94%           | 1149 | 18.63%             | 1149 |
| <b>3RD SUPERVISORIAL DISTRICT</b> |      |   |     |                     |      |                  |      |                    |      |
| Precinct TSx                      | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| Precinct OS                       | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| Precinct VBM                      | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| MB Precinct                       | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| Provisional                       | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| Total                             | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| <b>4TH SUPERVISORIAL DISTRICT</b> |      |   |     |                     |      |                  |      |                    |      |
| Precinct TSx                      | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| Precinct OS                       | 23   | 7.21%                                     | 7   | 2.19%               | 53   | 16.61%           | 64   | 20.06%             | 64   |
| Precinct VBM                      | 136  | 9.17%                                     | 67  | 4.52%               | 187  | 12.61%           | 253  | 17.06%             | 253  |
| MB Precinct                       | 22   | 6.96%                                     | 3   | 0.95%               | 62   | 19.62%           | 50   | 15.82%             | 50   |
| Provisional                       | 3    | 7.89%                                     | 1   | 2.63%               | 4    | 10.53%           | 7    | 18.42%             | 7    |
| Total                             | 184  | 8.53%                                     | 78  | 3.62%               | 306  | 14.19%           | 374  | 17.35%             | 374  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |      |   |     |                     |      |                  |      |                    |      |
| Precinct TSx                      | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| Precinct OS                       | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| Precinct VBM                      | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| MB Precinct                       | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| Provisional                       | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| Total                             | 0    | -   | 0   | -                   | 0    | -                | 0    | -                  | 0    |
| <b>Total</b>                      |      |   |     |                     |      |                  |      |                    |      |
| Precinct TSx                      | 1    | 25.00%                                    | 0   | 0.00%               | 0    | 0.00%            | 0    | 0.00%              | 0    |
| Precinct OS                       | 317  | 8.19%                                     | 105 | 2.71%               | 495  | 12.79%           | 718  | 18.56%             | 718  |
| Precinct VBM                      | 1307 | 8.44%                                     | 542 | 3.50%               | 1848 | 11.93%           | 2685 | 17.33%             | 2685 |
| MB Precinct                       | 51   | 7.32%                                     | 11  | 1.58%               | 133  | 19.08%           | 112  | 16.07%             | 112  |
| Provisional                       | 31   | 7.01%                                     | 10  | 2.26%               | 50   | 11.31%           | 88   | 19.91%             | 88   |
| Total                             | 1707 | 8.33%                                     | 668 | 3.26%               | 2526 | 12.32%           | 3603 | 17.57%             | 3603 |



**COUNTY OF EL DORADO**  
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|                                 | Member of the State Assembly 6th District |        |                     |       |                  |        |                    |        |
|---------------------------------|---|--------|---------------------|-------|------------------|--------|--------------------|--------|
|                                 | BILL HALLDIN (REP)                        |        | SUZANNE JONES (REP) |       | ANDY PUGNO (REP) |        | BRIAN CAPLES (DEM) |        |
| <b>CITIES</b>                   |   |        |                     |       |                  |        |                    |        |
| <b>CITY OF PLACERVILLE</b>      |   |        |                     |       |                  |        |                    |        |
| Precinct TSx                    | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct OS                     | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct VBM                    | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| MB Precinct                     | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Provisional                     | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Total                           | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| <b>CITY OF SOUTH LAKE TAHOE</b> |   |        |                     |       |                  |        |                    |        |
| Precinct TSx                    | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct OS                     | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Precinct VBM                    | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| MB Precinct                     | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Provisional                     | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| Total                           | 0   | -      | 0                   | -     | 0                | -      | 0                  | -      |
| <b>UNINCORPORATED AREA</b>      |   |        |                     |       |                  |        |                    |        |
| Precinct TSx                    | 1   | 25.00% | 0                   | 0.00% | 0                | 0.00%  | 0                  | 0.00%  |
| Precinct OS                     | 317                                       | 8.19%  | 105                 | 2.71% | 495              | 12.79% | 718                | 18.56% |
| Precinct VBM                    | 1307                                      | 8.44%  | 542                 | 3.50% | 1848             | 11.93% | 2685               | 17.33% |
| MB Precinct                     | 51  | 7.32%  | 11                  | 1.58% | 133              | 19.08% | 112                | 16.07% |
| Provisional                     | 31  | 7.01%  | 10                  | 2.26% | 50               | 11.31% | 88                 | 19.91% |
| Total                           | 1707                                      | 8.33%  | 668                 | 3.26% | 2526             | 12.32% | 3603               | 17.57% |
| <b>Total</b>                    |   |        |                     |       |                  |        |                    |        |
| Precinct TSx                    | 1   | 25.00% | 0                   | 0.00% | 0                | 0.00%  | 0                  | 0.00%  |
| Precinct OS                     | 317                                       | 8.19%  | 105                 | 2.71% | 495              | 12.79% | 718                | 18.56% |
| Precinct VBM                    | 1307                                      | 8.44%  | 542                 | 3.50% | 1848             | 11.93% | 2685               | 17.33% |
| MB Precinct                     | 51  | 7.32%  | 11                  | 1.58% | 133              | 19.08% | 112                | 16.07% |
| Provisional                     | 31  | 7.01%  | 10                  | 2.26% | 50               | 11.31% | 88                 | 19.91% |
| Total                           | 1707                                      | 8.33%  | 668                 | 3.26% | 2526             | 12.32% | 3603               | 17.57% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|------------------------|---|--------|-----------------------|--------|------------------------|--------|------------------------------|--------|
|                        | JOHN EDWARD<br>Z'BERG (DEM)               |        | KEVIN HANLEY<br>(REP) |        | CRISTI NELSON<br>(REP) |        | "BO"<br>AMBROZEWICZ<br>(NPP) |        |
| Jurisdiction Wide      |   |        |                       |        |                        |        |                              |        |
| 100001.11-CP 100001.11 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 12  | 7.41%  | 10                    | 6.17%  | 19                     | 11.73% | 5                            | 3.09%  |
| Precinct VBM           | 69  | 9.76%  | 47                    | 6.65%  | 93                     | 13.15% | 7                            | 0.99%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 1   | 7.69%  | 0                     | 0.00%  | 1                      | 7.69%  | 1                            | 7.69%  |
| Total                  | 82  | 9.30%  | 57                    | 6.46%  | 113                    | 12.81% | 13                           | 1.47%  |
| 100002.11-CP 100002.11 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 3   | 4.05%  | 6                     | 8.11%  | 7                      | 9.46%  | 3                            | 4.05%  |
| Precinct VBM           | 39  | 9.29%  | 31                    | 7.38%  | 47                     | 11.19% | 4                            | 0.95%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 3   | 16.67% | 1                     | 5.56%  | 2                      | 11.11% | 1                            | 5.56%  |
| Total                  | 45  | 8.79%  | 38                    | 7.42%  | 56                     | 10.94% | 8                            | 1.56%  |
| 100003.11-CP 100003.11 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 7   | 6.31%  | 10                    | 9.01%  | 7                      | 6.31%  | 4                            | 3.60%  |
| Precinct VBM           | 82  | 11.78% | 58                    | 8.33%  | 74                     | 10.63% | 8                            | 1.15%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 1   | 4.76%  | 2                     | 9.52%  | 3                      | 14.29% | 3                            | 14.29% |
| Total                  | 90  | 10.87% | 70                    | 8.45%  | 84                     | 10.14% | 15                           | 1.81%  |
| 100004.11-CP 100004.11 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 29  | 13.00% | 19                    | 8.52%  | 12                     | 5.38%  | 8                            | 3.59%  |
| Precinct VBM           | 44  | 6.76%  | 48                    | 7.37%  | 54                     | 8.29%  | 14                           | 2.15%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 1   | 33.33% | 0                     | 0.00%  | 0                      | 0.00%  | 1                            | 33.33% |
| Total                  | 74  | 8.44%  | 67                    | 7.64%  | 66                     | 7.53%  | 23                           | 2.62%  |
| 100005.11-CP 100005.11 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 9   | 6.87%  | 7                     | 5.34%  | 10                     | 7.63%  | 8                            | 6.11%  |
| Precinct VBM           | 33  | 4.05%  | 84                    | 10.31% | 93                     | 11.41% | 11                           | 1.35%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 2   | 10.00% | 1                     | 5.00%  | 0                      | 0.00%  | 2                            | 10.00% |
| Total                  | 44  | 4.55%  | 92                    | 9.52%  | 103                    | 10.66% | 21                           | 2.17%  |
| 100006.11-CP 100006.11 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 14  | 6.51%  | 13                    | 6.05%  | 22                     | 10.23% | 5                            | 2.33%  |
| Precinct VBM           | 79  | 9.40%  | 57                    | 6.79%  | 61                     | 7.26%  | 14                           | 1.67%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 3   | 14.29% | 1                     | 4.76%  | 2                      | 9.52%  | 2                            | 9.52%  |
| Total                  | 96  | 8.92%  | 71                    | 6.60%  | 85                     | 7.90%  | 21                           | 1.95%  |
| 100008.11-CP 100008.11 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 17  | 10.18% | 3                     | 1.80%  | 20                     | 11.98% | 2                            | 1.20%  |
| Precinct VBM           | 54  | 8.24%  | 57                    | 8.70%  | 68                     | 10.38% | 12                           | 1.83%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 4   | 30.77% | 0                     | 0.00%  | 0                      | 0.00%  | 0                            | 0.00%  |
| Total                  | 75  | 8.98%  | 60                    | 7.19%  | 88                     | 10.54% | 14                           | 1.68%  |
| 100009.11-CP 100009.11 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 17  | 11.26% | 13                    | 8.61%  | 19                     | 12.58% | 6                            | 3.97%  |
| Precinct VBM           | 70  | 10.25% | 55                    | 8.05%  | 62                     | 9.08%  | 14                           | 2.05%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 4   | 21.05% | 1                     | 5.26%  | 4                      | 21.05% | 0                            | 0.00%  |
| Total                  | 91  | 10.67% | 69                    | 8.09%  | 85                     | 9.96%  | 20                           | 2.34%  |
| 100012.11-CP 100012.11 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 13  | 5.75%  | 21                    | 9.29%  | 28                     | 12.39% | 6                            | 2.65%  |
| Precinct VBM           | 84  | 9.95%  | 72                    | 8.53%  | 109                    | 12.91% | 15                           | 1.78%  |

**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 6th District |        |                       |        |                        |        |                              |        |
|------------------------|---|--------|-----------------------|--------|------------------------|--------|------------------------------|--------|
|                        | JOHN EDWARD<br>Z'BERG (DEM)               |        | KEVIN HANLEY<br>(REP) |        | CRISTI NELSON<br>(REP) |        | "BO"<br>AMBROZEWICZ<br>(NPP) |        |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 2   | 8.33%  | 2                     | 8.33%  | 4                      | 16.67% | 3                            | 12.50% |
| Total                  | 99  | 9.05%  | 95                    | 8.68%  | 141                    | 12.89% | 24                           | 2.19%  |
| 100013.11-CP 100013.11 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 22  | 9.57%  | 21                    | 9.13%  | 15                     | 6.52%  | 4                            | 1.74%  |
| Precinct VBM           | 34  | 4.15%  | 73                    | 8.91%  | 89                     | 10.87% | 20                           | 2.44%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 4   | 19.05% | 5                     | 23.81% | 0                      | 0.00%  | 0                            | 0.00%  |
| Total                  | 60  | 5.61%  | 99                    | 9.25%  | 104                    | 9.72%  | 24                           | 2.24%  |
| 100014.11-CP 100014.11 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 16  | 8.74%  | 22                    | 12.02% | 29                     | 15.85% | 5                            | 2.73%  |
| Precinct VBM           | 44  | 6.97%  | 49                    | 7.77%  | 68                     | 10.78% | 11                           | 1.74%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 3   | 12.50% | 1                     | 4.17%  | 4                      | 16.67% | 0                            | 0.00%  |
| Total                  | 63  | 7.52%  | 72                    | 8.59%  | 101                    | 12.05% | 16                           | 1.91%  |
| 100022.11-CP 100022.11 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 19  | 11.88% | 16                    | 10.00% | 15                     | 9.38%  | 6                            | 3.75%  |
| Precinct VBM           | 37  | 6.17%  | 37                    | 6.17%  | 88                     | 14.67% | 9                            | 1.50%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 1   | 9.09%  | 0                     | 0.00%  | 1                      | 9.09%  | 1                            | 9.09%  |
| Total                  | 57  | 7.39%  | 53                    | 6.87%  | 104                    | 13.49% | 16                           | 2.08%  |
| 100023.11-CP 100023.11 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 20  | 10.00% | 22                    | 11.00% | 17                     | 8.50%  | 4                            | 2.00%  |
| Precinct VBM           | 61  | 6.33%  | 55                    | 5.71%  | 117                    | 12.15% | 15                           | 1.56%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 4   | 14.29% | 0                     | 0.00%  | 0                      | 0.00%  | 1                            | 3.57%  |
| Total                  | 85  | 7.14%  | 77                    | 6.47%  | 134                    | 11.25% | 20                           | 1.68%  |
| 200003.12-CP 200003.12 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 23  | 15.33% | 9                     | 6.00%  | 11                     | 7.33%  | 3                            | 2.00%  |
| Precinct VBM           | 52  | 10.28% | 46                    | 9.09%  | 36                     | 7.11%  | 10                           | 1.98%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 2   | 7.69%  | 1                     | 3.85%  | 2                      | 7.69%  | 3                            | 11.54% |
| Total                  | 77  | 11.29% | 56                    | 8.21%  | 49                     | 7.18%  | 16                           | 2.35%  |
| 200004.12-CP 200004.12 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 20  | 12.42% | 22                    | 13.66% | 11                     | 6.83%  | 3                            | 1.86%  |
| Precinct VBM           | 60  | 9.93%  | 58                    | 9.60%  | 50                     | 8.28%  | 10                           | 1.66%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 0   | 0.00%  | 0                     | 0.00%  | 2                      | 15.38% | 0                            | 0.00%  |
| Total                  | 80  | 10.28% | 80                    | 10.28% | 63                     | 8.10%  | 13                           | 1.67%  |
| 200005.02-CP 200005.02 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | -   | -      | -                     | -      | -                      | -      | -                            | -      |
| Precinct OS            | -   | -      | -                     | -      | -                      | -      | -                            | -      |
| Precinct VBM           | -   | -      | -                     | -      | -                      | -      | -                            | -      |
| MB Precinct            | -   | -      | -                     | -      | -                      | -      | -                            | -      |
| Provisional            | -   | -      | -                     | -      | -                      | -      | -                            | -      |
| Total                  | -   | -      | -                     | -      | -                      | -      | -                            | -      |
| 200006.12-CP 200006.12 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Precinct OS            | 7   | 6.31%  | 15                    | 13.51% | 6                      | 5.41%  | 2                            | 1.80%  |
| Precinct VBM           | 28  | 5.91%  | 64                    | 13.50% | 58                     | 12.24% | 9                            | 1.90%  |
| MB Precinct            | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |
| Provisional            | 1   | 3.70%  | 0                     | 0.00%  | 1                      | 3.70%  | 0                            | 0.00%  |
| Total                  | 36  | 5.88%  | 79                    | 12.91% | 65                     | 10.62% | 11                           | 1.80%  |
| 200008.12-CP 200008.12 |   |        |                       |        |                        |        |                              |        |
| Precinct TSx           | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -      |



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|                        | JOHN EDWARD<br>Z'BERG (DEM)               | KEVIN HANLEY<br>(REP) | CRISTI NELSON<br>(REP) | "BO"<br>AMBROZEWICZ<br>(NPP) |
| 200018.02-CP 200018.02 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 200020.02-CP 200020.02 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 200021.01-CP 200021.01 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 200021.02-CP 200021.02 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 200024.02-CP 200024.02 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 300001.04-CP 300001.04 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 300001.05-CP 300001.05 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 300003.05-CP 300003.05 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 300004.05-CP 300004.05 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |

**COUNTY OF EL DORADO**  
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|                        | JOHN EDWARD<br>Z'BERG (DEM)               | KEVIN HANLEY<br>(REP) | CRISTI NELSON<br>(REP) | "BO"<br>AMBROZEWICZ<br>(NPP) |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 300005.05-CP 300005.05 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 300008.05-CP 300008.05 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 300009.04-CP 300009.04 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 300010.04-CP 300010.04 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 300011.04-CP 300011.04 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 300013.05-CP 300013.05 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 300015.04-CP 300015.04 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 300015.05-CP 300015.05 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 300017.05-CP 300017.05 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |







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|                        | JOHN EDWARD<br>Z'BERG (DEM)               | KEVIN HANLEY<br>(REP) | CRISTI NELSON<br>(REP) | "BO"<br>AMBROZEWICZ<br>(NPP) |
| Total                  | -   | -                     | -                      | -                            |
| 400017.08-CP 400017.08 | -   | -                     | -                      | -                            |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 400018.08-CP 400018.08 | -   | -                     | -                      | -                            |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 400020.08-CP 400020.08 | -   | -                     | -                      | -                            |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 400021.07-CP 400021.07 | -   | -                     | -                      | -                            |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 500001.10-CP 500001.10 | -   | -                     | -                      | -                            |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 500002.10-CP 500002.10 | -   | -                     | -                      | -                            |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 500003.10-CP 500003.10 | -   | -                     | -                      | -                            |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 500004.10-CP 500004.10 | -   | -                     | -                      | -                            |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 500005.10-CP 500005.10 | -   | -                     | -                      | -                            |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |

**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 6th District |                       |                        |                              |
|------------------------|---|-----------------------|------------------------|------------------------------|
|                        | JOHN EDWARD<br>Z'BERG (DEM)               | KEVIN HANLEY<br>(REP) | CRISTI NELSON<br>(REP) | "BO"<br>AMBROZEWICZ<br>(NPP) |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 500006.10-CP 500006.10 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 500007.10-CP 500007.10 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 500009.10-CP 500009.10 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 500011.10-CP 500011.10 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 500012.10-CP 500012.10 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 500015.10-CP 500015.10 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 500020.10-CP 500020.10 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 500021.10-CP 500021.10 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U201.01-MB 95U201.01 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |

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|                        | Member of the State Assembly 6th District |                       |                        |                              |
|------------------------|---|-----------------------|------------------------|------------------------------|
|                        | JOHN EDWARD<br>Z'BERG (DEM)               | KEVIN HANLEY<br>(REP) | CRISTI NELSON<br>(REP) | "BO"<br>AMBROZEWICZ<br>(NPP) |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U201.02-MB 95U201.02 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U202.02-MB 95U202.02 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U203.02-MB 95U203.02 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U204.01-MB 95U204.01 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U301.05-MB 95U301.05 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U302.04-MB 95U302.04 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U302.05-MB 95U302.05 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U303.03-MB 95U303.03 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

Date:07/05/16  
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|                        | Member of the State Assembly 6th District |                       |                        |                              |
|------------------------|---|-----------------------|------------------------|------------------------------|
|                        | JOHN EDWARD<br>Z'BERG (DEM)               | KEVIN HANLEY<br>(REP) | CRISTI NELSON<br>(REP) | "BO"<br>AMBROZEWICZ<br>(NPP) |
| 95U303.05-MB 95U303.05 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U304.05-MB 95U304.05 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U305.05-MB 95U305.05 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U401.08-MB 95U401.08 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U402.07-MB 95U402.07 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U402.08-MB 95U402.08 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U403.08-MB 95U403.08 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U404.06-MB 95U404.06 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |
| Provisional            | -   | -                     | -                      | -                            |
| Total                  | -   | -                     | -                      | -                            |
| 95U404.08-MB 95U404.08 |   |                       |                        |                              |
| Precinct TSx           | -   | -                     | -                      | -                            |
| Precinct OS            | -   | -                     | -                      | -                            |
| Precinct VBM           | -   | -                     | -                      | -                            |
| MB Precinct            | -   | -                     | -                      | -                            |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

Date:07/05/16  
 Time:08:08:32  
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|                               | Member of the State Assembly 6th District |        |                       |         |                        |        |                              |        |
|-------------------------------|---|--------|-----------------------|---------|------------------------|--------|------------------------------|--------|
|                               | JOHN EDWARD<br>Z'BERG (DEM)               |        | KEVIN HANLEY<br>(REP) |         | CRISTI NELSON<br>(REP) |        | "BO"<br>AMBROZEWICZ<br>(NPP) |        |
| Provisional                   | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Total                         | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| 95U501.09-MB 95U501.09        |   |        |                       |         |                        |        |                              |        |
| Precinct TSx                  | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Precinct OS                   | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Precinct VBM                  | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| MB Precinct                   | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Provisional                   | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Total                         | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| 95U501.10-MB 95U501.10        |   |        |                       |         |                        |        |                              |        |
| Precinct TSx                  | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Precinct OS                   | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Precinct VBM                  | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| MB Precinct                   | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Provisional                   | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Total                         | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| 95U598.10-MB 95U598.10        |   |        |                       |         |                        |        |                              |        |
| Precinct TSx                  | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Precinct OS                   | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Precinct VBM                  | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| MB Precinct                   | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Provisional                   | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Total                         | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| 95U599.10-MB 95U599.10        |   |        |                       |         |                        |        |                              |        |
| Precinct TSx                  | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Precinct OS                   | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Precinct VBM                  | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| MB Precinct                   | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Provisional                   | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| Total                         | -   | -      | -                     | -       | -                      | -      | -                            | -      |
| 96U101.11-MB 96U101.11        |   |        |                       |         |                        |        |                              |        |
| Precinct TSx                  | 0   | -      | 0                     | -       | 0                      | -      | 0                            | -      |
| Precinct OS                   | 0   | -      | 0                     | -       | 0                      | -      | 0                            | -      |
| Precinct VBM                  | 0   | -      | 0                     | -       | 0                      | -      | 0                            | -      |
| MB Precinct                   | 28  | 7.35%  | 29                    | 7.61%   | 49                     | 12.86% | 2                            | 0.52%  |
| Provisional                   | 1   | 16.67% | 0                     | 0.00%   | 0                      | 0.00%  | 1                            | 16.67% |
| Total                         | 29  | 7.49%  | 29                    | 7.49%   | 49                     | 12.66% | 3                            | 0.78%  |
| 96U401.14-MB 96U401.14        |   |        |                       |         |                        |        |                              |        |
| Precinct TSx                  | 0   | -      | 0                     | -       | 0                      | -      | 0                            | -      |
| Precinct OS                   | 0   | -      | 0                     | -       | 0                      | -      | 0                            | -      |
| Precinct VBM                  | 0   | -      | 0                     | -       | 0                      | -      | 0                            | -      |
| MB Precinct                   | 26  | 8.23%  | 19                    | 6.01%   | 39                     | 12.34% | 15                           | 4.75%  |
| Provisional                   | 0   | 0.00%  | 1                     | 100.00% | 0                      | 0.00%  | 0                            | 0.00%  |
| Total                         | 26  | 8.20%  | 20                    | 6.31%   | 39                     | 12.30% | 15                           | 4.73%  |
| Total                         |   |        |                       |         |                        |        |                              |        |
| Precinct TSx                  | 1   | 25.00% | 0                     | 0.00%   | 1                      | 25.00% | 0                            | 0.00%  |
| Precinct OS                   | 361                                       | 9.33%  | 321                   | 8.30%   | 354                    | 9.15%  | 100                          | 2.58%  |
| Precinct VBM                  | 1319                                      | 8.51%  | 1208                  | 7.80%   | 1678                   | 10.83% | 270                          | 1.74%  |
| MB Precinct                   | 54  | 7.75%  | 48                    | 6.89%   | 88                     | 12.63% | 17                           | 2.44%  |
| Provisional                   | 55  | 12.44% | 20                    | 4.52%   | 41                     | 9.28%  | 22                           | 4.98%  |
| Total                         | 1790                                      | 8.73%  | 1597                  | 7.79%   | 2162                   | 10.54% | 409                          | 1.99%  |
| STATE BOARD OF EQUALIZATION   |   |        |                       |         |                        |        |                              |        |
| STATE BOARD OF EQUALIZATION 1 |   |        |                       |         |                        |        |                              |        |
| Precinct TSx                  | 1   | 25.00% | 0                     | 0.00%   | 1                      | 25.00% | 0                            | 0.00%  |
| Precinct OS                   | 361                                       | 9.33%  | 321                   | 8.30%   | 354                    | 9.15%  | 100                          | 2.58%  |
| Precinct VBM                  | 1319                                      | 8.51%  | 1208                  | 7.80%   | 1678                   | 10.83% | 270                          | 1.74%  |
| MB Precinct                   | 54  | 7.75%  | 48                    | 6.89%   | 88                     | 12.63% | 17                           | 2.44%  |
| Provisional                   | 55  | 12.44% | 20                    | 4.52%   | 41                     | 9.28%  | 22                           | 4.98%  |
| Total                         | 1790                                      | 8.73%  | 1597                  | 7.79%   | 2162                   | 10.54% | 409                          | 1.99%  |
| Total                         |   |        |                       |         |                        |        |                              |        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

Date:07/05/16  
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|                                     | Member of the State Assembly 6th District |              |                       |              |                        |               |                              |              |
|-------------------------------------|---|--------------|-----------------------|--------------|------------------------|---------------|------------------------------|--------------|
|                                     | JOHN EDWARD<br>Z'BERG (DEM)               |              | KEVIN HANLEY<br>(REP) |              | CRISTI NELSON<br>(REP) |               | "BO"<br>AMBROZEWICZ<br>(NPP) |              |
| Precinct TSx                        | 1   | 25.00%       | 0                     | 0.00%        | 1                      | 25.00%        | 0                            | 0.00%        |
| Precinct OS                         | 361                                       | 9.33%        | 321                   | 8.30%        | 354                    | 9.15%         | 100                          | 2.58%        |
| Precinct VBM                        | 1319                                      | 8.51%        | 1208                  | 7.80%        | 1678                   | 10.83%        | 270                          | 1.74%        |
| MB Precinct                         | 54  | 7.75%        | 48                    | 6.89%        | 88                     | 12.63%        | 17                           | 2.44%        |
| Provisional                         | 55  | 12.44%       | 20                    | 4.52%        | 41                     | 9.28%         | 22                           | 4.98%        |
| <b>Total</b>                        | <b>1790</b>                               | <b>8.73%</b> | <b>1597</b>           | <b>7.79%</b> | <b>2162</b>            | <b>10.54%</b> | <b>409</b>                   | <b>1.99%</b> |
| <b>UNITED STATES SENATE</b>         |   |              |                       |              |                        |               |                              |              |
| <b>UNITED STATES SENATE</b>         |   |              |                       |              |                        |               |                              |              |
| Precinct TSx                        | 1   | 25.00%       | 0                     | 0.00%        | 1                      | 25.00%        | 0                            | 0.00%        |
| Precinct OS                         | 361                                       | 9.33%        | 321                   | 8.30%        | 354                    | 9.15%         | 100                          | 2.58%        |
| Precinct VBM                        | 1319                                      | 8.51%        | 1208                  | 7.80%        | 1678                   | 10.83%        | 270                          | 1.74%        |
| MB Precinct                         | 54  | 7.75%        | 48                    | 6.89%        | 88                     | 12.63%        | 17                           | 2.44%        |
| Provisional                         | 55  | 12.44%       | 20                    | 4.52%        | 41                     | 9.28%         | 22                           | 4.98%        |
| <b>Total</b>                        | <b>1790</b>                               | <b>8.73%</b> | <b>1597</b>           | <b>7.79%</b> | <b>2162</b>            | <b>10.54%</b> | <b>409</b>                   | <b>1.99%</b> |
| <b>Total</b>                        |   |              |                       |              |                        |               |                              |              |
| Precinct TSx                        | 1   | 25.00%       | 0                     | 0.00%        | 1                      | 25.00%        | 0                            | 0.00%        |
| Precinct OS                         | 361                                       | 9.33%        | 321                   | 8.30%        | 354                    | 9.15%         | 100                          | 2.58%        |
| Precinct VBM                        | 1319                                      | 8.51%        | 1208                  | 7.80%        | 1678                   | 10.83%        | 270                          | 1.74%        |
| MB Precinct                         | 54  | 7.75%        | 48                    | 6.89%        | 88                     | 12.63%        | 17                           | 2.44%        |
| Provisional                         | 55  | 12.44%       | 20                    | 4.52%        | 41                     | 9.28%         | 22                           | 4.98%        |
| <b>Total</b>                        | <b>1790</b>                               | <b>8.73%</b> | <b>1597</b>           | <b>7.79%</b> | <b>2162</b>            | <b>10.54%</b> | <b>409</b>                   | <b>1.99%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |   |              |                       |              |                        |               |                              |              |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |   |              |                       |              |                        |               |                              |              |
| Precinct TSx                        | 1   | 25.00%       | 0                     | 0.00%        | 1                      | 25.00%        | 0                            | 0.00%        |
| Precinct OS                         | 361                                       | 9.33%        | 321                   | 8.30%        | 354                    | 9.15%         | 100                          | 2.58%        |
| Precinct VBM                        | 1319                                      | 8.51%        | 1208                  | 7.80%        | 1678                   | 10.83%        | 270                          | 1.74%        |
| MB Precinct                         | 54  | 7.75%        | 48                    | 6.89%        | 88                     | 12.63%        | 17                           | 2.44%        |
| Provisional                         | 55  | 12.44%       | 20                    | 4.52%        | 41                     | 9.28%         | 22                           | 4.98%        |
| <b>Total</b>                        | <b>1790</b>                               | <b>8.73%</b> | <b>1597</b>           | <b>7.79%</b> | <b>2162</b>            | <b>10.54%</b> | <b>409</b>                   | <b>1.99%</b> |
| <b>Total</b>                        |   |              |                       |              |                        |               |                              |              |
| Precinct TSx                        | 1   | 25.00%       | 0                     | 0.00%        | 1                      | 25.00%        | 0                            | 0.00%        |
| Precinct OS                         | 361                                       | 9.33%        | 321                   | 8.30%        | 354                    | 9.15%         | 100                          | 2.58%        |
| Precinct VBM                        | 1319                                      | 8.51%        | 1208                  | 7.80%        | 1678                   | 10.83%        | 270                          | 1.74%        |
| MB Precinct                         | 54  | 7.75%        | 48                    | 6.89%        | 88                     | 12.63%        | 17                           | 2.44%        |
| Provisional                         | 55  | 12.44%       | 20                    | 4.52%        | 41                     | 9.28%         | 22                           | 4.98%        |
| <b>Total</b>                        | <b>1790</b>                               | <b>8.73%</b> | <b>1597</b>           | <b>7.79%</b> | <b>2162</b>            | <b>10.54%</b> | <b>409</b>                   | <b>1.99%</b> |
| <b>STATE SENATE</b>                 |   |              |                       |              |                        |               |                              |              |
| <b>1ST STATE SENATE DISTRICT</b>    |   |              |                       |              |                        |               |                              |              |
| Precinct TSx                        | 1   | 25.00%       | 0                     | 0.00%        | 1                      | 25.00%        | 0                            | 0.00%        |
| Precinct OS                         | 361                                       | 9.33%        | 321                   | 8.30%        | 354                    | 9.15%         | 100                          | 2.58%        |
| Precinct VBM                        | 1319                                      | 8.51%        | 1208                  | 7.80%        | 1678                   | 10.83%        | 270                          | 1.74%        |
| MB Precinct                         | 54  | 7.75%        | 48                    | 6.89%        | 88                     | 12.63%        | 17                           | 2.44%        |
| Provisional                         | 55  | 12.44%       | 20                    | 4.52%        | 41                     | 9.28%         | 22                           | 4.98%        |
| <b>Total</b>                        | <b>1790</b>                               | <b>8.73%</b> | <b>1597</b>           | <b>7.79%</b> | <b>2162</b>            | <b>10.54%</b> | <b>409</b>                   | <b>1.99%</b> |
| <b>Total</b>                        |   |              |                       |              |                        |               |                              |              |
| Precinct TSx                        | 1   | 25.00%       | 0                     | 0.00%        | 1                      | 25.00%        | 0                            | 0.00%        |
| Precinct OS                         | 361                                       | 9.33%        | 321                   | 8.30%        | 354                    | 9.15%         | 100                          | 2.58%        |
| Precinct VBM                        | 1319                                      | 8.51%        | 1208                  | 7.80%        | 1678                   | 10.83%        | 270                          | 1.74%        |
| MB Precinct                         | 54  | 7.75%        | 48                    | 6.89%        | 88                     | 12.63%        | 17                           | 2.44%        |
| Provisional                         | 55  | 12.44%       | 20                    | 4.52%        | 41                     | 9.28%         | 22                           | 4.98%        |
| <b>Total</b>                        | <b>1790</b>                               | <b>8.73%</b> | <b>1597</b>           | <b>7.79%</b> | <b>2162</b>            | <b>10.54%</b> | <b>409</b>                   | <b>1.99%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |   |              |                       |              |                        |               |                              |              |
| <b>5TH STATE ASSEMBLY DIST</b>      |   |              |                       |              |                        |               |                              |              |
| Precinct TSx                        | 0   | -            | 0                     | -            | 0                      | -             | 0                            | -            |
| Precinct OS                         | 0   | -            | 0                     | -            | 0                      | -             | 0                            | -            |
| Precinct VBM                        | 0   | -            | 0                     | -            | 0                      | -             | 0                            | -            |
| MB Precinct                         | 0   | -            | 0                     | -            | 0                      | -             | 0                            | -            |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                   |  | Member of the State Assembly 6th District |        |                       |        |                        |        |                              |       |
|-----------------------------------|--|---|--------|-----------------------|--------|------------------------|--------|------------------------------|-------|
|                                   |  | JOHN EDWARD<br>Z'BERG (DEM)               |        | KEVIN HANLEY<br>(REP) |        | CRISTI NELSON<br>(REP) |        | "BO"<br>AMBROZEWICZ<br>(NPP) |       |
| Provisional                       |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| Total                             |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| <b>6TH STATE ASSEMBLY DIST</b>    |  |   |        |                       |        |                        |        |                              |       |
| Precinct TSx                      |  | 1   | 25.00% | 0                     | 0.00%  | 1                      | 25.00% | 0                            | 0.00% |
| Precinct OS                       |  | 361                                       | 9.33%  | 321                   | 8.30%  | 354                    | 9.15%  | 100                          | 2.58% |
| Precinct VBM                      |  | 1319                                      | 8.51%  | 1208                  | 7.80%  | 1678                   | 10.83% | 270                          | 1.74% |
| MB Precinct                       |  | 54  | 7.75%  | 48                    | 6.89%  | 88                     | 12.63% | 17                           | 2.44% |
| Provisional                       |  | 55  | 12.44% | 20                    | 4.52%  | 41                     | 9.28%  | 22                           | 4.98% |
| Total                             |  | 1790                                      | 8.73%  | 1597                  | 7.79%  | 2162                   | 10.54% | 409                          | 1.99% |
| <b>Total</b>                      |  |   |        |                       |        |                        |        |                              |       |
| Precinct TSx                      |  | 1   | 25.00% | 0                     | 0.00%  | 1                      | 25.00% | 0                            | 0.00% |
| Precinct OS                       |  | 361                                       | 9.33%  | 321                   | 8.30%  | 354                    | 9.15%  | 100                          | 2.58% |
| Precinct VBM                      |  | 1319                                      | 8.51%  | 1208                  | 7.80%  | 1678                   | 10.83% | 270                          | 1.74% |
| MB Precinct                       |  | 54  | 7.75%  | 48                    | 6.89%  | 88                     | 12.63% | 17                           | 2.44% |
| Provisional                       |  | 55  | 12.44% | 20                    | 4.52%  | 41                     | 9.28%  | 22                           | 4.98% |
| Total                             |  | 1790                                      | 8.73%  | 1597                  | 7.79%  | 2162                   | 10.54% | 409                          | 1.99% |
| <b>SUPERVISORIAL DISTRICTS</b>    |  |   |        |                       |        |                        |        |                              |       |
| <b>1ST SUPERVISORIAL DISTRICT</b> |  |   |        |                       |        |                        |        |                              |       |
| Precinct TSx                      |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| Precinct OS                       |  | 198                                       | 8.87%  | 183                   | 8.20%  | 220                    | 9.85%  | 66                           | 2.96% |
| Precinct VBM                      |  | 730                                       | 7.83%  | 723                   | 7.75%  | 1023                   | 10.97% | 154                          | 1.65% |
| MB Precinct                       |  | 28  | 7.35%  | 29                    | 7.61%  | 49                     | 12.86% | 2                            | 0.52% |
| Provisional                       |  | 34  | 14.05% | 14                    | 5.79%  | 21                     | 8.68%  | 16                           | 6.61% |
| Total                             |  | 990                                       | 8.13%  | 949                   | 7.79%  | 1313                   | 10.78% | 238                          | 1.95% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |  |   |        |                       |        |                        |        |                              |       |
| Precinct TSx                      |  | 1   | 25.00% | 0                     | 0.00%  | 1                      | 25.00% | 0                            | 0.00% |
| Precinct OS                       |  | 138                                       | 10.48% | 114                   | 8.66%  | 104                    | 7.90%  | 26                           | 1.97% |
| Precinct VBM                      |  | 446                                       | 9.52%  | 389                   | 8.30%  | 478                    | 10.20% | 93                           | 1.99% |
| MB Precinct                       |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| Provisional                       |  | 16  | 9.88%  | 2                     | 1.23%  | 16                     | 9.88%  | 4                            | 2.47% |
| Total                             |  | 601                                       | 9.74%  | 505                   | 8.19%  | 599                    | 9.71%  | 123                          | 1.99% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |  |   |        |                       |        |                        |        |                              |       |
| Precinct TSx                      |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| Precinct OS                       |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| Precinct VBM                      |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| MB Precinct                       |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| Provisional                       |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| Total                             |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| <b>4TH SUPERVISORIAL DISTRICT</b> |  |   |        |                       |        |                        |        |                              |       |
| Precinct TSx                      |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| Precinct OS                       |  | 25  | 7.84%  | 24                    | 7.52%  | 30                     | 9.40%  | 8                            | 2.51% |
| Precinct VBM                      |  | 143                                       | 9.64%  | 96                    | 6.47%  | 177                    | 11.94% | 23                           | 1.55% |
| MB Precinct                       |  | 26  | 8.23%  | 19                    | 6.01%  | 39                     | 12.34% | 15                           | 4.75% |
| Provisional                       |  | 5   | 13.16% | 4                     | 10.53% | 4                      | 10.53% | 2                            | 5.26% |
| Total                             |  | 199                                       | 9.23%  | 143                   | 6.63%  | 250                    | 11.60% | 48                           | 2.23% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |  |   |        |                       |        |                        |        |                              |       |
| Precinct TSx                      |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| Precinct OS                       |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| Precinct VBM                      |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| MB Precinct                       |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| Provisional                       |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| Total                             |  | 0   | -      | 0                     | -      | 0                      | -      | 0                            | -     |
| <b>Total</b>                      |  |   |        |                       |        |                        |        |                              |       |
| Precinct TSx                      |  | 1   | 25.00% | 0                     | 0.00%  | 1                      | 25.00% | 0                            | 0.00% |
| Precinct OS                       |  | 361                                       | 9.33%  | 321                   | 8.30%  | 354                    | 9.15%  | 100                          | 2.58% |
| Precinct VBM                      |  | 1319                                      | 8.51%  | 1208                  | 7.80%  | 1678                   | 10.83% | 270                          | 1.74% |
| MB Precinct                       |  | 54  | 7.75%  | 48                    | 6.89%  | 88                     | 12.63% | 17                           | 2.44% |
| Provisional                       |  | 55  | 12.44% | 20                    | 4.52%  | 41                     | 9.28%  | 22                           | 4.98% |
| Total                             |  | 1790                                      | 8.73%  | 1597                  | 7.79%  | 2162                   | 10.54% | 409                          | 1.99% |

**COUNTY OF EL DORADO**  
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|                                 | Member of the State Assembly 6th District |        |                       |       |                        |        |                              |       |
|---------------------------------|---|--------|-----------------------|-------|------------------------|--------|------------------------------|-------|
|                                 | JOHN EDWARD<br>Z'BERG (DEM)               |        | KEVIN HANLEY<br>(REP) |       | CRISTI NELSON<br>(REP) |        | "BO"<br>AMBROZEWICZ<br>(NPP) |       |
| <b>CITIES</b>                   |   |        |                       |       |                        |        |                              |       |
| <b>CITY OF PLACERVILLE</b>      |   |        |                       |       |                        |        |                              |       |
| Precinct TSx                    | 0   | -      | 0                     | -     | 0                      | -      | 0                            | -     |
| Precinct OS                     | 0   | -      | 0                     | -     | 0                      | -      | 0                            | -     |
| Precinct VBM                    | 0   | -      | 0                     | -     | 0                      | -      | 0                            | -     |
| MB Precinct                     | 0   | -      | 0                     | -     | 0                      | -      | 0                            | -     |
| Provisional                     | 0   | -      | 0                     | -     | 0                      | -      | 0                            | -     |
| Total                           | 0   | -      | 0                     | -     | 0                      | -      | 0                            | -     |
| <b>CITY OF SOUTH LAKE TAHOE</b> |   |        |                       |       |                        |        |                              |       |
| Precinct TSx                    | 0   | -      | 0                     | -     | 0                      | -      | 0                            | -     |
| Precinct OS                     | 0   | -      | 0                     | -     | 0                      | -      | 0                            | -     |
| Precinct VBM                    | 0   | -      | 0                     | -     | 0                      | -      | 0                            | -     |
| MB Precinct                     | 0   | -      | 0                     | -     | 0                      | -      | 0                            | -     |
| Provisional                     | 0   | -      | 0                     | -     | 0                      | -      | 0                            | -     |
| Total                           | 0   | -      | 0                     | -     | 0                      | -      | 0                            | -     |
| <b>UNINCORPORATED AREA</b>      |   |        |                       |       |                        |        |                              |       |
| Precinct TSx                    | 1   | 25.00% | 0                     | 0.00% | 1                      | 25.00% | 0                            | 0.00% |
| Precinct OS                     | 361                                       | 9.33%  | 321                   | 8.30% | 354                    | 9.15%  | 100                          | 2.58% |
| Precinct VBM                    | 1319                                      | 8.51%  | 1208                  | 7.80% | 1678                   | 10.83% | 270                          | 1.74% |
| MB Precinct                     | 54  | 7.75%  | 48                    | 6.89% | 88                     | 12.63% | 17                           | 2.44% |
| Provisional                     | 55  | 12.44% | 20                    | 4.52% | 41                     | 9.28%  | 22                           | 4.98% |
| Total                           | 1790                                      | 8.73%  | 1597                  | 7.79% | 2162                   | 10.54% | 409                          | 1.99% |
| <b>Total</b>                    |   |        |                       |       |                        |        |                              |       |
| Precinct TSx                    | 1   | 25.00% | 0                     | 0.00% | 1                      | 25.00% | 0                            | 0.00% |
| Precinct OS                     | 361                                       | 9.33%  | 321                   | 8.30% | 354                    | 9.15%  | 100                          | 2.58% |
| Precinct VBM                    | 1319                                      | 8.51%  | 1208                  | 7.80% | 1678                   | 10.83% | 270                          | 1.74% |
| MB Precinct                     | 54  | 7.75%  | 48                    | 6.89% | 88                     | 12.63% | 17                           | 2.44% |
| Provisional                     | 55  | 12.44% | 20                    | 4.52% | 41                     | 9.28%  | 22                           | 4.98% |
| Total                           | 1790                                      | 8.73%  | 1597                  | 7.79% | 2162                   | 10.54% | 409                          | 1.99% |



**COUNTY OF EL DORADO**  
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|------------------------|---|-------|-------------------|--------|
|                        | GABRIEL L.<br>HYDRICK (REP)               |       | KEVIN KILEY (REP) |        |
| Jurisdiction Wide      |   |       |                   |        |
| 100001.11-CP 100001.11 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 1   | 0.62% | 24                | 14.81% |
| Precinct VBM           | 6   | 0.85% | 106               | 14.99% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 0                 | 0.00%  |
| Total                  | 7   | 0.79% | 130               | 14.74% |
| 100002.11-CP 100002.11 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 1   | 1.35% | 18                | 24.32% |
| Precinct VBM           | 1   | 0.24% | 51                | 12.14% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 2                 | 11.11% |
| Total                  | 2   | 0.39% | 71                | 13.87% |
| 100003.11-CP 100003.11 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 1   | 0.90% | 13                | 11.71% |
| Precinct VBM           | 1   | 0.14% | 100               | 14.37% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 1   | 4.76% | 0                 | 0.00%  |
| Total                  | 3   | 0.36% | 113               | 13.65% |
| 100004.11-CP 100004.11 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 0   | 0.00% | 29                | 13.00% |
| Precinct VBM           | 1   | 0.15% | 121               | 18.59% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 1                 | 33.33% |
| Total                  | 1   | 0.11% | 151               | 17.22% |
| 100005.11-CP 100005.11 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 1   | 0.76% | 6                 | 4.58%  |
| Precinct VBM           | 5   | 0.61% | 133               | 16.32% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 2                 | 10.00% |
| Total                  | 6   | 0.62% | 141               | 14.60% |
| 100006.11-CP 100006.11 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 1   | 0.47% | 37                | 17.21% |
| Precinct VBM           | 2   | 0.24% | 170               | 20.24% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 3                 | 14.29% |
| Total                  | 3   | 0.28% | 210               | 19.52% |
| 100008.11-CP 100008.11 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 1   | 0.60% | 16                | 9.58%  |
| Precinct VBM           | 2   | 0.31% | 97                | 14.81% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 2                 | 15.38% |
| Total                  | 3   | 0.36% | 115               | 13.77% |
| 100009.11-CP 100009.11 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 0   | 0.00% | 20                | 13.25% |
| Precinct VBM           | 1   | 0.15% | 92                | 13.47% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 2                 | 10.53% |
| Total                  | 1   | 0.12% | 114               | 13.36% |
| 100012.11-CP 100012.11 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 2   | 0.88% | 20                | 8.85%  |
| Precinct VBM           | 5   | 0.59% | 123               | 14.57% |

**COUNTY OF EL DORADO**  
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|------------------------|---|-------|-------------------|--------|
|                        | GABRIEL L.<br>HYDRICK (REP)               |       | KEVIN KILEY (REP) |        |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 3                 | 12.50% |
| Total                  | 7   | 0.64% | 146               | 13.35% |
| 100013.11-CP 100013.11 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 3   | 1.30% | 30                | 13.04% |
| Precinct VBM           | 3   | 0.37% | 123               | 15.02% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 3                 | 14.29% |
| Total                  | 6   | 0.56% | 156               | 14.58% |
| 100014.11-CP 100014.11 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 0   | 0.00% | 22                | 12.02% |
| Precinct VBM           | 2   | 0.32% | 103               | 16.32% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 5                 | 20.83% |
| Total                  | 2   | 0.24% | 130               | 15.51% |
| 100022.11-CP 100022.11 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 1   | 0.63% | 23                | 14.38% |
| Precinct VBM           | 1   | 0.17% | 84                | 14.00% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 1   | 9.09% | 2                 | 18.18% |
| Total                  | 3   | 0.39% | 109               | 14.14% |
| 100023.11-CP 100023.11 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 2   | 1.00% | 27                | 13.50% |
| Precinct VBM           | 5   | 0.52% | 124               | 12.88% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 0                 | 0.00%  |
| Total                  | 7   | 0.59% | 151               | 12.68% |
| 200003.12-CP 200003.12 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 6   | 4.00% | 13                | 8.67%  |
| Precinct VBM           | 7   | 1.38% | 96                | 18.97% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 4                 | 15.38% |
| Total                  | 13  | 1.91% | 113               | 16.57% |
| 200004.12-CP 200004.12 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 0   | 0.00% | 15                | 9.32%  |
| Precinct VBM           | 3   | 0.50% | 61                | 10.10% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 1                 | 7.69%  |
| Total                  | 3   | 0.39% | 77                | 9.90%  |
| 200005.02-CP 200005.02 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 200006.12-CP 200006.12 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 2   | 1.80% | 12                | 10.81% |
| Precinct VBM           | 0   | 0.00% | 73                | 15.40% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 2                 | 7.41%  |
| Total                  | 2   | 0.33% | 87                | 14.22% |
| 200008.12-CP 200008.12 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |

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|------------------------|---|-------|-------------------|--------|
|                        | GABRIEL L.<br>HYDRICK (REP)               |       | KEVIN KILEY (REP) |        |
| Precinct OS            | 0   | 0.00% | 15                | 11.19% |
| Precinct VBM           | 3   | 0.94% | 29                | 9.12%  |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 1                 | 10.00% |
| Total                  | 3   | 0.65% | 45                | 9.74%  |
| 200010.12-CP 200010.12 |   |       |                   |        |
| Precinct TSx           | 0   | 0.00% | 0                 | 0.00%  |
| Precinct OS            | 2   | 1.15% | 27                | 15.52% |
| Precinct VBM           | 5   | 0.66% | 116               | 15.28% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 2                 | 8.33%  |
| Total                  | 7   | 0.73% | 145               | 15.09% |
| 200012.12-CP 200012.12 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 1   | 0.79% | 14                | 11.11% |
| Precinct VBM           | 3   | 0.73% | 66                | 15.98% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 1                 | 8.33%  |
| Total                  | 4   | 0.73% | 81                | 14.70% |
| 200013.12-CP 200013.12 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 1   | 0.53% | 26                | 13.83% |
| Precinct VBM           | 4   | 0.62% | 91                | 14.00% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 2                 | 11.11% |
| Total                  | 5   | 0.58% | 119               | 13.90% |
| 200014.12-CP 200014.12 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 1   | 0.56% | 16                | 8.89%  |
| Precinct VBM           | 9   | 1.67% | 55                | 10.19% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 4                 | 16.00% |
| Total                  | 10  | 1.34% | 75                | 10.07% |
| 200015.02-CP 200015.02 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 200016.12-CP 200016.12 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 0   | 0.00% | 3                 | 14.29% |
| Precinct VBM           | 0   | 0.00% | 27                | 21.77% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 0                 | 0.00%  |
| Total                  | 0   | 0.00% | 30                | 20.13% |
| 200016.13-CP 200016.13 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 0   | 0.00% | 5                 | 6.94%  |
| Precinct VBM           | 1   | 0.34% | 40                | 13.47% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 0                 | 0.00%  |
| Total                  | 1   | 0.27% | 45                | 12.10% |
| 200017.02-CP 200017.02 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |

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|------------------------|---|-------------------|
|                        | GABRIEL L.<br>HYDRICK (REP)               | KEVIN KILEY (REP) |
| 200018.02-CP 200018.02 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 200020.02-CP 200020.02 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 200021.01-CP 200021.01 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 200021.02-CP 200021.02 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 200024.02-CP 200024.02 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 300001.04-CP 300001.04 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 300001.05-CP 300001.05 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 300003.05-CP 300003.05 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 300004.05-CP 300004.05 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |

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|------------------------|---|-------------------|
|                        | GABRIEL L.<br>HYDRICK (REP)               | KEVIN KILEY (REP) |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 300005.05-CP 300005.05 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 300008.05-CP 300008.05 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 300009.04-CP 300009.04 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 300010.04-CP 300010.04 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 300011.04-CP 300011.04 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 300013.05-CP 300013.05 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 300015.04-CP 300015.04 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 300015.05-CP 300015.05 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 300017.05-CP 300017.05 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |

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|                        | GABRIEL L.<br>HYDRICK (REP)               |       | KEVIN KILEY (REP) |        |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 300018.04-CP 300018.04 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 300020.04-CP 300020.04 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 300021.03-CP 300021.03 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 300022.05-CP 300022.05 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 300023.03-CP 300023.03 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 300023.05-CP 300023.05 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 400001.14-CP 400001.14 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 1   | 0.66% | 18                | 11.92% |
| Precinct VBM           | 8   | 0.90% | 147               | 16.46% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 3                 | 14.29% |
| Total                  | 9   | 0.85% | 168               | 15.77% |
| 400002.08-CP 400002.08 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 400003.08-CP 400003.08 |   |       |                   |        |

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|                        | GABRIEL L.<br>HYDRICK (REP)               |       | KEVIN KILEY (REP) |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 400004.14-CP 400004.14 |   |       |                   |        |
| Precinct TSx           | 0   | -     | 0                 | -      |
| Precinct OS            | 4   | 2.38% | 19                | 11.31% |
| Precinct VBM           | 1   | 0.17% | 88                | 14.92% |
| MB Precinct            | 0   | -     | 0                 | -      |
| Provisional            | 0   | 0.00% | 1                 | 6.25%  |
| Total                  | 5   | 0.65% | 108               | 13.95% |
| 400005.08-CP 400005.08 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 400006.08-CP 400006.08 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 400007.08-CP 400007.08 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 400010.08-CP 400010.08 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 400012.08-CP 400012.08 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 400013.08-CP 400013.08 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |
| Total                  | -   | -     | -                 | -      |
| 400014.08-CP 400014.08 |   |       |                   |        |
| Precinct TSx           | -   | -     | -                 | -      |
| Precinct OS            | -   | -     | -                 | -      |
| Precinct VBM           | -   | -     | -                 | -      |
| MB Precinct            | -   | -     | -                 | -      |
| Provisional            | -   | -     | -                 | -      |

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|                        | GABRIEL L.<br>HYDRICK (REP)               | KEVIN KILEY (REP) |
| Total                  | -   | -                 |
| 400017.08-CP 400017.08 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 400018.08-CP 400018.08 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 400020.08-CP 400020.08 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 400021.07-CP 400021.07 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 500001.10-CP 500001.10 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 500002.10-CP 500002.10 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 500003.10-CP 500003.10 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 500004.10-CP 500004.10 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 500005.10-CP 500005.10 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |



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|                        | GABRIEL L.<br>HYDRICK (REP)               | KEVIN KILEY (REP) |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 500006.10-CP 500006.10 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 500007.10-CP 500007.10 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 500009.10-CP 500009.10 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 500011.10-CP 500011.10 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 500012.10-CP 500012.10 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 500015.10-CP 500015.10 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 500020.10-CP 500020.10 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 500021.10-CP 500021.10 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U201.01-MB 95U201.01 |   |                   |
| Precinct TSx           | -   | -                 |

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|                        | GABRIEL L.<br>HYDRICK (REP)               | KEVIN KILEY (REP) |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U201.02-MB 95U201.02 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U202.02-MB 95U202.02 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U203.02-MB 95U203.02 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U204.01-MB 95U204.01 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U301.05-MB 95U301.05 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U302.04-MB 95U302.04 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U302.05-MB 95U302.05 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U303.03-MB 95U303.03 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |

**COUNTY OF EL DORADO**  
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|                        | Member of the State Assembly 6th District |                   |
|------------------------|---|-------------------|
|                        | GABRIEL L.<br>HYDRICK (REP)               | KEVIN KILEY (REP) |
| 95U303.05-MB 95U303.05 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U304.05-MB 95U304.05 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U305.05-MB 95U305.05 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U401.08-MB 95U401.08 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U402.07-MB 95U402.07 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U402.08-MB 95U402.08 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U403.08-MB 95U403.08 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U404.06-MB 95U404.06 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |
| Provisional            | -   | -                 |
| Total                  | -   | -                 |
| 95U404.08-MB 95U404.08 |   |                   |
| Precinct TSx           | -   | -                 |
| Precinct OS            | -   | -                 |
| Precinct VBM           | -   | -                 |
| MB Precinct            | -   | -                 |

**COUNTY OF EL DORADO**  
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|                               | Member of the State Assembly 6th District |       |                   |        |
|-------------------------------|---|-------|-------------------|--------|
|                               | GABRIEL L.<br>HYDRICK (REP)               |       | KEVIN KILEY (REP) |        |
| Provisional                   | -   | -     | -                 | -      |
| Total                         | -   | -     | -                 | -      |
| 95U501.09-MB 95U501.09        |   |       |                   |        |
| Precinct TSx                  | -   | -     | -                 | -      |
| Precinct OS                   | -   | -     | -                 | -      |
| Precinct VBM                  | -   | -     | -                 | -      |
| MB Precinct                   | -   | -     | -                 | -      |
| Provisional                   | -   | -     | -                 | -      |
| Total                         | -   | -     | -                 | -      |
| 95U501.10-MB 95U501.10        |   |       |                   |        |
| Precinct TSx                  | -   | -     | -                 | -      |
| Precinct OS                   | -   | -     | -                 | -      |
| Precinct VBM                  | -   | -     | -                 | -      |
| MB Precinct                   | -   | -     | -                 | -      |
| Provisional                   | -   | -     | -                 | -      |
| Total                         | -   | -     | -                 | -      |
| 95U598.10-MB 95U598.10        |   |       |                   |        |
| Precinct TSx                  | -   | -     | -                 | -      |
| Precinct OS                   | -   | -     | -                 | -      |
| Precinct VBM                  | -   | -     | -                 | -      |
| MB Precinct                   | -   | -     | -                 | -      |
| Provisional                   | -   | -     | -                 | -      |
| Total                         | -   | -     | -                 | -      |
| 95U599.10-MB 95U599.10        |   |       |                   |        |
| Precinct TSx                  | -   | -     | -                 | -      |
| Precinct OS                   | -   | -     | -                 | -      |
| Precinct VBM                  | -   | -     | -                 | -      |
| MB Precinct                   | -   | -     | -                 | -      |
| Provisional                   | -   | -     | -                 | -      |
| Total                         | -   | -     | -                 | -      |
| 96U101.11-MB 96U101.11        |   |       |                   |        |
| Precinct TSx                  | 0   | -     | 0                 | -      |
| Precinct OS                   | 0   | -     | 0                 | -      |
| Precinct VBM                  | 0   | -     | 0                 | -      |
| MB Precinct                   | 1   | 0.26% | 61                | 16.01% |
| Provisional                   | 0   | 0.00% | 0                 | 0.00%  |
| Total                         | 1   | 0.26% | 61                | 15.76% |
| 96U401.14-MB 96U401.14        |   |       |                   |        |
| Precinct TSx                  | 0   | -     | 0                 | -      |
| Precinct OS                   | 0   | -     | 0                 | -      |
| Precinct VBM                  | 0   | -     | 0                 | -      |
| MB Precinct                   | 2   | 0.63% | 43                | 13.61% |
| Provisional                   | 0   | 0.00% | 0                 | 0.00%  |
| Total                         | 2   | 0.63% | 43                | 13.56% |
| Total                         |   |       |                   |        |
| Precinct TSx                  | 0   | 0.00% | 0                 | 0.00%  |
| Precinct OS                   | 32  | 0.83% | 468               | 12.10% |
| Precinct VBM                  | 79  | 0.51% | 2316              | 14.95% |
| MB Precinct                   | 3   | 0.43% | 104               | 14.92% |
| Provisional                   | 2   | 0.45% | 46                | 10.41% |
| Total                         | 116                                       | 0.57% | 2934              | 14.31% |
| STATE BOARD OF EQUALIZATION   |   |       |                   |        |
| STATE BOARD OF EQUALIZATION 1 |   |       |                   |        |
| Precinct TSx                  | 0   | 0.00% | 0                 | 0.00%  |
| Precinct OS                   | 32  | 0.83% | 468               | 12.10% |
| Precinct VBM                  | 79  | 0.51% | 2316              | 14.95% |
| MB Precinct                   | 3   | 0.43% | 104               | 14.92% |
| Provisional                   | 2   | 0.45% | 46                | 10.41% |
| Total                         | 116                                       | 0.57% | 2934              | 14.31% |
| Total                         |   |       |                   |        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                     | Member of the State Assembly 6th District |              |                   |               |
|-------------------------------------|---|--------------|-------------------|---------------|
|                                     | GABRIEL L.<br>HYDRICK (REP)               |              | KEVIN KILEY (REP) |               |
| Precinct TSx                        | 0   | 0.00%        | 0                 | 0.00%         |
| Precinct OS                         | 32  | 0.83%        | 468               | 12.10%        |
| Precinct VBM                        | 79  | 0.51%        | 2316              | 14.95%        |
| MB Precinct                         | 3   | 0.43%        | 104               | 14.92%        |
| Provisional                         | 2   | 0.45%        | 46                | 10.41%        |
| <b>Total</b>                        | <b>116</b>                                | <b>0.57%</b> | <b>2934</b>       | <b>14.31%</b> |
| <b>UNITED STATES SENATE</b>         |   |              |                   |               |
| <b>UNITED STATES SENATE</b>         |   |              |                   |               |
| Precinct TSx                        | 0   | 0.00%        | 0                 | 0.00%         |
| Precinct OS                         | 32  | 0.83%        | 468               | 12.10%        |
| Precinct VBM                        | 79  | 0.51%        | 2316              | 14.95%        |
| MB Precinct                         | 3   | 0.43%        | 104               | 14.92%        |
| Provisional                         | 2   | 0.45%        | 46                | 10.41%        |
| <b>Total</b>                        | <b>116</b>                                | <b>0.57%</b> | <b>2934</b>       | <b>14.31%</b> |
| <b>Total</b>                        |   |              |                   |               |
| Precinct TSx                        | 0   | 0.00%        | 0                 | 0.00%         |
| Precinct OS                         | 32  | 0.83%        | 468               | 12.10%        |
| Precinct VBM                        | 79  | 0.51%        | 2316              | 14.95%        |
| MB Precinct                         | 3   | 0.43%        | 104               | 14.92%        |
| Provisional                         | 2   | 0.45%        | 46                | 10.41%        |
| <b>Total</b>                        | <b>116</b>                                | <b>0.57%</b> | <b>2934</b>       | <b>14.31%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |   |              |                   |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |   |              |                   |               |
| Precinct TSx                        | 0   | 0.00%        | 0                 | 0.00%         |
| Precinct OS                         | 32  | 0.83%        | 468               | 12.10%        |
| Precinct VBM                        | 79  | 0.51%        | 2316              | 14.95%        |
| MB Precinct                         | 3   | 0.43%        | 104               | 14.92%        |
| Provisional                         | 2   | 0.45%        | 46                | 10.41%        |
| <b>Total</b>                        | <b>116</b>                                | <b>0.57%</b> | <b>2934</b>       | <b>14.31%</b> |
| <b>Total</b>                        |   |              |                   |               |
| Precinct TSx                        | 0   | 0.00%        | 0                 | 0.00%         |
| Precinct OS                         | 32  | 0.83%        | 468               | 12.10%        |
| Precinct VBM                        | 79  | 0.51%        | 2316              | 14.95%        |
| MB Precinct                         | 3   | 0.43%        | 104               | 14.92%        |
| Provisional                         | 2   | 0.45%        | 46                | 10.41%        |
| <b>Total</b>                        | <b>116</b>                                | <b>0.57%</b> | <b>2934</b>       | <b>14.31%</b> |
| <b>STATE SENATE</b>                 |   |              |                   |               |
| <b>1ST STATE SENATE DISTRICT</b>    |   |              |                   |               |
| Precinct TSx                        | 0   | 0.00%        | 0                 | 0.00%         |
| Precinct OS                         | 32  | 0.83%        | 468               | 12.10%        |
| Precinct VBM                        | 79  | 0.51%        | 2316              | 14.95%        |
| MB Precinct                         | 3   | 0.43%        | 104               | 14.92%        |
| Provisional                         | 2   | 0.45%        | 46                | 10.41%        |
| <b>Total</b>                        | <b>116</b>                                | <b>0.57%</b> | <b>2934</b>       | <b>14.31%</b> |
| <b>Total</b>                        |   |              |                   |               |
| Precinct TSx                        | 0   | 0.00%        | 0                 | 0.00%         |
| Precinct OS                         | 32  | 0.83%        | 468               | 12.10%        |
| Precinct VBM                        | 79  | 0.51%        | 2316              | 14.95%        |
| MB Precinct                         | 3   | 0.43%        | 104               | 14.92%        |
| Provisional                         | 2   | 0.45%        | 46                | 10.41%        |
| <b>Total</b>                        | <b>116</b>                                | <b>0.57%</b> | <b>2934</b>       | <b>14.31%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |   |              |                   |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |   |              |                   |               |
| Precinct TSx                        | 0   | -            | 0                 | -             |
| Precinct OS                         | 0   | -            | 0                 | -             |
| Precinct VBM                        | 0   | -            | 0                 | -             |
| MB Precinct                         | 0   | -            | 0                 | -             |

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|                                   | Member of the State Assembly 6th District |       |                   |        |
|-----------------------------------|---|-------|-------------------|--------|
|                                   | GABRIEL L.<br>HYDRICK (REP)               |       | KEVIN KILEY (REP) |        |
| Provisional                       | 0   | -     | 0                 | -      |
| Total                             | 0   | -     | 0                 | -      |
| <b>6TH STATE ASSEMBLY DIST</b>    |   |       |                   |        |
| Precinct TSx                      | 0   | 0.00% | 0                 | 0.00%  |
| Precinct OS                       | 32  | 0.83% | 468               | 12.10% |
| Precinct VBM                      | 79  | 0.51% | 2316              | 14.95% |
| MB Precinct                       | 3   | 0.43% | 104               | 14.92% |
| Provisional                       | 2   | 0.45% | 46                | 10.41% |
| Total                             | 116                                       | 0.57% | 2934              | 14.31% |
| <b>Total</b>                      |   |       |                   |        |
| Precinct TSx                      | 0   | 0.00% | 0                 | 0.00%  |
| Precinct OS                       | 32  | 0.83% | 468               | 12.10% |
| Precinct VBM                      | 79  | 0.51% | 2316              | 14.95% |
| MB Precinct                       | 3   | 0.43% | 104               | 14.92% |
| Provisional                       | 2   | 0.45% | 46                | 10.41% |
| Total                             | 116                                       | 0.57% | 2934              | 14.31% |
| <b>SUPERVISORIAL DISTRICTS</b>    |   |       |                   |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |   |       |                   |        |
| Precinct TSx                      | 0   | -     | 0                 | -      |
| Precinct OS                       | 14  | 0.63% | 285               | 12.76% |
| Precinct VBM                      | 35  | 0.38% | 1427              | 15.30% |
| MB Precinct                       | 1   | 0.26% | 61                | 16.01% |
| Provisional                       | 2   | 0.83% | 25                | 10.33% |
| Total                             | 52  | 0.43% | 1798              | 14.76% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |   |       |                   |        |
| Precinct TSx                      | 0   | 0.00% | 0                 | 0.00%  |
| Precinct OS                       | 13  | 0.99% | 146               | 11.09% |
| Precinct VBM                      | 35  | 0.75% | 654               | 13.96% |
| MB Precinct                       | 0   | -     | 0                 | -      |
| Provisional                       | 0   | 0.00% | 17                | 10.49% |
| Total                             | 48  | 0.78% | 817               | 13.25% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |   |       |                   |        |
| Precinct TSx                      | 0   | -     | 0                 | -      |
| Precinct OS                       | 0   | -     | 0                 | -      |
| Precinct VBM                      | 0   | -     | 0                 | -      |
| MB Precinct                       | 0   | -     | 0                 | -      |
| Provisional                       | 0   | -     | 0                 | -      |
| Total                             | 0   | -     | 0                 | -      |
| <b>4TH SUPERVISORIAL DISTRICT</b> |   |       |                   |        |
| Precinct TSx                      | 0   | -     | 0                 | -      |
| Precinct OS                       | 5   | 1.57% | 37                | 11.60% |
| Precinct VBM                      | 9   | 0.61% | 235               | 15.85% |
| MB Precinct                       | 2   | 0.63% | 43                | 13.61% |
| Provisional                       | 0   | 0.00% | 4                 | 10.53% |
| Total                             | 16  | 0.74% | 319               | 14.80% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |   |       |                   |        |
| Precinct TSx                      | 0   | -     | 0                 | -      |
| Precinct OS                       | 0   | -     | 0                 | -      |
| Precinct VBM                      | 0   | -     | 0                 | -      |
| MB Precinct                       | 0   | -     | 0                 | -      |
| Provisional                       | 0   | -     | 0                 | -      |
| Total                             | 0   | -     | 0                 | -      |
| <b>Total</b>                      |   |       |                   |        |
| Precinct TSx                      | 0   | 0.00% | 0                 | 0.00%  |
| Precinct OS                       | 32  | 0.83% | 468               | 12.10% |
| Precinct VBM                      | 79  | 0.51% | 2316              | 14.95% |
| MB Precinct                       | 3   | 0.43% | 104               | 14.92% |
| Provisional                       | 2   | 0.45% | 46                | 10.41% |
| Total                             | 116                                       | 0.57% | 2934              | 14.31% |

**COUNTY OF EL DORADO**  
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|---------------------------------|---|-------|-------------------|--------|
|                                 | GABRIEL L.<br>HYDRICK (REP)               |       | KEVIN KILEY (REP) |        |
| <b>CITIES</b>                   |   |       |                   |        |
| <b>CITY OF PLACERVILLE</b>      |   |       |                   |        |
| Precinct TSx                    | 0   | -     | 0                 | -      |
| Precinct OS                     | 0   | -     | 0                 | -      |
| Precinct VBM                    | 0   | -     | 0                 | -      |
| MB Precinct                     | 0   | -     | 0                 | -      |
| Provisional                     | 0   | -     | 0                 | -      |
| Total                           | 0   | -     | 0                 | -      |
| <b>CITY OF SOUTH LAKE TAHOE</b> |   |       |                   |        |
| Precinct TSx                    | 0   | -     | 0                 | -      |
| Precinct OS                     | 0   | -     | 0                 | -      |
| Precinct VBM                    | 0   | -     | 0                 | -      |
| MB Precinct                     | 0   | -     | 0                 | -      |
| Provisional                     | 0   | -     | 0                 | -      |
| Total                           | 0   | -     | 0                 | -      |
| <b>UNINCORPORATED AREA</b>      |   |       |                   |        |
| Precinct TSx                    | 0   | 0.00% | 0                 | 0.00%  |
| Precinct OS                     | 32  | 0.83% | 468               | 12.10% |
| Precinct VBM                    | 79  | 0.51% | 2316              | 14.95% |
| MB Precinct                     | 3   | 0.43% | 104               | 14.92% |
| Provisional                     | 2   | 0.45% | 46                | 10.41% |
| Total                           | 116                                       | 0.57% | 2934              | 14.31% |
| <b>Total</b>                    |   |       |                   |        |
| Precinct TSx                    | 0   | 0.00% | 0                 | 0.00%  |
| Precinct OS                     | 32  | 0.83% | 468               | 12.10% |
| Precinct VBM                    | 79  | 0.51% | 2316              | 14.95% |
| MB Precinct                     | 3   | 0.43% | 104               | 14.92% |
| Provisional                     | 2   | 0.45% | 46                | 10.41% |
| Total                           | 116                                       | 0.57% | 2934              | 14.31% |

**COUNTY OF EL DORADO**  
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|                          | Judge of the Superior Court Office 2 |          |               |             |                  |               |
|--------------------------|--------------------------------------|----------|---------------|-------------|------------------|---------------|
|                          | Reg. Voters                          | Vote For | Times Counted | Total Votes | NELSON K. BROOKS |               |
| <b>Jurisdiction Wide</b> |                                      |          |               |             |                  |               |
| 100001.11-CP 100001.11   |                                      |          |               |             |                  |               |
| Precinct TSx             | 1615                                 | 1        | 0             | 0           | 0                | -             |
| Precinct OS              | 1615                                 | 1        | 190           | 161         | 108              | 67.08%        |
| Precinct VBM             | 1615                                 | 1        | 781           | 673         | 382              | 56.76%        |
| MB Precinct              | 1615                                 | 1        | 0             | 0           | 0                | -             |
| Provisional              | 1615                                 | 1        | 17            | 13          | 7                | 53.85%        |
| <b>Total</b>             | <b>1615</b>                          | <b>5</b> | <b>988</b>    | <b>847</b>  | <b>497</b>       | <b>58.68%</b> |
| 100002.11-CP 100002.11   |                                      |          |               |             |                  |               |
| Precinct TSx             | 1000                                 | 1        | 0             | 0           | 0                | -             |
| Precinct OS              | 1000                                 | 1        | 89            | 68          | 44               | 64.71%        |
| Precinct VBM             | 1000                                 | 1        | 476           | 390         | 240              | 61.54%        |
| MB Precinct              | 1000                                 | 1        | 0             | 0           | 0                | -             |
| Provisional              | 1000                                 | 1        | 20            | 17          | 10               | 58.82%        |
| <b>Total</b>             | <b>1000</b>                          | <b>5</b> | <b>585</b>    | <b>475</b>  | <b>294</b>       | <b>61.89%</b> |
| 100003.11-CP 100003.11   |                                      |          |               |             |                  |               |
| Precinct TSx             | 1269                                 | 1        | 0             | 0           | 0                | -             |
| Precinct OS              | 1269                                 | 1        | 123           | 107         | 57               | 53.27%        |
| Precinct VBM             | 1269                                 | 1        | 773           | 683         | 460              | 67.35%        |
| MB Precinct              | 1269                                 | 1        | 0             | 0           | 0                | -             |
| Provisional              | 1269                                 | 1        | 24            | 21          | 8                | 38.10%        |
| <b>Total</b>             | <b>1269</b>                          | <b>5</b> | <b>920</b>    | <b>811</b>  | <b>525</b>       | <b>64.73%</b> |
| 100004.11-CP 100004.11   |                                      |          |               |             |                  |               |
| Precinct TSx             | 1633                                 | 1        | 0             | 0           | 0                | -             |
| Precinct OS              | 1633                                 | 1        | 254           | 207         | 144              | 69.57%        |
| Precinct VBM             | 1633                                 | 1        | 738           | 619         | 384              | 62.04%        |
| MB Precinct              | 1633                                 | 1        | 0             | 0           | 0                | -             |
| Provisional              | 1633                                 | 1        | 4             | 2           | 2                | 100.00%       |
| <b>Total</b>             | <b>1633</b>                          | <b>5</b> | <b>996</b>    | <b>828</b>  | <b>530</b>       | <b>64.01%</b> |
| 100005.11-CP 100005.11   |                                      |          |               |             |                  |               |
| Precinct TSx             | 1586                                 | 1        | 0             | 0           | 0                | -             |
| Precinct OS              | 1586                                 | 1        | 146           | 124         | 62               | 50.00%        |
| Precinct VBM             | 1586                                 | 1        | 895           | 767         | 405              | 52.80%        |
| MB Precinct              | 1586                                 | 1        | 0             | 0           | 0                | -             |
| Provisional              | 1586                                 | 1        | 23            | 19          | 9                | 47.37%        |
| <b>Total</b>             | <b>1586</b>                          | <b>5</b> | <b>1064</b>   | <b>910</b>  | <b>476</b>       | <b>52.31%</b> |
| 100006.11-CP 100006.11   |                                      |          |               |             |                  |               |
| Precinct TSx             | 1928                                 | 1        | 0             | 0           | 0                | -             |
| Precinct OS              | 1928                                 | 1        | 252           | 205         | 125              | 60.98%        |
| Precinct VBM             | 1928                                 | 1        | 944           | 799         | 532              | 66.58%        |
| MB Precinct              | 1928                                 | 1        | 0             | 0           | 0                | -             |
| Provisional              | 1928                                 | 1        | 31            | 19          | 9                | 47.37%        |
| <b>Total</b>             | <b>1928</b>                          | <b>5</b> | <b>1227</b>   | <b>1023</b> | <b>666</b>       | <b>65.10%</b> |
| 100008.11-CP 100008.11   |                                      |          |               |             |                  |               |
| Precinct TSx             | 1671                                 | 1        | 0             | 0           | 0                | -             |
| Precinct OS              | 1671                                 | 1        | 197           | 153         | 95               | 62.09%        |
| Precinct VBM             | 1671                                 | 1        | 740           | 631         | 381              | 60.38%        |
| MB Precinct              | 1671                                 | 1        | 0             | 0           | 0                | -             |
| Provisional              | 1671                                 | 1        | 17            | 13          | 7                | 53.85%        |
| <b>Total</b>             | <b>1671</b>                          | <b>5</b> | <b>954</b>    | <b>797</b>  | <b>483</b>       | <b>60.60%</b> |
| 100009.11-CP 100009.11   |                                      |          |               |             |                  |               |
| Precinct TSx             | 1838                                 | 1        | 0             | 0           | 0                | -             |
| Precinct OS              | 1838                                 | 1        | 169           | 145         | 86               | 59.31%        |
| Precinct VBM             | 1838                                 | 1        | 751           | 657         | 388              | 59.06%        |
| MB Precinct              | 1838                                 | 1        | 0             | 0           | 0                | -             |
| Provisional              | 1838                                 | 1        | 26            | 17          | 11               | 64.71%        |
| <b>Total</b>             | <b>1838</b>                          | <b>5</b> | <b>946</b>    | <b>819</b>  | <b>485</b>       | <b>59.22%</b> |
| 100012.11-CP 100012.11   |                                      |          |               |             |                  |               |
| Precinct TSx             | 2108                                 | 1        | 0             | 0           | 0                | -             |
| Precinct OS              | 2108                                 | 1        | 255           | 216         | 138              | 63.89%        |
| Precinct VBM             | 2108                                 | 1        | 968           | 822         | 498              | 60.58%        |



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|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | NELSON K. BROOKS |        |
| MB Precinct            | 2108                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 2108                                 | 1        | 29            | 23          | 8                | 34.78% |
| Total                  | 2108                                 | 5        | 1252          | 1061        | 644              | 60.70% |
| 100013.11-CP 100013.11 |                                      |          |               |             |                  |        |
| Precinct TSx           | 2087                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 2087                                 | 1        | 250           | 219         | 130              | 59.36% |
| Precinct VBM           | 2087                                 | 1        | 908           | 785         | 445              | 56.69% |
| MB Precinct            | 2087                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 2087                                 | 1        | 28            | 17          | 10               | 58.82% |
| Total                  | 2087                                 | 5        | 1186          | 1021        | 585              | 57.30% |
| 100014.11-CP 100014.11 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1659                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1659                                 | 1        | 211           | 172         | 85               | 49.42% |
| Precinct VBM           | 1659                                 | 1        | 706           | 626         | 372              | 59.42% |
| MB Precinct            | 1659                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1659                                 | 1        | 30            | 21          | 15               | 71.43% |
| Total                  | 1659                                 | 5        | 947           | 819         | 472              | 57.63% |
| 100022.11-CP 100022.11 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1432                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1432                                 | 1        | 177           | 152         | 76               | 50.00% |
| Precinct VBM           | 1432                                 | 1        | 678           | 577         | 346              | 59.97% |
| MB Precinct            | 1432                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1432                                 | 1        | 13            | 10          | 5                | 50.00% |
| Total                  | 1432                                 | 5        | 868           | 739         | 427              | 57.78% |
| 100023.11-CP 100023.11 |                                      |          |               |             |                  |        |
| Precinct TSx           | 2135                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 2135                                 | 1        | 217           | 186         | 92               | 49.46% |
| Precinct VBM           | 2135                                 | 1        | 1053          | 913         | 505              | 55.31% |
| MB Precinct            | 2135                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 2135                                 | 1        | 33            | 26          | 17               | 65.38% |
| Total                  | 2135                                 | 5        | 1303          | 1125        | 614              | 54.58% |
| 200003.12-CP 200003.12 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1362                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1362                                 | 1        | 166           | 139         | 76               | 54.68% |
| Precinct VBM           | 1362                                 | 1        | 558           | 480         | 305              | 63.54% |
| MB Precinct            | 1362                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1362                                 | 1        | 31            | 26          | 15               | 57.69% |
| Total                  | 1362                                 | 5        | 755           | 645         | 396              | 61.40% |
| 200004.12-CP 200004.12 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1507                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1507                                 | 1        | 182           | 159         | 80               | 50.31% |
| Precinct VBM           | 1507                                 | 1        | 662           | 573         | 322              | 56.20% |
| MB Precinct            | 1507                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1507                                 | 1        | 14            | 10          | 4                | 40.00% |
| Total                  | 1507                                 | 5        | 858           | 742         | 406              | 54.72% |
| 200005.02-CP 200005.02 |                                      |          |               |             |                  |        |
| Precinct TSx           | 867                                  | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 867                                  | 1        | 94            | 88          | 52               | 59.09% |
| Precinct VBM           | 867                                  | 1        | 452           | 394         | 214              | 54.31% |
| MB Precinct            | 867                                  | 1        | 0             | 0           | 0                | -      |
| Provisional            | 867                                  | 1        | 21            | 19          | 12               | 63.16% |
| Total                  | 867                                  | 5        | 567           | 501         | 278              | 55.49% |
| 200006.12-CP 200006.12 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1086                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1086                                 | 1        | 117           | 108         | 54               | 50.00% |
| Precinct VBM           | 1086                                 | 1        | 532           | 469         | 275              | 58.64% |
| MB Precinct            | 1086                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1086                                 | 1        | 29            | 25          | 16               | 64.00% |
| Total                  | 1086                                 | 5        | 678           | 602         | 345              | 57.31% |
| 200008.12-CP 200008.12 |                                      |          |               |             |                  |        |
| Precinct TSx           | 999                                  | 1        | 0             | 0           | 0                | -      |

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|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | NELSON K. BROOKS |         |
| Precinct OS            | 999                                  | 1        | 155           | 134         | 63               | 47.01%  |
| Precinct VBM           | 999                                  | 1        | 379           | 312         | 197              | 63.14%  |
| MB Precinct            | 999                                  | 1        | 0             | 0           | 0                | -       |
| Provisional            | 999                                  | 1        | 10            | 8           | 5                | 62.50%  |
| Total                  | 999                                  | 5        | 544           | 454         | 265              | 58.37%  |
| 200010.12-CP 200010.12 |                                      |          |               |             |                  |         |
| Precinct TSx           | 1976                                 | 1        | 4             | 3           | 3                | 100.00% |
| Precinct OS            | 1976                                 | 1        | 197           | 159         | 98               | 61.64%  |
| Precinct VBM           | 1976                                 | 1        | 859           | 721         | 406              | 56.31%  |
| MB Precinct            | 1976                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            | 1976                                 | 1        | 34            | 26          | 15               | 57.69%  |
| Total                  | 1976                                 | 5        | 1094          | 909         | 522              | 57.43%  |
| 200012.12-CP 200012.12 |                                      |          |               |             |                  |         |
| Precinct TSx           | 1064                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 1064                                 | 1        | 145           | 127         | 78               | 61.42%  |
| Precinct VBM           | 1064                                 | 1        | 464           | 398         | 218              | 54.77%  |
| MB Precinct            | 1064                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            | 1064                                 | 1        | 14            | 12          | 7                | 58.33%  |
| Total                  | 1064                                 | 5        | 623           | 537         | 303              | 56.42%  |
| 200013.12-CP 200013.12 |                                      |          |               |             |                  |         |
| Precinct TSx           | 1574                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 1574                                 | 1        | 211           | 186         | 99               | 53.23%  |
| Precinct VBM           | 1574                                 | 1        | 701           | 618         | 387              | 62.62%  |
| MB Precinct            | 1574                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            | 1574                                 | 1        | 23            | 19          | 9                | 47.37%  |
| Total                  | 1574                                 | 5        | 935           | 823         | 495              | 60.15%  |
| 200014.12-CP 200014.12 |                                      |          |               |             |                  |         |
| Precinct TSx           | 1438                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 1438                                 | 1        | 201           | 174         | 93               | 53.45%  |
| Precinct VBM           | 1438                                 | 1        | 613           | 533         | 316              | 59.29%  |
| MB Precinct            | 1438                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            | 1438                                 | 1        | 30            | 27          | 16               | 59.26%  |
| Total                  | 1438                                 | 5        | 844           | 734         | 425              | 57.90%  |
| 200015.02-CP 200015.02 |                                      |          |               |             |                  |         |
| Precinct TSx           | 1832                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 1832                                 | 1        | 218           | 187         | 83               | 44.39%  |
| Precinct VBM           | 1832                                 | 1        | 886           | 760         | 416              | 54.74%  |
| MB Precinct            | 1832                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            | 1832                                 | 1        | 22            | 21          | 11               | 52.38%  |
| Total                  | 1832                                 | 5        | 1126          | 968         | 510              | 52.69%  |
| 200016.12-CP 200016.12 |                                      |          |               |             |                  |         |
| Precinct TSx           | 301                                  | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 301                                  | 1        | 26            | 21          | 11               | 52.38%  |
| Precinct VBM           | 301                                  | 1        | 139           | 120         | 74               | 61.67%  |
| MB Precinct            | 301                                  | 1        | 0             | 0           | 0                | -       |
| Provisional            | 301                                  | 1        | 6             | 5           | 1                | 20.00%  |
| Total                  | 301                                  | 5        | 171           | 146         | 86               | 58.90%  |
| 200016.13-CP 200016.13 |                                      |          |               |             |                  |         |
| Precinct TSx           | 565                                  | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 565                                  | 1        | 80            | 68          | 29               | 42.65%  |
| Precinct VBM           | 565                                  | 1        | 338           | 288         | 177              | 61.46%  |
| MB Precinct            | 565                                  | 1        | 0             | 0           | 0                | -       |
| Provisional            | 565                                  | 1        | 6             | 3           | 1                | 33.33%  |
| Total                  | 565                                  | 5        | 424           | 359         | 207              | 57.66%  |
| 200017.02-CP 200017.02 |                                      |          |               |             |                  |         |
| Precinct TSx           | 1286                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 1286                                 | 1        | 146           | 126         | 70               | 55.56%  |
| Precinct VBM           | 1286                                 | 1        | 594           | 518         | 289              | 55.79%  |
| MB Precinct            | 1286                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            | 1286                                 | 1        | 12            | 10          | 5                | 50.00%  |
| Total                  | 1286                                 | 5        | 752           | 654         | 364              | 55.66%  |

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|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | NELSON K. BROOKS |        |
| 200018.02-CP 200018.02 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1454                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1454                                 | 1        | 169           | 151         | 76               | 50.33% |
| Precinct VBM           | 1454                                 | 1        | 704           | 616         | 322              | 52.27% |
| MB Precinct            | 1454                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1454                                 | 1        | 13            | 9           | 5                | 55.56% |
| Total                  | 1454                                 | 5        | 886           | 776         | 403              | 51.93% |
| 200020.02-CP 200020.02 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1213                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1213                                 | 1        | 139           | 122         | 69               | 56.56% |
| Precinct VBM           | 1213                                 | 1        | 526           | 476         | 265              | 55.67% |
| MB Precinct            | 1213                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1213                                 | 1        | 19            | 16          | 9                | 56.25% |
| Total                  | 1213                                 | 5        | 684           | 614         | 343              | 55.86% |
| 200021.01-CP 200021.01 |                                      |          |               |             |                  |        |
| Precinct TSx           | 436                                  | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 436                                  | 1        | 38            | 28          | 16               | 57.14% |
| Precinct VBM           | 436                                  | 1        | 206           | 176         | 84               | 47.73% |
| MB Precinct            | 436                                  | 1        | 0             | 0           | 0                | -      |
| Provisional            | 436                                  | 1        | 7             | 4           | 1                | 25.00% |
| Total                  | 436                                  | 5        | 251           | 208         | 101              | 48.56% |
| 200021.02-CP 200021.02 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1296                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1296                                 | 1        | 170           | 144         | 71               | 49.31% |
| Precinct VBM           | 1296                                 | 1        | 752           | 630         | 361              | 57.30% |
| MB Precinct            | 1296                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1296                                 | 1        | 20            | 13          | 7                | 53.85% |
| Total                  | 1296                                 | 5        | 942           | 787         | 439              | 55.78% |
| 200024.02-CP 200024.02 |                                      |          |               |             |                  |        |
| Precinct TSx           | 555                                  | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 555                                  | 1        | 55            | 46          | 18               | 39.13% |
| Precinct VBM           | 555                                  | 1        | 282           | 251         | 125              | 49.80% |
| MB Precinct            | 555                                  | 1        | 0             | 0           | 0                | -      |
| Provisional            | 555                                  | 1        | 9             | 5           | 2                | 40.00% |
| Total                  | 555                                  | 5        | 346           | 302         | 145              | 48.01% |
| 300001.04-CP 300001.04 |                                      |          |               |             |                  |        |
| Precinct TSx           | 259                                  | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 259                                  | 1        | 12            | 9           | 7                | 77.78% |
| Precinct VBM           | 259                                  | 1        | 100           | 93          | 52               | 55.91% |
| MB Precinct            | 259                                  | 1        | 0             | 0           | 0                | -      |
| Provisional            | 259                                  | 1        | 0             | 0           | 0                | -      |
| Total                  | 259                                  | 5        | 112           | 102         | 59               | 57.84% |
| 300001.05-CP 300001.05 |                                      |          |               |             |                  |        |
| Precinct TSx           | 731                                  | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 731                                  | 1        | 76            | 66          | 31               | 46.97% |
| Precinct VBM           | 731                                  | 1        | 330           | 288         | 184              | 63.89% |
| MB Precinct            | 731                                  | 1        | 0             | 0           | 0                | -      |
| Provisional            | 731                                  | 1        | 10            | 9           | 7                | 77.78% |
| Total                  | 731                                  | 5        | 416           | 363         | 222              | 61.16% |
| 300003.05-CP 300003.05 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1932                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1932                                 | 1        | 205           | 168         | 104              | 61.90% |
| Precinct VBM           | 1932                                 | 1        | 924           | 813         | 533              | 65.56% |
| MB Precinct            | 1932                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1932                                 | 1        | 27            | 23          | 12               | 52.17% |
| Total                  | 1932                                 | 5        | 1156          | 1004        | 649              | 64.64% |
| 300004.05-CP 300004.05 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1273                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1273                                 | 1        | 175           | 145         | 82               | 56.55% |
| Precinct VBM           | 1273                                 | 1        | 576           | 497         | 310              | 62.37% |
| MB Precinct            | 1273                                 | 1        | 0             | 0           | 0                | -      |

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|                        |  | Reg. Voters                          | Vote For | Times Counted | Total Votes | NELSON K. BROOKS |         |
| Provisional            |  | 1273                                 | 1        | 17            | 14          | 9                | 64.29%  |
| Total                  |  | 1273                                 | 5        | 768           | 656         | 401              | 61.13%  |
| 300005.05-CP 300005.05 |  |                                      |          |               |             |                  |         |
| Precinct TSx           |  | 1374                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            |  | 1374                                 | 1        | 163           | 134         | 80               | 59.70%  |
| Precinct VBM           |  | 1374                                 | 1        | 660           | 594         | 374              | 62.96%  |
| MB Precinct            |  | 1374                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            |  | 1374                                 | 1        | 26            | 19          | 10               | 52.63%  |
| Total                  |  | 1374                                 | 5        | 849           | 747         | 464              | 62.12%  |
| 300008.05-CP 300008.05 |  |                                      |          |               |             |                  |         |
| Precinct TSx           |  | 1366                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            |  | 1366                                 | 1        | 139           | 125         | 66               | 52.80%  |
| Precinct VBM           |  | 1366                                 | 1        | 740           | 646         | 369              | 57.12%  |
| MB Precinct            |  | 1366                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            |  | 1366                                 | 1        | 14            | 10          | 7                | 70.00%  |
| Total                  |  | 1366                                 | 5        | 893           | 781         | 442              | 56.59%  |
| 300009.04-CP 300009.04 |  |                                      |          |               |             |                  |         |
| Precinct TSx           |  | 873                                  | 1        | 0             | 0           | 0                | -       |
| Precinct OS            |  | 873                                  | 1        | 121           | 107         | 51               | 47.66%  |
| Precinct VBM           |  | 873                                  | 1        | 395           | 341         | 171              | 50.15%  |
| MB Precinct            |  | 873                                  | 1        | 0             | 0           | 0                | -       |
| Provisional            |  | 873                                  | 1        | 13            | 8           | 3                | 37.50%  |
| Total                  |  | 873                                  | 5        | 529           | 456         | 225              | 49.34%  |
| 300010.04-CP 300010.04 |  |                                      |          |               |             |                  |         |
| Precinct TSx           |  | 1340                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            |  | 1340                                 | 1        | 171           | 146         | 70               | 47.95%  |
| Precinct VBM           |  | 1340                                 | 1        | 656           | 584         | 336              | 57.53%  |
| MB Precinct            |  | 1340                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            |  | 1340                                 | 1        | 11            | 10          | 3                | 30.00%  |
| Total                  |  | 1340                                 | 5        | 838           | 740         | 409              | 55.27%  |
| 300011.04-CP 300011.04 |  |                                      |          |               |             |                  |         |
| Precinct TSx           |  | 1491                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            |  | 1491                                 | 1        | 142           | 121         | 64               | 52.89%  |
| Precinct VBM           |  | 1491                                 | 1        | 711           | 620         | 338              | 54.52%  |
| MB Precinct            |  | 1491                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            |  | 1491                                 | 1        | 22            | 16          | 6                | 37.50%  |
| Total                  |  | 1491                                 | 5        | 875           | 757         | 408              | 53.90%  |
| 300013.05-CP 300013.05 |  |                                      |          |               |             |                  |         |
| Precinct TSx           |  | 1490                                 | 1        | 3             | 2           | 2                | 100.00% |
| Precinct OS            |  | 1490                                 | 1        | 128           | 114         | 54               | 47.37%  |
| Precinct VBM           |  | 1490                                 | 1        | 721           | 640         | 358              | 55.94%  |
| MB Precinct            |  | 1490                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            |  | 1490                                 | 1        | 15            | 11          | 2                | 18.18%  |
| Total                  |  | 1490                                 | 5        | 867           | 767         | 416              | 54.24%  |
| 300015.04-CP 300015.04 |  |                                      |          |               |             |                  |         |
| Precinct TSx           |  | 119                                  | 1        | 0             | 0           | 0                | -       |
| Precinct OS            |  | 119                                  | 1        | 10            | 9           | 5                | 55.56%  |
| Precinct VBM           |  | 119                                  | 1        | 63            | 50          | 37               | 74.00%  |
| MB Precinct            |  | 119                                  | 1        | 0             | 0           | 0                | -       |
| Provisional            |  | 119                                  | 1        | 4             | 4           | 3                | 75.00%  |
| Total                  |  | 119                                  | 5        | 77            | 63          | 45               | 71.43%  |
| 300015.05-CP 300015.05 |  |                                      |          |               |             |                  |         |
| Precinct TSx           |  | 503                                  | 1        | 0             | 0           | 0                | -       |
| Precinct OS            |  | 503                                  | 1        | 66            | 56          | 33               | 58.93%  |
| Precinct VBM           |  | 503                                  | 1        | 266           | 224         | 141              | 62.95%  |
| MB Precinct            |  | 503                                  | 1        | 0             | 0           | 0                | -       |
| Provisional            |  | 503                                  | 1        | 2             | 1           | 1                | 100.00% |
| Total                  |  | 503                                  | 5        | 334           | 281         | 175              | 62.28%  |
| 300017.05-CP 300017.05 |  |                                      |          |               |             |                  |         |
| Precinct TSx           |  | 1240                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            |  | 1240                                 | 1        | 117           | 110         | 62               | 56.36%  |

**COUNTY OF EL DORADO**  
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|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | NELSON K. BROOKS |        |
| Precinct VBM           | 1240                                 | 1        | 655           | 592         | 352              | 59.46% |
| MB Precinct            | 1240                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1240                                 | 1        | 9             | 8           | 3                | 37.50% |
| Total                  | 1240                                 | 5        | 781           | 710         | 417              | 58.73% |
| 300018.04-CP 300018.04 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1306                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1306                                 | 1        | 148           | 130         | 61               | 46.92% |
| Precinct VBM           | 1306                                 | 1        | 505           | 447         | 240              | 53.69% |
| MB Precinct            | 1306                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1306                                 | 1        | 31            | 23          | 13               | 56.52% |
| Total                  | 1306                                 | 5        | 684           | 600         | 314              | 52.33% |
| 300020.04-CP 300020.04 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1401                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1401                                 | 1        | 168           | 145         | 73               | 50.34% |
| Precinct VBM           | 1401                                 | 1        | 632           | 552         | 329              | 59.60% |
| MB Precinct            | 1401                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1401                                 | 1        | 14            | 8           | 5                | 62.50% |
| Total                  | 1401                                 | 5        | 814           | 705         | 407              | 57.73% |
| 300021.03-CP 300021.03 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1234                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1234                                 | 1        | 153           | 128         | 75               | 58.59% |
| Precinct VBM           | 1234                                 | 1        | 614           | 525         | 330              | 62.86% |
| MB Precinct            | 1234                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1234                                 | 1        | 13            | 10          | 8                | 80.00% |
| Total                  | 1234                                 | 5        | 780           | 663         | 413              | 62.29% |
| 300022.05-CP 300022.05 |                                      |          |               |             |                  |        |
| Precinct TSx           | 872                                  | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 872                                  | 1        | 101           | 88          | 40               | 45.45% |
| Precinct VBM           | 872                                  | 1        | 356           | 308         | 171              | 55.52% |
| MB Precinct            | 872                                  | 1        | 0             | 0           | 0                | -      |
| Provisional            | 872                                  | 1        | 12            | 9           | 4                | 44.44% |
| Total                  | 872                                  | 5        | 469           | 405         | 215              | 53.09% |
| 300023.03-CP 300023.03 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1819                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1819                                 | 1        | 228           | 192         | 123              | 64.06% |
| Precinct VBM           | 1819                                 | 1        | 924           | 820         | 518              | 63.17% |
| MB Precinct            | 1819                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1819                                 | 1        | 26            | 18          | 7                | 38.89% |
| Total                  | 1819                                 | 5        | 1178          | 1030        | 648              | 62.91% |
| 300023.05-CP 300023.05 |                                      |          |               |             |                  |        |
| Precinct TSx           | 386                                  | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 386                                  | 1        | 45            | 38          | 21               | 55.26% |
| Precinct VBM           | 386                                  | 1        | 193           | 171         | 107              | 62.57% |
| MB Precinct            | 386                                  | 1        | 0             | 0           | 0                | -      |
| Provisional            | 386                                  | 1        | 8             | 7           | 3                | 42.86% |
| Total                  | 386                                  | 5        | 246           | 216         | 131              | 60.65% |
| 400001.14-CP 400001.14 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1957                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1957                                 | 1        | 178           | 153         | 83               | 54.25% |
| Precinct VBM           | 1957                                 | 1        | 992           | 874         | 515              | 58.92% |
| MB Precinct            | 1957                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1957                                 | 1        | 23            | 20          | 9                | 45.00% |
| Total                  | 1957                                 | 5        | 1193          | 1047        | 607              | 57.98% |
| 400002.08-CP 400002.08 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1508                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1508                                 | 1        | 200           | 170         | 80               | 47.06% |
| Precinct VBM           | 1508                                 | 1        | 730           | 620         | 312              | 50.32% |
| MB Precinct            | 1508                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1508                                 | 1        | 15            | 13          | 6                | 46.15% |
| Total                  | 1508                                 | 5        | 945           | 803         | 398              | 49.56% |
| 400003.08-CP 400003.08 |                                      |          |               |             |                  |        |

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|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | NELSON K. BROOKS |        |
| Precinct TSx           | 1800                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1800                                 | 1        | 245           | 193         | 87               | 45.08% |
| Precinct VBM           | 1800                                 | 1        | 773           | 649         | 350              | 53.93% |
| MB Precinct            | 1800                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1800                                 | 1        | 35            | 29          | 16               | 55.17% |
| Total                  | 1800                                 | 5        | 1053          | 871         | 453              | 52.01% |
| 400004.14-CP 400004.14 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1536                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1536                                 | 1        | 194           | 160         | 85               | 53.13% |
| Precinct VBM           | 1536                                 | 1        | 662           | 575         | 316              | 54.96% |
| MB Precinct            | 1536                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1536                                 | 1        | 19            | 16          | 7                | 43.75% |
| Total                  | 1536                                 | 5        | 875           | 751         | 408              | 54.33% |
| 400005.08-CP 400005.08 |                                      |          |               |             |                  |        |
| Precinct TSx           | 655                                  | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 655                                  | 1        | 79            | 63          | 35               | 55.56% |
| Precinct VBM           | 655                                  | 1        | 262           | 225         | 124              | 55.11% |
| MB Precinct            | 655                                  | 1        | 0             | 0           | 0                | -      |
| Provisional            | 655                                  | 1        | 11            | 8           | 3                | 37.50% |
| Total                  | 655                                  | 5        | 352           | 296         | 162              | 54.73% |
| 400006.08-CP 400006.08 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1537                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1537                                 | 1        | 228           | 186         | 97               | 52.15% |
| Precinct VBM           | 1537                                 | 1        | 722           | 607         | 351              | 57.83% |
| MB Precinct            | 1537                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1537                                 | 1        | 19            | 13          | 10               | 76.92% |
| Total                  | 1537                                 | 5        | 969           | 806         | 458              | 56.82% |
| 400007.08-CP 400007.08 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1766                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1766                                 | 1        | 228           | 202         | 90               | 44.55% |
| Precinct VBM           | 1766                                 | 1        | 765           | 667         | 328              | 49.18% |
| MB Precinct            | 1766                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1766                                 | 1        | 22            | 20          | 9                | 45.00% |
| Total                  | 1766                                 | 5        | 1015          | 889         | 427              | 48.03% |
| 400010.08-CP 400010.08 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1875                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1875                                 | 1        | 244           | 202         | 96               | 47.52% |
| Precinct VBM           | 1875                                 | 1        | 869           | 764         | 442              | 57.85% |
| MB Precinct            | 1875                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1875                                 | 1        | 31            | 22          | 12               | 54.55% |
| Total                  | 1875                                 | 5        | 1144          | 988         | 550              | 55.67% |
| 400012.08-CP 400012.08 |                                      |          |               |             |                  |        |
| Precinct TSx           | 877                                  | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 877                                  | 1        | 113           | 106         | 60               | 56.60% |
| Precinct VBM           | 877                                  | 1        | 434           | 378         | 221              | 58.47% |
| MB Precinct            | 877                                  | 1        | 0             | 0           | 0                | -      |
| Provisional            | 877                                  | 1        | 22            | 17          | 11               | 64.71% |
| Total                  | 877                                  | 5        | 569           | 501         | 292              | 58.28% |
| 400013.08-CP 400013.08 |                                      |          |               |             |                  |        |
| Precinct TSx           | 758                                  | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 758                                  | 1        | 127           | 103         | 43               | 41.75% |
| Precinct VBM           | 758                                  | 1        | 315           | 273         | 159              | 58.24% |
| MB Precinct            | 758                                  | 1        | 0             | 0           | 0                | -      |
| Provisional            | 758                                  | 1        | 4             | 2           | 1                | 50.00% |
| Total                  | 758                                  | 5        | 446           | 378         | 203              | 53.70% |
| 400014.08-CP 400014.08 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1526                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1526                                 | 1        | 230           | 184         | 95               | 51.63% |
| Precinct VBM           | 1526                                 | 1        | 736           | 638         | 373              | 58.46% |
| MB Precinct            | 1526                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1526                                 | 1        | 36            | 31          | 24               | 77.42% |

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|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | NELSON K. BROOKS |        |
| Total                  | 1526                                 | 5        | 1002          | 853         | 492              | 57.68% |
| 400017.08-CP 400017.08 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1495                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1495                                 | 1        | 175           | 149         | 66               | 44.30% |
| Precinct VBM           | 1495                                 | 1        | 752           | 671         | 344              | 51.27% |
| MB Precinct            | 1495                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1495                                 | 1        | 28            | 26          | 8                | 30.77% |
| Total                  | 1495                                 | 5        | 955           | 846         | 418              | 49.41% |
| 400018.08-CP 400018.08 |                                      |          |               |             |                  |        |
| Precinct TSx           | 597                                  | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 597                                  | 1        | 86            | 73          | 40               | 54.79% |
| Precinct VBM           | 597                                  | 1        | 276           | 237         | 106              | 44.73% |
| MB Precinct            | 597                                  | 1        | 0             | 0           | 0                | -      |
| Provisional            | 597                                  | 1        | 7             | 4           | 2                | 50.00% |
| Total                  | 597                                  | 5        | 369           | 314         | 148              | 47.13% |
| 400020.08-CP 400020.08 |                                      |          |               |             |                  |        |
| Precinct TSx           | 724                                  | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 724                                  | 1        | 114           | 100         | 52               | 52.00% |
| Precinct VBM           | 724                                  | 1        | 343           | 291         | 162              | 55.67% |
| MB Precinct            | 724                                  | 1        | 0             | 0           | 0                | -      |
| Provisional            | 724                                  | 1        | 2             | 2           | 1                | 50.00% |
| Total                  | 724                                  | 5        | 459           | 393         | 215              | 54.71% |
| 400021.07-CP 400021.07 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1332                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1332                                 | 1        | 127           | 114         | 62               | 54.39% |
| Precinct VBM           | 1332                                 | 1        | 767           | 677         | 388              | 57.31% |
| MB Precinct            | 1332                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1332                                 | 1        | 18            | 13          | 7                | 53.85% |
| Total                  | 1332                                 | 5        | 912           | 804         | 457              | 56.84% |
| 500001.10-CP 500001.10 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1717                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1717                                 | 1        | 210           | 168         | 110              | 65.48% |
| Precinct VBM           | 1717                                 | 1        | 617           | 510         | 367              | 71.96% |
| MB Precinct            | 1717                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1717                                 | 1        | 19            | 13          | 11               | 84.62% |
| Total                  | 1717                                 | 5        | 846           | 691         | 488              | 70.62% |
| 500002.10-CP 500002.10 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1381                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1381                                 | 1        | 137           | 106         | 70               | 66.04% |
| Precinct VBM           | 1381                                 | 1        | 412           | 345         | 245              | 71.01% |
| MB Precinct            | 1381                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1381                                 | 1        | 31            | 23          | 12               | 52.17% |
| Total                  | 1381                                 | 5        | 580           | 474         | 327              | 68.99% |
| 500003.10-CP 500003.10 |                                      |          |               |             |                  |        |
| Precinct TSx           | 1065                                 | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 1065                                 | 1        | 131           | 98          | 68               | 69.39% |
| Precinct VBM           | 1065                                 | 1        | 346           | 288         | 220              | 76.39% |
| MB Precinct            | 1065                                 | 1        | 0             | 0           | 0                | -      |
| Provisional            | 1065                                 | 1        | 11            | 4           | 1                | 25.00% |
| Total                  | 1065                                 | 5        | 488           | 390         | 289              | 74.10% |
| 500004.10-CP 500004.10 |                                      |          |               |             |                  |        |
| Precinct TSx           | 981                                  | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 981                                  | 1        | 122           | 103         | 60               | 58.25% |
| Precinct VBM           | 981                                  | 1        | 264           | 208         | 117              | 56.25% |
| MB Precinct            | 981                                  | 1        | 0             | 0           | 0                | -      |
| Provisional            | 981                                  | 1        | 21            | 16          | 11               | 68.75% |
| Total                  | 981                                  | 5        | 407           | 327         | 188              | 57.49% |
| 500005.10-CP 500005.10 |                                      |          |               |             |                  |        |
| Precinct TSx           | 946                                  | 1        | 0             | 0           | 0                | -      |
| Precinct OS            | 946                                  | 1        | 107           | 90          | 64               | 71.11% |
| Precinct VBM           | 946                                  | 1        | 250           | 217         | 151              | 69.59% |

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|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | NELSON K. BROOKS |         |
| MB Precinct            | 946                                  | 1        | 0             | 0           | 0                | -       |
| Provisional            | 946                                  | 1        | 16            | 11          | 7                | 63.64%  |
| Total                  | 946                                  | 5        | 373           | 318         | 222              | 69.81%  |
| 500006.10-CP 500006.10 |                                      |          |               |             |                  |         |
| Precinct TSx           | 1661                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 1661                                 | 1        | 190           | 147         | 83               | 56.46%  |
| Precinct VBM           | 1661                                 | 1        | 498           | 412         | 291              | 70.63%  |
| MB Precinct            | 1661                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            | 1661                                 | 1        | 58            | 40          | 24               | 60.00%  |
| Total                  | 1661                                 | 5        | 746           | 599         | 398              | 66.44%  |
| 500007.10-CP 500007.10 |                                      |          |               |             |                  |         |
| Precinct TSx           | 1788                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 1788                                 | 1        | 208           | 172         | 98               | 56.98%  |
| Precinct VBM           | 1788                                 | 1        | 688           | 594         | 407              | 68.52%  |
| MB Precinct            | 1788                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            | 1788                                 | 1        | 20            | 16          | 10               | 62.50%  |
| Total                  | 1788                                 | 5        | 916           | 782         | 515              | 65.86%  |
| 500009.10-CP 500009.10 |                                      |          |               |             |                  |         |
| Precinct TSx           | 1073                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 1073                                 | 1        | 121           | 98          | 61               | 62.24%  |
| Precinct VBM           | 1073                                 | 1        | 304           | 247         | 179              | 72.47%  |
| MB Precinct            | 1073                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            | 1073                                 | 1        | 38            | 22          | 12               | 54.55%  |
| Total                  | 1073                                 | 5        | 463           | 367         | 252              | 68.66%  |
| 500011.10-CP 500011.10 |                                      |          |               |             |                  |         |
| Precinct TSx           | 1635                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 1635                                 | 1        | 178           | 143         | 91               | 63.64%  |
| Precinct VBM           | 1635                                 | 1        | 380           | 331         | 219              | 66.16%  |
| MB Precinct            | 1635                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            | 1635                                 | 1        | 40            | 29          | 18               | 62.07%  |
| Total                  | 1635                                 | 5        | 598           | 503         | 328              | 65.21%  |
| 500012.10-CP 500012.10 |                                      |          |               |             |                  |         |
| Precinct TSx           | 1574                                 | 1        | 1             | 1           | 1                | 100.00% |
| Precinct OS            | 1574                                 | 1        | 195           | 155         | 89               | 57.42%  |
| Precinct VBM           | 1574                                 | 1        | 601           | 513         | 345              | 67.25%  |
| MB Precinct            | 1574                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            | 1574                                 | 1        | 37            | 25          | 18               | 72.00%  |
| Total                  | 1574                                 | 5        | 834           | 694         | 453              | 65.27%  |
| 500015.10-CP 500015.10 |                                      |          |               |             |                  |         |
| Precinct TSx           | 978                                  | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 978                                  | 1        | 105           | 83          | 59               | 71.08%  |
| Precinct VBM           | 978                                  | 1        | 407           | 341         | 248              | 72.73%  |
| MB Precinct            | 978                                  | 1        | 0             | 0           | 0                | -       |
| Provisional            | 978                                  | 1        | 13            | 10          | 6                | 60.00%  |
| Total                  | 978                                  | 5        | 525           | 434         | 313              | 72.12%  |
| 500020.10-CP 500020.10 |                                      |          |               |             |                  |         |
| Precinct TSx           | 1530                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 1530                                 | 1        | 161           | 135         | 75               | 55.56%  |
| Precinct VBM           | 1530                                 | 1        | 659           | 582         | 330              | 56.70%  |
| MB Precinct            | 1530                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            | 1530                                 | 1        | 27            | 26          | 17               | 65.38%  |
| Total                  | 1530                                 | 5        | 847           | 743         | 422              | 56.80%  |
| 500021.10-CP 500021.10 |                                      |          |               |             |                  |         |
| Precinct TSx           | 1404                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 1404                                 | 1        | 169           | 146         | 68               | 46.58%  |
| Precinct VBM           | 1404                                 | 1        | 632           | 571         | 336              | 58.84%  |
| MB Precinct            | 1404                                 | 1        | 0             | 0           | 0                | -       |
| Provisional            | 1404                                 | 1        | 28            | 22          | 15               | 68.18%  |
| Total                  | 1404                                 | 5        | 829           | 739         | 419              | 56.70%  |
| 95U201.01-MB 95U201.01 |                                      |          |               |             |                  |         |
| Precinct TSx           | 8                                    | 1        | 0             | 0           | 0                | -       |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | Judge of the Superior Court Office 2 |          |               |             |                  |         |   |
|------------------------|--------------------------------------|----------|---------------|-------------|------------------|---------|---|
|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | NELSON K. BROOKS |         |   |
| Precinct OS            | 8                                    | 1        | 0             | 0           | 0                | -       | - |
| Precinct VBM           | 8                                    | 1        | 0             | 0           | 0                | -       | - |
| MB Precinct            | 8                                    | 1        | 7             | 6           | 2                | 33.33%  | - |
| Provisional            | 8                                    | 1        | 0             | 0           | 0                | -       | - |
| Total                  | 8                                    | 5        | 7             | 6           | 2                | 33.33%  | - |
| 95U201.02-MB 95U201.02 |                                      |          |               |             |                  |         |   |
| Precinct TSx           | 541                                  | 1        | 0             | 0           | 0                | -       | - |
| Precinct OS            | 541                                  | 1        | 0             | 0           | 0                | -       | - |
| Precinct VBM           | 541                                  | 1        | 0             | 0           | 0                | -       | - |
| MB Precinct            | 541                                  | 1        | 358           | 307         | 152              | 49.51%  | - |
| Provisional            | 541                                  | 1        | 1             | 1           | 0                | 0.00%   | - |
| Total                  | 541                                  | 5        | 359           | 308         | 152              | 49.35%  | - |
| 95U202.02-MB 95U202.02 |                                      |          |               |             |                  |         |   |
| Precinct TSx           | 859                                  | 1        | 0             | 0           | 0                | -       | - |
| Precinct OS            | 859                                  | 1        | 0             | 0           | 0                | -       | - |
| Precinct VBM           | 859                                  | 1        | 0             | 0           | 0                | -       | - |
| MB Precinct            | 859                                  | 1        | 574           | 500         | 268              | 53.60%  | - |
| Provisional            | 859                                  | 1        | 4             | 3           | 3                | 100.00% | - |
| Total                  | 859                                  | 5        | 578           | 503         | 271              | 53.88%  | - |
| 95U203.02-MB 95U203.02 |                                      |          |               |             |                  |         |   |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0                | -       | - |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0                | -       | - |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0                | -       | - |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0                | -       | - |
| Provisional            | 0                                    | 1        | 0             | 0           | 0                | -       | - |
| Total                  | 0                                    | 5        | 0             | 0           | 0                | -       | - |
| 95U204.01-MB 95U204.01 |                                      |          |               |             |                  |         |   |
| Precinct TSx           | 895                                  | 1        | 0             | 0           | 0                | -       | - |
| Precinct OS            | 895                                  | 1        | 0             | 0           | 0                | -       | - |
| Precinct VBM           | 895                                  | 1        | 0             | 0           | 0                | -       | - |
| MB Precinct            | 895                                  | 1        | 651           | 584         | 323              | 55.31%  | - |
| Provisional            | 895                                  | 1        | 2             | 2           | 1                | 50.00%  | - |
| Total                  | 895                                  | 5        | 653           | 586         | 324              | 55.29%  | - |
| 95U301.05-MB 95U301.05 |                                      |          |               |             |                  |         |   |
| Precinct TSx           | 244                                  | 1        | 0             | 0           | 0                | -       | - |
| Precinct OS            | 244                                  | 1        | 0             | 0           | 0                | -       | - |
| Precinct VBM           | 244                                  | 1        | 0             | 0           | 0                | -       | - |
| MB Precinct            | 244                                  | 1        | 151           | 131         | 73               | 55.73%  | - |
| Provisional            | 244                                  | 1        | 2             | 2           | 0                | 0.00%   | - |
| Total                  | 244                                  | 5        | 153           | 133         | 73               | 54.89%  | - |
| 95U302.04-MB 95U302.04 |                                      |          |               |             |                  |         |   |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0                | -       | - |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0                | -       | - |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0                | -       | - |
| MB Precinct            | 0                                    | 1        | 1             | 1           | 1                | 100.00% | - |
| Provisional            | 0                                    | 1        | 0             | 0           | 0                | -       | - |
| Total                  | 0                                    | 5        | 1             | 1           | 1                | 100.00% | - |
| 95U302.05-MB 95U302.05 |                                      |          |               |             |                  |         |   |
| Precinct TSx           | 128                                  | 1        | 0             | 0           | 0                | -       | - |
| Precinct OS            | 128                                  | 1        | 0             | 0           | 0                | -       | - |
| Precinct VBM           | 128                                  | 1        | 0             | 0           | 0                | -       | - |
| MB Precinct            | 128                                  | 1        | 86            | 74          | 48               | 64.86%  | - |
| Provisional            | 128                                  | 1        | 0             | 0           | 0                | -       | - |
| Total                  | 128                                  | 5        | 86            | 74          | 48               | 64.86%  | - |
| 95U303.03-MB 95U303.03 |                                      |          |               |             |                  |         |   |
| Precinct TSx           | 144                                  | 1        | 0             | 0           | 0                | -       | - |
| Precinct OS            | 144                                  | 1        | 0             | 0           | 0                | -       | - |
| Precinct VBM           | 144                                  | 1        | 0             | 0           | 0                | -       | - |
| MB Precinct            | 144                                  | 1        | 84            | 76          | 35               | 46.05%  | - |
| Provisional            | 144                                  | 1        | 0             | 0           | 0                | -       | - |
| Total                  | 144                                  | 5        | 84            | 76          | 35               | 46.05%  | - |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|------------------------|--------------------------------------|----------|---------------|-------------|------------------|---------|
|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes | NELSON K. BROOKS |         |
| 95U303.05-MB 95U303.05 |                                      |          |               |             |                  |         |
| Precinct TSx           | 159                                  | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 159                                  | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 159                                  | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 159                                  | 1        | 111           | 92          | 49               | 53.26%  |
| Provisional            | 159                                  | 1        | 1             | 0           | 0                | -       |
| Total                  | 159                                  | 5        | 112           | 92          | 49               | 53.26%  |
| 95U304.05-MB 95U304.05 |                                      |          |               |             |                  |         |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0                | -       |
| Provisional            | 0                                    | 1        | 0             | 0           | 0                | -       |
| Total                  | 0                                    | 5        | 0             | 0           | 0                | -       |
| 95U305.05-MB 95U305.05 |                                      |          |               |             |                  |         |
| Precinct TSx           | 7                                    | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 7                                    | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 7                                    | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 7                                    | 1        | 7             | 7           | 1                | 14.29%  |
| Provisional            | 7                                    | 1        | 0             | 0           | 0                | -       |
| Total                  | 7                                    | 5        | 7             | 7           | 1                | 14.29%  |
| 95U401.08-MB 95U401.08 |                                      |          |               |             |                  |         |
| Precinct TSx           | 39                                   | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 39                                   | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 39                                   | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 39                                   | 1        | 16            | 12          | 5                | 41.67%  |
| Provisional            | 39                                   | 1        | 0             | 0           | 0                | -       |
| Total                  | 39                                   | 5        | 16            | 12          | 5                | 41.67%  |
| 95U402.07-MB 95U402.07 |                                      |          |               |             |                  |         |
| Precinct TSx           | 413                                  | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 413                                  | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 413                                  | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 413                                  | 1        | 256           | 229         | 127              | 55.46%  |
| Provisional            | 413                                  | 1        | 1             | 1           | 1                | 100.00% |
| Total                  | 413                                  | 5        | 257           | 230         | 128              | 55.65%  |
| 95U402.08-MB 95U402.08 |                                      |          |               |             |                  |         |
| Precinct TSx           | 873                                  | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 873                                  | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 873                                  | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 873                                  | 1        | 566           | 486         | 286              | 58.85%  |
| Provisional            | 873                                  | 1        | 3             | 3           | 1                | 33.33%  |
| Total                  | 873                                  | 5        | 569           | 489         | 287              | 58.69%  |
| 95U403.08-MB 95U403.08 |                                      |          |               |             |                  |         |
| Precinct TSx           | 1591                                 | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 1591                                 | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 1591                                 | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 1591                                 | 1        | 1042          | 910         | 512              | 56.26%  |
| Provisional            | 1591                                 | 1        | 13            | 8           | 2                | 25.00%  |
| Total                  | 1591                                 | 5        | 1055          | 918         | 514              | 55.99%  |
| 95U404.06-MB 95U404.06 |                                      |          |               |             |                  |         |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0                | -       |
| Provisional            | 0                                    | 1        | 0             | 0           | 0                | -       |
| Total                  | 0                                    | 5        | 0             | 0           | 0                | -       |
| 95U404.08-MB 95U404.08 |                                      |          |               |             |                  |         |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0                | -       |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0                | -       |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0                | -       |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0                | -       |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|-------------------------------|--------------------------------------|----------|---------------|-------------|------------------|---------|
|                               | Reg. Voters                          | Vote For | Times Counted | Total Votes | NELSON K. BROOKS |         |
| Provisional                   | 0                                    | 1        | 0             | 0           | 0                | -       |
| Total                         | 0                                    | 5        | 0             | 0           | 0                | -       |
| 95U501.09-MB 95U501.09        |                                      |          |               |             |                  |         |
| Precinct TSx                  | 0                                    | 1        | 0             | 0           | 0                | -       |
| Precinct OS                   | 0                                    | 1        | 0             | 0           | 0                | -       |
| Precinct VBM                  | 0                                    | 1        | 0             | 0           | 0                | -       |
| MB Precinct                   | 0                                    | 1        | 2             | 2           | 2                | 100.00% |
| Provisional                   | 0                                    | 1        | 0             | 0           | 0                | -       |
| Total                         | 0                                    | 5        | 2             | 2           | 2                | 100.00% |
| 95U501.10-MB 95U501.10        |                                      |          |               |             |                  |         |
| Precinct TSx                  | 286                                  | 1        | 0             | 0           | 0                | -       |
| Precinct OS                   | 286                                  | 1        | 0             | 0           | 0                | -       |
| Precinct VBM                  | 286                                  | 1        | 0             | 0           | 0                | -       |
| MB Precinct                   | 286                                  | 1        | 172           | 145         | 84               | 57.93%  |
| Provisional                   | 286                                  | 1        | 2             | 2           | 2                | 100.00% |
| Total                         | 286                                  | 5        | 174           | 147         | 86               | 58.50%  |
| 95U598.10-MB 95U598.10        |                                      |          |               |             |                  |         |
| Precinct TSx                  | 568                                  | 1        | 0             | 0           | 0                | -       |
| Precinct OS                   | 568                                  | 1        | 0             | 0           | 0                | -       |
| Precinct VBM                  | 568                                  | 1        | 0             | 0           | 0                | -       |
| MB Precinct                   | 568                                  | 1        | 239           | 167         | 119              | 71.26%  |
| Provisional                   | 568                                  | 1        | 3             | 1           | 0                | 0.00%   |
| Total                         | 568                                  | 5        | 242           | 168         | 119              | 70.83%  |
| 95U599.10-MB 95U599.10        |                                      |          |               |             |                  |         |
| Precinct TSx                  | 75                                   | 1        | 0             | 0           | 0                | -       |
| Precinct OS                   | 75                                   | 1        | 0             | 0           | 0                | -       |
| Precinct VBM                  | 75                                   | 1        | 0             | 0           | 0                | -       |
| MB Precinct                   | 75                                   | 1        | 38            | 34          | 24               | 70.59%  |
| Provisional                   | 75                                   | 1        | 0             | 0           | 0                | -       |
| Total                         | 75                                   | 5        | 38            | 34          | 24               | 70.59%  |
| 96U101.11-MB 96U101.11        |                                      |          |               |             |                  |         |
| Precinct TSx                  | 626                                  | 1        | 0             | 0           | 0                | -       |
| Precinct OS                   | 626                                  | 1        | 0             | 0           | 0                | -       |
| Precinct VBM                  | 626                                  | 1        | 0             | 0           | 0                | -       |
| MB Precinct                   | 626                                  | 1        | 423           | 361         | 186              | 51.52%  |
| Provisional                   | 626                                  | 1        | 6             | 6           | 3                | 50.00%  |
| Total                         | 626                                  | 5        | 429           | 367         | 189              | 51.50%  |
| 96U401.14-MB 96U401.14        |                                      |          |               |             |                  |         |
| Precinct TSx                  | 566                                  | 1        | 0             | 0           | 0                | -       |
| Precinct OS                   | 566                                  | 1        | 0             | 0           | 0                | -       |
| Precinct VBM                  | 566                                  | 1        | 0             | 0           | 0                | -       |
| MB Precinct                   | 566                                  | 1        | 359           | 270         | 154              | 57.04%  |
| Provisional                   | 566                                  | 1        | 6             | 5           | 1                | 20.00%  |
| Total                         | 566                                  | 5        | 365           | 275         | 155              | 56.36%  |
| Total                         |                                      |          |               |             |                  |         |
| Precinct TSx                  | 109479                               | 102      | 8             | 6           | 6                | 100.00% |
| Precinct OS                   | 109479                               | 102      | 12009         | 10115       | 5547             | 54.84%  |
| Precinct VBM                  | 109479                               | 102      | 45535         | 39385       | 23287            | 59.13%  |
| MB Precinct                   | 109479                               | 102      | 5143          | 4394        | 2451             | 55.78%  |
| Provisional                   | 109479                               | 102      | 1584          | 1211        | 667              | 55.08%  |
| Total                         | 109479                               | 510      | 64279         | 55111       | 31958            | 57.99%  |
| STATE BOARD OF EQUALIZATION   |                                      |          |               |             |                  |         |
| STATE BOARD OF EQUALIZATION 1 |                                      |          |               |             |                  |         |
| Precinct TSx                  | 109479                               | 102      | 8             | 6           | 6                | 100.00% |
| Precinct OS                   | 109479                               | 102      | 12009         | 10115       | 5547             | 54.84%  |
| Precinct VBM                  | 109479                               | 102      | 45535         | 39385       | 23287            | 59.13%  |
| MB Precinct                   | 109479                               | 102      | 5143          | 4394        | 2451             | 55.78%  |
| Provisional                   | 109479                               | 102      | 1584          | 1211        | 667              | 55.08%  |
| Total                         | 109479                               | 510      | 64279         | 55111       | 31958            | 57.99%  |
| Total                         |                                      |          |               |             |                  |         |

**COUNTY OF EL DORADO**  
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|--------------------------------------|---------------|------------|---------------|--------------|------------------|---------------|
|                                      | Reg. Voters   | Vote For   | Times Counted | Total Votes  | NELSON K. BROOKS |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 10115        | 5547             | 54.84%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 39385        | 23287            | 59.13%        |
| MB Precinct                          | 109479        | 102        | 5143          | 4394         | 2451             | 55.78%        |
| Provisional                          | 109479        | 102        | 1584          | 1211         | 667              | 55.08%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>55111</b> | <b>31958</b>     | <b>57.99%</b> |
| <b>UNITED STATES SENATE</b>          |               |            |               |              |                  |               |
| <b>UNITED STATES SENATE</b>          |               |            |               |              |                  |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 10115        | 5547             | 54.84%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 39385        | 23287            | 59.13%        |
| MB Precinct                          | 109479        | 102        | 5143          | 4394         | 2451             | 55.78%        |
| Provisional                          | 109479        | 102        | 1584          | 1211         | 667              | 55.08%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>55111</b> | <b>31958</b>     | <b>57.99%</b> |
| <b>Total</b>                         |               |            |               |              |                  |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 10115        | 5547             | 54.84%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 39385        | 23287            | 59.13%        |
| MB Precinct                          | 109479        | 102        | 5143          | 4394         | 2451             | 55.78%        |
| Provisional                          | 109479        | 102        | 1584          | 1211         | 667              | 55.08%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>55111</b> | <b>31958</b>     | <b>57.99%</b> |
| <b>Total</b>                         |               |            |               |              |                  |               |
| <b>UNITED STATES REPRESENTATIVE</b>  |               |            |               |              |                  |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>    |               |            |               |              |                  |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 10115        | 5547             | 54.84%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 39385        | 23287            | 59.13%        |
| MB Precinct                          | 109479        | 102        | 5143          | 4394         | 2451             | 55.78%        |
| Provisional                          | 109479        | 102        | 1584          | 1211         | 667              | 55.08%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>55111</b> | <b>31958</b>     | <b>57.99%</b> |
| <b>Total</b>                         |               |            |               |              |                  |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 10115        | 5547             | 54.84%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 39385        | 23287            | 59.13%        |
| MB Precinct                          | 109479        | 102        | 5143          | 4394         | 2451             | 55.78%        |
| Provisional                          | 109479        | 102        | 1584          | 1211         | 667              | 55.08%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>55111</b> | <b>31958</b>     | <b>57.99%</b> |
| <b>Total</b>                         |               |            |               |              |                  |               |
| <b>STATE SENATE</b>                  |               |            |               |              |                  |               |
| <b>1ST STATE SENATE DISTRICT</b>     |               |            |               |              |                  |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 10115        | 5547             | 54.84%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 39385        | 23287            | 59.13%        |
| MB Precinct                          | 109479        | 102        | 5143          | 4394         | 2451             | 55.78%        |
| Provisional                          | 109479        | 102        | 1584          | 1211         | 667              | 55.08%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>55111</b> | <b>31958</b>     | <b>57.99%</b> |
| <b>Total</b>                         |               |            |               |              |                  |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 10115        | 5547             | 54.84%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 39385        | 23287            | 59.13%        |
| MB Precinct                          | 109479        | 102        | 5143          | 4394         | 2451             | 55.78%        |
| Provisional                          | 109479        | 102        | 1584          | 1211         | 667              | 55.08%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>55111</b> | <b>31958</b>     | <b>57.99%</b> |
| <b>Total</b>                         |               |            |               |              |                  |               |
| <b>STATE ASSEMBLY DISTRICTS</b>      |               |            |               |              |                  |               |
| <b>5TH STATE ASSEMBLY DIST</b>       |               |            |               |              |                  |               |
| Precinct TSx                         | 70961         | 75         | 4             | 3            | 3                | 100.00%       |
| Precinct OS                          | 70961         | 75         | 7627          | 6412         | 3456             | 53.90%        |
| Precinct VBM                         | 70961         | 75         | 28225         | 24482        | 14441            | 58.99%        |
| MB Precinct                          | 70961         | 75         | 4361          | 3763         | 2111             | 56.10%        |

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|--------------------------------------|-------------|----------|---------------|-------------|------------------|---------|
|                                      | Reg. Voters | Vote For | Times Counted | Total Votes | NELSON K. BROOKS |         |
| Provisional                          | 70961       | 75       | 1038          | 785         | 440              | 56.05%  |
| Total                                | 70961       | 375      | 41255         | 35445       | 20451            | 57.70%  |
| <b>6TH STATE ASSEMBLY DIST</b>       |             |          |               |             |                  |         |
| Precinct TSx                         | 38518       | 27       | 4             | 3           | 3                | 100.00% |
| Precinct OS                          | 38518       | 27       | 4382          | 3703        | 2091             | 56.47%  |
| Precinct VBM                         | 38518       | 27       | 17310         | 14903       | 8846             | 59.36%  |
| MB Precinct                          | 38518       | 27       | 782           | 631         | 340              | 53.88%  |
| Provisional                          | 38518       | 27       | 546           | 426         | 227              | 53.29%  |
| Total                                | 38518       | 135      | 23024         | 19666       | 11507            | 58.51%  |
| <b>Total</b>                         |             |          |               |             |                  |         |
| Precinct TSx                         | 109479      | 102      | 8             | 6           | 6                | 100.00% |
| Precinct OS                          | 109479      | 102      | 12009         | 10115       | 5547             | 54.84%  |
| Precinct VBM                         | 109479      | 102      | 45535         | 39385       | 23287            | 59.13%  |
| MB Precinct                          | 109479      | 102      | 5143          | 4394        | 2451             | 55.78%  |
| Provisional                          | 109479      | 102      | 1584          | 1211        | 667              | 55.08%  |
| Total                                | 109479      | 510      | 64279         | 55111       | 31958            | 57.99%  |
| <b>SUPERVISORIAL DISTRICTS</b>       |             |          |               |             |                  |         |
| <b>1ST SUPERVISORIAL DISTRICT</b>    |             |          |               |             |                  |         |
| Precinct TSx                         | 22587       | 14       | 0             | 0           | 0                | -       |
| Precinct OS                          | 22587       | 14       | 2530          | 2115        | 1242             | 58.72%  |
| Precinct VBM                         | 22587       | 14       | 10411         | 8942        | 5338             | 59.70%  |
| MB Precinct                          | 22587       | 14       | 423           | 361         | 186              | 51.52%  |
| Provisional                          | 22587       | 14       | 301           | 224         | 121              | 54.02%  |
| Total                                | 22587       | 70       | 13665         | 11642       | 6887             | 59.16%  |
| <b>2ND SUPERVISORIAL DISTRICT</b>    |             |          |               |             |                  |         |
| Precinct TSx                         | 23114       | 23       | 4             | 3           | 3                | 100.00% |
| Precinct OS                          | 23114       | 23       | 2509          | 2167        | 1136             | 52.42%  |
| Precinct VBM                         | 23114       | 23       | 9647          | 8333        | 4753             | 57.04%  |
| MB Precinct                          | 23114       | 23       | 1590          | 1397        | 745              | 53.33%  |
| Provisional                          | 23114       | 23       | 327           | 264         | 145              | 54.92%  |
| Total                                | 23114       | 115      | 14077         | 12164       | 6782             | 55.75%  |
| <b>3RD SUPERVISORIAL DISTRICT</b>    |             |          |               |             |                  |         |
| Precinct TSx                         | 21691       | 26       | 3             | 2           | 2                | 100.00% |
| Precinct OS                          | 21691       | 26       | 2368          | 2031        | 1102             | 54.26%  |
| Precinct VBM                         | 21691       | 26       | 10021         | 8805        | 5250             | 59.63%  |
| MB Precinct                          | 21691       | 26       | 440           | 381         | 207              | 54.33%  |
| Provisional                          | 21691       | 26       | 277           | 210         | 106              | 50.48%  |
| Total                                | 21691       | 130      | 13109         | 11429       | 6667             | 58.33%  |
| <b>4TH SUPERVISORIAL DISTRICT</b>    |             |          |               |             |                  |         |
| Precinct TSx                         | 23425       | 22       | 0             | 0           | 0                | -       |
| Precinct OS                          | 23425       | 22       | 2568          | 2158        | 1071             | 49.63%  |
| Precinct VBM                         | 23425       | 22       | 9398          | 8146        | 4491             | 55.13%  |
| MB Precinct                          | 23425       | 22       | 2239          | 1907        | 1084             | 56.84%  |
| Provisional                          | 23425       | 22       | 315           | 253         | 131              | 51.78%  |
| Total                                | 23425       | 110      | 14520         | 12464       | 6777             | 54.37%  |
| <b>5TH SUPERVISORIAL DISTRICT</b>    |             |          |               |             |                  |         |
| Precinct TSx                         | 18662       | 17       | 1             | 1           | 1                | 100.00% |
| Precinct OS                          | 18662       | 17       | 2034          | 1644        | 996              | 60.58%  |
| Precinct VBM                         | 18662       | 17       | 6058          | 5159        | 3455             | 66.97%  |
| MB Precinct                          | 18662       | 17       | 451           | 348         | 229              | 65.80%  |
| Provisional                          | 18662       | 17       | 364           | 260         | 164              | 63.08%  |
| Total                                | 18662       | 85       | 8908          | 7412        | 4845             | 65.37%  |
| <b>Total</b>                         |             |          |               |             |                  |         |
| Precinct TSx                         | 109479      | 102      | 8             | 6           | 6                | 100.00% |
| Precinct OS                          | 109479      | 102      | 12009         | 10115       | 5547             | 54.84%  |
| Precinct VBM                         | 109479      | 102      | 45535         | 39385       | 23287            | 59.13%  |
| MB Precinct                          | 109479      | 102      | 5143          | 4394        | 2451             | 55.78%  |
| Provisional                          | 109479      | 102      | 1584          | 1211        | 667              | 55.08%  |
| Total                                | 109479      | 510      | 64279         | 55111       | 31958            | 57.99%  |

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|---------------------------------|--|--------------------------------------|------------|---------------|--------------|---------------------|
|                                 |  | Reg. Voters                          | Vote For   | Times Counted | Total Votes  | NELSON K. BROOKS    |
| <b>CITIES</b>                   |  |                                      |            |               |              |                     |
| <b>CITY OF PLACERVILLE</b>      |  |                                      |            |               |              |                     |
| Precinct TSx                    |  | 5569                                 | 5          | 0             | 0            | 0 -                 |
| Precinct OS                     |  | 5569                                 | 5          | 631           | 522          | 304 58.24%          |
| Precinct VBM                    |  | 5569                                 | 5          | 2590          | 2285         | 1453 63.59%         |
| MB Precinct                     |  | 5569                                 | 5          | 0             | 0            | 0 -                 |
| Provisional                     |  | 5569                                 | 5          | 80            | 65           | 38 58.46%           |
| <b>Total</b>                    |  | <b>5569</b>                          | <b>25</b>  | <b>3301</b>   | <b>2872</b>  | <b>1795 62.50%</b>  |
| <b>CITY OF SOUTH LAKE TAHOE</b> |  |                                      |            |               |              |                     |
| Precinct TSx                    |  | 9465                                 | 7          | 0             | 0            | 0 -                 |
| Precinct OS                     |  | 9465                                 | 7          | 1063          | 859          | 527 61.35%          |
| Precinct VBM                    |  | 9465                                 | 7          | 2796          | 2354         | 1609 68.35%         |
| MB Precinct                     |  | 9465                                 | 7          | 0             | 0            | 0 -                 |
| Provisional                     |  | 9465                                 | 7          | 224           | 157          | 94 59.87%           |
| <b>Total</b>                    |  | <b>9465</b>                          | <b>35</b>  | <b>4083</b>   | <b>3370</b>  | <b>2230 66.17%</b>  |
| <b>UNINCORPORATED AREA</b>      |  |                                      |            |               |              |                     |
| Precinct TSx                    |  | 94445                                | 90         | 8             | 6            | 6 100.00%           |
| Precinct OS                     |  | 94445                                | 90         | 10315         | 8734         | 4716 54.00%         |
| Precinct VBM                    |  | 94445                                | 90         | 40149         | 34746        | 20225 58.21%        |
| MB Precinct                     |  | 94445                                | 90         | 5143          | 4394         | 2451 55.78%         |
| Provisional                     |  | 94445                                | 90         | 1280          | 989          | 535 54.10%          |
| <b>Total</b>                    |  | <b>94445</b>                         | <b>450</b> | <b>56895</b>  | <b>48869</b> | <b>27933 57.16%</b> |
| <b>Total</b>                    |  |                                      |            |               |              |                     |
| Precinct TSx                    |  | 109479                               | 102        | 8             | 6            | 6 100.00%           |
| Precinct OS                     |  | 109479                               | 102        | 12009         | 10115        | 5547 54.84%         |
| Precinct VBM                    |  | 109479                               | 102        | 45535         | 39385        | 23287 59.13%        |
| MB Precinct                     |  | 109479                               | 102        | 5143          | 4394         | 2451 55.78%         |
| Provisional                     |  | 109479                               | 102        | 1584          | 1211         | 667 55.08%          |
| <b>Total</b>                    |  | <b>109479</b>                        | <b>510</b> | <b>64279</b>  | <b>55111</b> | <b>31958 57.99%</b> |

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|                        | ROLAND TIEMANN                          |
| Jurisdiction Wide      |   |
| 100001.11-CP 100001.11 |   |
| Precinct TSx           | 0 -                                     |
| Precinct OS            | 53 32.92%                               |
| Precinct VBM           | 287 42.64%                              |
| MB Precinct            | 0 -                                     |
| Provisional            | 6 46.15%                                |
| Total                  | 346 40.85%                              |
| 100002.11-CP 100002.11 |   |
| Precinct TSx           | 0 -                                     |
| Precinct OS            | 24 35.29%                               |
| Precinct VBM           | 150 38.46%                              |
| MB Precinct            | 0 -                                     |
| Provisional            | 6 35.29%                                |
| Total                  | 180 37.89%                              |
| 100003.11-CP 100003.11 |   |
| Precinct TSx           | 0 -                                     |
| Precinct OS            | 50 46.73%                               |
| Precinct VBM           | 222 32.50%                              |
| MB Precinct            | 0 -                                     |
| Provisional            | 12 57.14%                               |
| Total                  | 284 35.02%                              |
| 100004.11-CP 100004.11 |   |
| Precinct TSx           | 0 -                                     |
| Precinct OS            | 61 29.47%                               |
| Precinct VBM           | 233 37.64%                              |
| MB Precinct            | 0 -                                     |
| Provisional            | 0 0.00%                                 |
| Total                  | 294 35.51%                              |
| 100005.11-CP 100005.11 |   |
| Precinct TSx           | 0 -                                     |
| Precinct OS            | 62 50.00%                               |
| Precinct VBM           | 361 47.07%                              |
| MB Precinct            | 0 -                                     |
| Provisional            | 10 52.63%                               |
| Total                  | 433 47.58%                              |
| 100006.11-CP 100006.11 |   |
| Precinct TSx           | 0 -                                     |
| Precinct OS            | 80 39.02%                               |
| Precinct VBM           | 266 33.29%                              |
| MB Precinct            | 0 -                                     |
| Provisional            | 10 52.63%                               |
| Total                  | 356 34.80%                              |
| 100008.11-CP 100008.11 |   |
| Precinct TSx           | 0 -                                     |
| Precinct OS            | 58 37.91%                               |
| Precinct VBM           | 247 39.14%                              |
| MB Precinct            | 0 -                                     |
| Provisional            | 6 46.15%                                |
| Total                  | 311 39.02%                              |
| 100009.11-CP 100009.11 |   |
| Precinct TSx           | 0 -                                     |
| Precinct OS            | 59 40.69%                               |
| Precinct VBM           | 264 40.18%                              |
| MB Precinct            | 0 -                                     |
| Provisional            | 6 35.29%                                |
| Total                  | 329 40.17%                              |
| 100012.11-CP 100012.11 |   |
| Precinct TSx           | 0 -                                     |
| Precinct OS            | 78 36.11%                               |
| Precinct VBM           | 321 39.05%                              |

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|                        | ROLAND TIEMANN                          |        |
| MB Precinct            | 0                                       | -      |
| Provisional            | 15                                      | 65.22% |
| Total                  | 414                                     | 39.02% |
| 100013.11-CP 100013.11 |   |        |
| Precinct TSx           | 0                                       | -      |
| Precinct OS            | 89                                      | 40.64% |
| Precinct VBM           | 338                                     | 43.06% |
| MB Precinct            | 0                                       | -      |
| Provisional            | 7                                       | 41.18% |
| Total                  | 434                                     | 42.51% |
| 100014.11-CP 100014.11 |   |        |
| Precinct TSx           | 0                                       | -      |
| Precinct OS            | 87                                      | 50.58% |
| Precinct VBM           | 253                                     | 40.42% |
| MB Precinct            | 0                                       | -      |
| Provisional            | 6                                       | 28.57% |
| Total                  | 346                                     | 42.25% |
| 100022.11-CP 100022.11 |   |        |
| Precinct TSx           | 0                                       | -      |
| Precinct OS            | 75                                      | 49.34% |
| Precinct VBM           | 230                                     | 39.86% |
| MB Precinct            | 0                                       | -      |
| Provisional            | 5                                       | 50.00% |
| Total                  | 310                                     | 41.95% |
| 100023.11-CP 100023.11 |   |        |
| Precinct TSx           | 0                                       | -      |
| Precinct OS            | 91                                      | 48.92% |
| Precinct VBM           | 407                                     | 44.58% |
| MB Precinct            | 0                                       | -      |
| Provisional            | 9                                       | 34.62% |
| Total                  | 507                                     | 45.07% |
| 200003.12-CP 200003.12 |   |        |
| Precinct TSx           | 0                                       | -      |
| Precinct OS            | 62                                      | 44.60% |
| Precinct VBM           | 175                                     | 36.46% |
| MB Precinct            | 0                                       | -      |
| Provisional            | 11                                      | 42.31% |
| Total                  | 248                                     | 38.45% |
| 200004.12-CP 200004.12 |   |        |
| Precinct TSx           | 0                                       | -      |
| Precinct OS            | 79                                      | 49.69% |
| Precinct VBM           | 248                                     | 43.28% |
| MB Precinct            | 0                                       | -      |
| Provisional            | 6                                       | 60.00% |
| Total                  | 333                                     | 44.88% |
| 200005.02-CP 200005.02 |   |        |
| Precinct TSx           | 0                                       | -      |
| Precinct OS            | 36                                      | 40.91% |
| Precinct VBM           | 179                                     | 45.43% |
| MB Precinct            | 0                                       | -      |
| Provisional            | 7                                       | 36.84% |
| Total                  | 222                                     | 44.31% |
| 200006.12-CP 200006.12 |   |        |
| Precinct TSx           | 0                                       | -      |
| Precinct OS            | 54                                      | 50.00% |
| Precinct VBM           | 194                                     | 41.36% |
| MB Precinct            | 0                                       | -      |
| Provisional            | 9                                       | 36.00% |
| Total                  | 257                                     | 42.69% |
| 200008.12-CP 200008.12 |   |        |
| Precinct TSx           | 0                                       | -      |



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| ROLAND TIEMANN                          |            |
| Precinct OS                             | 70 52.24%  |
| Precinct VBM                            | 115 36.86% |
| MB Precinct                             | 0 -        |
| Provisional                             | 3 37.50%   |
| Total                                   | 188 41.41% |
| 200010.12-CP 200010.12                  |            |
| Precinct TSx                            | 0 0.00%    |
| Precinct OS                             | 61 38.36%  |
| Precinct VBM                            | 314 43.55% |
| MB Precinct                             | 0 -        |
| Provisional                             | 11 42.31%  |
| Total                                   | 386 42.46% |
| 200012.12-CP 200012.12                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 49 38.58%  |
| Precinct VBM                            | 180 45.23% |
| MB Precinct                             | 0 -        |
| Provisional                             | 5 41.67%   |
| Total                                   | 234 43.58% |
| 200013.12-CP 200013.12                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 87 46.77%  |
| Precinct VBM                            | 228 36.89% |
| MB Precinct                             | 0 -        |
| Provisional                             | 9 47.37%   |
| Total                                   | 324 39.37% |
| 200014.12-CP 200014.12                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 81 46.55%  |
| Precinct VBM                            | 217 40.71% |
| MB Precinct                             | 0 -        |
| Provisional                             | 11 40.74%  |
| Total                                   | 309 42.10% |
| 200015.02-CP 200015.02                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 104 55.61% |
| Precinct VBM                            | 339 44.61% |
| MB Precinct                             | 0 -        |
| Provisional                             | 10 47.62%  |
| Total                                   | 453 46.80% |
| 200016.12-CP 200016.12                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 10 47.62%  |
| Precinct VBM                            | 46 38.33%  |
| MB Precinct                             | 0 -        |
| Provisional                             | 4 80.00%   |
| Total                                   | 60 41.10%  |
| 200016.13-CP 200016.13                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 39 57.35%  |
| Precinct VBM                            | 111 38.54% |
| MB Precinct                             | 0 -        |
| Provisional                             | 2 66.67%   |
| Total                                   | 152 42.34% |
| 200017.02-CP 200017.02                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 56 44.44%  |
| Precinct VBM                            | 228 44.02% |
| MB Precinct                             | 0 -        |
| Provisional                             | 5 50.00%   |
| Total                                   | 289 44.19% |

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| ROLAND TIEMANN                          |            |
| 200018.02-CP 200018.02                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 74 49.01%  |
| Precinct VBM                            | 292 47.40% |
| MB Precinct                             | 0 -        |
| Provisional                             | 4 44.44%   |
| Total                                   | 370 47.68% |
| 200020.02-CP 200020.02                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 53 43.44%  |
| Precinct VBM                            | 206 43.28% |
| MB Precinct                             | 0 -        |
| Provisional                             | 6 37.50%   |
| Total                                   | 265 43.16% |
| 200021.01-CP 200021.01                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 12 42.86%  |
| Precinct VBM                            | 92 52.27%  |
| MB Precinct                             | 0 -        |
| Provisional                             | 3 75.00%   |
| Total                                   | 107 51.44% |
| 200021.02-CP 200021.02                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 73 50.69%  |
| Precinct VBM                            | 269 42.70% |
| MB Precinct                             | 0 -        |
| Provisional                             | 6 46.15%   |
| Total                                   | 348 44.22% |
| 200024.02-CP 200024.02                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 27 58.70%  |
| Precinct VBM                            | 125 49.80% |
| MB Precinct                             | 0 -        |
| Provisional                             | 3 60.00%   |
| Total                                   | 155 51.32% |
| 300001.04-CP 300001.04                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 2 22.22%   |
| Precinct VBM                            | 40 43.01%  |
| MB Precinct                             | 0 -        |
| Provisional                             | 0 -        |
| Total                                   | 42 41.18%  |
| 300001.05-CP 300001.05                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 35 53.03%  |
| Precinct VBM                            | 102 35.42% |
| MB Precinct                             | 0 -        |
| Provisional                             | 2 22.22%   |
| Total                                   | 139 38.29% |
| 300003.05-CP 300003.05                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 63 37.50%  |
| Precinct VBM                            | 280 34.44% |
| MB Precinct                             | 0 -        |
| Provisional                             | 11 47.83%  |
| Total                                   | 354 35.26% |
| 300004.05-CP 300004.05                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 63 43.45%  |
| Precinct VBM                            | 185 37.22% |
| MB Precinct                             | 0 -        |

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| ROLAND TIEMANN                          |            |
| Provisional                             | 5 35.71%   |
| Total                                   | 253 38.57% |
| 300005.05-CP 300005.05                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 54 40.30%  |
| Precinct VBM                            | 214 36.03% |
| MB Precinct                             | 0 -        |
| Provisional                             | 9 47.37%   |
| Total                                   | 277 37.08% |
| 300008.05-CP 300008.05                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 59 47.20%  |
| Precinct VBM                            | 275 42.57% |
| MB Precinct                             | 0 -        |
| Provisional                             | 3 30.00%   |
| Total                                   | 337 43.15% |
| 300009.04-CP 300009.04                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 56 52.34%  |
| Precinct VBM                            | 170 49.85% |
| MB Precinct                             | 0 -        |
| Provisional                             | 5 62.50%   |
| Total                                   | 231 50.66% |
| 300010.04-CP 300010.04                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 76 52.05%  |
| Precinct VBM                            | 247 42.29% |
| MB Precinct                             | 0 -        |
| Provisional                             | 7 70.00%   |
| Total                                   | 330 44.59% |
| 300011.04-CP 300011.04                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 56 46.28%  |
| Precinct VBM                            | 279 45.00% |
| MB Precinct                             | 0 -        |
| Provisional                             | 10 62.50%  |
| Total                                   | 345 45.57% |
| 300013.05-CP 300013.05                  |            |
| Precinct TSx                            | 0 0.00%    |
| Precinct OS                             | 60 52.63%  |
| Precinct VBM                            | 281 43.91% |
| MB Precinct                             | 0 -        |
| Provisional                             | 9 81.82%   |
| Total                                   | 350 45.63% |
| 300015.04-CP 300015.04                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 4 44.44%   |
| Precinct VBM                            | 13 26.00%  |
| MB Precinct                             | 0 -        |
| Provisional                             | 1 25.00%   |
| Total                                   | 18 28.57%  |
| 300015.05-CP 300015.05                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 23 41.07%  |
| Precinct VBM                            | 82 36.61%  |
| MB Precinct                             | 0 -        |
| Provisional                             | 0 0.00%    |
| Total                                   | 105 37.37% |
| 300017.05-CP 300017.05                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 47 42.73%  |

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| ROLAND TIEMANN                          |            |
| Precinct VBM                            | 239 40.37% |
| MB Precinct                             | 0 -        |
| Provisional                             | 4 50.00%   |
| Total                                   | 290 40.85% |
| 300018.04-CP 300018.04                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 66 50.77%  |
| Precinct VBM                            | 206 46.09% |
| MB Precinct                             | 0 -        |
| Provisional                             | 9 39.13%   |
| Total                                   | 281 46.83% |
| 300020.04-CP 300020.04                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 71 48.97%  |
| Precinct VBM                            | 220 39.86% |
| MB Precinct                             | 0 -        |
| Provisional                             | 3 37.50%   |
| Total                                   | 294 41.70% |
| 300021.03-CP 300021.03                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 53 41.41%  |
| Precinct VBM                            | 194 36.95% |
| MB Precinct                             | 0 -        |
| Provisional                             | 2 20.00%   |
| Total                                   | 249 37.56% |
| 300022.05-CP 300022.05                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 48 54.55%  |
| Precinct VBM                            | 137 44.48% |
| MB Precinct                             | 0 -        |
| Provisional                             | 5 55.56%   |
| Total                                   | 190 46.91% |
| 300023.03-CP 300023.03                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 69 35.94%  |
| Precinct VBM                            | 301 36.71% |
| MB Precinct                             | 0 -        |
| Provisional                             | 11 61.11%  |
| Total                                   | 381 36.99% |
| 300023.05-CP 300023.05                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 17 44.74%  |
| Precinct VBM                            | 63 36.84%  |
| MB Precinct                             | 0 -        |
| Provisional                             | 4 57.14%   |
| Total                                   | 84 38.89%  |
| 400001.14-CP 400001.14                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 70 45.75%  |
| Precinct VBM                            | 355 40.62% |
| MB Precinct                             | 0 -        |
| Provisional                             | 11 55.00%  |
| Total                                   | 436 41.64% |
| 400002.08-CP 400002.08                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 90 52.94%  |
| Precinct VBM                            | 308 49.68% |
| MB Precinct                             | 0 -        |
| Provisional                             | 7 53.85%   |
| Total                                   | 405 50.44% |
| 400003.08-CP 400003.08                  |            |

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|                        |  | ROLAND TIEMANN                          |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 104                                     | 53.89% |
| Precinct VBM           |  | 296                                     | 45.61% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 13                                      | 44.83% |
| Total                  |  | 413                                     | 47.42% |
| 400004.14-CP 400004.14 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 75                                      | 46.88% |
| Precinct VBM           |  | 259                                     | 45.04% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 8                                       | 50.00% |
| Total                  |  | 342                                     | 45.54% |
| 400005.08-CP 400005.08 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 28                                      | 44.44% |
| Precinct VBM           |  | 100                                     | 44.44% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 5                                       | 62.50% |
| Total                  |  | 133                                     | 44.93% |
| 400006.08-CP 400006.08 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 89                                      | 47.85% |
| Precinct VBM           |  | 255                                     | 42.01% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 3                                       | 23.08% |
| Total                  |  | 347                                     | 43.05% |
| 400007.08-CP 400007.08 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 108                                     | 53.47% |
| Precinct VBM           |  | 335                                     | 50.22% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 11                                      | 55.00% |
| Total                  |  | 454                                     | 51.07% |
| 400010.08-CP 400010.08 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 106                                     | 52.48% |
| Precinct VBM           |  | 319                                     | 41.75% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 9                                       | 40.91% |
| Total                  |  | 434                                     | 43.93% |
| 400012.08-CP 400012.08 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 46                                      | 43.40% |
| Precinct VBM           |  | 155                                     | 41.01% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 6                                       | 35.29% |
| Total                  |  | 207                                     | 41.32% |
| 400013.08-CP 400013.08 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 60                                      | 58.25% |
| Precinct VBM           |  | 114                                     | 41.76% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 1                                       | 50.00% |
| Total                  |  | 175                                     | 46.30% |
| 400014.08-CP 400014.08 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 89                                      | 48.37% |
| Precinct VBM           |  | 263                                     | 41.22% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 7                                       | 22.58% |

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|                        |  | ROLAND TIEMANN                          |        |
| Total                  |  | 359                                     | 42.09% |
| 400017.08-CP 400017.08 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 82                                      | 55.03% |
| Precinct VBM           |  | 327                                     | 48.73% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 18                                      | 69.23% |
| Total                  |  | 427                                     | 50.47% |
| 400018.08-CP 400018.08 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 32                                      | 43.84% |
| Precinct VBM           |  | 131                                     | 55.27% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 2                                       | 50.00% |
| Total                  |  | 165                                     | 52.55% |
| 400020.08-CP 400020.08 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 48                                      | 48.00% |
| Precinct VBM           |  | 127                                     | 43.64% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 1                                       | 50.00% |
| Total                  |  | 176                                     | 44.78% |
| 400021.07-CP 400021.07 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 52                                      | 45.61% |
| Precinct VBM           |  | 288                                     | 42.54% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 6                                       | 46.15% |
| Total                  |  | 346                                     | 43.03% |
| 500001.10-CP 500001.10 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 58                                      | 34.52% |
| Precinct VBM           |  | 142                                     | 27.84% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 2                                       | 15.38% |
| Total                  |  | 202                                     | 29.23% |
| 500002.10-CP 500002.10 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 36                                      | 33.96% |
| Precinct VBM           |  | 100                                     | 28.99% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 10                                      | 43.48% |
| Total                  |  | 146                                     | 30.80% |
| 500003.10-CP 500003.10 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 29                                      | 29.59% |
| Precinct VBM           |  | 66                                      | 22.92% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 3                                       | 75.00% |
| Total                  |  | 98                                      | 25.13% |
| 500004.10-CP 500004.10 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 42                                      | 40.78% |
| Precinct VBM           |  | 88                                      | 42.31% |
| MB Precinct            |  | 0                                       | -      |
| Provisional            |  | 5                                       | 31.25% |
| Total                  |  | 135                                     | 41.28% |
| 500005.10-CP 500005.10 |  |   |        |
| Precinct TSx           |  | 0                                       | -      |
| Precinct OS            |  | 25                                      | 27.78% |
| Precinct VBM           |  | 65                                      | 29.95% |

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| ROLAND TIEMANN                          |            |
| MB Precinct                             | 0 -        |
| Provisional                             | 4 36.36%   |
| Total                                   | 94 29.56%  |
| 500006.10-CP 500006.10                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 62 42.18%  |
| Precinct VBM                            | 119 28.88% |
| MB Precinct                             | 0 -        |
| Provisional                             | 16 40.00%  |
| Total                                   | 197 32.89% |
| 500007.10-CP 500007.10                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 74 43.02%  |
| Precinct VBM                            | 185 31.14% |
| MB Precinct                             | 0 -        |
| Provisional                             | 6 37.50%   |
| Total                                   | 265 33.89% |
| 500009.10-CP 500009.10                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 35 35.71%  |
| Precinct VBM                            | 68 27.53%  |
| MB Precinct                             | 0 -        |
| Provisional                             | 10 45.45%  |
| Total                                   | 113 30.79% |
| 500011.10-CP 500011.10                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 49 34.27%  |
| Precinct VBM                            | 110 33.23% |
| MB Precinct                             | 0 -        |
| Provisional                             | 10 34.48%  |
| Total                                   | 169 33.60% |
| 500012.10-CP 500012.10                  |            |
| Precinct TSx                            | 0 0.00%    |
| Precinct OS                             | 66 42.58%  |
| Precinct VBM                            | 165 32.16% |
| MB Precinct                             | 0 -        |
| Provisional                             | 7 28.00%   |
| Total                                   | 238 34.29% |
| 500015.10-CP 500015.10                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 24 28.92%  |
| Precinct VBM                            | 90 26.39%  |
| MB Precinct                             | 0 -        |
| Provisional                             | 4 40.00%   |
| Total                                   | 118 27.19% |
| 500020.10-CP 500020.10                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 59 43.70%  |
| Precinct VBM                            | 249 42.78% |
| MB Precinct                             | 0 -        |
| Provisional                             | 8 30.77%   |
| Total                                   | 316 42.53% |
| 500021.10-CP 500021.10                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 78 53.42%  |
| Precinct VBM                            | 230 40.28% |
| MB Precinct                             | 0 -        |
| Provisional                             | 7 31.82%   |
| Total                                   | 315 42.63% |
| 95U201.01-MB 95U201.01                  |            |
| Precinct TSx                            | 0 -        |

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| ROLAND TIEMANN                          |            |
| Precinct OS                             | 0 -        |
| Precinct VBM                            | 0 -        |
| MB Precinct                             | 4 66.67%   |
| Provisional                             | 0 -        |
| Total                                   | 4 66.67%   |
| 95U201.02-MB 95U201.02                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 0 -        |
| Precinct VBM                            | 0 -        |
| MB Precinct                             | 154 50.16% |
| Provisional                             | 1 100.00%  |
| Total                                   | 155 50.32% |
| 95U202.02-MB 95U202.02                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 0 -        |
| Precinct VBM                            | 0 -        |
| MB Precinct                             | 232 46.40% |
| Provisional                             | 0 0.00%    |
| Total                                   | 232 46.12% |
| 95U203.02-MB 95U203.02                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 0 -        |
| Precinct VBM                            | 0 -        |
| MB Precinct                             | 0 -        |
| Provisional                             | 0 -        |
| Total                                   | 0 -        |
| 95U204.01-MB 95U204.01                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 0 -        |
| Precinct VBM                            | 0 -        |
| MB Precinct                             | 257 44.01% |
| Provisional                             | 1 50.00%   |
| Total                                   | 258 44.03% |
| 95U301.05-MB 95U301.05                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 0 -        |
| Precinct VBM                            | 0 -        |
| MB Precinct                             | 58 44.27%  |
| Provisional                             | 2 100.00%  |
| Total                                   | 60 45.11%  |
| 95U302.04-MB 95U302.04                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 0 -        |
| Precinct VBM                            | 0 -        |
| MB Precinct                             | 0 0.00%    |
| Provisional                             | 0 -        |
| Total                                   | 0 0.00%    |
| 95U302.05-MB 95U302.05                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 0 -        |
| Precinct VBM                            | 0 -        |
| MB Precinct                             | 26 35.14%  |
| Provisional                             | 0 -        |
| Total                                   | 26 35.14%  |
| 95U303.03-MB 95U303.03                  |            |
| Precinct TSx                            | 0 -        |
| Precinct OS                             | 0 -        |
| Precinct VBM                            | 0 -        |
| MB Precinct                             | 41 53.95%  |
| Provisional                             | 0 -        |
| Total                                   | 41 53.95%  |



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|                        |     | ROLAND TIEMANN                          |
| 95U303.05-MB 95U303.05 |     |   |
| Precinct TSx           |     | 0 -                                     |
| Precinct OS            |     | 0 -                                     |
| Precinct VBM           |     | 0 -                                     |
| MB Precinct            | 43  | 46.74%                                  |
| Provisional            |     | 0 -                                     |
| Total                  | 43  | 46.74%                                  |
| 95U304.05-MB 95U304.05 |     |   |
| Precinct TSx           |     | 0 -                                     |
| Precinct OS            |     | 0 -                                     |
| Precinct VBM           |     | 0 -                                     |
| MB Precinct            |     | 0 -                                     |
| Provisional            |     | 0 -                                     |
| Total                  |     | 0 -                                     |
| 95U305.05-MB 95U305.05 |     |   |
| Precinct TSx           |     | 0 -                                     |
| Precinct OS            |     | 0 -                                     |
| Precinct VBM           |     | 0 -                                     |
| MB Precinct            | 6   | 85.71%                                  |
| Provisional            |     | 0 -                                     |
| Total                  | 6   | 85.71%                                  |
| 95U401.08-MB 95U401.08 |     |   |
| Precinct TSx           |     | 0 -                                     |
| Precinct OS            |     | 0 -                                     |
| Precinct VBM           |     | 0 -                                     |
| MB Precinct            | 7   | 58.33%                                  |
| Provisional            |     | 0 -                                     |
| Total                  | 7   | 58.33%                                  |
| 95U402.07-MB 95U402.07 |     |   |
| Precinct TSx           |     | 0 -                                     |
| Precinct OS            |     | 0 -                                     |
| Precinct VBM           |     | 0 -                                     |
| MB Precinct            | 101 | 44.10%                                  |
| Provisional            |     | 0 0.00%                                 |
| Total                  | 101 | 43.91%                                  |
| 95U402.08-MB 95U402.08 |     |   |
| Precinct TSx           |     | 0 -                                     |
| Precinct OS            |     | 0 -                                     |
| Precinct VBM           |     | 0 -                                     |
| MB Precinct            | 200 | 41.15%                                  |
| Provisional            | 2   | 66.67%                                  |
| Total                  | 202 | 41.31%                                  |
| 95U403.08-MB 95U403.08 |     |   |
| Precinct TSx           |     | 0 -                                     |
| Precinct OS            |     | 0 -                                     |
| Precinct VBM           |     | 0 -                                     |
| MB Precinct            | 395 | 43.41%                                  |
| Provisional            | 5   | 62.50%                                  |
| Total                  | 400 | 43.57%                                  |
| 95U404.06-MB 95U404.06 |     |   |
| Precinct TSx           |     | 0 -                                     |
| Precinct OS            |     | 0 -                                     |
| Precinct VBM           |     | 0 -                                     |
| MB Precinct            |     | 0 -                                     |
| Provisional            |     | 0 -                                     |
| Total                  |     | 0 -                                     |
| 95U404.08-MB 95U404.08 |     |   |
| Precinct TSx           |     | 0 -                                     |
| Precinct OS            |     | 0 -                                     |
| Precinct VBM           |     | 0 -                                     |
| MB Precinct            |     | 0 -                                     |

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|---|--------------|
| ROLAND TIEMANN                          |              |
| Provisional                             | 0 -          |
| Total                                   | 0 -          |
| 95U501.09-MB 95U501.09                  |              |
| Precinct TSx                            | 0 -          |
| Precinct OS                             | 0 -          |
| Precinct VBM                            | 0 -          |
| MB Precinct                             | 0 0.00%      |
| Provisional                             | 0 -          |
| Total                                   | 0 0.00%      |
| 95U501.10-MB 95U501.10                  |              |
| Precinct TSx                            | 0 -          |
| Precinct OS                             | 0 -          |
| Precinct VBM                            | 0 -          |
| MB Precinct                             | 60 41.38%    |
| Provisional                             | 0 0.00%      |
| Total                                   | 60 40.82%    |
| 95U598.10-MB 95U598.10                  |              |
| Precinct TSx                            | 0 -          |
| Precinct OS                             | 0 -          |
| Precinct VBM                            | 0 -          |
| MB Precinct                             | 46 27.54%    |
| Provisional                             | 1 100.00%    |
| Total                                   | 47 27.98%    |
| 95U599.10-MB 95U599.10                  |              |
| Precinct TSx                            | 0 -          |
| Precinct OS                             | 0 -          |
| Precinct VBM                            | 0 -          |
| MB Precinct                             | 10 29.41%    |
| Provisional                             | 0 -          |
| Total                                   | 10 29.41%    |
| 96U101.11-MB 96U101.11                  |              |
| Precinct TSx                            | 0 -          |
| Precinct OS                             | 0 -          |
| Precinct VBM                            | 0 -          |
| MB Precinct                             | 175 48.48%   |
| Provisional                             | 3 50.00%     |
| Total                                   | 178 48.50%   |
| 96U401.14-MB 96U401.14                  |              |
| Precinct TSx                            | 0 -          |
| Precinct OS                             | 0 -          |
| Precinct VBM                            | 0 -          |
| MB Precinct                             | 116 42.96%   |
| Provisional                             | 4 80.00%     |
| Total                                   | 120 43.64%   |
| Total                                   |              |
| Precinct TSx                            | 0 0.00%      |
| Precinct OS                             | 4532 44.80%  |
| Precinct VBM                            | 15974 40.56% |
| MB Precinct                             | 1931 43.95%  |
| Provisional                             | 532 43.93%   |
| Total                                   | 22969 41.68% |
| STATE BOARD OF EQUALIZATION             |              |
| STATE BOARD OF EQUALIZATION 1           |              |
| Precinct TSx                            | 0 0.00%      |
| Precinct OS                             | 4532 44.80%  |
| Precinct VBM                            | 15974 40.56% |
| MB Precinct                             | 1931 43.95%  |
| Provisional                             | 532 43.93%   |
| Total                                   | 22969 41.68% |
| Total                                   |              |

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|-------------------------------------|--|---|---------------|
|                                     |  | ROLAND TIEMANN                          |               |
| Precinct TSx                        |  | 0                                       | 0.00%         |
| Precinct OS                         |  | 4532                                    | 44.80%        |
| Precinct VBM                        |  | 15974                                   | 40.56%        |
| MB Precinct                         |  | 1931                                    | 43.95%        |
| Provisional                         |  | 532                                     | 43.93%        |
| <b>Total</b>                        |  | <b>22969</b>                            | <b>41.68%</b> |
| <b>UNITED STATES SENATE</b>         |  |   |               |
| <b>UNITED STATES SENATE</b>         |  |   |               |
| Precinct TSx                        |  | 0                                       | 0.00%         |
| Precinct OS                         |  | 4532                                    | 44.80%        |
| Precinct VBM                        |  | 15974                                   | 40.56%        |
| MB Precinct                         |  | 1931                                    | 43.95%        |
| Provisional                         |  | 532                                     | 43.93%        |
| <b>Total</b>                        |  | <b>22969</b>                            | <b>41.68%</b> |
| <b>Total</b>                        |  |   |               |
| Precinct TSx                        |  | 0                                       | 0.00%         |
| Precinct OS                         |  | 4532                                    | 44.80%        |
| Precinct VBM                        |  | 15974                                   | 40.56%        |
| MB Precinct                         |  | 1931                                    | 43.95%        |
| Provisional                         |  | 532                                     | 43.93%        |
| <b>Total</b>                        |  | <b>22969</b>                            | <b>41.68%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |  |   |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |  |   |               |
| Precinct TSx                        |  | 0                                       | 0.00%         |
| Precinct OS                         |  | 4532                                    | 44.80%        |
| Precinct VBM                        |  | 15974                                   | 40.56%        |
| MB Precinct                         |  | 1931                                    | 43.95%        |
| Provisional                         |  | 532                                     | 43.93%        |
| <b>Total</b>                        |  | <b>22969</b>                            | <b>41.68%</b> |
| <b>Total</b>                        |  |   |               |
| Precinct TSx                        |  | 0                                       | 0.00%         |
| Precinct OS                         |  | 4532                                    | 44.80%        |
| Precinct VBM                        |  | 15974                                   | 40.56%        |
| MB Precinct                         |  | 1931                                    | 43.95%        |
| Provisional                         |  | 532                                     | 43.93%        |
| <b>Total</b>                        |  | <b>22969</b>                            | <b>41.68%</b> |
| <b>STATE SENATE</b>                 |  |   |               |
| <b>1ST STATE SENATE DISTRICT</b>    |  |   |               |
| Precinct TSx                        |  | 0                                       | 0.00%         |
| Precinct OS                         |  | 4532                                    | 44.80%        |
| Precinct VBM                        |  | 15974                                   | 40.56%        |
| MB Precinct                         |  | 1931                                    | 43.95%        |
| Provisional                         |  | 532                                     | 43.93%        |
| <b>Total</b>                        |  | <b>22969</b>                            | <b>41.68%</b> |
| <b>Total</b>                        |  |   |               |
| Precinct TSx                        |  | 0                                       | 0.00%         |
| Precinct OS                         |  | 4532                                    | 44.80%        |
| Precinct VBM                        |  | 15974                                   | 40.56%        |
| MB Precinct                         |  | 1931                                    | 43.95%        |
| Provisional                         |  | 532                                     | 43.93%        |
| <b>Total</b>                        |  | <b>22969</b>                            | <b>41.68%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |  |   |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |  |   |               |
| Precinct TSx                        |  | 0                                       | 0.00%         |
| Precinct OS                         |  | 2928                                    | 45.66%        |
| Precinct VBM                        |  | 9953                                    | 40.65%        |
| MB Precinct                         |  | 1640                                    | 43.58%        |

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|---|--------------|
| ROLAND TIEMANN                          |              |
| Provisional                             | 337 42.93%   |
| Total                                   | 14858 41.92% |
| <b>6TH STATE ASSEMBLY DIST</b>          |              |
| Precinct TSx                            | 0 0.00%      |
| Precinct OS                             | 1604 43.32%  |
| Precinct VBM                            | 6021 40.40%  |
| MB Precinct                             | 291 46.12%   |
| Provisional                             | 195 45.77%   |
| Total                                   | 8111 41.24%  |
| <b>Total</b>                            |              |
| Precinct TSx                            | 0 0.00%      |
| Precinct OS                             | 4532 44.80%  |
| Precinct VBM                            | 15974 40.56% |
| MB Precinct                             | 1931 43.95%  |
| Provisional                             | 532 43.93%   |
| Total                                   | 22969 41.68% |
| <b>SUPERVISORIAL DISTRICTS</b>          |              |
| <b>1ST SUPERVISORIAL DISTRICT</b>       |              |
| Precinct TSx                            | 0 -          |
| Precinct OS                             | 867 40.99%   |
| Precinct VBM                            | 3579 40.02%  |
| MB Precinct                             | 175 48.48%   |
| Provisional                             | 101 45.09%   |
| Total                                   | 4722 40.56%  |
| <b>2ND SUPERVISORIAL DISTRICT</b>       |              |
| Precinct TSx                            | 0 0.00%      |
| Precinct OS                             | 1027 47.39%  |
| Precinct VBM                            | 3558 42.70%  |
| MB Precinct                             | 647 46.31%   |
| Provisional                             | 117 44.32%   |
| Total                                   | 5349 43.97%  |
| <b>3RD SUPERVISORIAL DISTRICT</b>       |              |
| Precinct TSx                            | 0 0.00%      |
| Precinct OS                             | 922 45.40%   |
| Precinct VBM                            | 3528 40.07%  |
| MB Precinct                             | 174 45.67%   |
| Provisional                             | 102 48.57%   |
| Total                                   | 4726 41.35%  |
| <b>4TH SUPERVISORIAL DISTRICT</b>       |              |
| Precinct TSx                            | 0 -          |
| Precinct OS                             | 1079 50.00%  |
| Precinct VBM                            | 3632 44.59%  |
| MB Precinct                             | 819 42.95%   |
| Provisional                             | 119 47.04%   |
| Total                                   | 5649 45.32%  |
| <b>5TH SUPERVISORIAL DISTRICT</b>       |              |
| Precinct TSx                            | 0 0.00%      |
| Precinct OS                             | 637 38.75%   |
| Precinct VBM                            | 1677 32.51%  |
| MB Precinct                             | 116 33.33%   |
| Provisional                             | 93 35.77%    |
| Total                                   | 2523 34.04%  |
| <b>Total</b>                            |              |
| Precinct TSx                            | 0 0.00%      |
| Precinct OS                             | 4532 44.80%  |
| Precinct VBM                            | 15974 40.56% |
| MB Precinct                             | 1931 43.95%  |
| Provisional                             | 532 43.93%   |
| Total                                   | 22969 41.68% |

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|---------------------------------|---|--------|
|                                 | ROLAND TIEMANN                          |        |
| <b>CITIES</b>                   |   |        |
| <b>CITY OF PLACERVILLE</b>      |   |        |
| Precinct TSx                    | 0                                       | -      |
| Precinct OS                     | 217                                     | 41.57% |
| Precinct VBM                    | 821                                     | 35.93% |
| MB Precinct                     | 0                                       | -      |
| Provisional                     | 27                                      | 41.54% |
| Total                           | 1065                                    | 37.08% |
| <b>CITY OF SOUTH LAKE TAHOE</b> |   |        |
| Precinct TSx                    | 0                                       | -      |
| Precinct OS                     | 323                                     | 37.60% |
| Precinct VBM                    | 735                                     | 31.22% |
| MB Precinct                     | 0                                       | -      |
| Provisional                     | 61                                      | 38.85% |
| Total                           | 1119                                    | 33.20% |
| <b>UNINCORPORATED AREA</b>      |   |        |
| Precinct TSx                    | 0                                       | 0.00%  |
| Precinct OS                     | 3992                                    | 45.71% |
| Precinct VBM                    | 14418                                   | 41.50% |
| MB Precinct                     | 1931                                    | 43.95% |
| Provisional                     | 444                                     | 44.89% |
| Total                           | 20785                                   | 42.53% |
| <b>Total</b>                    |   |        |
| Precinct TSx                    | 0                                       | 0.00%  |
| Precinct OS                     | 4532                                    | 44.80% |
| Precinct VBM                    | 15974                                   | 40.56% |
| MB Precinct                     | 1931                                    | 43.95% |
| Provisional                     | 532                                     | 43.93% |
| Total                           | 22969                                   | 41.68% |

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|--------------------------------------|-------------|----------|---------------|-------------|------------------------|---------|
|                                      | Reg. Voters | Vote For | Times Counted | Total Votes | KENNETH J.<br>MELIKIAN |         |
| <b>Jurisdiction Wide</b>             |             |          |               |             |                        |         |
| 100001.11-CP 100001.11               |             |          |               |             |                        |         |
| Precinct TSx                         | 1615        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 1615        | 1        | 190           | 115         | 114                    | 99.13%  |
| Precinct VBM                         | 1615        | 1        | 781           | 496         | 490                    | 98.79%  |
| MB Precinct                          | 1615        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1615        | 1        | 17            | 11          | 11                     | 100.00% |
| Total                                | 1615        | 5        | 988           | 622         | 615                    | 98.87%  |
| 100002.11-CP 100002.11               |             |          |               |             |                        |         |
| Precinct TSx                         | 1000        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 1000        | 1        | 89            | 56          | 56                     | 100.00% |
| Precinct VBM                         | 1000        | 1        | 476           | 303         | 303                    | 100.00% |
| MB Precinct                          | 1000        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1000        | 1        | 20            | 14          | 13                     | 92.86%  |
| Total                                | 1000        | 5        | 585           | 373         | 372                    | 99.73%  |
| 100003.11-CP 100003.11               |             |          |               |             |                        |         |
| Precinct TSx                         | 1269        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 1269        | 1        | 123           | 86          | 86                     | 100.00% |
| Precinct VBM                         | 1269        | 1        | 773           | 532         | 528                    | 99.25%  |
| MB Precinct                          | 1269        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1269        | 1        | 24            | 17          | 17                     | 100.00% |
| Total                                | 1269        | 5        | 920           | 635         | 631                    | 99.37%  |
| 100004.11-CP 100004.11               |             |          |               |             |                        |         |
| Precinct TSx                         | 1633        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 1633        | 1        | 254           | 156         | 153                    | 98.08%  |
| Precinct VBM                         | 1633        | 1        | 738           | 478         | 467                    | 97.70%  |
| MB Precinct                          | 1633        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1633        | 1        | 4             | 1           | 1                      | 100.00% |
| Total                                | 1633        | 5        | 996           | 635         | 621                    | 97.80%  |
| 100005.11-CP 100005.11               |             |          |               |             |                        |         |
| Precinct TSx                         | 1586        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 1586        | 1        | 146           | 94          | 91                     | 96.81%  |
| Precinct VBM                         | 1586        | 1        | 895           | 579         | 574                    | 99.14%  |
| MB Precinct                          | 1586        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1586        | 1        | 23            | 18          | 17                     | 94.44%  |
| Total                                | 1586        | 5        | 1064          | 691         | 682                    | 98.70%  |
| 100006.11-CP 100006.11               |             |          |               |             |                        |         |
| Precinct TSx                         | 1928        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 1928        | 1        | 252           | 148         | 148                    | 100.00% |
| Precinct VBM                         | 1928        | 1        | 944           | 612         | 604                    | 98.69%  |
| MB Precinct                          | 1928        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1928        | 1        | 31            | 12          | 12                     | 100.00% |
| Total                                | 1928        | 5        | 1227          | 772         | 764                    | 98.96%  |
| 100008.11-CP 100008.11               |             |          |               |             |                        |         |
| Precinct TSx                         | 1671        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 1671        | 1        | 197           | 124         | 123                    | 99.19%  |
| Precinct VBM                         | 1671        | 1        | 740           | 492         | 485                    | 98.58%  |
| MB Precinct                          | 1671        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1671        | 1        | 17            | 11          | 11                     | 100.00% |
| Total                                | 1671        | 5        | 954           | 627         | 619                    | 98.72%  |
| 100009.11-CP 100009.11               |             |          |               |             |                        |         |
| Precinct TSx                         | 1838        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 1838        | 1        | 169           | 111         | 106                    | 95.50%  |
| Precinct VBM                         | 1838        | 1        | 751           | 545         | 534                    | 97.98%  |
| MB Precinct                          | 1838        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1838        | 1        | 26            | 13          | 13                     | 100.00% |
| Total                                | 1838        | 5        | 946           | 669         | 653                    | 97.61%  |
| 100012.11-CP 100012.11               |             |          |               |             |                        |         |
| Precinct TSx                         | 2108        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 2108        | 1        | 255           | 161         | 161                    | 100.00% |
| Precinct VBM                         | 2108        | 1        | 968           | 635         | 627                    | 98.74%  |

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|--------------------------------------|-------------|----------|---------------|-------------|---------------------|---------|
|                                      | Reg. Voters | Vote For | Times Counted | Total Votes | KENNETH J. MELIKIAN |         |
| MB Precinct                          | 2108        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 2108        | 1        | 29            | 19          | 18                  | 94.74%  |
| Total                                | 2108        | 5        | 1252          | 815         | 806                 | 98.90%  |
| 100013.11-CP 100013.11               |             |          |               |             |                     |         |
| Precinct TSx                         | 2087        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 2087        | 1        | 250           | 160         | 157                 | 98.13%  |
| Precinct VBM                         | 2087        | 1        | 908           | 613         | 605                 | 98.69%  |
| MB Precinct                          | 2087        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 2087        | 1        | 28            | 15          | 15                  | 100.00% |
| Total                                | 2087        | 5        | 1186          | 788         | 777                 | 98.60%  |
| 100014.11-CP 100014.11               |             |          |               |             |                     |         |
| Precinct TSx                         | 1659        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1659        | 1        | 211           | 128         | 125                 | 97.66%  |
| Precinct VBM                         | 1659        | 1        | 706           | 453         | 447                 | 98.68%  |
| MB Precinct                          | 1659        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1659        | 1        | 30            | 15          | 15                  | 100.00% |
| Total                                | 1659        | 5        | 947           | 596         | 587                 | 98.49%  |
| 100022.11-CP 100022.11               |             |          |               |             |                     |         |
| Precinct TSx                         | 1432        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1432        | 1        | 177           | 109         | 109                 | 100.00% |
| Precinct VBM                         | 1432        | 1        | 678           | 421         | 415                 | 98.57%  |
| MB Precinct                          | 1432        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1432        | 1        | 13            | 7           | 7                   | 100.00% |
| Total                                | 1432        | 5        | 868           | 537         | 531                 | 98.88%  |
| 100023.11-CP 100023.11               |             |          |               |             |                     |         |
| Precinct TSx                         | 2135        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 2135        | 1        | 217           | 142         | 139                 | 97.89%  |
| Precinct VBM                         | 2135        | 1        | 1053          | 715         | 710                 | 99.30%  |
| MB Precinct                          | 2135        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 2135        | 1        | 33            | 22          | 21                  | 95.45%  |
| Total                                | 2135        | 5        | 1303          | 879         | 870                 | 98.98%  |
| 200003.12-CP 200003.12               |             |          |               |             |                     |         |
| Precinct TSx                         | 1362        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1362        | 1        | 166           | 103         | 99                  | 96.12%  |
| Precinct VBM                         | 1362        | 1        | 558           | 382         | 377                 | 98.69%  |
| MB Precinct                          | 1362        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1362        | 1        | 31            | 24          | 23                  | 95.83%  |
| Total                                | 1362        | 5        | 755           | 509         | 499                 | 98.04%  |
| 200004.12-CP 200004.12               |             |          |               |             |                     |         |
| Precinct TSx                         | 1507        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1507        | 1        | 182           | 117         | 114                 | 97.44%  |
| Precinct VBM                         | 1507        | 1        | 662           | 459         | 451                 | 98.26%  |
| MB Precinct                          | 1507        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1507        | 1        | 14            | 7           | 7                   | 100.00% |
| Total                                | 1507        | 5        | 858           | 583         | 572                 | 98.11%  |
| 200005.02-CP 200005.02               |             |          |               |             |                     |         |
| Precinct TSx                         | 867         | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 867         | 1        | 94            | 63          | 61                  | 96.83%  |
| Precinct VBM                         | 867         | 1        | 452           | 289         | 288                 | 99.65%  |
| MB Precinct                          | 867         | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 867         | 1        | 21            | 13          | 13                  | 100.00% |
| Total                                | 867         | 5        | 567           | 365         | 362                 | 99.18%  |
| 200006.12-CP 200006.12               |             |          |               |             |                     |         |
| Precinct TSx                         | 1086        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1086        | 1        | 117           | 68          | 67                  | 98.53%  |
| Precinct VBM                         | 1086        | 1        | 532           | 362         | 348                 | 96.13%  |
| MB Precinct                          | 1086        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1086        | 1        | 29            | 16          | 16                  | 100.00% |
| Total                                | 1086        | 5        | 678           | 446         | 431                 | 96.64%  |
| 200008.12-CP 200008.12               |             |          |               |             |                     |         |
| Precinct TSx                         | 999         | 1        | 0             | 0           | 0                   | -       |

**COUNTY OF EL DORADO**  
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|--------------------------------------|-------------|----------|---------------|-------------|---------------------|---------|
|                                      | Reg. Voters | Vote For | Times Counted | Total Votes | KENNETH J. MELIKIAN |         |
| Precinct OS                          | 999         | 1        | 155           | 95          | 93                  | 97.89%  |
| Precinct VBM                         | 999         | 1        | 379           | 248         | 245                 | 98.79%  |
| MB Precinct                          | 999         | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 999         | 1        | 10            | 5           | 5                   | 100.00% |
| Total                                | 999         | 5        | 544           | 348         | 343                 | 98.56%  |
| 200010.12-CP 200010.12               |             |          |               |             |                     |         |
| Precinct TSx                         | 1976        | 1        | 4             | 3           | 3                   | 100.00% |
| Precinct OS                          | 1976        | 1        | 197           | 117         | 116                 | 99.15%  |
| Precinct VBM                         | 1976        | 1        | 859           | 582         | 574                 | 98.63%  |
| MB Precinct                          | 1976        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1976        | 1        | 34            | 18          | 18                  | 100.00% |
| Total                                | 1976        | 5        | 1094          | 720         | 711                 | 98.75%  |
| 200012.12-CP 200012.12               |             |          |               |             |                     |         |
| Precinct TSx                         | 1064        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1064        | 1        | 145           | 98          | 98                  | 100.00% |
| Precinct VBM                         | 1064        | 1        | 464           | 306         | 303                 | 99.02%  |
| MB Precinct                          | 1064        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1064        | 1        | 14            | 10          | 10                  | 100.00% |
| Total                                | 1064        | 5        | 623           | 414         | 411                 | 99.28%  |
| 200013.12-CP 200013.12               |             |          |               |             |                     |         |
| Precinct TSx                         | 1574        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1574        | 1        | 211           | 140         | 136                 | 97.14%  |
| Precinct VBM                         | 1574        | 1        | 701           | 492         | 480                 | 97.56%  |
| MB Precinct                          | 1574        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1574        | 1        | 23            | 15          | 15                  | 100.00% |
| Total                                | 1574        | 5        | 935           | 647         | 631                 | 97.53%  |
| 200014.12-CP 200014.12               |             |          |               |             |                     |         |
| Precinct TSx                         | 1438        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1438        | 1        | 201           | 134         | 133                 | 99.25%  |
| Precinct VBM                         | 1438        | 1        | 613           | 407         | 398                 | 97.79%  |
| MB Precinct                          | 1438        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1438        | 1        | 30            | 24          | 24                  | 100.00% |
| Total                                | 1438        | 5        | 844           | 565         | 555                 | 98.23%  |
| 200015.02-CP 200015.02               |             |          |               |             |                     |         |
| Precinct TSx                         | 1832        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1832        | 1        | 218           | 124         | 122                 | 98.39%  |
| Precinct VBM                         | 1832        | 1        | 886           | 563         | 556                 | 98.76%  |
| MB Precinct                          | 1832        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1832        | 1        | 22            | 17          | 17                  | 100.00% |
| Total                                | 1832        | 5        | 1126          | 704         | 695                 | 98.72%  |
| 200016.12-CP 200016.12               |             |          |               |             |                     |         |
| Precinct TSx                         | 301         | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 301         | 1        | 26            | 14          | 14                  | 100.00% |
| Precinct VBM                         | 301         | 1        | 139           | 98          | 93                  | 94.90%  |
| MB Precinct                          | 301         | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 301         | 1        | 6             | 4           | 4                   | 100.00% |
| Total                                | 301         | 5        | 171           | 116         | 111                 | 95.69%  |
| 200016.13-CP 200016.13               |             |          |               |             |                     |         |
| Precinct TSx                         | 565         | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 565         | 1        | 80            | 43          | 41                  | 95.35%  |
| Precinct VBM                         | 565         | 1        | 338           | 208         | 203                 | 97.60%  |
| MB Precinct                          | 565         | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 565         | 1        | 6             | 2           | 2                   | 100.00% |
| Total                                | 565         | 5        | 424           | 253         | 246                 | 97.23%  |
| 200017.02-CP 200017.02               |             |          |               |             |                     |         |
| Precinct TSx                         | 1286        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1286        | 1        | 146           | 96          | 94                  | 97.92%  |
| Precinct VBM                         | 1286        | 1        | 594           | 412         | 404                 | 98.06%  |
| MB Precinct                          | 1286        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1286        | 1        | 12            | 8           | 8                   | 100.00% |
| Total                                | 1286        | 5        | 752           | 516         | 506                 | 98.06%  |



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|------------------------|--------------------------------------|----------|---------------|-------------|-----|------------------------|
|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes |     |                        |
| 200018.02-CP 200018.02 |                                      |          |               |             |     |                        |
| Precinct TSx           | 1454                                 | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 1454                                 | 1        | 169           | 108         | 107 | 99.07%                 |
| Precinct VBM           | 1454                                 | 1        | 704           | 468         | 458 | 97.86%                 |
| MB Precinct            | 1454                                 | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 1454                                 | 1        | 13            | 6           | 6   | 100.00%                |
| Total                  | 1454                                 | 5        | 886           | 582         | 571 | 98.11%                 |
| 200020.02-CP 200020.02 |                                      |          |               |             |     |                        |
| Precinct TSx           | 1213                                 | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 1213                                 | 1        | 139           | 98          | 97  | 98.98%                 |
| Precinct VBM           | 1213                                 | 1        | 526           | 359         | 350 | 97.49%                 |
| MB Precinct            | 1213                                 | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 1213                                 | 1        | 19            | 12          | 11  | 91.67%                 |
| Total                  | 1213                                 | 5        | 684           | 469         | 458 | 97.65%                 |
| 200021.01-CP 200021.01 |                                      |          |               |             |     |                        |
| Precinct TSx           | 436                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 436                                  | 1        | 38            | 17          | 17  | 100.00%                |
| Precinct VBM           | 436                                  | 1        | 206           | 133         | 129 | 96.99%                 |
| MB Precinct            | 436                                  | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 436                                  | 1        | 7             | 3           | 2   | 66.67%                 |
| Total                  | 436                                  | 5        | 251           | 153         | 148 | 96.73%                 |
| 200021.02-CP 200021.02 |                                      |          |               |             |     |                        |
| Precinct TSx           | 1296                                 | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 1296                                 | 1        | 170           | 111         | 109 | 98.20%                 |
| Precinct VBM           | 1296                                 | 1        | 752           | 449         | 440 | 98.00%                 |
| MB Precinct            | 1296                                 | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 1296                                 | 1        | 20            | 9           | 9   | 100.00%                |
| Total                  | 1296                                 | 5        | 942           | 569         | 558 | 98.07%                 |
| 200024.02-CP 200024.02 |                                      |          |               |             |     |                        |
| Precinct TSx           | 555                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 555                                  | 1        | 55            | 37          | 35  | 94.59%                 |
| Precinct VBM           | 555                                  | 1        | 282           | 204         | 200 | 98.04%                 |
| MB Precinct            | 555                                  | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 555                                  | 1        | 9             | 4           | 3   | 75.00%                 |
| Total                  | 555                                  | 5        | 346           | 245         | 238 | 97.14%                 |
| 300001.04-CP 300001.04 |                                      |          |               |             |     |                        |
| Precinct TSx           | 259                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 259                                  | 1        | 12            | 6           | 6   | 100.00%                |
| Precinct VBM           | 259                                  | 1        | 100           | 79          | 76  | 96.20%                 |
| MB Precinct            | 259                                  | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 259                                  | 1        | 0             | 0           | 0   | -                      |
| Total                  | 259                                  | 5        | 112           | 85          | 82  | 96.47%                 |
| 300001.05-CP 300001.05 |                                      |          |               |             |     |                        |
| Precinct TSx           | 731                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 731                                  | 1        | 76            | 56          | 56  | 100.00%                |
| Precinct VBM           | 731                                  | 1        | 330           | 231         | 228 | 98.70%                 |
| MB Precinct            | 731                                  | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 731                                  | 1        | 10            | 9           | 9   | 100.00%                |
| Total                  | 731                                  | 5        | 416           | 296         | 293 | 98.99%                 |
| 300003.05-CP 300003.05 |                                      |          |               |             |     |                        |
| Precinct TSx           | 1932                                 | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 1932                                 | 1        | 205           | 132         | 130 | 98.48%                 |
| Precinct VBM           | 1932                                 | 1        | 924           | 601         | 595 | 99.00%                 |
| MB Precinct            | 1932                                 | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 1932                                 | 1        | 27            | 14          | 14  | 100.00%                |
| Total                  | 1932                                 | 5        | 1156          | 747         | 739 | 98.93%                 |
| 300004.05-CP 300004.05 |                                      |          |               |             |     |                        |
| Precinct TSx           | 1273                                 | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 1273                                 | 1        | 175           | 112         | 110 | 98.21%                 |
| Precinct VBM           | 1273                                 | 1        | 576           | 398         | 393 | 98.74%                 |
| MB Precinct            | 1273                                 | 1        | 0             | 0           | 0   | -                      |

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|                                      | Reg. Voters | Vote For | Times Counted | Total Votes | KENNETH J. MELIKIAN |         |
| Provisional                          | 1273        | 1        | 17            | 11          | 11                  | 100.00% |
| Total                                | 1273        | 5        | 768           | 521         | 514                 | 98.66%  |
| 300005.05-CP 300005.05               |             |          |               |             |                     |         |
| Precinct TSx                         | 1374        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1374        | 1        | 163           | 102         | 102                 | 100.00% |
| Precinct VBM                         | 1374        | 1        | 660           | 433         | 421                 | 97.23%  |
| MB Precinct                          | 1374        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1374        | 1        | 26            | 15          | 15                  | 100.00% |
| Total                                | 1374        | 5        | 849           | 550         | 538                 | 97.82%  |
| 300008.05-CP 300008.05               |             |          |               |             |                     |         |
| Precinct TSx                         | 1366        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1366        | 1        | 139           | 88          | 88                  | 100.00% |
| Precinct VBM                         | 1366        | 1        | 740           | 491         | 481                 | 97.96%  |
| MB Precinct                          | 1366        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1366        | 1        | 14            | 7           | 7                   | 100.00% |
| Total                                | 1366        | 5        | 893           | 586         | 576                 | 98.29%  |
| 300009.04-CP 300009.04               |             |          |               |             |                     |         |
| Precinct TSx                         | 873         | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 873         | 1        | 121           | 84          | 81                  | 96.43%  |
| Precinct VBM                         | 873         | 1        | 395           | 243         | 239                 | 98.35%  |
| MB Precinct                          | 873         | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 873         | 1        | 13            | 5           | 5                   | 100.00% |
| Total                                | 873         | 5        | 529           | 332         | 325                 | 97.89%  |
| 300010.04-CP 300010.04               |             |          |               |             |                     |         |
| Precinct TSx                         | 1340        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1340        | 1        | 171           | 106         | 103                 | 97.17%  |
| Precinct VBM                         | 1340        | 1        | 656           | 456         | 452                 | 99.12%  |
| MB Precinct                          | 1340        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1340        | 1        | 11            | 4           | 4                   | 100.00% |
| Total                                | 1340        | 5        | 838           | 566         | 559                 | 98.76%  |
| 300011.04-CP 300011.04               |             |          |               |             |                     |         |
| Precinct TSx                         | 1491        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1491        | 1        | 142           | 96          | 92                  | 95.83%  |
| Precinct VBM                         | 1491        | 1        | 711           | 469         | 459                 | 97.87%  |
| MB Precinct                          | 1491        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1491        | 1        | 22            | 12          | 12                  | 100.00% |
| Total                                | 1491        | 5        | 875           | 577         | 563                 | 97.57%  |
| 300013.05-CP 300013.05               |             |          |               |             |                     |         |
| Precinct TSx                         | 1490        | 1        | 3             | 2           | 2                   | 100.00% |
| Precinct OS                          | 1490        | 1        | 128           | 82          | 82                  | 100.00% |
| Precinct VBM                         | 1490        | 1        | 721           | 491         | 482                 | 98.17%  |
| MB Precinct                          | 1490        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1490        | 1        | 15            | 6           | 6                   | 100.00% |
| Total                                | 1490        | 5        | 867           | 581         | 572                 | 98.45%  |
| 300015.04-CP 300015.04               |             |          |               |             |                     |         |
| Precinct TSx                         | 119         | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 119         | 1        | 10            | 7           | 7                   | 100.00% |
| Precinct VBM                         | 119         | 1        | 63            | 37          | 36                  | 97.30%  |
| MB Precinct                          | 119         | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 119         | 1        | 4             | 4           | 4                   | 100.00% |
| Total                                | 119         | 5        | 77            | 48          | 47                  | 97.92%  |
| 300015.05-CP 300015.05               |             |          |               |             |                     |         |
| Precinct TSx                         | 503         | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 503         | 1        | 66            | 43          | 43                  | 100.00% |
| Precinct VBM                         | 503         | 1        | 266           | 164         | 161                 | 98.17%  |
| MB Precinct                          | 503         | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 503         | 1        | 2             | 1           | 1                   | 100.00% |
| Total                                | 503         | 5        | 334           | 208         | 205                 | 98.56%  |
| 300017.05-CP 300017.05               |             |          |               |             |                     |         |
| Precinct TSx                         | 1240        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1240        | 1        | 117           | 70          | 66                  | 94.29%  |

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|                                      | Reg. Voters | Vote For | Times Counted | Total Votes | KENNETH J.<br>MELIKIAN |         |
| Precinct VBM                         | 1240        | 1        | 655           | 447         | 441                    | 98.66%  |
| MB Precinct                          | 1240        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1240        | 1        | 9             | 6           | 6                      | 100.00% |
| Total                                | 1240        | 5        | 781           | 523         | 513                    | 98.09%  |
| 300018.04-CP 300018.04               |             |          |               |             |                        |         |
| Precinct TSx                         | 1306        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 1306        | 1        | 148           | 102         | 100                    | 98.04%  |
| Precinct VBM                         | 1306        | 1        | 505           | 337         | 330                    | 97.92%  |
| MB Precinct                          | 1306        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1306        | 1        | 31            | 20          | 19                     | 95.00%  |
| Total                                | 1306        | 5        | 684           | 459         | 449                    | 97.82%  |
| 300020.04-CP 300020.04               |             |          |               |             |                        |         |
| Precinct TSx                         | 1401        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 1401        | 1        | 168           | 112         | 111                    | 99.11%  |
| Precinct VBM                         | 1401        | 1        | 632           | 426         | 415                    | 97.42%  |
| MB Precinct                          | 1401        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1401        | 1        | 14            | 6           | 6                      | 100.00% |
| Total                                | 1401        | 5        | 814           | 544         | 532                    | 97.79%  |
| 300021.03-CP 300021.03               |             |          |               |             |                        |         |
| Precinct TSx                         | 1234        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 1234        | 1        | 153           | 100         | 97                     | 97.00%  |
| Precinct VBM                         | 1234        | 1        | 614           | 405         | 396                    | 97.78%  |
| MB Precinct                          | 1234        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1234        | 1        | 13            | 5           | 5                      | 100.00% |
| Total                                | 1234        | 5        | 780           | 510         | 498                    | 97.65%  |
| 300022.05-CP 300022.05               |             |          |               |             |                        |         |
| Precinct TSx                         | 872         | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 872         | 1        | 101           | 68          | 66                     | 97.06%  |
| Precinct VBM                         | 872         | 1        | 356           | 255         | 248                    | 97.25%  |
| MB Precinct                          | 872         | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 872         | 1        | 12            | 8           | 7                      | 87.50%  |
| Total                                | 872         | 5        | 469           | 331         | 321                    | 96.98%  |
| 300023.03-CP 300023.03               |             |          |               |             |                        |         |
| Precinct TSx                         | 1819        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 1819        | 1        | 228           | 154         | 154                    | 100.00% |
| Precinct VBM                         | 1819        | 1        | 924           | 622         | 615                    | 98.87%  |
| MB Precinct                          | 1819        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1819        | 1        | 26            | 15          | 14                     | 93.33%  |
| Total                                | 1819        | 5        | 1178          | 791         | 783                    | 98.99%  |
| 300023.05-CP 300023.05               |             |          |               |             |                        |         |
| Precinct TSx                         | 386         | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 386         | 1        | 45            | 30          | 30                     | 100.00% |
| Precinct VBM                         | 386         | 1        | 193           | 134         | 133                    | 99.25%  |
| MB Precinct                          | 386         | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 386         | 1        | 8             | 4           | 4                      | 100.00% |
| Total                                | 386         | 5        | 246           | 168         | 167                    | 99.40%  |
| 400001.14-CP 400001.14               |             |          |               |             |                        |         |
| Precinct TSx                         | 1957        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 1957        | 1        | 178           | 101         | 100                    | 99.01%  |
| Precinct VBM                         | 1957        | 1        | 992           | 682         | 670                    | 98.24%  |
| MB Precinct                          | 1957        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1957        | 1        | 23            | 18          | 18                     | 100.00% |
| Total                                | 1957        | 5        | 1193          | 801         | 788                    | 98.38%  |
| 400002.08-CP 400002.08               |             |          |               |             |                        |         |
| Precinct TSx                         | 1508        | 1        | 0             | 0           | 0                      | -       |
| Precinct OS                          | 1508        | 1        | 200           | 131         | 130                    | 99.24%  |
| Precinct VBM                         | 1508        | 1        | 730           | 450         | 443                    | 98.44%  |
| MB Precinct                          | 1508        | 1        | 0             | 0           | 0                      | -       |
| Provisional                          | 1508        | 1        | 15            | 7           | 7                      | 100.00% |
| Total                                | 1508        | 5        | 945           | 588         | 580                    | 98.64%  |
| 400003.08-CP 400003.08               |             |          |               |             |                        |         |

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**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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**OFFICIAL STATEMENT OF VOTES CAST**

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| Judge of the Superior Court Office 7 |             |          |               |             |                        |               |
|--------------------------------------|-------------|----------|---------------|-------------|------------------------|---------------|
|                                      | Reg. Voters | Vote For | Times Counted | Total Votes | KENNETH J.<br>MELIKIAN |               |
| Precinct TSx                         | 1800        | 1        | 0             | 0           | 0                      | -             |
| Precinct OS                          | 1800        | 1        | 245           | 139         | 135                    | 97.12%        |
| Precinct VBM                         | 1800        | 1        | 773           | 504         | 494                    | 98.02%        |
| MB Precinct                          | 1800        | 1        | 0             | 0           | 0                      | -             |
| Provisional                          | 1800        | 1        | 35            | 17          | 17                     | 100.00%       |
| <b>Total</b>                         | <b>1800</b> | <b>5</b> | <b>1053</b>   | <b>660</b>  | <b>646</b>             | <b>97.88%</b> |
| 400004.14-CP 400004.14               |             |          |               |             |                        |               |
| Precinct TSx                         | 1536        | 1        | 0             | 0           | 0                      | -             |
| Precinct OS                          | 1536        | 1        | 194           | 124         | 119                    | 95.97%        |
| Precinct VBM                         | 1536        | 1        | 662           | 450         | 437                    | 97.11%        |
| MB Precinct                          | 1536        | 1        | 0             | 0           | 0                      | -             |
| Provisional                          | 1536        | 1        | 19            | 15          | 14                     | 93.33%        |
| <b>Total</b>                         | <b>1536</b> | <b>5</b> | <b>875</b>    | <b>589</b>  | <b>570</b>             | <b>96.77%</b> |
| 400005.08-CP 400005.08               |             |          |               |             |                        |               |
| Precinct TSx                         | 655         | 1        | 0             | 0           | 0                      | -             |
| Precinct OS                          | 655         | 1        | 79            | 44          | 44                     | 100.00%       |
| Precinct VBM                         | 655         | 1        | 262           | 162         | 159                    | 98.15%        |
| MB Precinct                          | 655         | 1        | 0             | 0           | 0                      | -             |
| Provisional                          | 655         | 1        | 11            | 7           | 7                      | 100.00%       |
| <b>Total</b>                         | <b>655</b>  | <b>5</b> | <b>352</b>    | <b>213</b>  | <b>210</b>             | <b>98.59%</b> |
| 400006.08-CP 400006.08               |             |          |               |             |                        |               |
| Precinct TSx                         | 1537        | 1        | 0             | 0           | 0                      | -             |
| Precinct OS                          | 1537        | 1        | 228           | 142         | 137                    | 96.48%        |
| Precinct VBM                         | 1537        | 1        | 722           | 478         | 473                    | 98.95%        |
| MB Precinct                          | 1537        | 1        | 0             | 0           | 0                      | -             |
| Provisional                          | 1537        | 1        | 19            | 12          | 11                     | 91.67%        |
| <b>Total</b>                         | <b>1537</b> | <b>5</b> | <b>969</b>    | <b>632</b>  | <b>621</b>             | <b>98.26%</b> |
| 400007.08-CP 400007.08               |             |          |               |             |                        |               |
| Precinct TSx                         | 1766        | 1        | 0             | 0           | 0                      | -             |
| Precinct OS                          | 1766        | 1        | 228           | 147         | 140                    | 95.24%        |
| Precinct VBM                         | 1766        | 1        | 765           | 505         | 491                    | 97.23%        |
| MB Precinct                          | 1766        | 1        | 0             | 0           | 0                      | -             |
| Provisional                          | 1766        | 1        | 22            | 15          | 15                     | 100.00%       |
| <b>Total</b>                         | <b>1766</b> | <b>5</b> | <b>1015</b>   | <b>667</b>  | <b>646</b>             | <b>96.85%</b> |
| 400010.08-CP 400010.08               |             |          |               |             |                        |               |
| Precinct TSx                         | 1875        | 1        | 0             | 0           | 0                      | -             |
| Precinct OS                          | 1875        | 1        | 244           | 160         | 156                    | 97.50%        |
| Precinct VBM                         | 1875        | 1        | 869           | 584         | 565                    | 96.75%        |
| MB Precinct                          | 1875        | 1        | 0             | 0           | 0                      | -             |
| Provisional                          | 1875        | 1        | 31            | 22          | 20                     | 90.91%        |
| <b>Total</b>                         | <b>1875</b> | <b>5</b> | <b>1144</b>   | <b>766</b>  | <b>741</b>             | <b>96.74%</b> |
| 400012.08-CP 400012.08               |             |          |               |             |                        |               |
| Precinct TSx                         | 877         | 1        | 0             | 0           | 0                      | -             |
| Precinct OS                          | 877         | 1        | 113           | 84          | 84                     | 100.00%       |
| Precinct VBM                         | 877         | 1        | 434           | 282         | 278                    | 98.58%        |
| MB Precinct                          | 877         | 1        | 0             | 0           | 0                      | -             |
| Provisional                          | 877         | 1        | 22            | 12          | 12                     | 100.00%       |
| <b>Total</b>                         | <b>877</b>  | <b>5</b> | <b>569</b>    | <b>378</b>  | <b>374</b>             | <b>98.94%</b> |
| 400013.08-CP 400013.08               |             |          |               |             |                        |               |
| Precinct TSx                         | 758         | 1        | 0             | 0           | 0                      | -             |
| Precinct OS                          | 758         | 1        | 127           | 75          | 70                     | 93.33%        |
| Precinct VBM                         | 758         | 1        | 315           | 211         | 208                    | 98.58%        |
| MB Precinct                          | 758         | 1        | 0             | 0           | 0                      | -             |
| Provisional                          | 758         | 1        | 4             | 2           | 2                      | 100.00%       |
| <b>Total</b>                         | <b>758</b>  | <b>5</b> | <b>446</b>    | <b>288</b>  | <b>280</b>             | <b>97.22%</b> |
| 400014.08-CP 400014.08               |             |          |               |             |                        |               |
| Precinct TSx                         | 1526        | 1        | 0             | 0           | 0                      | -             |
| Precinct OS                          | 1526        | 1        | 230           | 127         | 124                    | 97.64%        |
| Precinct VBM                         | 1526        | 1        | 736           | 466         | 454                    | 97.42%        |
| MB Precinct                          | 1526        | 1        | 0             | 0           | 0                      | -             |
| Provisional                          | 1526        | 1        | 36            | 19          | 19                     | 100.00%       |

**COUNTY OF EL DORADO**  
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|                        | Judge of the Superior Court Office 7 |          |               |             |     | KENNETH J.<br>MELIKIAN |
|------------------------|--------------------------------------|----------|---------------|-------------|-----|------------------------|
|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes |     |                        |
| Total                  | 1526                                 | 5        | 1002          | 612         | 597 | 97.55%                 |
| 400017.08-CP 400017.08 |                                      |          |               |             |     |                        |
| Precinct TSx           | 1495                                 | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 1495                                 | 1        | 175           | 114         | 113 | 99.12%                 |
| Precinct VBM           | 1495                                 | 1        | 752           | 502         | 497 | 99.00%                 |
| MB Precinct            | 1495                                 | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 1495                                 | 1        | 28            | 20          | 18  | 90.00%                 |
| Total                  | 1495                                 | 5        | 955           | 636         | 628 | 98.74%                 |
| 400018.08-CP 400018.08 |                                      |          |               |             |     |                        |
| Precinct TSx           | 597                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 597                                  | 1        | 86            | 52          | 51  | 98.08%                 |
| Precinct VBM           | 597                                  | 1        | 276           | 165         | 163 | 98.79%                 |
| MB Precinct            | 597                                  | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 597                                  | 1        | 7             | 3           | 3   | 100.00%                |
| Total                  | 597                                  | 5        | 369           | 220         | 217 | 98.64%                 |
| 400020.08-CP 400020.08 |                                      |          |               |             |     |                        |
| Precinct TSx           | 724                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 724                                  | 1        | 114           | 78          | 78  | 100.00%                |
| Precinct VBM           | 724                                  | 1        | 343           | 224         | 215 | 95.98%                 |
| MB Precinct            | 724                                  | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 724                                  | 1        | 2             | 2           | 2   | 100.00%                |
| Total                  | 724                                  | 5        | 459           | 304         | 295 | 97.04%                 |
| 400021.07-CP 400021.07 |                                      |          |               |             |     |                        |
| Precinct TSx           | 1332                                 | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 1332                                 | 1        | 127           | 84          | 82  | 97.62%                 |
| Precinct VBM           | 1332                                 | 1        | 767           | 504         | 487 | 96.63%                 |
| MB Precinct            | 1332                                 | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 1332                                 | 1        | 18            | 8           | 8   | 100.00%                |
| Total                  | 1332                                 | 5        | 912           | 596         | 577 | 96.81%                 |
| 500001.10-CP 500001.10 |                                      |          |               |             |     |                        |
| Precinct TSx           | 1717                                 | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 1717                                 | 1        | 210           | 128         | 125 | 97.66%                 |
| Precinct VBM           | 1717                                 | 1        | 617           | 391         | 386 | 98.72%                 |
| MB Precinct            | 1717                                 | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 1717                                 | 1        | 19            | 10          | 10  | 100.00%                |
| Total                  | 1717                                 | 5        | 846           | 529         | 521 | 98.49%                 |
| 500002.10-CP 500002.10 |                                      |          |               |             |     |                        |
| Precinct TSx           | 1381                                 | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 1381                                 | 1        | 137           | 82          | 77  | 93.90%                 |
| Precinct VBM           | 1381                                 | 1        | 412           | 271         | 265 | 97.79%                 |
| MB Precinct            | 1381                                 | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 1381                                 | 1        | 31            | 18          | 17  | 94.44%                 |
| Total                  | 1381                                 | 5        | 580           | 371         | 359 | 96.77%                 |
| 500003.10-CP 500003.10 |                                      |          |               |             |     |                        |
| Precinct TSx           | 1065                                 | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 1065                                 | 1        | 131           | 69          | 68  | 98.55%                 |
| Precinct VBM           | 1065                                 | 1        | 346           | 220         | 212 | 96.36%                 |
| MB Precinct            | 1065                                 | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 1065                                 | 1        | 11            | 2           | 2   | 100.00%                |
| Total                  | 1065                                 | 5        | 488           | 291         | 282 | 96.91%                 |
| 500004.10-CP 500004.10 |                                      |          |               |             |     |                        |
| Precinct TSx           | 981                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 981                                  | 1        | 122           | 83          | 83  | 100.00%                |
| Precinct VBM           | 981                                  | 1        | 264           | 161         | 156 | 96.89%                 |
| MB Precinct            | 981                                  | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 981                                  | 1        | 21            | 14          | 14  | 100.00%                |
| Total                  | 981                                  | 5        | 407           | 258         | 253 | 98.06%                 |
| 500005.10-CP 500005.10 |                                      |          |               |             |     |                        |
| Precinct TSx           | 946                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 946                                  | 1        | 107           | 74          | 71  | 95.95%                 |
| Precinct VBM           | 946                                  | 1        | 250           | 163         | 161 | 98.77%                 |

**COUNTY OF EL DORADO**  
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|--------------------------------------|-------------|----------|---------------|-------------|---------------------|---------|
|                                      | Reg. Voters | Vote For | Times Counted | Total Votes | KENNETH J. MELIKIAN |         |
| MB Precinct                          | 946         | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 946         | 1        | 16            | 11          | 11                  | 100.00% |
| Total                                | 946         | 5        | 373           | 248         | 243                 | 97.98%  |
| 500006.10-CP 500006.10               |             |          |               |             |                     |         |
| Precinct TSx                         | 1661        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1661        | 1        | 190           | 112         | 109                 | 97.32%  |
| Precinct VBM                         | 1661        | 1        | 498           | 323         | 316                 | 97.83%  |
| MB Precinct                          | 1661        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1661        | 1        | 58            | 27          | 27                  | 100.00% |
| Total                                | 1661        | 5        | 746           | 462         | 452                 | 97.84%  |
| 500007.10-CP 500007.10               |             |          |               |             |                     |         |
| Precinct TSx                         | 1788        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1788        | 1        | 208           | 131         | 126                 | 96.18%  |
| Precinct VBM                         | 1788        | 1        | 688           | 432         | 426                 | 98.61%  |
| MB Precinct                          | 1788        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1788        | 1        | 20            | 15          | 15                  | 100.00% |
| Total                                | 1788        | 5        | 916           | 578         | 567                 | 98.10%  |
| 500009.10-CP 500009.10               |             |          |               |             |                     |         |
| Precinct TSx                         | 1073        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1073        | 1        | 121           | 73          | 71                  | 97.26%  |
| Precinct VBM                         | 1073        | 1        | 304           | 191         | 191                 | 100.00% |
| MB Precinct                          | 1073        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1073        | 1        | 38            | 18          | 18                  | 100.00% |
| Total                                | 1073        | 5        | 463           | 282         | 280                 | 99.29%  |
| 500011.10-CP 500011.10               |             |          |               |             |                     |         |
| Precinct TSx                         | 1635        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1635        | 1        | 178           | 114         | 112                 | 98.25%  |
| Precinct VBM                         | 1635        | 1        | 380           | 272         | 265                 | 97.43%  |
| MB Precinct                          | 1635        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1635        | 1        | 40            | 20          | 19                  | 95.00%  |
| Total                                | 1635        | 5        | 598           | 406         | 396                 | 97.54%  |
| 500012.10-CP 500012.10               |             |          |               |             |                     |         |
| Precinct TSx                         | 1574        | 1        | 1             | 1           | 1                   | 100.00% |
| Precinct OS                          | 1574        | 1        | 195           | 101         | 98                  | 97.03%  |
| Precinct VBM                         | 1574        | 1        | 601           | 385         | 375                 | 97.40%  |
| MB Precinct                          | 1574        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1574        | 1        | 37            | 17          | 17                  | 100.00% |
| Total                                | 1574        | 5        | 834           | 504         | 491                 | 97.42%  |
| 500015.10-CP 500015.10               |             |          |               |             |                     |         |
| Precinct TSx                         | 978         | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 978         | 1        | 105           | 64          | 63                  | 98.44%  |
| Precinct VBM                         | 978         | 1        | 407           | 255         | 249                 | 97.65%  |
| MB Precinct                          | 978         | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 978         | 1        | 13            | 7           | 7                   | 100.00% |
| Total                                | 978         | 5        | 525           | 326         | 319                 | 97.85%  |
| 500020.10-CP 500020.10               |             |          |               |             |                     |         |
| Precinct TSx                         | 1530        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1530        | 1        | 161           | 108         | 104                 | 96.30%  |
| Precinct VBM                         | 1530        | 1        | 659           | 466         | 458                 | 98.28%  |
| MB Precinct                          | 1530        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1530        | 1        | 27            | 24          | 22                  | 91.67%  |
| Total                                | 1530        | 5        | 847           | 598         | 584                 | 97.66%  |
| 500021.10-CP 500021.10               |             |          |               |             |                     |         |
| Precinct TSx                         | 1404        | 1        | 0             | 0           | 0                   | -       |
| Precinct OS                          | 1404        | 1        | 169           | 111         | 111                 | 100.00% |
| Precinct VBM                         | 1404        | 1        | 632           | 456         | 442                 | 96.93%  |
| MB Precinct                          | 1404        | 1        | 0             | 0           | 0                   | -       |
| Provisional                          | 1404        | 1        | 28            | 17          | 17                  | 100.00% |
| Total                                | 1404        | 5        | 829           | 584         | 570                 | 97.60%  |
| 95U201.01-MB 95U201.01               |             |          |               |             |                     |         |
| Precinct TSx                         | 8           | 1        | 0             | 0           | 0                   | -       |

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|                        | Judge of the Superior Court Office 7 |          |               |             |     | KENNETH J.<br>MELIKIAN |
|------------------------|--------------------------------------|----------|---------------|-------------|-----|------------------------|
|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes |     |                        |
| Precinct OS            | 8                                    | 1        | 0             | 0           | 0   | -                      |
| Precinct VBM           | 8                                    | 1        | 0             | 0           | 0   | -                      |
| MB Precinct            | 8                                    | 1        | 7             | 2           | 2   | 100.00%                |
| Provisional            | 8                                    | 1        | 0             | 0           | 0   | -                      |
| Total                  | 8                                    | 5        | 7             | 2           | 2   | 100.00%                |
| 95U201.02-MB 95U201.02 |                                      |          |               |             |     |                        |
| Precinct TSx           | 541                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 541                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct VBM           | 541                                  | 1        | 0             | 0           | 0   | -                      |
| MB Precinct            | 541                                  | 1        | 358           | 216         | 214 | 99.07%                 |
| Provisional            | 541                                  | 1        | 1             | 0           | 0   | -                      |
| Total                  | 541                                  | 5        | 359           | 216         | 214 | 99.07%                 |
| 95U202.02-MB 95U202.02 |                                      |          |               |             |     |                        |
| Precinct TSx           | 859                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 859                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct VBM           | 859                                  | 1        | 0             | 0           | 0   | -                      |
| MB Precinct            | 859                                  | 1        | 574           | 362         | 356 | 98.34%                 |
| Provisional            | 859                                  | 1        | 4             | 3           | 3   | 100.00%                |
| Total                  | 859                                  | 5        | 578           | 365         | 359 | 98.36%                 |
| 95U203.02-MB 95U203.02 |                                      |          |               |             |     |                        |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0   | -                      |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0   | -                      |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0   | -                      |
| Provisional            | 0                                    | 1        | 0             | 0           | 0   | -                      |
| Total                  | 0                                    | 5        | 0             | 0           | 0   | -                      |
| 95U204.01-MB 95U204.01 |                                      |          |               |             |     |                        |
| Precinct TSx           | 895                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 895                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct VBM           | 895                                  | 1        | 0             | 0           | 0   | -                      |
| MB Precinct            | 895                                  | 1        | 651           | 427         | 418 | 97.89%                 |
| Provisional            | 895                                  | 1        | 2             | 0           | 0   | -                      |
| Total                  | 895                                  | 5        | 653           | 427         | 418 | 97.89%                 |
| 95U301.05-MB 95U301.05 |                                      |          |               |             |     |                        |
| Precinct TSx           | 244                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 244                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct VBM           | 244                                  | 1        | 0             | 0           | 0   | -                      |
| MB Precinct            | 244                                  | 1        | 151           | 103         | 100 | 97.09%                 |
| Provisional            | 244                                  | 1        | 2             | 1           | 1   | 100.00%                |
| Total                  | 244                                  | 5        | 153           | 104         | 101 | 97.12%                 |
| 95U302.04-MB 95U302.04 |                                      |          |               |             |     |                        |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0   | -                      |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0   | -                      |
| MB Precinct            | 0                                    | 1        | 1             | 1           | 1   | 100.00%                |
| Provisional            | 0                                    | 1        | 0             | 0           | 0   | -                      |
| Total                  | 0                                    | 5        | 1             | 1           | 1   | 100.00%                |
| 95U302.05-MB 95U302.05 |                                      |          |               |             |     |                        |
| Precinct TSx           | 128                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 128                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct VBM           | 128                                  | 1        | 0             | 0           | 0   | -                      |
| MB Precinct            | 128                                  | 1        | 86            | 52          | 50  | 96.15%                 |
| Provisional            | 128                                  | 1        | 0             | 0           | 0   | -                      |
| Total                  | 128                                  | 5        | 86            | 52          | 50  | 96.15%                 |
| 95U303.03-MB 95U303.03 |                                      |          |               |             |     |                        |
| Precinct TSx           | 144                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct OS            | 144                                  | 1        | 0             | 0           | 0   | -                      |
| Precinct VBM           | 144                                  | 1        | 0             | 0           | 0   | -                      |
| MB Precinct            | 144                                  | 1        | 84            | 61          | 61  | 100.00%                |
| Provisional            | 144                                  | 1        | 0             | 0           | 0   | -                      |
| Total                  | 144                                  | 5        | 84            | 61          | 61  | 100.00%                |

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|                        | Judge of the Superior Court Office 7 |          |               |             |     | KENNETH J.<br>MELIKIAN |
|------------------------|--------------------------------------|----------|---------------|-------------|-----|------------------------|
|                        | Reg. Voters                          | Vote For | Times Counted | Total Votes |     |                        |
| 95U303.05-MB 95U303.05 |                                      |          |               |             |     |                        |
| Precinct TSx           | 159                                  | 1        | 0             | 0           | 0   |                        |
| Precinct OS            | 159                                  | 1        | 0             | 0           | 0   |                        |
| Precinct VBM           | 159                                  | 1        | 0             | 0           | 0   |                        |
| MB Precinct            | 159                                  | 1        | 111           | 73          | 72  | 98.63%                 |
| Provisional            | 159                                  | 1        | 1             | 0           | 0   |                        |
| Total                  | 159                                  | 5        | 112           | 73          | 72  | 98.63%                 |
| 95U304.05-MB 95U304.05 |                                      |          |               |             |     |                        |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0   |                        |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0   |                        |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0   |                        |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0   |                        |
| Provisional            | 0                                    | 1        | 0             | 0           | 0   |                        |
| Total                  | 0                                    | 5        | 0             | 0           | 0   |                        |
| 95U305.05-MB 95U305.05 |                                      |          |               |             |     |                        |
| Precinct TSx           | 7                                    | 1        | 0             | 0           | 0   |                        |
| Precinct OS            | 7                                    | 1        | 0             | 0           | 0   |                        |
| Precinct VBM           | 7                                    | 1        | 0             | 0           | 0   |                        |
| MB Precinct            | 7                                    | 1        | 7             | 0           | 0   |                        |
| Provisional            | 7                                    | 1        | 0             | 0           | 0   |                        |
| Total                  | 7                                    | 5        | 7             | 0           | 0   |                        |
| 95U401.08-MB 95U401.08 |                                      |          |               |             |     |                        |
| Precinct TSx           | 39                                   | 1        | 0             | 0           | 0   |                        |
| Precinct OS            | 39                                   | 1        | 0             | 0           | 0   |                        |
| Precinct VBM           | 39                                   | 1        | 0             | 0           | 0   |                        |
| MB Precinct            | 39                                   | 1        | 16            | 9           | 9   | 100.00%                |
| Provisional            | 39                                   | 1        | 0             | 0           | 0   |                        |
| Total                  | 39                                   | 5        | 16            | 9           | 9   | 100.00%                |
| 95U402.07-MB 95U402.07 |                                      |          |               |             |     |                        |
| Precinct TSx           | 413                                  | 1        | 0             | 0           | 0   |                        |
| Precinct OS            | 413                                  | 1        | 0             | 0           | 0   |                        |
| Precinct VBM           | 413                                  | 1        | 0             | 0           | 0   |                        |
| MB Precinct            | 413                                  | 1        | 256           | 162         | 159 | 98.15%                 |
| Provisional            | 413                                  | 1        | 1             | 0           | 0   |                        |
| Total                  | 413                                  | 5        | 257           | 162         | 159 | 98.15%                 |
| 95U402.08-MB 95U402.08 |                                      |          |               |             |     |                        |
| Precinct TSx           | 873                                  | 1        | 0             | 0           | 0   |                        |
| Precinct OS            | 873                                  | 1        | 0             | 0           | 0   |                        |
| Precinct VBM           | 873                                  | 1        | 0             | 0           | 0   |                        |
| MB Precinct            | 873                                  | 1        | 566           | 369         | 362 | 98.10%                 |
| Provisional            | 873                                  | 1        | 3             | 2           | 2   | 100.00%                |
| Total                  | 873                                  | 5        | 569           | 371         | 364 | 98.11%                 |
| 95U403.08-MB 95U403.08 |                                      |          |               |             |     |                        |
| Precinct TSx           | 1591                                 | 1        | 0             | 0           | 0   |                        |
| Precinct OS            | 1591                                 | 1        | 0             | 0           | 0   |                        |
| Precinct VBM           | 1591                                 | 1        | 0             | 0           | 0   |                        |
| MB Precinct            | 1591                                 | 1        | 1042          | 681         | 667 | 97.94%                 |
| Provisional            | 1591                                 | 1        | 13            | 2           | 2   | 100.00%                |
| Total                  | 1591                                 | 5        | 1055          | 683         | 669 | 97.95%                 |
| 95U404.06-MB 95U404.06 |                                      |          |               |             |     |                        |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0   |                        |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0   |                        |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0   |                        |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0   |                        |
| Provisional            | 0                                    | 1        | 0             | 0           | 0   |                        |
| Total                  | 0                                    | 5        | 0             | 0           | 0   |                        |
| 95U404.08-MB 95U404.08 |                                      |          |               |             |     |                        |
| Precinct TSx           | 0                                    | 1        | 0             | 0           | 0   |                        |
| Precinct OS            | 0                                    | 1        | 0             | 0           | 0   |                        |
| Precinct VBM           | 0                                    | 1        | 0             | 0           | 0   |                        |
| MB Precinct            | 0                                    | 1        | 0             | 0           | 0   |                        |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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| Judge of the Superior Court Office 7 |             |          |               |             |                     |         |
|--------------------------------------|-------------|----------|---------------|-------------|---------------------|---------|
|                                      | Reg. Voters | Vote For | Times Counted | Total Votes | KENNETH J. MELIKIAN |         |
| Provisional                          | 0           | 1        | 0             | 0           | 0                   | 0       |
| Total                                | 0           | 5        | 0             | 0           | 0                   | 0       |
| 95U501.09-MB 95U501.09               |             |          |               |             |                     |         |
| Precinct TSx                         | 0           | 1        | 0             | 0           | 0                   | 0       |
| Precinct OS                          | 0           | 1        | 0             | 0           | 0                   | 0       |
| Precinct VBM                         | 0           | 1        | 0             | 0           | 0                   | 0       |
| MB Precinct                          | 0           | 1        | 2             | 2           | 2                   | 100.00% |
| Provisional                          | 0           | 1        | 0             | 0           | 0                   | 0       |
| Total                                | 0           | 5        | 2             | 2           | 2                   | 100.00% |
| 95U501.10-MB 95U501.10               |             |          |               |             |                     |         |
| Precinct TSx                         | 286         | 1        | 0             | 0           | 0                   | 0       |
| Precinct OS                          | 286         | 1        | 0             | 0           | 0                   | 0       |
| Precinct VBM                         | 286         | 1        | 0             | 0           | 0                   | 0       |
| MB Precinct                          | 286         | 1        | 172           | 115         | 109                 | 94.78%  |
| Provisional                          | 286         | 1        | 2             | 2           | 2                   | 100.00% |
| Total                                | 286         | 5        | 174           | 117         | 111                 | 94.87%  |
| 95U598.10-MB 95U598.10               |             |          |               |             |                     |         |
| Precinct TSx                         | 568         | 1        | 0             | 0           | 0                   | 0       |
| Precinct OS                          | 568         | 1        | 0             | 0           | 0                   | 0       |
| Precinct VBM                         | 568         | 1        | 0             | 0           | 0                   | 0       |
| MB Precinct                          | 568         | 1        | 239           | 136         | 134                 | 98.53%  |
| Provisional                          | 568         | 1        | 3             | 1           | 1                   | 100.00% |
| Total                                | 568         | 5        | 242           | 137         | 135                 | 98.54%  |
| 95U599.10-MB 95U599.10               |             |          |               |             |                     |         |
| Precinct TSx                         | 75          | 1        | 0             | 0           | 0                   | 0       |
| Precinct OS                          | 75          | 1        | 0             | 0           | 0                   | 0       |
| Precinct VBM                         | 75          | 1        | 0             | 0           | 0                   | 0       |
| MB Precinct                          | 75          | 1        | 38            | 30          | 30                  | 100.00% |
| Provisional                          | 75          | 1        | 0             | 0           | 0                   | 0       |
| Total                                | 75          | 5        | 38            | 30          | 30                  | 100.00% |
| 96U101.11-MB 96U101.11               |             |          |               |             |                     |         |
| Precinct TSx                         | 626         | 1        | 0             | 0           | 0                   | 0       |
| Precinct OS                          | 626         | 1        | 0             | 0           | 0                   | 0       |
| Precinct VBM                         | 626         | 1        | 0             | 0           | 0                   | 0       |
| MB Precinct                          | 626         | 1        | 423           | 273         | 264                 | 96.70%  |
| Provisional                          | 626         | 1        | 6             | 6           | 6                   | 100.00% |
| Total                                | 626         | 5        | 429           | 279         | 270                 | 96.77%  |
| 96U401.14-MB 96U401.14               |             |          |               |             |                     |         |
| Precinct TSx                         | 566         | 1        | 0             | 0           | 0                   | 0       |
| Precinct OS                          | 566         | 1        | 0             | 0           | 0                   | 0       |
| Precinct VBM                         | 566         | 1        | 0             | 0           | 0                   | 0       |
| MB Precinct                          | 566         | 1        | 359           | 193         | 188                 | 97.41%  |
| Provisional                          | 566         | 1        | 6             | 5           | 5                   | 100.00% |
| Total                                | 566         | 5        | 365           | 198         | 193                 | 97.47%  |
| Total                                |             |          |               |             |                     |         |
| Precinct TSx                         | 109479      | 102      | 8             | 6           | 6                   | 100.00% |
| Precinct OS                          | 109479      | 102      | 12009         | 7575        | 7426                | 98.03%  |
| Precinct VBM                         | 109479      | 102      | 45535         | 30169       | 29623               | 98.19%  |
| MB Precinct                          | 109479      | 102      | 5143          | 3267        | 3198                | 97.89%  |
| Provisional                          | 109479      | 102      | 1584          | 925         | 904                 | 97.73%  |
| Total                                | 109479      | 510      | 64279         | 41942       | 41157               | 98.13%  |
| STATE BOARD OF EQUALIZATION          |             |          |               |             |                     |         |
| STATE BOARD OF EQUALIZATION 1        |             |          |               |             |                     |         |
| Precinct TSx                         | 109479      | 102      | 8             | 6           | 6                   | 100.00% |
| Precinct OS                          | 109479      | 102      | 12009         | 7575        | 7426                | 98.03%  |
| Precinct VBM                         | 109479      | 102      | 45535         | 30169       | 29623               | 98.19%  |
| MB Precinct                          | 109479      | 102      | 5143          | 3267        | 3198                | 97.89%  |
| Provisional                          | 109479      | 102      | 1584          | 925         | 904                 | 97.73%  |
| Total                                | 109479      | 510      | 64279         | 41942       | 41157               | 98.13%  |
| Total                                |             |          |               |             |                     |         |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|--------------------------------------|---------------|------------|---------------|--------------|---------------------|---------------|
|                                      | Reg. Voters   | Vote For   | Times Counted | Total Votes  | KENNETH J. MELIKIAN |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                   | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 7575         | 7426                | 98.03%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 30169        | 29623               | 98.19%        |
| MB Precinct                          | 109479        | 102        | 5143          | 3267         | 3198                | 97.89%        |
| Provisional                          | 109479        | 102        | 1584          | 925          | 904                 | 97.73%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>41942</b> | <b>41157</b>        | <b>98.13%</b> |
| <b>UNITED STATES SENATE</b>          |               |            |               |              |                     |               |
| <b>UNITED STATES SENATE</b>          |               |            |               |              |                     |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                   | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 7575         | 7426                | 98.03%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 30169        | 29623               | 98.19%        |
| MB Precinct                          | 109479        | 102        | 5143          | 3267         | 3198                | 97.89%        |
| Provisional                          | 109479        | 102        | 1584          | 925          | 904                 | 97.73%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>41942</b> | <b>41157</b>        | <b>98.13%</b> |
| <b>Total</b>                         |               |            |               |              |                     |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                   | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 7575         | 7426                | 98.03%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 30169        | 29623               | 98.19%        |
| MB Precinct                          | 109479        | 102        | 5143          | 3267         | 3198                | 97.89%        |
| Provisional                          | 109479        | 102        | 1584          | 925          | 904                 | 97.73%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>41942</b> | <b>41157</b>        | <b>98.13%</b> |
| <b>Total</b>                         |               |            |               |              |                     |               |
| <b>UNITED STATES REPRESENTATIVE</b>  |               |            |               |              |                     |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>    |               |            |               |              |                     |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                   | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 7575         | 7426                | 98.03%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 30169        | 29623               | 98.19%        |
| MB Precinct                          | 109479        | 102        | 5143          | 3267         | 3198                | 97.89%        |
| Provisional                          | 109479        | 102        | 1584          | 925          | 904                 | 97.73%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>41942</b> | <b>41157</b>        | <b>98.13%</b> |
| <b>Total</b>                         |               |            |               |              |                     |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                   | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 7575         | 7426                | 98.03%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 30169        | 29623               | 98.19%        |
| MB Precinct                          | 109479        | 102        | 5143          | 3267         | 3198                | 97.89%        |
| Provisional                          | 109479        | 102        | 1584          | 925          | 904                 | 97.73%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>41942</b> | <b>41157</b>        | <b>98.13%</b> |
| <b>Total</b>                         |               |            |               |              |                     |               |
| <b>STATE SENATE</b>                  |               |            |               |              |                     |               |
| <b>1ST STATE SENATE DISTRICT</b>     |               |            |               |              |                     |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                   | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 7575         | 7426                | 98.03%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 30169        | 29623               | 98.19%        |
| MB Precinct                          | 109479        | 102        | 5143          | 3267         | 3198                | 97.89%        |
| Provisional                          | 109479        | 102        | 1584          | 925          | 904                 | 97.73%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>41942</b> | <b>41157</b>        | <b>98.13%</b> |
| <b>Total</b>                         |               |            |               |              |                     |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                   | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 7575         | 7426                | 98.03%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 30169        | 29623               | 98.19%        |
| MB Precinct                          | 109479        | 102        | 5143          | 3267         | 3198                | 97.89%        |
| Provisional                          | 109479        | 102        | 1584          | 925          | 904                 | 97.73%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>41942</b> | <b>41157</b>        | <b>98.13%</b> |
| <b>Total</b>                         |               |            |               |              |                     |               |
| <b>STATE ASSEMBLY DISTRICTS</b>      |               |            |               |              |                     |               |
| <b>5TH STATE ASSEMBLY DIST</b>       |               |            |               |              |                     |               |
| Precinct TSx                         | 70961         | 75         | 4             | 3            | 3                   | 100.00%       |
| Precinct OS                          | 70961         | 75         | 7627          | 4831         | 4728                | 97.87%        |
| Precinct VBM                         | 70961         | 75         | 28225         | 18619        | 18255               | 98.05%        |
| MB Precinct                          | 70961         | 75         | 4361          | 2801         | 2746                | 98.04%        |

**COUNTY OF EL DORADO**  
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|                                      | Reg. Voters | Vote For | Times Counted | Total Votes | KENNETH J. MELIKIAN |         |
| Provisional                          | 70961       | 75       | 1038          | 581         | 566                 | 97.42%  |
| Total                                | 70961       | 375      | 41255         | 26835       | 26298               | 98.00%  |
| <b>6TH STATE ASSEMBLY DIST</b>       |             |          |               |             |                     |         |
| Precinct TSx                         | 38518       | 27       | 4             | 3           | 3                   | 100.00% |
| Precinct OS                          | 38518       | 27       | 4382          | 2744        | 2698                | 98.32%  |
| Precinct VBM                         | 38518       | 27       | 17310         | 11550       | 11368               | 98.42%  |
| MB Precinct                          | 38518       | 27       | 782           | 466         | 452                 | 97.00%  |
| Provisional                          | 38518       | 27       | 546           | 344         | 338                 | 98.26%  |
| Total                                | 38518       | 135      | 23024         | 15107       | 14859               | 98.36%  |
| <b>Total</b>                         |             |          |               |             |                     |         |
| Precinct TSx                         | 109479      | 102      | 8             | 6           | 6                   | 100.00% |
| Precinct OS                          | 109479      | 102      | 12009         | 7575        | 7426                | 98.03%  |
| Precinct VBM                         | 109479      | 102      | 45535         | 30169       | 29623               | 98.19%  |
| MB Precinct                          | 109479      | 102      | 5143          | 3267        | 3198                | 97.89%  |
| Provisional                          | 109479      | 102      | 1584          | 925         | 904                 | 97.73%  |
| Total                                | 109479      | 510      | 64279         | 41942       | 41157               | 98.13%  |
| <b>SUPERVISORIAL DISTRICTS</b>       |             |          |               |             |                     |         |
| <b>1ST SUPERVISORIAL DISTRICT</b>    |             |          |               |             |                     |         |
| Precinct TSx                         | 22587       | 14       | 0             | 0           | 0                   | -       |
| Precinct OS                          | 22587       | 14       | 2530          | 1590        | 1568                | 98.62%  |
| Precinct VBM                         | 22587       | 14       | 10411         | 6874        | 6789                | 98.76%  |
| MB Precinct                          | 22587       | 14       | 423           | 273         | 264                 | 96.70%  |
| Provisional                          | 22587       | 14       | 301           | 181         | 177                 | 97.79%  |
| Total                                | 22587       | 70       | 13665         | 8918        | 8798                | 98.65%  |
| <b>2ND SUPERVISORIAL DISTRICT</b>    |             |          |               |             |                     |         |
| Precinct TSx                         | 23114       | 23       | 4             | 3           | 3                   | 100.00% |
| Precinct OS                          | 23114       | 23       | 2509          | 1583        | 1553                | 98.10%  |
| Precinct VBM                         | 23114       | 23       | 9647          | 6421        | 6297                | 98.07%  |
| MB Precinct                          | 23114       | 23       | 1590          | 1007        | 990                 | 98.31%  |
| Provisional                          | 23114       | 23       | 327           | 200         | 196                 | 98.00%  |
| Total                                | 23114       | 115      | 14077         | 9214        | 9039                | 98.10%  |
| <b>3RD SUPERVISORIAL DISTRICT</b>    |             |          |               |             |                     |         |
| Precinct TSx                         | 21691       | 26       | 3             | 2           | 2                   | 100.00% |
| Precinct OS                          | 21691       | 26       | 2368          | 1550        | 1524                | 98.32%  |
| Precinct VBM                         | 21691       | 26       | 10021         | 6719        | 6601                | 98.24%  |
| MB Precinct                          | 21691       | 26       | 440           | 290         | 284                 | 97.93%  |
| Provisional                          | 21691       | 26       | 277           | 153         | 150                 | 98.04%  |
| Total                                | 21691       | 130      | 13109         | 8714        | 8561                | 98.24%  |
| <b>4TH SUPERVISORIAL DISTRICT</b>    |             |          |               |             |                     |         |
| Precinct TSx                         | 23425       | 22       | 0             | 0           | 0                   | -       |
| Precinct OS                          | 23425       | 22       | 2568          | 1602        | 1563                | 97.57%  |
| Precinct VBM                         | 23425       | 22       | 9398          | 6169        | 6034                | 97.81%  |
| MB Precinct                          | 23425       | 22       | 2239          | 1414        | 1385                | 97.95%  |
| Provisional                          | 23425       | 22       | 315           | 188         | 182                 | 96.81%  |
| Total                                | 23425       | 110      | 14520         | 9373        | 9164                | 97.77%  |
| <b>5TH SUPERVISORIAL DISTRICT</b>    |             |          |               |             |                     |         |
| Precinct TSx                         | 18662       | 17       | 1             | 1           | 1                   | 100.00% |
| Precinct OS                          | 18662       | 17       | 2034          | 1250        | 1218                | 97.44%  |
| Precinct VBM                         | 18662       | 17       | 6058          | 3986        | 3902                | 97.89%  |
| MB Precinct                          | 18662       | 17       | 451           | 283         | 275                 | 97.17%  |
| Provisional                          | 18662       | 17       | 364           | 203         | 199                 | 98.03%  |
| Total                                | 18662       | 85       | 8908          | 5723        | 5595                | 97.76%  |
| <b>Total</b>                         |             |          |               |             |                     |         |
| Precinct TSx                         | 109479      | 102      | 8             | 6           | 6                   | 100.00% |
| Precinct OS                          | 109479      | 102      | 12009         | 7575        | 7426                | 98.03%  |
| Precinct VBM                         | 109479      | 102      | 45535         | 30169       | 29623               | 98.19%  |
| MB Precinct                          | 109479      | 102      | 5143          | 3267        | 3198                | 97.89%  |
| Provisional                          | 109479      | 102      | 1584          | 925         | 904                 | 97.73%  |
| Total                                | 109479      | 510      | 64279         | 41942       | 41157               | 98.13%  |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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| Judge of the Superior Court Office 7 |               |            |               |              |                        |               |
|--------------------------------------|---------------|------------|---------------|--------------|------------------------|---------------|
|                                      | Reg. Voters   | Vote For   | Times Counted | Total Votes  | KENNETH J.<br>MELIKIAN |               |
| <b>CITIES</b>                        |               |            |               |              |                        |               |
| <b>CITY OF PLACERVILLE</b>           |               |            |               |              |                        |               |
| Precinct TSx                         | 5569          | 5          | 0             | 0            | 0                      | -             |
| Precinct OS                          | 5569          | 5          | 631           | 408          | 404                    | 99.02%        |
| Precinct VBM                         | 5569          | 5          | 2590          | 1742         | 1713                   | 98.34%        |
| MB Precinct                          | 5569          | 5          | 0             | 0            | 0                      | -             |
| Provisional                          | 5569          | 5          | 80            | 49           | 49                     | 100.00%       |
| <b>Total</b>                         | <b>5569</b>   | <b>25</b>  | <b>3301</b>   | <b>2199</b>  | <b>2166</b>            | <b>98.50%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b>      |               |            |               |              |                        |               |
| Precinct TSx                         | 9465          | 7          | 0             | 0            | 0                      | -             |
| Precinct OS                          | 9465          | 7          | 1063          | 669          | 649                    | 97.01%        |
| Precinct VBM                         | 9465          | 7          | 2796          | 1813         | 1780                   | 98.18%        |
| MB Precinct                          | 9465          | 7          | 0             | 0            | 0                      | -             |
| Provisional                          | 9465          | 7          | 224           | 123          | 121                    | 98.37%        |
| <b>Total</b>                         | <b>9465</b>   | <b>35</b>  | <b>4083</b>   | <b>2605</b>  | <b>2550</b>            | <b>97.89%</b> |
| <b>UNINCORPORATED AREA</b>           |               |            |               |              |                        |               |
| Precinct TSx                         | 94445         | 90         | 8             | 6            | 6                      | 100.00%       |
| Precinct OS                          | 94445         | 90         | 10315         | 6498         | 6373                   | 98.08%        |
| Precinct VBM                         | 94445         | 90         | 40149         | 26614        | 26130                  | 98.18%        |
| MB Precinct                          | 94445         | 90         | 5143          | 3267         | 3198                   | 97.89%        |
| Provisional                          | 94445         | 90         | 1280          | 753          | 734                    | 97.48%        |
| <b>Total</b>                         | <b>94445</b>  | <b>450</b> | <b>56895</b>  | <b>37138</b> | <b>36441</b>           | <b>98.12%</b> |
| <b>Total</b>                         |               |            |               |              |                        |               |
| Precinct TSx                         | 109479        | 102        | 8             | 6            | 6                      | 100.00%       |
| Precinct OS                          | 109479        | 102        | 12009         | 7575         | 7426                   | 98.03%        |
| Precinct VBM                         | 109479        | 102        | 45535         | 30169        | 29623                  | 98.19%        |
| MB Precinct                          | 109479        | 102        | 5143          | 3267         | 3198                   | 97.89%        |
| Provisional                          | 109479        | 102        | 1584          | 925          | 904                    | 97.73%        |
| <b>Total</b>                         | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>41942</b> | <b>41157</b>           | <b>98.13%</b> |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                               |  | County Supervisor 1st District |          |               |             |                   |                   |
|-------------------------------|--|--------------------------------|----------|---------------|-------------|-------------------|-------------------|
|                               |  | Reg. Voters                    | Vote For | Times Counted | Total Votes | JOHN W. HIDAHL    | NOELLE MATTOCK    |
| <b>Jurisdiction Wide</b>      |  |                                |          |               |             |                   |                   |
| <b>100001.11-CP 100001.11</b> |  |                                |          |               |             |                   |                   |
| Precinct TSx                  |  | 1615                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Precinct OS                   |  | 1615                           | 1        | 190           | 171         | 41 23.98%         | 37 21.64%         |
| Precinct VBM                  |  | 1615                           | 1        | 781           | 721         | 172 23.86%        | 129 17.89%        |
| MB Precinct                   |  | 1615                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Provisional                   |  | 1615                           | 1        | 17            | 15          | 5 33.33%          | 1 6.67%           |
| <b>Total</b>                  |  | <b>1615</b>                    | <b>5</b> | <b>988</b>    | <b>907</b>  | <b>218 24.04%</b> | <b>167 18.41%</b> |
| <b>100002.11-CP 100002.11</b> |  |                                |          |               |             |                   |                   |
| Precinct TSx                  |  | 1000                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Precinct OS                   |  | 1000                           | 1        | 89            | 74          | 12 16.22%         | 23 31.08%         |
| Precinct VBM                  |  | 1000                           | 1        | 476           | 428         | 101 23.60%        | 86 20.09%         |
| MB Precinct                   |  | 1000                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Provisional                   |  | 1000                           | 1        | 20            | 18          | 4 22.22%          | 5 27.78%          |
| <b>Total</b>                  |  | <b>1000</b>                    | <b>5</b> | <b>585</b>    | <b>520</b>  | <b>117 22.50%</b> | <b>114 21.92%</b> |
| <b>100003.11-CP 100003.11</b> |  |                                |          |               |             |                   |                   |
| Precinct TSx                  |  | 1269                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Precinct OS                   |  | 1269                           | 1        | 123           | 115         | 21 18.26%         | 25 21.74%         |
| Precinct VBM                  |  | 1269                           | 1        | 773           | 723         | 157 21.72%        | 226 31.26%        |
| MB Precinct                   |  | 1269                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Provisional                   |  | 1269                           | 1        | 24            | 24          | 3 12.50%          | 5 20.83%          |
| <b>Total</b>                  |  | <b>1269</b>                    | <b>5</b> | <b>920</b>    | <b>862</b>  | <b>181 21.00%</b> | <b>256 29.70%</b> |
| <b>100004.11-CP 100004.11</b> |  |                                |          |               |             |                   |                   |
| Precinct TSx                  |  | 1633                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Precinct OS                   |  | 1633                           | 1        | 254           | 226         | 60 26.55%         | 52 23.01%         |
| Precinct VBM                  |  | 1633                           | 1        | 738           | 663         | 207 31.22%        | 163 24.59%        |
| MB Precinct                   |  | 1633                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Provisional                   |  | 1633                           | 1        | 4             | 3           | 1 33.33%          | 1 33.33%          |
| <b>Total</b>                  |  | <b>1633</b>                    | <b>5</b> | <b>996</b>    | <b>892</b>  | <b>268 30.04%</b> | <b>216 24.22%</b> |
| <b>100005.11-CP 100005.11</b> |  |                                |          |               |             |                   |                   |
| Precinct TSx                  |  | 1586                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Precinct OS                   |  | 1586                           | 1        | 146           | 132         | 23 17.42%         | 25 18.94%         |
| Precinct VBM                  |  | 1586                           | 1        | 895           | 832         | 136 16.35%        | 115 13.82%        |
| MB Precinct                   |  | 1586                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Provisional                   |  | 1586                           | 1        | 23            | 19          | 3 15.79%          | 5 26.32%          |
| <b>Total</b>                  |  | <b>1586</b>                    | <b>5</b> | <b>1064</b>   | <b>983</b>  | <b>162 16.48%</b> | <b>145 14.75%</b> |
| <b>100006.11-CP 100006.11</b> |  |                                |          |               |             |                   |                   |
| Precinct TSx                  |  | 1928                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Precinct OS                   |  | 1928                           | 1        | 252           | 224         | 57 25.45%         | 55 24.55%         |
| Precinct VBM                  |  | 1928                           | 1        | 944           | 852         | 222 26.06%        | 182 21.36%        |
| MB Precinct                   |  | 1928                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Provisional                   |  | 1928                           | 1        | 31            | 21          | 3 14.29%          | 7 33.33%          |
| <b>Total</b>                  |  | <b>1928</b>                    | <b>5</b> | <b>1227</b>   | <b>1097</b> | <b>282 25.71%</b> | <b>244 22.24%</b> |
| <b>100008.11-CP 100008.11</b> |  |                                |          |               |             |                   |                   |
| Precinct TSx                  |  | 1671                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Precinct OS                   |  | 1671                           | 1        | 197           | 170         | 44 25.88%         | 29 17.06%         |
| Precinct VBM                  |  | 1671                           | 1        | 740           | 666         | 131 19.67%        | 149 22.37%        |
| MB Precinct                   |  | 1671                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Provisional                   |  | 1671                           | 1        | 17            | 12          | 1 8.33%           | 7 58.33%          |
| <b>Total</b>                  |  | <b>1671</b>                    | <b>5</b> | <b>954</b>    | <b>848</b>  | <b>176 20.75%</b> | <b>185 21.82%</b> |
| <b>100009.11-CP 100009.11</b> |  |                                |          |               |             |                   |                   |
| Precinct TSx                  |  | 1838                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Precinct OS                   |  | 1838                           | 1        | 169           | 153         | 29 18.95%         | 23 15.03%         |
| Precinct VBM                  |  | 1838                           | 1        | 751           | 695         | 159 22.88%        | 124 17.84%        |
| MB Precinct                   |  | 1838                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Provisional                   |  | 1838                           | 1        | 26            | 20          | 3 15.00%          | 4 20.00%          |
| <b>Total</b>                  |  | <b>1838</b>                    | <b>5</b> | <b>946</b>    | <b>868</b>  | <b>191 22.00%</b> | <b>151 17.40%</b> |
| <b>100012.11-CP 100012.11</b> |  |                                |          |               |             |                   |                   |
| Precinct TSx                  |  | 2108                           | 1        | 0             | 0           | 0 -               | 0 -               |
| Precinct OS                   |  | 2108                           | 1        | 255           | 235         | 91 38.72%         | 38 16.17%         |
| Precinct VBM                  |  | 2108                           | 1        | 968           | 893         | 288 32.25%        | 152 17.02%        |











**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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| County Supervisor 1st District |             |          |               |             |                |                |
|--------------------------------|-------------|----------|---------------|-------------|----------------|----------------|
|                                | Reg. Voters | Vote For | Times Counted | Total Votes | JOHN W. HIDAHL | NOELLE MATTOCK |
| Precinct VBM                   | -           | -        | -             | -           | -              | -              |
| MB Precinct                    | -           | -        | -             | -           | -              | -              |
| Provisional                    | -           | -        | -             | -           | -              | -              |
| Total                          | -           | -        | -             | -           | -              | -              |
| 300018.04-CP 300018.04         |             |          |               |             |                |                |
| Precinct TSx                   | -           | -        | -             | -           | -              | -              |
| Precinct OS                    | -           | -        | -             | -           | -              | -              |
| Precinct VBM                   | -           | -        | -             | -           | -              | -              |
| MB Precinct                    | -           | -        | -             | -           | -              | -              |
| Provisional                    | -           | -        | -             | -           | -              | -              |
| Total                          | -           | -        | -             | -           | -              | -              |
| 300020.04-CP 300020.04         |             |          |               |             |                |                |
| Precinct TSx                   | -           | -        | -             | -           | -              | -              |
| Precinct OS                    | -           | -        | -             | -           | -              | -              |
| Precinct VBM                   | -           | -        | -             | -           | -              | -              |
| MB Precinct                    | -           | -        | -             | -           | -              | -              |
| Provisional                    | -           | -        | -             | -           | -              | -              |
| Total                          | -           | -        | -             | -           | -              | -              |
| 300021.03-CP 300021.03         |             |          |               |             |                |                |
| Precinct TSx                   | -           | -        | -             | -           | -              | -              |
| Precinct OS                    | -           | -        | -             | -           | -              | -              |
| Precinct VBM                   | -           | -        | -             | -           | -              | -              |
| MB Precinct                    | -           | -        | -             | -           | -              | -              |
| Provisional                    | -           | -        | -             | -           | -              | -              |
| Total                          | -           | -        | -             | -           | -              | -              |
| 300022.05-CP 300022.05         |             |          |               |             |                |                |
| Precinct TSx                   | -           | -        | -             | -           | -              | -              |
| Precinct OS                    | -           | -        | -             | -           | -              | -              |
| Precinct VBM                   | -           | -        | -             | -           | -              | -              |
| MB Precinct                    | -           | -        | -             | -           | -              | -              |
| Provisional                    | -           | -        | -             | -           | -              | -              |
| Total                          | -           | -        | -             | -           | -              | -              |
| 300023.03-CP 300023.03         |             |          |               |             |                |                |
| Precinct TSx                   | -           | -        | -             | -           | -              | -              |
| Precinct OS                    | -           | -        | -             | -           | -              | -              |
| Precinct VBM                   | -           | -        | -             | -           | -              | -              |
| MB Precinct                    | -           | -        | -             | -           | -              | -              |
| Provisional                    | -           | -        | -             | -           | -              | -              |
| Total                          | -           | -        | -             | -           | -              | -              |
| 300023.05-CP 300023.05         |             |          |               |             |                |                |
| Precinct TSx                   | -           | -        | -             | -           | -              | -              |
| Precinct OS                    | -           | -        | -             | -           | -              | -              |
| Precinct VBM                   | -           | -        | -             | -           | -              | -              |
| MB Precinct                    | -           | -        | -             | -           | -              | -              |
| Provisional                    | -           | -        | -             | -           | -              | -              |
| Total                          | -           | -        | -             | -           | -              | -              |
| 400001.14-CP 400001.14         |             |          |               |             |                |                |
| Precinct TSx                   | -           | -        | -             | -           | -              | -              |
| Precinct OS                    | -           | -        | -             | -           | -              | -              |
| Precinct VBM                   | -           | -        | -             | -           | -              | -              |
| MB Precinct                    | -           | -        | -             | -           | -              | -              |
| Provisional                    | -           | -        | -             | -           | -              | -              |
| Total                          | -           | -        | -             | -           | -              | -              |
| 400002.08-CP 400002.08         |             |          |               |             |                |                |
| Precinct TSx                   | -           | -        | -             | -           | -              | -              |
| Precinct OS                    | -           | -        | -             | -           | -              | -              |
| Precinct VBM                   | -           | -        | -             | -           | -              | -              |
| MB Precinct                    | -           | -        | -             | -           | -              | -              |
| Provisional                    | -           | -        | -             | -           | -              | -              |
| Total                          | -           | -        | -             | -           | -              | -              |
| 400003.08-CP 400003.08         |             |          |               |             |                |                |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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County Supervisor 1st District

|                        | Reg. Voters | Vote For | Times Counted | Total Votes | JOHN W. HIDAHL | NOELLE MATTOCK |
|------------------------|-------------|----------|---------------|-------------|----------------|----------------|
| Total                  | -           | -        | -             | -           | -              | -              |
| 400017.08-CP 400017.08 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 400018.08-CP 400018.08 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 400020.08-CP 400020.08 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 400021.07-CP 400021.07 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 500001.10-CP 500001.10 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 500002.10-CP 500002.10 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 500003.10-CP 500003.10 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 500004.10-CP 500004.10 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 500005.10-CP 500005.10 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |





**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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County Supervisor 1st District

|                        | Reg. Voters | Vote For | Times Counted | Total Votes | JOHN W. HIDAHL | NOELLE MATTOCK |
|------------------------|-------------|----------|---------------|-------------|----------------|----------------|
| 95U303.05-MB 95U303.05 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 95U304.05-MB 95U304.05 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 95U305.05-MB 95U305.05 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 95U401.08-MB 95U401.08 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 95U402.07-MB 95U402.07 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 95U402.08-MB 95U402.08 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 95U403.08-MB 95U403.08 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 95U404.06-MB 95U404.06 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |
| Provisional            | -           | -        | -             | -           | -              | -              |
| Total                  | -           | -        | -             | -           | -              | -              |
| 95U404.08-MB 95U404.08 |             |          |               |             |                |                |
| Precinct TSx           | -           | -        | -             | -           | -              | -              |
| Precinct OS            | -           | -        | -             | -           | -              | -              |
| Precinct VBM           | -           | -        | -             | -           | -              | -              |
| MB Precinct            | -           | -        | -             | -           | -              | -              |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                               | County Supervisor 1st District |          |               |             |                |                |
|-------------------------------|--------------------------------|----------|---------------|-------------|----------------|----------------|
|                               | Reg. Voters                    | Vote For | Times Counted | Total Votes | JOHN W. HIDAHL | NOELLE MATTOCK |
| Provisional                   | -                              | -        | -             | -           | -              | -              |
| Total                         | -                              | -        | -             | -           | -              | -              |
| 95U501.09-MB 95U501.09        |                                |          |               |             |                |                |
| Precinct TSx                  | -                              | -        | -             | -           | -              | -              |
| Precinct OS                   | -                              | -        | -             | -           | -              | -              |
| Precinct VBM                  | -                              | -        | -             | -           | -              | -              |
| MB Precinct                   | -                              | -        | -             | -           | -              | -              |
| Provisional                   | -                              | -        | -             | -           | -              | -              |
| Total                         | -                              | -        | -             | -           | -              | -              |
| 95U501.10-MB 95U501.10        |                                |          |               |             |                |                |
| Precinct TSx                  | -                              | -        | -             | -           | -              | -              |
| Precinct OS                   | -                              | -        | -             | -           | -              | -              |
| Precinct VBM                  | -                              | -        | -             | -           | -              | -              |
| MB Precinct                   | -                              | -        | -             | -           | -              | -              |
| Provisional                   | -                              | -        | -             | -           | -              | -              |
| Total                         | -                              | -        | -             | -           | -              | -              |
| 95U598.10-MB 95U598.10        |                                |          |               |             |                |                |
| Precinct TSx                  | -                              | -        | -             | -           | -              | -              |
| Precinct OS                   | -                              | -        | -             | -           | -              | -              |
| Precinct VBM                  | -                              | -        | -             | -           | -              | -              |
| MB Precinct                   | -                              | -        | -             | -           | -              | -              |
| Provisional                   | -                              | -        | -             | -           | -              | -              |
| Total                         | -                              | -        | -             | -           | -              | -              |
| 95U599.10-MB 95U599.10        |                                |          |               |             |                |                |
| Precinct TSx                  | -                              | -        | -             | -           | -              | -              |
| Precinct OS                   | -                              | -        | -             | -           | -              | -              |
| Precinct VBM                  | -                              | -        | -             | -           | -              | -              |
| MB Precinct                   | -                              | -        | -             | -           | -              | -              |
| Provisional                   | -                              | -        | -             | -           | -              | -              |
| Total                         | -                              | -        | -             | -           | -              | -              |
| 96U101.11-MB 96U101.11        |                                |          |               |             |                |                |
| Precinct TSx                  | 626                            | 1        | 0             | 0           | 0              | 0              |
| Precinct OS                   | 626                            | 1        | 0             | 0           | 0              | 0              |
| Precinct VBM                  | 626                            | 1        | 0             | 0           | 0              | 0              |
| MB Precinct                   | 626                            | 1        | 423           | 390         | 89 22.82%      | 70 17.95%      |
| Provisional                   | 626                            | 1        | 6             | 3           | 1 33.33%       | 2 66.67%       |
| Total                         | 626                            | 5        | 429           | 393         | 90 22.90%      | 72 18.32%      |
| 96U401.14-MB 96U401.14        |                                |          |               |             |                |                |
| Precinct TSx                  | -                              | -        | -             | -           | -              | -              |
| Precinct OS                   | -                              | -        | -             | -           | -              | -              |
| Precinct VBM                  | -                              | -        | -             | -           | -              | -              |
| MB Precinct                   | -                              | -        | -             | -           | -              | -              |
| Provisional                   | -                              | -        | -             | -           | -              | -              |
| Total                         | -                              | -        | -             | -           | -              | -              |
| Total                         | 22587                          | 14       | 0             | 0           | 0              | 0              |
| Precinct TSx                  | 22587                          | 14       | 2530          | 2287        | 555 24.27%     | 475 20.77%     |
| Precinct OS                   | 22587                          | 14       | 10411         | 9538        | 2239 23.47%    | 1946 20.40%    |
| Precinct VBM                  | 22587                          | 14       | 423           | 390         | 89 22.82%      | 70 17.95%      |
| MB Precinct                   | 22587                          | 14       | 301           | 241         | 41 17.01%      | 62 25.73%      |
| Provisional                   | 22587                          | 14       | 301           | 241         | 41 17.01%      | 62 25.73%      |
| Total                         | 22587                          | 70       | 13665         | 12456       | 2924 23.47%    | 2553 20.50%    |
| STATE BOARD OF EQUALIZATION   |                                |          |               |             |                |                |
| STATE BOARD OF EQUALIZATION 1 |                                |          |               |             |                |                |
| Precinct TSx                  | 22587                          | 14       | 0             | 0           | 0              | 0              |
| Precinct OS                   | 22587                          | 14       | 2530          | 2287        | 555 24.27%     | 475 20.77%     |
| Precinct VBM                  | 22587                          | 14       | 10411         | 9538        | 2239 23.47%    | 1946 20.40%    |
| MB Precinct                   | 22587                          | 14       | 423           | 390         | 89 22.82%      | 70 17.95%      |
| Provisional                   | 22587                          | 14       | 301           | 241         | 41 17.01%      | 62 25.73%      |
| Total                         | 22587                          | 70       | 13665         | 12456       | 2924 23.47%    | 2553 20.50%    |
| Total                         |                                |          |               |             |                |                |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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County Supervisor 1st District

|                                     | Reg. Voters  | Vote For  | Times Counted | Total Votes  | JOHN W. HIDAHL |               | NOELLE MATTOCK |               |
|-------------------------------------|--------------|-----------|---------------|--------------|----------------|---------------|----------------|---------------|
| Precinct TSx                        | 22587        | 14        | 0             | 0            | 0              | -             | 0              | -             |
| Precinct OS                         | 22587        | 14        | 2530          | 2287         | 555            | 24.27%        | 475            | 20.77%        |
| Precinct VBM                        | 22587        | 14        | 10411         | 9538         | 2239           | 23.47%        | 1946           | 20.40%        |
| MB Precinct                         | 22587        | 14        | 423           | 390          | 89             | 22.82%        | 70             | 17.95%        |
| Provisional                         | 22587        | 14        | 301           | 241          | 41             | 17.01%        | 62             | 25.73%        |
| <b>Total</b>                        | <b>22587</b> | <b>70</b> | <b>13665</b>  | <b>12456</b> | <b>2924</b>    | <b>23.47%</b> | <b>2553</b>    | <b>20.50%</b> |
| <b>UNITED STATES SENATE</b>         |              |           |               |              |                |               |                |               |
| <b>UNITED STATES SENATE</b>         |              |           |               |              |                |               |                |               |
| Precinct TSx                        | 22587        | 14        | 0             | 0            | 0              | -             | 0              | -             |
| Precinct OS                         | 22587        | 14        | 2530          | 2287         | 555            | 24.27%        | 475            | 20.77%        |
| Precinct VBM                        | 22587        | 14        | 10411         | 9538         | 2239           | 23.47%        | 1946           | 20.40%        |
| MB Precinct                         | 22587        | 14        | 423           | 390          | 89             | 22.82%        | 70             | 17.95%        |
| Provisional                         | 22587        | 14        | 301           | 241          | 41             | 17.01%        | 62             | 25.73%        |
| <b>Total</b>                        | <b>22587</b> | <b>70</b> | <b>13665</b>  | <b>12456</b> | <b>2924</b>    | <b>23.47%</b> | <b>2553</b>    | <b>20.50%</b> |
| <b>Total</b>                        |              |           |               |              |                |               |                |               |
| Precinct TSx                        | 22587        | 14        | 0             | 0            | 0              | -             | 0              | -             |
| Precinct OS                         | 22587        | 14        | 2530          | 2287         | 555            | 24.27%        | 475            | 20.77%        |
| Precinct VBM                        | 22587        | 14        | 10411         | 9538         | 2239           | 23.47%        | 1946           | 20.40%        |
| MB Precinct                         | 22587        | 14        | 423           | 390          | 89             | 22.82%        | 70             | 17.95%        |
| Provisional                         | 22587        | 14        | 301           | 241          | 41             | 17.01%        | 62             | 25.73%        |
| <b>Total</b>                        | <b>22587</b> | <b>70</b> | <b>13665</b>  | <b>12456</b> | <b>2924</b>    | <b>23.47%</b> | <b>2553</b>    | <b>20.50%</b> |
| <b>Total</b>                        |              |           |               |              |                |               |                |               |
| <b>UNITED STATES REPRESENTATIVE</b> |              |           |               |              |                |               |                |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |              |           |               |              |                |               |                |               |
| Precinct TSx                        | 22587        | 14        | 0             | 0            | 0              | -             | 0              | -             |
| Precinct OS                         | 22587        | 14        | 2530          | 2287         | 555            | 24.27%        | 475            | 20.77%        |
| Precinct VBM                        | 22587        | 14        | 10411         | 9538         | 2239           | 23.47%        | 1946           | 20.40%        |
| MB Precinct                         | 22587        | 14        | 423           | 390          | 89             | 22.82%        | 70             | 17.95%        |
| Provisional                         | 22587        | 14        | 301           | 241          | 41             | 17.01%        | 62             | 25.73%        |
| <b>Total</b>                        | <b>22587</b> | <b>70</b> | <b>13665</b>  | <b>12456</b> | <b>2924</b>    | <b>23.47%</b> | <b>2553</b>    | <b>20.50%</b> |
| <b>Total</b>                        |              |           |               |              |                |               |                |               |
| Precinct TSx                        | 22587        | 14        | 0             | 0            | 0              | -             | 0              | -             |
| Precinct OS                         | 22587        | 14        | 2530          | 2287         | 555            | 24.27%        | 475            | 20.77%        |
| Precinct VBM                        | 22587        | 14        | 10411         | 9538         | 2239           | 23.47%        | 1946           | 20.40%        |
| MB Precinct                         | 22587        | 14        | 423           | 390          | 89             | 22.82%        | 70             | 17.95%        |
| Provisional                         | 22587        | 14        | 301           | 241          | 41             | 17.01%        | 62             | 25.73%        |
| <b>Total</b>                        | <b>22587</b> | <b>70</b> | <b>13665</b>  | <b>12456</b> | <b>2924</b>    | <b>23.47%</b> | <b>2553</b>    | <b>20.50%</b> |
| <b>Total</b>                        |              |           |               |              |                |               |                |               |
| <b>STATE SENATE</b>                 |              |           |               |              |                |               |                |               |
| <b>1ST STATE SENATE DISTRICT</b>    |              |           |               |              |                |               |                |               |
| Precinct TSx                        | 22587        | 14        | 0             | 0            | 0              | -             | 0              | -             |
| Precinct OS                         | 22587        | 14        | 2530          | 2287         | 555            | 24.27%        | 475            | 20.77%        |
| Precinct VBM                        | 22587        | 14        | 10411         | 9538         | 2239           | 23.47%        | 1946           | 20.40%        |
| MB Precinct                         | 22587        | 14        | 423           | 390          | 89             | 22.82%        | 70             | 17.95%        |
| Provisional                         | 22587        | 14        | 301           | 241          | 41             | 17.01%        | 62             | 25.73%        |
| <b>Total</b>                        | <b>22587</b> | <b>70</b> | <b>13665</b>  | <b>12456</b> | <b>2924</b>    | <b>23.47%</b> | <b>2553</b>    | <b>20.50%</b> |
| <b>Total</b>                        |              |           |               |              |                |               |                |               |
| Precinct TSx                        | 22587        | 14        | 0             | 0            | 0              | -             | 0              | -             |
| Precinct OS                         | 22587        | 14        | 2530          | 2287         | 555            | 24.27%        | 475            | 20.77%        |
| Precinct VBM                        | 22587        | 14        | 10411         | 9538         | 2239           | 23.47%        | 1946           | 20.40%        |
| MB Precinct                         | 22587        | 14        | 423           | 390          | 89             | 22.82%        | 70             | 17.95%        |
| Provisional                         | 22587        | 14        | 301           | 241          | 41             | 17.01%        | 62             | 25.73%        |
| <b>Total</b>                        | <b>22587</b> | <b>70</b> | <b>13665</b>  | <b>12456</b> | <b>2924</b>    | <b>23.47%</b> | <b>2553</b>    | <b>20.50%</b> |
| <b>Total</b>                        |              |           |               |              |                |               |                |               |
| <b>STATE ASSEMBLY DISTRICTS</b>     |              |           |               |              |                |               |                |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |              |           |               |              |                |               |                |               |
| Precinct TSx                        | 0            | 0         | 0             | 0            | 0              | -             | 0              | -             |
| Precinct OS                         | 0            | 0         | 0             | 0            | 0              | -             | 0              | -             |
| Precinct VBM                        | 0            | 0         | 0             | 0            | 0              | -             | 0              | -             |
| MB Precinct                         | 0            | 0         | 0             | 0            | 0              | -             | 0              | -             |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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County Supervisor 1st District

|                                   | Reg. Voters | Vote For | Times Counted | Total Votes | JOHN W. HIDAHL |        | NOELLE MATTOCK |        |  |
|-----------------------------------|-------------|----------|---------------|-------------|----------------|--------|----------------|--------|--|
| Provisional                       | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Total                             | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| <b>6TH STATE ASSEMBLY DIST</b>    |             |          |               |             |                |        |                |        |  |
| Precinct TSx                      | 22587       | 14       | 0             | 0           | 0              | -      | 0              | -      |  |
| Precinct OS                       | 22587       | 14       | 2530          | 2287        | 555            | 24.27% | 475            | 20.77% |  |
| Precinct VBM                      | 22587       | 14       | 10411         | 9538        | 2239           | 23.47% | 1946           | 20.40% |  |
| MB Precinct                       | 22587       | 14       | 423           | 390         | 89             | 22.82% | 70             | 17.95% |  |
| Provisional                       | 22587       | 14       | 301           | 241         | 41             | 17.01% | 62             | 25.73% |  |
| Total                             | 22587       | 70       | 13665         | 12456       | 2924           | 23.47% | 2553           | 20.50% |  |
| <b>Total</b>                      |             |          |               |             |                |        |                |        |  |
| Precinct TSx                      | 22587       | 14       | 0             | 0           | 0              | -      | 0              | -      |  |
| Precinct OS                       | 22587       | 14       | 2530          | 2287        | 555            | 24.27% | 475            | 20.77% |  |
| Precinct VBM                      | 22587       | 14       | 10411         | 9538        | 2239           | 23.47% | 1946           | 20.40% |  |
| MB Precinct                       | 22587       | 14       | 423           | 390         | 89             | 22.82% | 70             | 17.95% |  |
| Provisional                       | 22587       | 14       | 301           | 241         | 41             | 17.01% | 62             | 25.73% |  |
| Total                             | 22587       | 70       | 13665         | 12456       | 2924           | 23.47% | 2553           | 20.50% |  |
| <b>SUPERVISORIAL DISTRICTS</b>    |             |          |               |             |                |        |                |        |  |
| <b>1ST SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |                |        |  |
| Precinct TSx                      | 22587       | 14       | 0             | 0           | 0              | -      | 0              | -      |  |
| Precinct OS                       | 22587       | 14       | 2530          | 2287        | 555            | 24.27% | 475            | 20.77% |  |
| Precinct VBM                      | 22587       | 14       | 10411         | 9538        | 2239           | 23.47% | 1946           | 20.40% |  |
| MB Precinct                       | 22587       | 14       | 423           | 390         | 89             | 22.82% | 70             | 17.95% |  |
| Provisional                       | 22587       | 14       | 301           | 241         | 41             | 17.01% | 62             | 25.73% |  |
| Total                             | 22587       | 70       | 13665         | 12456       | 2924           | 23.47% | 2553           | 20.50% |  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |                |        |  |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Provisional                       | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Total                             | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |                |        |  |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Provisional                       | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Total                             | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |                |        |  |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Provisional                       | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Total                             | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |                |        |  |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Provisional                       | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| Total                             | 0           | 0        | 0             | 0           | 0              | -      | 0              | -      |  |
| <b>Total</b>                      |             |          |               |             |                |        |                |        |  |
| Precinct TSx                      | 22587       | 14       | 0             | 0           | 0              | -      | 0              | -      |  |
| Precinct OS                       | 22587       | 14       | 2530          | 2287        | 555            | 24.27% | 475            | 20.77% |  |
| Precinct VBM                      | 22587       | 14       | 10411         | 9538        | 2239           | 23.47% | 1946           | 20.40% |  |
| MB Precinct                       | 22587       | 14       | 423           | 390         | 89             | 22.82% | 70             | 17.95% |  |
| Provisional                       | 22587       | 14       | 301           | 241         | 41             | 17.01% | 62             | 25.73% |  |
| Total                             | 22587       | 70       | 13665         | 12456       | 2924           | 23.47% | 2553           | 20.50% |  |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                 |  | County Supervisor 1st District |           |               |              |                |               |                |               |
|---------------------------------|--|--------------------------------|-----------|---------------|--------------|----------------|---------------|----------------|---------------|
|                                 |  | Reg. Voters                    | Vote For  | Times Counted | Total Votes  | JOHN W. HIDAHL |               | NOELLE MATTOCK |               |
| <b>CITIES</b>                   |  |                                |           |               |              |                |               |                |               |
| <b>CITY OF PLACERVILLE</b>      |  |                                |           |               |              |                |               |                |               |
| Precinct TSx                    |  | 0                              | 0         | 0             | 0            | 0              | -             | 0              | -             |
| Precinct OS                     |  | 0                              | 0         | 0             | 0            | 0              | -             | 0              | -             |
| Precinct VBM                    |  | 0                              | 0         | 0             | 0            | 0              | -             | 0              | -             |
| MB Precinct                     |  | 0                              | 0         | 0             | 0            | 0              | -             | 0              | -             |
| Provisional                     |  | 0                              | 0         | 0             | 0            | 0              | -             | 0              | -             |
| <b>Total</b>                    |  | <b>0</b>                       | <b>0</b>  | <b>0</b>      | <b>0</b>     | <b>0</b>       | <b>-</b>      | <b>0</b>       | <b>-</b>      |
| <b>CITY OF SOUTH LAKE TAHOE</b> |  |                                |           |               |              |                |               |                |               |
| Precinct TSx                    |  | 0                              | 0         | 0             | 0            | 0              | -             | 0              | -             |
| Precinct OS                     |  | 0                              | 0         | 0             | 0            | 0              | -             | 0              | -             |
| Precinct VBM                    |  | 0                              | 0         | 0             | 0            | 0              | -             | 0              | -             |
| MB Precinct                     |  | 0                              | 0         | 0             | 0            | 0              | -             | 0              | -             |
| Provisional                     |  | 0                              | 0         | 0             | 0            | 0              | -             | 0              | -             |
| <b>Total</b>                    |  | <b>0</b>                       | <b>0</b>  | <b>0</b>      | <b>0</b>     | <b>0</b>       | <b>-</b>      | <b>0</b>       | <b>-</b>      |
| <b>UNINCORPORATED AREA</b>      |  |                                |           |               |              |                |               |                |               |
| Precinct TSx                    |  | 22587                          | 14        | 0             | 0            | 0              | -             | 0              | -             |
| Precinct OS                     |  | 22587                          | 14        | 2530          | 2287         | 555            | 24.27%        | 475            | 20.77%        |
| Precinct VBM                    |  | 22587                          | 14        | 10411         | 9538         | 2239           | 23.47%        | 1946           | 20.40%        |
| MB Precinct                     |  | 22587                          | 14        | 423           | 390          | 89             | 22.82%        | 70             | 17.95%        |
| Provisional                     |  | 22587                          | 14        | 301           | 241          | 41             | 17.01%        | 62             | 25.73%        |
| <b>Total</b>                    |  | <b>22587</b>                   | <b>70</b> | <b>13665</b>  | <b>12456</b> | <b>2924</b>    | <b>23.47%</b> | <b>2553</b>    | <b>20.50%</b> |
| <b>Total</b>                    |  |                                |           |               |              |                |               |                |               |
| Precinct TSx                    |  | 22587                          | 14        | 0             | 0            | 0              | -             | 0              | -             |
| Precinct OS                     |  | 22587                          | 14        | 2530          | 2287         | 555            | 24.27%        | 475            | 20.77%        |
| Precinct VBM                    |  | 22587                          | 14        | 10411         | 9538         | 2239           | 23.47%        | 1946           | 20.40%        |
| MB Precinct                     |  | 22587                          | 14        | 423           | 390          | 89             | 22.82%        | 70             | 17.95%        |
| Provisional                     |  | 22587                          | 14        | 301           | 241          | 41             | 17.01%        | 62             | 25.73%        |
| <b>Total</b>                    |  | <b>22587</b>                   | <b>70</b> | <b>13665</b>  | <b>12456</b> | <b>2924</b>    | <b>23.47%</b> | <b>2553</b>    | <b>20.50%</b> |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | County Supervisor 1st District |        |             |        |          |        |
|------------------------|--------------------------------|--------|-------------|--------|----------|--------|
|                        | STEVE FERRY                    |        | BETH GAINES |        | ALAN DAY |        |
| Jurisdiction Wide      |                                |        |             |        |          |        |
| 100001.11-CP 100001.11 |                                |        |             |        |          |        |
| Precinct TSx           | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS            | 34                             | 19.88% | 25          | 14.62% | 34       | 19.88% |
| Precinct VBM           | 147                            | 20.39% | 155         | 21.50% | 117      | 16.23% |
| MB Precinct            | 0                              | -      | 0           | -      | 0        | -      |
| Provisional            | 2                              | 13.33% | 5           | 33.33% | 2        | 13.33% |
| Total                  | 183                            | 20.18% | 185         | 20.40% | 153      | 16.87% |
| 100002.11-CP 100002.11 |                                |        |             |        |          |        |
| Precinct TSx           | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS            | 14                             | 18.92% | 11          | 14.86% | 14       | 18.92% |
| Precinct VBM           | 84                             | 19.63% | 97          | 22.66% | 59       | 13.79% |
| MB Precinct            | 0                              | -      | 0           | -      | 0        | -      |
| Provisional            | 2                              | 11.11% | 4           | 22.22% | 3        | 16.67% |
| Total                  | 100                            | 19.23% | 112         | 21.54% | 76       | 14.62% |
| 100003.11-CP 100003.11 |                                |        |             |        |          |        |
| Precinct TSx           | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS            | 15                             | 13.04% | 40          | 34.78% | 14       | 12.17% |
| Precinct VBM           | 122                            | 16.87% | 142         | 19.64% | 75       | 10.37% |
| MB Precinct            | 0                              | -      | 0           | -      | 0        | -      |
| Provisional            | 2                              | 8.33%  | 11          | 45.83% | 2        | 8.33%  |
| Total                  | 139                            | 16.13% | 193         | 22.39% | 91       | 10.56% |
| 100004.11-CP 100004.11 |                                |        |             |        |          |        |
| Precinct TSx           | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS            | 34                             | 15.04% | 64          | 28.32% | 15       | 6.64%  |
| Precinct VBM           | 60                             | 9.05%  | 150         | 22.62% | 80       | 12.07% |
| MB Precinct            | 0                              | -      | 0           | -      | 0        | -      |
| Provisional            | 1                              | 33.33% | 0           | 0.00%  | 0        | 0.00%  |
| Total                  | 95                             | 10.65% | 214         | 23.99% | 95       | 10.65% |
| 100005.11-CP 100005.11 |                                |        |             |        |          |        |
| Precinct TSx           | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS            | 38                             | 28.79% | 33          | 25.00% | 13       | 9.85%  |
| Precinct VBM           | 355                            | 42.67% | 151         | 18.15% | 75       | 9.01%  |
| MB Precinct            | 0                              | -      | 0           | -      | 0        | -      |
| Provisional            | 4                              | 21.05% | 7           | 36.84% | 0        | 0.00%  |
| Total                  | 397                            | 40.39% | 191         | 19.43% | 88       | 8.95%  |
| 100006.11-CP 100006.11 |                                |        |             |        |          |        |
| Precinct TSx           | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS            | 29                             | 12.95% | 60          | 26.79% | 23       | 10.27% |
| Precinct VBM           | 116                            | 13.62% | 248         | 29.11% | 84       | 9.86%  |
| MB Precinct            | 0                              | -      | 0           | -      | 0        | -      |
| Provisional            | 5                              | 23.81% | 5           | 23.81% | 1        | 4.76%  |
| Total                  | 150                            | 13.67% | 313         | 28.53% | 108      | 9.85%  |
| 100008.11-CP 100008.11 |                                |        |             |        |          |        |
| Precinct TSx           | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS            | 30                             | 17.65% | 48          | 28.24% | 19       | 11.18% |
| Precinct VBM           | 141                            | 21.17% | 171         | 25.68% | 72       | 10.81% |
| MB Precinct            | 0                              | -      | 0           | -      | 0        | -      |
| Provisional            | 1                              | 8.33%  | 1           | 8.33%  | 2        | 16.67% |
| Total                  | 172                            | 20.28% | 220         | 25.94% | 93       | 10.97% |
| 100009.11-CP 100009.11 |                                |        |             |        |          |        |
| Precinct TSx           | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS            | 33                             | 21.57% | 48          | 31.37% | 20       | 13.07% |
| Precinct VBM           | 127                            | 18.27% | 206         | 29.64% | 77       | 11.08% |
| MB Precinct            | 0                              | -      | 0           | -      | 0        | -      |
| Provisional            | 5                              | 25.00% | 6           | 30.00% | 2        | 10.00% |
| Total                  | 165                            | 19.01% | 260         | 29.95% | 99       | 11.41% |
| 100012.11-CP 100012.11 |                                |        |             |        |          |        |
| Precinct TSx           | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS            | 41                             | 17.45% | 42          | 17.87% | 23       | 9.79%  |
| Precinct VBM           | 140                            | 15.68% | 223         | 24.97% | 89       | 9.97%  |

**COUNTY OF EL DORADO**  
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|                        | County Supervisor 1st District |        |             |        |          |        |
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|                        | STEVE FERRY                    |        | BETH GAINES |        | ALAN DAY |        |
| MB Precinct            | 0                              | -      | 0           | -      | 0        | -      |
| Provisional            | 5                              | 20.83% | 7           | 29.17% | 3        | 12.50% |
| Total                  | 186                            | 16.15% | 272         | 23.61% | 115      | 9.98%  |
| 100013.11-CP 100013.11 |                                |        |             |        |          |        |
| Precinct TSx           | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS            | 28                             | 12.17% | 60          | 26.09% | 38       | 16.52% |
| Precinct VBM           | 107                            | 12.95% | 250         | 30.27% | 99       | 11.99% |
| MB Precinct            | 0                              | -      | 0           | -      | 0        | -      |
| Provisional            | 2                              | 8.70%  | 9           | 39.13% | 2        | 8.70%  |
| Total                  | 137                            | 12.70% | 319         | 29.56% | 139      | 12.88% |
| 100014.11-CP 100014.11 |                                |        |             |        |          |        |
| Precinct TSx           | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS            | 30                             | 15.87% | 51          | 26.98% | 23       | 12.17% |
| Precinct VBM           | 154                            | 23.55% | 149         | 22.78% | 70       | 10.70% |
| MB Precinct            | 0                              | -      | 0           | -      | 0        | -      |
| Provisional            | 4                              | 17.39% | 14          | 60.87% | 0        | 0.00%  |
| Total                  | 188                            | 21.71% | 214         | 24.71% | 93       | 10.74% |
| 100022.11-CP 100022.11 |                                |        |             |        |          |        |
| Precinct TSx           | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS            | 23                             | 14.11% | 36          | 22.09% | 30       | 18.40% |
| Precinct VBM           | 111                            | 17.85% | 176         | 28.30% | 74       | 11.90% |
| MB Precinct            | 0                              | -      | 0           | -      | 0        | -      |
| Provisional            | 0                              | 0.00%  | 3           | 30.00% | 0        | 0.00%  |
| Total                  | 134                            | 16.86% | 215         | 27.04% | 104      | 13.08% |
| 100023.11-CP 100023.11 |                                |        |             |        |          |        |
| Precinct TSx           | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS            | 40                             | 19.51% | 67          | 32.68% | 15       | 7.32%  |
| Precinct VBM           | 198                            | 20.56% | 272         | 28.25% | 110      | 11.42% |
| MB Precinct            | 0                              | -      | 0           | -      | 0        | -      |
| Provisional            | 6                              | 23.08% | 3           | 11.54% | 5        | 19.23% |
| Total                  | 244                            | 20.44% | 342         | 28.64% | 130      | 10.89% |
| 200003.12-CP 200003.12 |                                |        |             |        |          |        |
| Precinct TSx           | -                              | -      | -           | -      | -        | -      |
| Precinct OS            | -                              | -      | -           | -      | -        | -      |
| Precinct VBM           | -                              | -      | -           | -      | -        | -      |
| MB Precinct            | -                              | -      | -           | -      | -        | -      |
| Provisional            | -                              | -      | -           | -      | -        | -      |
| Total                  | -                              | -      | -           | -      | -        | -      |
| 200004.12-CP 200004.12 |                                |        |             |        |          |        |
| Precinct TSx           | -                              | -      | -           | -      | -        | -      |
| Precinct OS            | -                              | -      | -           | -      | -        | -      |
| Precinct VBM           | -                              | -      | -           | -      | -        | -      |
| MB Precinct            | -                              | -      | -           | -      | -        | -      |
| Provisional            | -                              | -      | -           | -      | -        | -      |
| Total                  | -                              | -      | -           | -      | -        | -      |
| 200005.02-CP 200005.02 |                                |        |             |        |          |        |
| Precinct TSx           | -                              | -      | -           | -      | -        | -      |
| Precinct OS            | -                              | -      | -           | -      | -        | -      |
| Precinct VBM           | -                              | -      | -           | -      | -        | -      |
| MB Precinct            | -                              | -      | -           | -      | -        | -      |
| Provisional            | -                              | -      | -           | -      | -        | -      |
| Total                  | -                              | -      | -           | -      | -        | -      |
| 200006.12-CP 200006.12 |                                |        |             |        |          |        |
| Precinct TSx           | -                              | -      | -           | -      | -        | -      |
| Precinct OS            | -                              | -      | -           | -      | -        | -      |
| Precinct VBM           | -                              | -      | -           | -      | -        | -      |
| MB Precinct            | -                              | -      | -           | -      | -        | -      |
| Provisional            | -                              | -      | -           | -      | -        | -      |
| Total                  | -                              | -      | -           | -      | -        | -      |
| 200008.12-CP 200008.12 |                                |        |             |        |          |        |
| Precinct TSx           | -                              | -      | -           | -      | -        | -      |

**COUNTY OF EL DORADO**  
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|                        | STEVE FERRY                    | BETH GAINES | ALAN DAY |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 200010.12-CP 200010.12 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 200012.12-CP 200012.12 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 200013.12-CP 200013.12 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 200014.12-CP 200014.12 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 200015.02-CP 200015.02 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 200016.12-CP 200016.12 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 200016.13-CP 200016.13 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 200017.02-CP 200017.02 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |

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|                        | STEVE FERRY                    | BETH GAINES | ALAN DAY |
| 200018.02-CP 200018.02 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 200020.02-CP 200020.02 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 200021.01-CP 200021.01 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 200021.02-CP 200021.02 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 200024.02-CP 200024.02 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300001.04-CP 300001.04 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300001.05-CP 300001.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300003.05-CP 300003.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300004.05-CP 300004.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |

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|                        | STEVE FERRY                    | BETH GAINES | ALAN DAY |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300005.05-CP 300005.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300008.05-CP 300008.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300009.04-CP 300009.04 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300010.04-CP 300010.04 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300011.04-CP 300011.04 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300013.05-CP 300013.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300015.04-CP 300015.04 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300015.05-CP 300015.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300017.05-CP 300017.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |



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|                        | STEVE FERRY                    | BETH GAINES | ALAN DAY |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300018.04-CP 300018.04 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300020.04-CP 300020.04 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300021.03-CP 300021.03 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300022.05-CP 300022.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300023.03-CP 300023.03 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 300023.05-CP 300023.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 400001.14-CP 400001.14 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 400002.08-CP 400002.08 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 400003.08-CP 400003.08 |                                |             |          |

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|                        | STEVE FERRY                    | BETH GAINES | ALAN DAY |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 400004.14-CP 400004.14 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 400005.08-CP 400005.08 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 400006.08-CP 400006.08 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 400007.08-CP 400007.08 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 400010.08-CP 400010.08 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 400012.08-CP 400012.08 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 400013.08-CP 400013.08 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 400014.08-CP 400014.08 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |

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|                        | STEVE FERRY                    | BETH GAINES | ALAN DAY |
| Total                  | -                              | -           | -        |
| 400017.08-CP 400017.08 | -                              | -           | -        |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 400018.08-CP 400018.08 | -                              | -           | -        |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 400020.08-CP 400020.08 | -                              | -           | -        |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 400021.07-CP 400021.07 | -                              | -           | -        |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 500001.10-CP 500001.10 | -                              | -           | -        |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 500002.10-CP 500002.10 | -                              | -           | -        |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 500003.10-CP 500003.10 | -                              | -           | -        |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 500004.10-CP 500004.10 | -                              | -           | -        |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 500005.10-CP 500005.10 | -                              | -           | -        |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |

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|                        | STEVE FERRY                    | BETH GAINES | ALAN DAY |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 500006.10-CP 500006.10 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 500007.10-CP 500007.10 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 500009.10-CP 500009.10 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 500011.10-CP 500011.10 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 500012.10-CP 500012.10 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 500015.10-CP 500015.10 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 500020.10-CP 500020.10 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 500021.10-CP 500021.10 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U201.01-MB 95U201.01 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |

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| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U201.02-MB 95U201.02 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U202.02-MB 95U202.02 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U203.02-MB 95U203.02 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U204.01-MB 95U204.01 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U301.05-MB 95U301.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U302.04-MB 95U302.04 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U302.05-MB 95U302.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U303.03-MB 95U303.03 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |

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| 95U303.05-MB 95U303.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U304.05-MB 95U304.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U305.05-MB 95U305.05 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U401.08-MB 95U401.08 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U402.07-MB 95U402.07 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U402.08-MB 95U402.08 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U403.08-MB 95U403.08 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U404.06-MB 95U404.06 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |
| Provisional            | -                              | -           | -        |
| Total                  | -                              | -           | -        |
| 95U404.08-MB 95U404.08 |                                |             |          |
| Precinct TSx           | -                              | -           | -        |
| Precinct OS            | -                              | -           | -        |
| Precinct VBM           | -                              | -           | -        |
| MB Precinct            | -                              | -           | -        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                               | County Supervisor 1st District |        |             |        |          |        |
|-------------------------------|--------------------------------|--------|-------------|--------|----------|--------|
|                               | STEVE FERRY                    |        | BETH GAINES |        | ALAN DAY |        |
| Provisional                   | -                              | -      | -           | -      | -        | -      |
| Total                         | -                              | -      | -           | -      | -        | -      |
| 95U501.09-MB 95U501.09        |                                |        |             |        |          |        |
| Precinct TSx                  | -                              | -      | -           | -      | -        | -      |
| Precinct OS                   | -                              | -      | -           | -      | -        | -      |
| Precinct VBM                  | -                              | -      | -           | -      | -        | -      |
| MB Precinct                   | -                              | -      | -           | -      | -        | -      |
| Provisional                   | -                              | -      | -           | -      | -        | -      |
| Total                         | -                              | -      | -           | -      | -        | -      |
| 95U501.10-MB 95U501.10        |                                |        |             |        |          |        |
| Precinct TSx                  | -                              | -      | -           | -      | -        | -      |
| Precinct OS                   | -                              | -      | -           | -      | -        | -      |
| Precinct VBM                  | -                              | -      | -           | -      | -        | -      |
| MB Precinct                   | -                              | -      | -           | -      | -        | -      |
| Provisional                   | -                              | -      | -           | -      | -        | -      |
| Total                         | -                              | -      | -           | -      | -        | -      |
| 95U598.10-MB 95U598.10        |                                |        |             |        |          |        |
| Precinct TSx                  | -                              | -      | -           | -      | -        | -      |
| Precinct OS                   | -                              | -      | -           | -      | -        | -      |
| Precinct VBM                  | -                              | -      | -           | -      | -        | -      |
| MB Precinct                   | -                              | -      | -           | -      | -        | -      |
| Provisional                   | -                              | -      | -           | -      | -        | -      |
| Total                         | -                              | -      | -           | -      | -        | -      |
| 95U599.10-MB 95U599.10        |                                |        |             |        |          |        |
| Precinct TSx                  | -                              | -      | -           | -      | -        | -      |
| Precinct OS                   | -                              | -      | -           | -      | -        | -      |
| Precinct VBM                  | -                              | -      | -           | -      | -        | -      |
| MB Precinct                   | -                              | -      | -           | -      | -        | -      |
| Provisional                   | -                              | -      | -           | -      | -        | -      |
| Total                         | -                              | -      | -           | -      | -        | -      |
| 96U101.11-MB 96U101.11        |                                |        |             |        |          |        |
| Precinct TSx                  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                   | 0                              | -      | 0           | -      | 0        | -      |
| Precinct VBM                  | 0                              | -      | 0           | -      | 0        | -      |
| MB Precinct                   | 54                             | 13.85% | 131         | 33.59% | 44       | 11.28% |
| Provisional                   | 0                              | 0.00%  | 0           | 0.00%  | 0        | 0.00%  |
| Total                         | 54                             | 13.74% | 131         | 33.33% | 44       | 11.20% |
| 96U401.14-MB 96U401.14        |                                |        |             |        |          |        |
| Precinct TSx                  | -                              | -      | -           | -      | -        | -      |
| Precinct OS                   | -                              | -      | -           | -      | -        | -      |
| Precinct VBM                  | -                              | -      | -           | -      | -        | -      |
| MB Precinct                   | -                              | -      | -           | -      | -        | -      |
| Provisional                   | -                              | -      | -           | -      | -        | -      |
| Total                         | -                              | -      | -           | -      | -        | -      |
| Total                         |                                |        |             |        |          |        |
| Precinct TSx                  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                   | 389                            | 17.01% | 585         | 25.58% | 281      | 12.29% |
| Precinct VBM                  | 1862                           | 19.52% | 2390        | 25.06% | 1081     | 11.33% |
| MB Precinct                   | 54                             | 13.85% | 131         | 33.59% | 44       | 11.28% |
| Provisional                   | 39                             | 16.18% | 75          | 31.12% | 22       | 9.13%  |
| Total                         | 2344                           | 18.82% | 3181        | 25.54% | 1428     | 11.46% |
| STATE BOARD OF EQUALIZATION   |                                |        |             |        |          |        |
| STATE BOARD OF EQUALIZATION 1 |                                |        |             |        |          |        |
| Precinct TSx                  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                   | 389                            | 17.01% | 585         | 25.58% | 281      | 12.29% |
| Precinct VBM                  | 1862                           | 19.52% | 2390        | 25.06% | 1081     | 11.33% |
| MB Precinct                   | 54                             | 13.85% | 131         | 33.59% | 44       | 11.28% |
| Provisional                   | 39                             | 16.18% | 75          | 31.12% | 22       | 9.13%  |
| Total                         | 2344                           | 18.82% | 3181        | 25.54% | 1428     | 11.46% |
| Total                         |                                |        |             |        |          |        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                     | County Supervisor 1st District |               |             |               |             |               |
|-------------------------------------|--------------------------------|---------------|-------------|---------------|-------------|---------------|
|                                     | STEVE FERRY                    |               | BETH GAINES |               | ALAN DAY    |               |
| Precinct TSx                        | 0                              | -             | 0           | -             | 0           | -             |
| Precinct OS                         | 389                            | 17.01%        | 585         | 25.58%        | 281         | 12.29%        |
| Precinct VBM                        | 1862                           | 19.52%        | 2390        | 25.06%        | 1081        | 11.33%        |
| MB Precinct                         | 54                             | 13.85%        | 131         | 33.59%        | 44          | 11.28%        |
| Provisional                         | 39                             | 16.18%        | 75          | 31.12%        | 22          | 9.13%         |
| <b>Total</b>                        | <b>2344</b>                    | <b>18.82%</b> | <b>3181</b> | <b>25.54%</b> | <b>1428</b> | <b>11.46%</b> |
| <b>UNITED STATES SENATE</b>         |                                |               |             |               |             |               |
| <b>UNITED STATES SENATE</b>         |                                |               |             |               |             |               |
| Precinct TSx                        | 0                              | -             | 0           | -             | 0           | -             |
| Precinct OS                         | 389                            | 17.01%        | 585         | 25.58%        | 281         | 12.29%        |
| Precinct VBM                        | 1862                           | 19.52%        | 2390        | 25.06%        | 1081        | 11.33%        |
| MB Precinct                         | 54                             | 13.85%        | 131         | 33.59%        | 44          | 11.28%        |
| Provisional                         | 39                             | 16.18%        | 75          | 31.12%        | 22          | 9.13%         |
| <b>Total</b>                        | <b>2344</b>                    | <b>18.82%</b> | <b>3181</b> | <b>25.54%</b> | <b>1428</b> | <b>11.46%</b> |
| <b>Total</b>                        |                                |               |             |               |             |               |
| Precinct TSx                        | 0                              | -             | 0           | -             | 0           | -             |
| Precinct OS                         | 389                            | 17.01%        | 585         | 25.58%        | 281         | 12.29%        |
| Precinct VBM                        | 1862                           | 19.52%        | 2390        | 25.06%        | 1081        | 11.33%        |
| MB Precinct                         | 54                             | 13.85%        | 131         | 33.59%        | 44          | 11.28%        |
| Provisional                         | 39                             | 16.18%        | 75          | 31.12%        | 22          | 9.13%         |
| <b>Total</b>                        | <b>2344</b>                    | <b>18.82%</b> | <b>3181</b> | <b>25.54%</b> | <b>1428</b> | <b>11.46%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |                                |               |             |               |             |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                                |               |             |               |             |               |
| Precinct TSx                        | 0                              | -             | 0           | -             | 0           | -             |
| Precinct OS                         | 389                            | 17.01%        | 585         | 25.58%        | 281         | 12.29%        |
| Precinct VBM                        | 1862                           | 19.52%        | 2390        | 25.06%        | 1081        | 11.33%        |
| MB Precinct                         | 54                             | 13.85%        | 131         | 33.59%        | 44          | 11.28%        |
| Provisional                         | 39                             | 16.18%        | 75          | 31.12%        | 22          | 9.13%         |
| <b>Total</b>                        | <b>2344</b>                    | <b>18.82%</b> | <b>3181</b> | <b>25.54%</b> | <b>1428</b> | <b>11.46%</b> |
| <b>Total</b>                        |                                |               |             |               |             |               |
| Precinct TSx                        | 0                              | -             | 0           | -             | 0           | -             |
| Precinct OS                         | 389                            | 17.01%        | 585         | 25.58%        | 281         | 12.29%        |
| Precinct VBM                        | 1862                           | 19.52%        | 2390        | 25.06%        | 1081        | 11.33%        |
| MB Precinct                         | 54                             | 13.85%        | 131         | 33.59%        | 44          | 11.28%        |
| Provisional                         | 39                             | 16.18%        | 75          | 31.12%        | 22          | 9.13%         |
| <b>Total</b>                        | <b>2344</b>                    | <b>18.82%</b> | <b>3181</b> | <b>25.54%</b> | <b>1428</b> | <b>11.46%</b> |
| <b>STATE SENATE</b>                 |                                |               |             |               |             |               |
| <b>1ST STATE SENATE DISTRICT</b>    |                                |               |             |               |             |               |
| Precinct TSx                        | 0                              | -             | 0           | -             | 0           | -             |
| Precinct OS                         | 389                            | 17.01%        | 585         | 25.58%        | 281         | 12.29%        |
| Precinct VBM                        | 1862                           | 19.52%        | 2390        | 25.06%        | 1081        | 11.33%        |
| MB Precinct                         | 54                             | 13.85%        | 131         | 33.59%        | 44          | 11.28%        |
| Provisional                         | 39                             | 16.18%        | 75          | 31.12%        | 22          | 9.13%         |
| <b>Total</b>                        | <b>2344</b>                    | <b>18.82%</b> | <b>3181</b> | <b>25.54%</b> | <b>1428</b> | <b>11.46%</b> |
| <b>Total</b>                        |                                |               |             |               |             |               |
| Precinct TSx                        | 0                              | -             | 0           | -             | 0           | -             |
| Precinct OS                         | 389                            | 17.01%        | 585         | 25.58%        | 281         | 12.29%        |
| Precinct VBM                        | 1862                           | 19.52%        | 2390        | 25.06%        | 1081        | 11.33%        |
| MB Precinct                         | 54                             | 13.85%        | 131         | 33.59%        | 44          | 11.28%        |
| Provisional                         | 39                             | 16.18%        | 75          | 31.12%        | 22          | 9.13%         |
| <b>Total</b>                        | <b>2344</b>                    | <b>18.82%</b> | <b>3181</b> | <b>25.54%</b> | <b>1428</b> | <b>11.46%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                                |               |             |               |             |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |                                |               |             |               |             |               |
| Precinct TSx                        | 0                              | -             | 0           | -             | 0           | -             |
| Precinct OS                         | 0                              | -             | 0           | -             | 0           | -             |
| Precinct VBM                        | 0                              | -             | 0           | -             | 0           | -             |
| MB Precinct                         | 0                              | -             | 0           | -             | 0           | -             |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                   |  | County Supervisor 1st District |        |             |        |          |        |
|-----------------------------------|--|--------------------------------|--------|-------------|--------|----------|--------|
|                                   |  | STEVE FERRY                    |        | BETH GAINES |        | ALAN DAY |        |
| Provisional                       |  | 0                              | -      | 0           | -      | 0        | -      |
| Total                             |  | 0                              | -      | 0           | -      | 0        | -      |
| <b>6TH STATE ASSEMBLY DIST</b>    |  |                                |        |             |        |          |        |
| Precinct TSx                      |  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                       |  | 389                            | 17.01% | 585         | 25.58% | 281      | 12.29% |
| Precinct VBM                      |  | 1862                           | 19.52% | 2390        | 25.06% | 1081     | 11.33% |
| MB Precinct                       |  | 54                             | 13.85% | 131         | 33.59% | 44       | 11.28% |
| Provisional                       |  | 39                             | 16.18% | 75          | 31.12% | 22       | 9.13%  |
| Total                             |  | 2344                           | 18.82% | 3181        | 25.54% | 1428     | 11.46% |
| <b>Total</b>                      |  |                                |        |             |        |          |        |
| Precinct TSx                      |  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                       |  | 389                            | 17.01% | 585         | 25.58% | 281      | 12.29% |
| Precinct VBM                      |  | 1862                           | 19.52% | 2390        | 25.06% | 1081     | 11.33% |
| MB Precinct                       |  | 54                             | 13.85% | 131         | 33.59% | 44       | 11.28% |
| Provisional                       |  | 39                             | 16.18% | 75          | 31.12% | 22       | 9.13%  |
| Total                             |  | 2344                           | 18.82% | 3181        | 25.54% | 1428     | 11.46% |
| <b>SUPERVISORIAL DISTRICTS</b>    |  |                                |        |             |        |          |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |  |                                |        |             |        |          |        |
| Precinct TSx                      |  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                       |  | 389                            | 17.01% | 585         | 25.58% | 281      | 12.29% |
| Precinct VBM                      |  | 1862                           | 19.52% | 2390        | 25.06% | 1081     | 11.33% |
| MB Precinct                       |  | 54                             | 13.85% | 131         | 33.59% | 44       | 11.28% |
| Provisional                       |  | 39                             | 16.18% | 75          | 31.12% | 22       | 9.13%  |
| Total                             |  | 2344                           | 18.82% | 3181        | 25.54% | 1428     | 11.46% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |  |                                |        |             |        |          |        |
| Precinct TSx                      |  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                       |  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct VBM                      |  | 0                              | -      | 0           | -      | 0        | -      |
| MB Precinct                       |  | 0                              | -      | 0           | -      | 0        | -      |
| Provisional                       |  | 0                              | -      | 0           | -      | 0        | -      |
| Total                             |  | 0                              | -      | 0           | -      | 0        | -      |
| <b>3RD SUPERVISORIAL DISTRICT</b> |  |                                |        |             |        |          |        |
| Precinct TSx                      |  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                       |  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct VBM                      |  | 0                              | -      | 0           | -      | 0        | -      |
| MB Precinct                       |  | 0                              | -      | 0           | -      | 0        | -      |
| Provisional                       |  | 0                              | -      | 0           | -      | 0        | -      |
| Total                             |  | 0                              | -      | 0           | -      | 0        | -      |
| <b>4TH SUPERVISORIAL DISTRICT</b> |  |                                |        |             |        |          |        |
| Precinct TSx                      |  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                       |  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct VBM                      |  | 0                              | -      | 0           | -      | 0        | -      |
| MB Precinct                       |  | 0                              | -      | 0           | -      | 0        | -      |
| Provisional                       |  | 0                              | -      | 0           | -      | 0        | -      |
| Total                             |  | 0                              | -      | 0           | -      | 0        | -      |
| <b>5TH SUPERVISORIAL DISTRICT</b> |  |                                |        |             |        |          |        |
| Precinct TSx                      |  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                       |  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct VBM                      |  | 0                              | -      | 0           | -      | 0        | -      |
| MB Precinct                       |  | 0                              | -      | 0           | -      | 0        | -      |
| Provisional                       |  | 0                              | -      | 0           | -      | 0        | -      |
| Total                             |  | 0                              | -      | 0           | -      | 0        | -      |
| <b>Total</b>                      |  |                                |        |             |        |          |        |
| Precinct TSx                      |  | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                       |  | 389                            | 17.01% | 585         | 25.58% | 281      | 12.29% |
| Precinct VBM                      |  | 1862                           | 19.52% | 2390        | 25.06% | 1081     | 11.33% |
| MB Precinct                       |  | 54                             | 13.85% | 131         | 33.59% | 44       | 11.28% |
| Provisional                       |  | 39                             | 16.18% | 75          | 31.12% | 22       | 9.13%  |
| Total                             |  | 2344                           | 18.82% | 3181        | 25.54% | 1428     | 11.46% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                 | County Supervisor 1st District |        |             |        |          |        |
|---------------------------------|--------------------------------|--------|-------------|--------|----------|--------|
|                                 | STEVE FERRY                    |        | BETH GAINES |        | ALAN DAY |        |
| <b>CITIES</b>                   |                                |        |             |        |          |        |
| <b>CITY OF PLACERVILLE</b>      |                                |        |             |        |          |        |
| Precinct TSx                    | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                     | 0                              | -      | 0           | -      | 0        | -      |
| Precinct VBM                    | 0                              | -      | 0           | -      | 0        | -      |
| MB Precinct                     | 0                              | -      | 0           | -      | 0        | -      |
| Provisional                     | 0                              | -      | 0           | -      | 0        | -      |
| Total                           | 0                              | -      | 0           | -      | 0        | -      |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                                |        |             |        |          |        |
| Precinct TSx                    | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                     | 0                              | -      | 0           | -      | 0        | -      |
| Precinct VBM                    | 0                              | -      | 0           | -      | 0        | -      |
| MB Precinct                     | 0                              | -      | 0           | -      | 0        | -      |
| Provisional                     | 0                              | -      | 0           | -      | 0        | -      |
| Total                           | 0                              | -      | 0           | -      | 0        | -      |
| <b>UNINCORPORATED AREA</b>      |                                |        |             |        |          |        |
| Precinct TSx                    | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                     | 389                            | 17.01% | 585         | 25.58% | 281      | 12.29% |
| Precinct VBM                    | 1862                           | 19.52% | 2390        | 25.06% | 1081     | 11.33% |
| MB Precinct                     | 54                             | 13.85% | 131         | 33.59% | 44       | 11.28% |
| Provisional                     | 39                             | 16.18% | 75          | 31.12% | 22       | 9.13%  |
| Total                           | 2344                           | 18.82% | 3181        | 25.54% | 1428     | 11.46% |
| <b>Total</b>                    |                                |        |             |        |          |        |
| Precinct TSx                    | 0                              | -      | 0           | -      | 0        | -      |
| Precinct OS                     | 389                            | 17.01% | 585         | 25.58% | 281      | 12.29% |
| Precinct VBM                    | 1862                           | 19.52% | 2390        | 25.06% | 1081     | 11.33% |
| MB Precinct                     | 54                             | 13.85% | 131         | 33.59% | 44       | 11.28% |
| Provisional                     | 39                             | 16.18% | 75          | 31.12% | 22       | 9.13%  |
| Total                           | 2344                           | 18.82% | 3181        | 25.54% | 1428     | 11.46% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | County Supervisor 2nd District |          |               |             |                |
|------------------------|--------------------------------|----------|---------------|-------------|----------------|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | SHIVA FRENTZEN |
| Jurisdiction Wide      |                                |          |               |             |                |
| 100001.11-CP 100001.11 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 100002.11-CP 100002.11 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 100003.11-CP 100003.11 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 100004.11-CP 100004.11 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 100005.11-CP 100005.11 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 100006.11-CP 100006.11 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 100008.11-CP 100008.11 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 100009.11-CP 100009.11 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 100012.11-CP 100012.11 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |

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|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | SHIVA FRENTZEN |        |
| MB Precinct            | -                              | -        | -             | -           | -              | -      |
| Provisional            | -                              | -        | -             | -           | -              | -      |
| Total                  | -                              | -        | -             | -           | -              | -      |
| 100013.11-CP 100013.11 |                                |          |               |             |                |        |
| Precinct TSx           | -                              | -        | -             | -           | -              | -      |
| Precinct OS            | -                              | -        | -             | -           | -              | -      |
| Precinct VBM           | -                              | -        | -             | -           | -              | -      |
| MB Precinct            | -                              | -        | -             | -           | -              | -      |
| Provisional            | -                              | -        | -             | -           | -              | -      |
| Total                  | -                              | -        | -             | -           | -              | -      |
| 100014.11-CP 100014.11 |                                |          |               |             |                |        |
| Precinct TSx           | -                              | -        | -             | -           | -              | -      |
| Precinct OS            | -                              | -        | -             | -           | -              | -      |
| Precinct VBM           | -                              | -        | -             | -           | -              | -      |
| MB Precinct            | -                              | -        | -             | -           | -              | -      |
| Provisional            | -                              | -        | -             | -           | -              | -      |
| Total                  | -                              | -        | -             | -           | -              | -      |
| 100022.11-CP 100022.11 |                                |          |               |             |                |        |
| Precinct TSx           | -                              | -        | -             | -           | -              | -      |
| Precinct OS            | -                              | -        | -             | -           | -              | -      |
| Precinct VBM           | -                              | -        | -             | -           | -              | -      |
| MB Precinct            | -                              | -        | -             | -           | -              | -      |
| Provisional            | -                              | -        | -             | -           | -              | -      |
| Total                  | -                              | -        | -             | -           | -              | -      |
| 100023.11-CP 100023.11 |                                |          |               |             |                |        |
| Precinct TSx           | -                              | -        | -             | -           | -              | -      |
| Precinct OS            | -                              | -        | -             | -           | -              | -      |
| Precinct VBM           | -                              | -        | -             | -           | -              | -      |
| MB Precinct            | -                              | -        | -             | -           | -              | -      |
| Provisional            | -                              | -        | -             | -           | -              | -      |
| Total                  | -                              | -        | -             | -           | -              | -      |
| 200003.12-CP 200003.12 |                                |          |               |             |                |        |
| Precinct TSx           | 1362                           | 1        | 0             | 0           | 0              | -      |
| Precinct OS            | 1362                           | 1        | 166           | 140         | 84             | 60.00% |
| Precinct VBM           | 1362                           | 1        | 558           | 499         | 314            | 62.93% |
| MB Precinct            | 1362                           | 1        | 0             | 0           | 0              | -      |
| Provisional            | 1362                           | 1        | 31            | 26          | 13             | 50.00% |
| Total                  | 1362                           | 5        | 755           | 665         | 411            | 61.80% |
| 200004.12-CP 200004.12 |                                |          |               |             |                |        |
| Precinct TSx           | 1507                           | 1        | 0             | 0           | 0              | -      |
| Precinct OS            | 1507                           | 1        | 182           | 167         | 102            | 61.08% |
| Precinct VBM           | 1507                           | 1        | 662           | 588         | 406            | 69.05% |
| MB Precinct            | 1507                           | 1        | 0             | 0           | 0              | -      |
| Provisional            | 1507                           | 1        | 14            | 12          | 6              | 50.00% |
| Total                  | 1507                           | 5        | 858           | 767         | 514            | 67.01% |
| 200005.02-CP 200005.02 |                                |          |               |             |                |        |
| Precinct TSx           | 867                            | 1        | 0             | 0           | 0              | -      |
| Precinct OS            | 867                            | 1        | 94            | 85          | 44             | 51.76% |
| Precinct VBM           | 867                            | 1        | 452           | 417         | 204            | 48.92% |
| MB Precinct            | 867                            | 1        | 0             | 0           | 0              | -      |
| Provisional            | 867                            | 1        | 21            | 19          | 11             | 57.89% |
| Total                  | 867                            | 5        | 567           | 521         | 259            | 49.71% |
| 200006.12-CP 200006.12 |                                |          |               |             |                |        |
| Precinct TSx           | 1086                           | 1        | 0             | 0           | 0              | -      |
| Precinct OS            | 1086                           | 1        | 117           | 107         | 56             | 52.34% |
| Precinct VBM           | 1086                           | 1        | 532           | 481         | 295            | 61.33% |
| MB Precinct            | 1086                           | 1        | 0             | 0           | 0              | -      |
| Provisional            | 1086                           | 1        | 29            | 24          | 15             | 62.50% |
| Total                  | 1086                           | 5        | 678           | 612         | 366            | 59.80% |
| 200008.12-CP 200008.12 |                                |          |               |             |                |        |
| Precinct TSx           | 999                            | 1        | 0             | 0           | 0              | -      |

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|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | SHIVA FRENTZEN |         |  |
| Precinct OS            | 999                            | 1        | 155           | 130         | 75             | 57.69%  |  |
| Precinct VBM           | 999                            | 1        | 379           | 325         | 211            | 64.92%  |  |
| MB Precinct            | 999                            | 1        | 0             | 0           | 0              | -       |  |
| Provisional            | 999                            | 1        | 10            | 8           | 4              | 50.00%  |  |
| Total                  | 999                            | 5        | 544           | 463         | 290            | 62.63%  |  |
| 200010.12-CP 200010.12 |                                |          |               |             |                |         |  |
| Precinct TSx           | 1976                           | 1        | 4             | 4           | 1              | 25.00%  |  |
| Precinct OS            | 1976                           | 1        | 197           | 161         | 91             | 56.52%  |  |
| Precinct VBM           | 1976                           | 1        | 859           | 736         | 429            | 58.29%  |  |
| MB Precinct            | 1976                           | 1        | 0             | 0           | 0              | -       |  |
| Provisional            | 1976                           | 1        | 34            | 23          | 12             | 52.17%  |  |
| Total                  | 1976                           | 5        | 1094          | 924         | 533            | 57.68%  |  |
| 200012.12-CP 200012.12 |                                |          |               |             |                |         |  |
| Precinct TSx           | 1064                           | 1        | 0             | 0           | 0              | -       |  |
| Precinct OS            | 1064                           | 1        | 145           | 135         | 102            | 75.56%  |  |
| Precinct VBM           | 1064                           | 1        | 464           | 433         | 319            | 73.67%  |  |
| MB Precinct            | 1064                           | 1        | 0             | 0           | 0              | -       |  |
| Provisional            | 1064                           | 1        | 14            | 13          | 8              | 61.54%  |  |
| Total                  | 1064                           | 5        | 623           | 581         | 429            | 73.84%  |  |
| 200013.12-CP 200013.12 |                                |          |               |             |                |         |  |
| Precinct TSx           | 1574                           | 1        | 0             | 0           | 0              | -       |  |
| Precinct OS            | 1574                           | 1        | 211           | 191         | 127            | 66.49%  |  |
| Precinct VBM           | 1574                           | 1        | 701           | 648         | 394            | 60.80%  |  |
| MB Precinct            | 1574                           | 1        | 0             | 0           | 0              | -       |  |
| Provisional            | 1574                           | 1        | 23            | 21          | 15             | 71.43%  |  |
| Total                  | 1574                           | 5        | 935           | 860         | 536            | 62.33%  |  |
| 200014.12-CP 200014.12 |                                |          |               |             |                |         |  |
| Precinct TSx           | 1438                           | 1        | 0             | 0           | 0              | -       |  |
| Precinct OS            | 1438                           | 1        | 201           | 186         | 130            | 69.89%  |  |
| Precinct VBM           | 1438                           | 1        | 613           | 547         | 358            | 65.45%  |  |
| MB Precinct            | 1438                           | 1        | 0             | 0           | 0              | -       |  |
| Provisional            | 1438                           | 1        | 30            | 25          | 20             | 80.00%  |  |
| Total                  | 1438                           | 5        | 844           | 758         | 508            | 67.02%  |  |
| 200015.02-CP 200015.02 |                                |          |               |             |                |         |  |
| Precinct TSx           | 1832                           | 1        | 0             | 0           | 0              | -       |  |
| Precinct OS            | 1832                           | 1        | 218           | 196         | 94             | 47.96%  |  |
| Precinct VBM           | 1832                           | 1        | 886           | 814         | 418            | 51.35%  |  |
| MB Precinct            | 1832                           | 1        | 0             | 0           | 0              | -       |  |
| Provisional            | 1832                           | 1        | 22            | 19          | 9              | 47.37%  |  |
| Total                  | 1832                           | 5        | 1126          | 1029        | 521            | 50.63%  |  |
| 200016.12-CP 200016.12 |                                |          |               |             |                |         |  |
| Precinct TSx           | 301                            | 1        | 0             | 0           | 0              | -       |  |
| Precinct OS            | 301                            | 1        | 26            | 23          | 14             | 60.87%  |  |
| Precinct VBM           | 301                            | 1        | 139           | 124         | 88             | 70.97%  |  |
| MB Precinct            | 301                            | 1        | 0             | 0           | 0              | -       |  |
| Provisional            | 301                            | 1        | 6             | 2           | 2              | 100.00% |  |
| Total                  | 301                            | 5        | 171           | 149         | 104            | 69.80%  |  |
| 200016.13-CP 200016.13 |                                |          |               |             |                |         |  |
| Precinct TSx           | 565                            | 1        | 0             | 0           | 0              | -       |  |
| Precinct OS            | 565                            | 1        | 80            | 72          | 58             | 80.56%  |  |
| Precinct VBM           | 565                            | 1        | 338           | 316         | 252            | 79.75%  |  |
| MB Precinct            | 565                            | 1        | 0             | 0           | 0              | -       |  |
| Provisional            | 565                            | 1        | 6             | 5           | 3              | 60.00%  |  |
| Total                  | 565                            | 5        | 424           | 393         | 313            | 79.64%  |  |
| 200017.02-CP 200017.02 |                                |          |               |             |                |         |  |
| Precinct TSx           | 1286                           | 1        | 0             | 0           | 0              | -       |  |
| Precinct OS            | 1286                           | 1        | 146           | 130         | 65             | 50.00%  |  |
| Precinct VBM           | 1286                           | 1        | 594           | 530         | 287            | 54.15%  |  |
| MB Precinct            | 1286                           | 1        | 0             | 0           | 0              | -       |  |
| Provisional            | 1286                           | 1        | 12            | 9           | 1              | 11.11%  |  |
| Total                  | 1286                           | 5        | 752           | 669         | 353            | 52.77%  |  |

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|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | SHIVA FRENTZEN |        |  |
| 200018.02-CP 200018.02 |                                |          |               |             |                |        |  |
| Precinct TSx           | 1454                           | 1        | 0             | 0           | 0              | -      |  |
| Precinct OS            | 1454                           | 1        | 169           | 152         | 70             | 46.05% |  |
| Precinct VBM           | 1454                           | 1        | 704           | 641         | 315            | 49.14% |  |
| MB Precinct            | 1454                           | 1        | 0             | 0           | 0              | -      |  |
| Provisional            | 1454                           | 1        | 13            | 6           | 3              | 50.00% |  |
| Total                  | 1454                           | 5        | 886           | 799         | 388            | 48.56% |  |
| 200020.02-CP 200020.02 |                                |          |               |             |                |        |  |
| Precinct TSx           | 1213                           | 1        | 0             | 0           | 0              | -      |  |
| Precinct OS            | 1213                           | 1        | 139           | 123         | 74             | 60.16% |  |
| Precinct VBM           | 1213                           | 1        | 526           | 496         | 296            | 59.68% |  |
| MB Precinct            | 1213                           | 1        | 0             | 0           | 0              | -      |  |
| Provisional            | 1213                           | 1        | 19            | 14          | 10             | 71.43% |  |
| Total                  | 1213                           | 5        | 684           | 633         | 380            | 60.03% |  |
| 200021.01-CP 200021.01 |                                |          |               |             |                |        |  |
| Precinct TSx           | 436                            | 1        | 0             | 0           | 0              | -      |  |
| Precinct OS            | 436                            | 1        | 38            | 30          | 17             | 56.67% |  |
| Precinct VBM           | 436                            | 1        | 206           | 189         | 102            | 53.97% |  |
| MB Precinct            | 436                            | 1        | 0             | 0           | 0              | -      |  |
| Provisional            | 436                            | 1        | 7             | 5           | 1              | 20.00% |  |
| Total                  | 436                            | 5        | 251           | 224         | 120            | 53.57% |  |
| 200021.02-CP 200021.02 |                                |          |               |             |                |        |  |
| Precinct TSx           | 1296                           | 1        | 0             | 0           | 0              | -      |  |
| Precinct OS            | 1296                           | 1        | 170           | 155         | 100            | 64.52% |  |
| Precinct VBM           | 1296                           | 1        | 752           | 689         | 485            | 70.39% |  |
| MB Precinct            | 1296                           | 1        | 0             | 0           | 0              | -      |  |
| Provisional            | 1296                           | 1        | 20            | 16          | 13             | 81.25% |  |
| Total                  | 1296                           | 5        | 942           | 860         | 598            | 69.53% |  |
| 200024.02-CP 200024.02 |                                |          |               |             |                |        |  |
| Precinct TSx           | 555                            | 1        | 0             | 0           | 0              | -      |  |
| Precinct OS            | 555                            | 1        | 55            | 51          | 27             | 52.94% |  |
| Precinct VBM           | 555                            | 1        | 282           | 266         | 131            | 49.25% |  |
| MB Precinct            | 555                            | 1        | 0             | 0           | 0              | -      |  |
| Provisional            | 555                            | 1        | 9             | 6           | 3              | 50.00% |  |
| Total                  | 555                            | 5        | 346           | 323         | 161            | 49.85% |  |
| 300001.04-CP 300001.04 |                                |          |               |             |                |        |  |
| Precinct TSx           | -                              | -        | -             | -           | -              | -      |  |
| Precinct OS            | -                              | -        | -             | -           | -              | -      |  |
| Precinct VBM           | -                              | -        | -             | -           | -              | -      |  |
| MB Precinct            | -                              | -        | -             | -           | -              | -      |  |
| Provisional            | -                              | -        | -             | -           | -              | -      |  |
| Total                  | -                              | -        | -             | -           | -              | -      |  |
| 300001.05-CP 300001.05 |                                |          |               |             |                |        |  |
| Precinct TSx           | -                              | -        | -             | -           | -              | -      |  |
| Precinct OS            | -                              | -        | -             | -           | -              | -      |  |
| Precinct VBM           | -                              | -        | -             | -           | -              | -      |  |
| MB Precinct            | -                              | -        | -             | -           | -              | -      |  |
| Provisional            | -                              | -        | -             | -           | -              | -      |  |
| Total                  | -                              | -        | -             | -           | -              | -      |  |
| 300003.05-CP 300003.05 |                                |          |               |             |                |        |  |
| Precinct TSx           | -                              | -        | -             | -           | -              | -      |  |
| Precinct OS            | -                              | -        | -             | -           | -              | -      |  |
| Precinct VBM           | -                              | -        | -             | -           | -              | -      |  |
| MB Precinct            | -                              | -        | -             | -           | -              | -      |  |
| Provisional            | -                              | -        | -             | -           | -              | -      |  |
| Total                  | -                              | -        | -             | -           | -              | -      |  |
| 300004.05-CP 300004.05 |                                |          |               |             |                |        |  |
| Precinct TSx           | -                              | -        | -             | -           | -              | -      |  |
| Precinct OS            | -                              | -        | -             | -           | -              | -      |  |
| Precinct VBM           | -                              | -        | -             | -           | -              | -      |  |
| MB Precinct            | -                              | -        | -             | -           | -              | -      |  |

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|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | SHIVA FRENTZEN |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300005.05-CP 300005.05 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300008.05-CP 300008.05 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300009.04-CP 300009.04 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300010.04-CP 300010.04 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300011.04-CP 300011.04 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300013.05-CP 300013.05 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300015.04-CP 300015.04 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300015.05-CP 300015.05 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300017.05-CP 300017.05 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |

**COUNTY OF EL DORADO**  
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|                        | County Supervisor 2nd District |          |               |             |                |
|------------------------|--------------------------------|----------|---------------|-------------|----------------|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | SHIVA FRENTZEN |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300018.04-CP 300018.04 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300020.04-CP 300020.04 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300021.03-CP 300021.03 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300022.05-CP 300022.05 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300023.03-CP 300023.03 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 300023.05-CP 300023.05 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 400001.14-CP 400001.14 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 400002.08-CP 400002.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 400003.08-CP 400003.08 |                                |          |               |             |                |



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|                        | County Supervisor 2nd District |          |               |             |                |
|------------------------|--------------------------------|----------|---------------|-------------|----------------|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | SHIVA FRENTZEN |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 400004.14-CP 400004.14 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 400005.08-CP 400005.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 400006.08-CP 400006.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 400007.08-CP 400007.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 400010.08-CP 400010.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 400012.08-CP 400012.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 400013.08-CP 400013.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 400014.08-CP 400014.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |

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|                        | County Supervisor 2nd District |          |               |             |                |
|------------------------|--------------------------------|----------|---------------|-------------|----------------|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | SHIVA FRENTZEN |
| Total                  | -                              | -        | -             | -           | -              |
| 400017.08-CP 400017.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 400018.08-CP 400018.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 400020.08-CP 400020.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 400021.07-CP 400021.07 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 500001.10-CP 500001.10 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 500002.10-CP 500002.10 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 500003.10-CP 500003.10 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 500004.10-CP 500004.10 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 500005.10-CP 500005.10 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |

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|------------------------|--------------------------------|----------|---------------|-------------|----------------|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | SHIVA FRENTZEN |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 500006.10-CP 500006.10 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 500007.10-CP 500007.10 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 500009.10-CP 500009.10 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 500011.10-CP 500011.10 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 500012.10-CP 500012.10 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 500015.10-CP 500015.10 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 500020.10-CP 500020.10 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 500021.10-CP 500021.10 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 95U201.01-MB 95U201.01 |                                |          |               |             |                |
| Precinct TSx           | 8                              | 1        | 0             | 0           | 0              |



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|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | SHIVA FRENTZEN |
| 95U303.05-MB 95U303.05 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 95U304.05-MB 95U304.05 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 95U305.05-MB 95U305.05 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 95U401.08-MB 95U401.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 95U402.07-MB 95U402.07 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 95U402.08-MB 95U402.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 95U403.08-MB 95U403.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 95U404.06-MB 95U404.06 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |
| Provisional            | -                              | -        | -             | -           | -              |
| Total                  | -                              | -        | -             | -           | -              |
| 95U404.08-MB 95U404.08 |                                |          |               |             |                |
| Precinct TSx           | -                              | -        | -             | -           | -              |
| Precinct OS            | -                              | -        | -             | -           | -              |
| Precinct VBM           | -                              | -        | -             | -           | -              |
| MB Precinct            | -                              | -        | -             | -           | -              |

**COUNTY OF EL DORADO**  
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|                               | County Supervisor 2nd District |          |               |             |                |
|-------------------------------|--------------------------------|----------|---------------|-------------|----------------|
|                               | Reg. Voters                    | Vote For | Times Counted | Total Votes | SHIVA FRENTZEN |
| Provisional                   | -                              | -        | -             | -           | -              |
| Total                         | -                              | -        | -             | -           | -              |
| 95U501.09-MB 95U501.09        |                                |          |               |             |                |
| Precinct TSx                  | -                              | -        | -             | -           | -              |
| Precinct OS                   | -                              | -        | -             | -           | -              |
| Precinct VBM                  | -                              | -        | -             | -           | -              |
| MB Precinct                   | -                              | -        | -             | -           | -              |
| Provisional                   | -                              | -        | -             | -           | -              |
| Total                         | -                              | -        | -             | -           | -              |
| 95U501.10-MB 95U501.10        |                                |          |               |             |                |
| Precinct TSx                  | -                              | -        | -             | -           | -              |
| Precinct OS                   | -                              | -        | -             | -           | -              |
| Precinct VBM                  | -                              | -        | -             | -           | -              |
| MB Precinct                   | -                              | -        | -             | -           | -              |
| Provisional                   | -                              | -        | -             | -           | -              |
| Total                         | -                              | -        | -             | -           | -              |
| 95U598.10-MB 95U598.10        |                                |          |               |             |                |
| Precinct TSx                  | -                              | -        | -             | -           | -              |
| Precinct OS                   | -                              | -        | -             | -           | -              |
| Precinct VBM                  | -                              | -        | -             | -           | -              |
| MB Precinct                   | -                              | -        | -             | -           | -              |
| Provisional                   | -                              | -        | -             | -           | -              |
| Total                         | -                              | -        | -             | -           | -              |
| 95U599.10-MB 95U599.10        |                                |          |               |             |                |
| Precinct TSx                  | -                              | -        | -             | -           | -              |
| Precinct OS                   | -                              | -        | -             | -           | -              |
| Precinct VBM                  | -                              | -        | -             | -           | -              |
| MB Precinct                   | -                              | -        | -             | -           | -              |
| Provisional                   | -                              | -        | -             | -           | -              |
| Total                         | -                              | -        | -             | -           | -              |
| 96U101.11-MB 96U101.11        |                                |          |               |             |                |
| Precinct TSx                  | -                              | -        | -             | -           | -              |
| Precinct OS                   | -                              | -        | -             | -           | -              |
| Precinct VBM                  | -                              | -        | -             | -           | -              |
| MB Precinct                   | -                              | -        | -             | -           | -              |
| Provisional                   | -                              | -        | -             | -           | -              |
| Total                         | -                              | -        | -             | -           | -              |
| 96U401.14-MB 96U401.14        |                                |          |               |             |                |
| Precinct TSx                  | -                              | -        | -             | -           | -              |
| Precinct OS                   | -                              | -        | -             | -           | -              |
| Precinct VBM                  | -                              | -        | -             | -           | -              |
| MB Precinct                   | -                              | -        | -             | -           | -              |
| Provisional                   | -                              | -        | -             | -           | -              |
| Total                         | -                              | -        | -             | -           | -              |
| Total                         |                                |          |               |             |                |
| Precinct TSx                  | 23114                          | 23       | 4             | 4           | 1 25.00%       |
| Precinct OS                   | 23114                          | 23       | 2509          | 2234        | 1330 59.53%    |
| Precinct VBM                  | 23114                          | 23       | 9647          | 8739        | 5304 60.69%    |
| MB Precinct                   | 23114                          | 23       | 1590          | 1442        | 833 57.77%     |
| Provisional                   | 23114                          | 23       | 327           | 254         | 150 59.06%     |
| Total                         | 23114                          | 115      | 14077         | 12673       | 7618 60.11%    |
| STATE BOARD OF EQUALIZATION   |                                |          |               |             |                |
| STATE BOARD OF EQUALIZATION 1 |                                |          |               |             |                |
| Precinct TSx                  | 23114                          | 23       | 4             | 4           | 1 25.00%       |
| Precinct OS                   | 23114                          | 23       | 2509          | 2234        | 1330 59.53%    |
| Precinct VBM                  | 23114                          | 23       | 9647          | 8739        | 5304 60.69%    |
| MB Precinct                   | 23114                          | 23       | 1590          | 1442        | 833 57.77%     |
| Provisional                   | 23114                          | 23       | 327           | 254         | 150 59.06%     |
| Total                         | 23114                          | 115      | 14077         | 12673       | 7618 60.11%    |
| Total                         |                                |          |               |             |                |

**COUNTY OF EL DORADO**  
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| County Supervisor 2nd District      |              |            |               |              |                |               |
|-------------------------------------|--------------|------------|---------------|--------------|----------------|---------------|
|                                     | Reg. Voters  | Vote For   | Times Counted | Total Votes  | SHIVA FRENTZEN |               |
| Precinct TSx                        | 23114        | 23         | 4             | 4            | 1              | 25.00%        |
| Precinct OS                         | 23114        | 23         | 2509          | 2234         | 1330           | 59.53%        |
| Precinct VBM                        | 23114        | 23         | 9647          | 8739         | 5304           | 60.69%        |
| MB Precinct                         | 23114        | 23         | 1590          | 1442         | 833            | 57.77%        |
| Provisional                         | 23114        | 23         | 327           | 254          | 150            | 59.06%        |
| <b>Total</b>                        | <b>23114</b> | <b>115</b> | <b>14077</b>  | <b>12673</b> | <b>7618</b>    | <b>60.11%</b> |
| <b>UNITED STATES SENATE</b>         |              |            |               |              |                |               |
| <b>UNITED STATES SENATE</b>         |              |            |               |              |                |               |
| Precinct TSx                        | 23114        | 23         | 4             | 4            | 1              | 25.00%        |
| Precinct OS                         | 23114        | 23         | 2509          | 2234         | 1330           | 59.53%        |
| Precinct VBM                        | 23114        | 23         | 9647          | 8739         | 5304           | 60.69%        |
| MB Precinct                         | 23114        | 23         | 1590          | 1442         | 833            | 57.77%        |
| Provisional                         | 23114        | 23         | 327           | 254          | 150            | 59.06%        |
| <b>Total</b>                        | <b>23114</b> | <b>115</b> | <b>14077</b>  | <b>12673</b> | <b>7618</b>    | <b>60.11%</b> |
| <b>Total</b>                        |              |            |               |              |                |               |
| Precinct TSx                        | 23114        | 23         | 4             | 4            | 1              | 25.00%        |
| Precinct OS                         | 23114        | 23         | 2509          | 2234         | 1330           | 59.53%        |
| Precinct VBM                        | 23114        | 23         | 9647          | 8739         | 5304           | 60.69%        |
| MB Precinct                         | 23114        | 23         | 1590          | 1442         | 833            | 57.77%        |
| Provisional                         | 23114        | 23         | 327           | 254          | 150            | 59.06%        |
| <b>Total</b>                        | <b>23114</b> | <b>115</b> | <b>14077</b>  | <b>12673</b> | <b>7618</b>    | <b>60.11%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |              |            |               |              |                |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |              |            |               |              |                |               |
| Precinct TSx                        | 23114        | 23         | 4             | 4            | 1              | 25.00%        |
| Precinct OS                         | 23114        | 23         | 2509          | 2234         | 1330           | 59.53%        |
| Precinct VBM                        | 23114        | 23         | 9647          | 8739         | 5304           | 60.69%        |
| MB Precinct                         | 23114        | 23         | 1590          | 1442         | 833            | 57.77%        |
| Provisional                         | 23114        | 23         | 327           | 254          | 150            | 59.06%        |
| <b>Total</b>                        | <b>23114</b> | <b>115</b> | <b>14077</b>  | <b>12673</b> | <b>7618</b>    | <b>60.11%</b> |
| <b>Total</b>                        |              |            |               |              |                |               |
| Precinct TSx                        | 23114        | 23         | 4             | 4            | 1              | 25.00%        |
| Precinct OS                         | 23114        | 23         | 2509          | 2234         | 1330           | 59.53%        |
| Precinct VBM                        | 23114        | 23         | 9647          | 8739         | 5304           | 60.69%        |
| MB Precinct                         | 23114        | 23         | 1590          | 1442         | 833            | 57.77%        |
| Provisional                         | 23114        | 23         | 327           | 254          | 150            | 59.06%        |
| <b>Total</b>                        | <b>23114</b> | <b>115</b> | <b>14077</b>  | <b>12673</b> | <b>7618</b>    | <b>60.11%</b> |
| <b>STATE SENATE</b>                 |              |            |               |              |                |               |
| <b>1ST STATE SENATE DISTRICT</b>    |              |            |               |              |                |               |
| Precinct TSx                        | 23114        | 23         | 4             | 4            | 1              | 25.00%        |
| Precinct OS                         | 23114        | 23         | 2509          | 2234         | 1330           | 59.53%        |
| Precinct VBM                        | 23114        | 23         | 9647          | 8739         | 5304           | 60.69%        |
| MB Precinct                         | 23114        | 23         | 1590          | 1442         | 833            | 57.77%        |
| Provisional                         | 23114        | 23         | 327           | 254          | 150            | 59.06%        |
| <b>Total</b>                        | <b>23114</b> | <b>115</b> | <b>14077</b>  | <b>12673</b> | <b>7618</b>    | <b>60.11%</b> |
| <b>Total</b>                        |              |            |               |              |                |               |
| Precinct TSx                        | 23114        | 23         | 4             | 4            | 1              | 25.00%        |
| Precinct OS                         | 23114        | 23         | 2509          | 2234         | 1330           | 59.53%        |
| Precinct VBM                        | 23114        | 23         | 9647          | 8739         | 5304           | 60.69%        |
| MB Precinct                         | 23114        | 23         | 1590          | 1442         | 833            | 57.77%        |
| Provisional                         | 23114        | 23         | 327           | 254          | 150            | 59.06%        |
| <b>Total</b>                        | <b>23114</b> | <b>115</b> | <b>14077</b>  | <b>12673</b> | <b>7618</b>    | <b>60.11%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |              |            |               |              |                |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |              |            |               |              |                |               |
| Precinct TSx                        | 11242        | 13         | 0             | 0            | 0              | -             |
| Precinct OS                         | 11242        | 13         | 1029          | 922          | 491            | 53.25%        |
| Precinct VBM                        | 11242        | 13         | 4402          | 4042         | 2238           | 55.37%        |
| MB Precinct                         | 11242        | 13         | 1590          | 1442         | 833            | 57.77%        |

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| County Supervisor 2nd District    |             |          |               |             |                |        |
|-----------------------------------|-------------|----------|---------------|-------------|----------------|--------|
|                                   | Reg. Voters | Vote For | Times Counted | Total Votes | SHIVA FRENTZEN |        |
| Provisional                       | 11242       | 13       | 130           | 95          | 52             | 54.74% |
| Total                             | 11242       | 65       | 7151          | 6501        | 3614           | 55.59% |
| <b>6TH STATE ASSEMBLY DIST</b>    |             |          |               |             |                |        |
| Precinct TSx                      | 11872       | 10       | 4             | 4           | 1              | 25.00% |
| Precinct OS                       | 11872       | 10       | 1480          | 1312        | 839            | 63.95% |
| Precinct VBM                      | 11872       | 10       | 5245          | 4697        | 3066           | 65.28% |
| MB Precinct                       | 11872       | 10       | 0             | 0           | 0              | -      |
| Provisional                       | 11872       | 10       | 197           | 159         | 98             | 61.64% |
| Total                             | 11872       | 50       | 6926          | 6172        | 4004           | 64.87% |
| <b>Total</b>                      |             |          |               |             |                |        |
| Precinct TSx                      | 23114       | 23       | 4             | 4           | 1              | 25.00% |
| Precinct OS                       | 23114       | 23       | 2509          | 2234        | 1330           | 59.53% |
| Precinct VBM                      | 23114       | 23       | 9647          | 8739        | 5304           | 60.69% |
| MB Precinct                       | 23114       | 23       | 1590          | 1442        | 833            | 57.77% |
| Provisional                       | 23114       | 23       | 327           | 254         | 150            | 59.06% |
| Total                             | 23114       | 115      | 14077         | 12673       | 7618           | 60.11% |
| <b>SUPERVISORIAL DISTRICTS</b>    |             |          |               |             |                |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0              | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0              | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0              | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0              | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0              | -      |
| Total                             | 0           | 0        | 0             | 0           | 0              | -      |
| <b>2ND SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |
| Precinct TSx                      | 23114       | 23       | 4             | 4           | 1              | 25.00% |
| Precinct OS                       | 23114       | 23       | 2509          | 2234        | 1330           | 59.53% |
| Precinct VBM                      | 23114       | 23       | 9647          | 8739        | 5304           | 60.69% |
| MB Precinct                       | 23114       | 23       | 1590          | 1442        | 833            | 57.77% |
| Provisional                       | 23114       | 23       | 327           | 254         | 150            | 59.06% |
| Total                             | 23114       | 115      | 14077         | 12673       | 7618           | 60.11% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0              | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0              | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0              | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0              | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0              | -      |
| Total                             | 0           | 0        | 0             | 0           | 0              | -      |
| <b>4TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0              | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0              | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0              | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0              | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0              | -      |
| Total                             | 0           | 0        | 0             | 0           | 0              | -      |
| <b>5TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |                |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0              | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0              | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0              | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0              | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0              | -      |
| Total                             | 0           | 0        | 0             | 0           | 0              | -      |
| <b>Total</b>                      |             |          |               |             |                |        |
| Precinct TSx                      | 23114       | 23       | 4             | 4           | 1              | 25.00% |
| Precinct OS                       | 23114       | 23       | 2509          | 2234        | 1330           | 59.53% |
| Precinct VBM                      | 23114       | 23       | 9647          | 8739        | 5304           | 60.69% |
| MB Precinct                       | 23114       | 23       | 1590          | 1442        | 833            | 57.77% |
| Provisional                       | 23114       | 23       | 327           | 254         | 150            | 59.06% |
| Total                             | 23114       | 115      | 14077         | 12673       | 7618           | 60.11% |



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|                                 | County Supervisor 2nd District |          |               |             |                |        |
|---------------------------------|--------------------------------|----------|---------------|-------------|----------------|--------|
|                                 | Reg. Voters                    | Vote For | Times Counted | Total Votes | SHIVA FRENTZEN |        |
| <b>CITIES</b>                   |                                |          |               |             |                |        |
| <b>CITY OF PLACERVILLE</b>      |                                |          |               |             |                |        |
| Precinct TSx                    | 0                              | 0        | 0             | 0           | 0              | -      |
| Precinct OS                     | 0                              | 0        | 0             | 0           | 0              | -      |
| Precinct VBM                    | 0                              | 0        | 0             | 0           | 0              | -      |
| MB Precinct                     | 0                              | 0        | 0             | 0           | 0              | -      |
| Provisional                     | 0                              | 0        | 0             | 0           | 0              | -      |
| Total                           | 0                              | 0        | 0             | 0           | 0              | -      |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                                |          |               |             |                |        |
| Precinct TSx                    | 0                              | 0        | 0             | 0           | 0              | -      |
| Precinct OS                     | 0                              | 0        | 0             | 0           | 0              | -      |
| Precinct VBM                    | 0                              | 0        | 0             | 0           | 0              | -      |
| MB Precinct                     | 0                              | 0        | 0             | 0           | 0              | -      |
| Provisional                     | 0                              | 0        | 0             | 0           | 0              | -      |
| Total                           | 0                              | 0        | 0             | 0           | 0              | -      |
| <b>UNINCORPORATED AREA</b>      |                                |          |               |             |                |        |
| Precinct TSx                    | 23114                          | 23       | 4             | 4           | 1              | 25.00% |
| Precinct OS                     | 23114                          | 23       | 2509          | 2234        | 1330           | 59.53% |
| Precinct VBM                    | 23114                          | 23       | 9647          | 8739        | 5304           | 60.69% |
| MB Precinct                     | 23114                          | 23       | 1590          | 1442        | 833            | 57.77% |
| Provisional                     | 23114                          | 23       | 327           | 254         | 150            | 59.06% |
| Total                           | 23114                          | 115      | 14077         | 12673       | 7618           | 60.11% |
| <b>Total</b>                    |                                |          |               |             |                |        |
| Precinct TSx                    | 23114                          | 23       | 4             | 4           | 1              | 25.00% |
| Precinct OS                     | 23114                          | 23       | 2509          | 2234        | 1330           | 59.53% |
| Precinct VBM                    | 23114                          | 23       | 9647          | 8739        | 5304           | 60.69% |
| MB Precinct                     | 23114                          | 23       | 1590          | 1442        | 833            | 57.77% |
| Provisional                     | 23114                          | 23       | 327           | 254         | 150            | 59.06% |
| Total                           | 23114                          | 115      | 14077         | 12673       | 7618           | 60.11% |

**COUNTY OF EL DORADO**  
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|                        | County Supervisor 2nd District |
|------------------------|--------------------------------|
|                        | DAVE PRATT                     |
| Jurisdiction Wide      |                                |
| 100001.11-CP 100001.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100002.11-CP 100002.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100003.11-CP 100003.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100004.11-CP 100004.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100005.11-CP 100005.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100006.11-CP 100006.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100008.11-CP 100008.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100009.11-CP 100009.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100012.11-CP 100012.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |

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|                        | County Supervisor 2nd District |        |
|------------------------|--------------------------------|--------|
|                        | DAVE PRATT                     |        |
| MB Precinct            | -                              | -      |
| Provisional            | -                              | -      |
| Total                  | -                              | -      |
| 100013.11-CP 100013.11 |                                |        |
| Precinct TSx           | -                              | -      |
| Precinct OS            | -                              | -      |
| Precinct VBM           | -                              | -      |
| MB Precinct            | -                              | -      |
| Provisional            | -                              | -      |
| Total                  | -                              | -      |
| 100014.11-CP 100014.11 |                                |        |
| Precinct TSx           | -                              | -      |
| Precinct OS            | -                              | -      |
| Precinct VBM           | -                              | -      |
| MB Precinct            | -                              | -      |
| Provisional            | -                              | -      |
| Total                  | -                              | -      |
| 100022.11-CP 100022.11 |                                |        |
| Precinct TSx           | -                              | -      |
| Precinct OS            | -                              | -      |
| Precinct VBM           | -                              | -      |
| MB Precinct            | -                              | -      |
| Provisional            | -                              | -      |
| Total                  | -                              | -      |
| 100023.11-CP 100023.11 |                                |        |
| Precinct TSx           | -                              | -      |
| Precinct OS            | -                              | -      |
| Precinct VBM           | -                              | -      |
| MB Precinct            | -                              | -      |
| Provisional            | -                              | -      |
| Total                  | -                              | -      |
| 200003.12-CP 200003.12 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 56                             | 40.00% |
| Precinct VBM           | 183                            | 36.67% |
| MB Precinct            | 0                              | -      |
| Provisional            | 13                             | 50.00% |
| Total                  | 252                            | 37.89% |
| 200004.12-CP 200004.12 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 65                             | 38.92% |
| Precinct VBM           | 176                            | 29.93% |
| MB Precinct            | 0                              | -      |
| Provisional            | 6                              | 50.00% |
| Total                  | 247                            | 32.20% |
| 200005.02-CP 200005.02 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 38                             | 44.71% |
| Precinct VBM           | 203                            | 48.68% |
| MB Precinct            | 0                              | -      |
| Provisional            | 8                              | 42.11% |
| Total                  | 249                            | 47.79% |
| 200006.12-CP 200006.12 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 49                             | 45.79% |
| Precinct VBM           | 183                            | 38.05% |
| MB Precinct            | 0                              | -      |
| Provisional            | 9                              | 37.50% |
| Total                  | 241                            | 39.38% |
| 200008.12-CP 200008.12 |                                |        |
| Precinct TSx           | 0                              | -      |

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|                        | DAVE PRATT                        |        |
| Precinct OS            | 54                                | 41.54% |
| Precinct VBM           | 113                               | 34.77% |
| MB Precinct            | 0                                 | -      |
| Provisional            | 4                                 | 50.00% |
| Total                  | 171                               | 36.93% |
| 200010.12-CP 200010.12 |                                   |        |
| Precinct TSx           | 3                                 | 75.00% |
| Precinct OS            | 69                                | 42.86% |
| Precinct VBM           | 304                               | 41.30% |
| MB Precinct            | 0                                 | -      |
| Provisional            | 11                                | 47.83% |
| Total                  | 387                               | 41.88% |
| 200012.12-CP 200012.12 |                                   |        |
| Precinct TSx           | 0                                 | -      |
| Precinct OS            | 32                                | 23.70% |
| Precinct VBM           | 113                               | 26.10% |
| MB Precinct            | 0                                 | -      |
| Provisional            | 5                                 | 38.46% |
| Total                  | 150                               | 25.82% |
| 200013.12-CP 200013.12 |                                   |        |
| Precinct TSx           | 0                                 | -      |
| Precinct OS            | 63                                | 32.98% |
| Precinct VBM           | 249                               | 38.43% |
| MB Precinct            | 0                                 | -      |
| Provisional            | 6                                 | 28.57% |
| Total                  | 318                               | 36.98% |
| 200014.12-CP 200014.12 |                                   |        |
| Precinct TSx           | 0                                 | -      |
| Precinct OS            | 55                                | 29.57% |
| Precinct VBM           | 189                               | 34.55% |
| MB Precinct            | 0                                 | -      |
| Provisional            | 3                                 | 12.00% |
| Total                  | 247                               | 32.59% |
| 200015.02-CP 200015.02 |                                   |        |
| Precinct TSx           | 0                                 | -      |
| Precinct OS            | 95                                | 48.47% |
| Precinct VBM           | 363                               | 44.59% |
| MB Precinct            | 0                                 | -      |
| Provisional            | 10                                | 52.63% |
| Total                  | 468                               | 45.48% |
| 200016.12-CP 200016.12 |                                   |        |
| Precinct TSx           | 0                                 | -      |
| Precinct OS            | 9                                 | 39.13% |
| Precinct VBM           | 36                                | 29.03% |
| MB Precinct            | 0                                 | -      |
| Provisional            | 0                                 | 0.00%  |
| Total                  | 45                                | 30.20% |
| 200016.13-CP 200016.13 |                                   |        |
| Precinct TSx           | 0                                 | -      |
| Precinct OS            | 14                                | 19.44% |
| Precinct VBM           | 63                                | 19.94% |
| MB Precinct            | 0                                 | -      |
| Provisional            | 2                                 | 40.00% |
| Total                  | 79                                | 20.10% |
| 200017.02-CP 200017.02 |                                   |        |
| Precinct TSx           | 0                                 | -      |
| Precinct OS            | 64                                | 49.23% |
| Precinct VBM           | 240                               | 45.28% |
| MB Precinct            | 0                                 | -      |
| Provisional            | 8                                 | 88.89% |
| Total                  | 312                               | 46.64% |

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|                        | County Supervisor 2nd District |        |
|------------------------|--------------------------------|--------|
|                        | DAVE PRATT                     |        |
| 200018.02-CP 200018.02 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 78                             | 51.32% |
| Precinct VBM           | 318                            | 49.61% |
| MB Precinct            | 0                              | -      |
| Provisional            | 3                              | 50.00% |
| Total                  | 399                            | 49.94% |
| 200020.02-CP 200020.02 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 47                             | 38.21% |
| Precinct VBM           | 189                            | 38.10% |
| MB Precinct            | 0                              | -      |
| Provisional            | 3                              | 21.43% |
| Total                  | 239                            | 37.76% |
| 200021.01-CP 200021.01 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 10                             | 33.33% |
| Precinct VBM           | 70                             | 37.04% |
| MB Precinct            | 0                              | -      |
| Provisional            | 4                              | 80.00% |
| Total                  | 84                             | 37.50% |
| 200021.02-CP 200021.02 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 52                             | 33.55% |
| Precinct VBM           | 195                            | 28.30% |
| MB Precinct            | 0                              | -      |
| Provisional            | 3                              | 18.75% |
| Total                  | 250                            | 29.07% |
| 200024.02-CP 200024.02 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 22                             | 43.14% |
| Precinct VBM           | 129                            | 48.50% |
| MB Precinct            | 0                              | -      |
| Provisional            | 2                              | 33.33% |
| Total                  | 153                            | 47.37% |
| 300001.04-CP 300001.04 |                                |        |
| Precinct TSx           | -                              | -      |
| Precinct OS            | -                              | -      |
| Precinct VBM           | -                              | -      |
| MB Precinct            | -                              | -      |
| Provisional            | -                              | -      |
| Total                  | -                              | -      |
| 300001.05-CP 300001.05 |                                |        |
| Precinct TSx           | -                              | -      |
| Precinct OS            | -                              | -      |
| Precinct VBM           | -                              | -      |
| MB Precinct            | -                              | -      |
| Provisional            | -                              | -      |
| Total                  | -                              | -      |
| 300003.05-CP 300003.05 |                                |        |
| Precinct TSx           | -                              | -      |
| Precinct OS            | -                              | -      |
| Precinct VBM           | -                              | -      |
| MB Precinct            | -                              | -      |
| Provisional            | -                              | -      |
| Total                  | -                              | -      |
| 300004.05-CP 300004.05 |                                |        |
| Precinct TSx           | -                              | -      |
| Precinct OS            | -                              | -      |
| Precinct VBM           | -                              | -      |
| MB Precinct            | -                              | -      |

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|                        | DAVE PRATT                     |
| Provisional            | -                              |
| Total                  | -                              |
| 300005.05-CP 300005.05 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 300008.05-CP 300008.05 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 300009.04-CP 300009.04 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 300010.04-CP 300010.04 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 300011.04-CP 300011.04 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 300013.05-CP 300013.05 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 300015.04-CP 300015.04 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 300015.05-CP 300015.05 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 300017.05-CP 300017.05 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |

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|                        | DAVE PRATT                     |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 300018.04-CP 300018.04 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 300020.04-CP 300020.04 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 300021.03-CP 300021.03 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 300022.05-CP 300022.05 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 300023.03-CP 300023.03 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 300023.05-CP 300023.05 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400001.14-CP 400001.14 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400002.08-CP 400002.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400003.08-CP 400003.08 |                                |

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|                        | DAVE PRATT                     |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400004.14-CP 400004.14 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400005.08-CP 400005.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400006.08-CP 400006.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400007.08-CP 400007.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400010.08-CP 400010.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400012.08-CP 400012.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400013.08-CP 400013.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400014.08-CP 400014.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |



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|                        | DAVE PRATT                        |
| Total                  | -                                 |
| 400017.08-CP 400017.08 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 400018.08-CP 400018.08 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 400020.08-CP 400020.08 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 400021.07-CP 400021.07 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 500001.10-CP 500001.10 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 500002.10-CP 500002.10 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 500003.10-CP 500003.10 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 500004.10-CP 500004.10 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 500005.10-CP 500005.10 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |

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|                        | DAVE PRATT                     |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 500006.10-CP 500006.10 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 500007.10-CP 500007.10 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 500009.10-CP 500009.10 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 500011.10-CP 500011.10 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 500012.10-CP 500012.10 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 500015.10-CP 500015.10 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 500020.10-CP 500020.10 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 500021.10-CP 500021.10 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 95U201.01-MB 95U201.01 |                                |
| Precinct TSx           | 0                              |

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|                        | County Supervisor 2nd District |        |
|------------------------|--------------------------------|--------|
|                        | DAVE PRATT                     |        |
| Precinct OS            | 0                              | -      |
| Precinct VBM           | 0                              | -      |
| MB Precinct            | 2                              | 33.33% |
| Provisional            | 0                              | -      |
| Total                  | 2                              | 33.33% |
| 95U201.02-MB 95U201.02 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 0                              | -      |
| Precinct VBM           | 0                              | -      |
| MB Precinct            | 88                             | 27.33% |
| Provisional            | 0                              | -      |
| Total                  | 88                             | 27.33% |
| 95U202.02-MB 95U202.02 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 0                              | -      |
| Precinct VBM           | 0                              | -      |
| MB Precinct            | 241                            | 46.44% |
| Provisional            | 0                              | 0.00%  |
| Total                  | 241                            | 46.35% |
| 95U203.02-MB 95U203.02 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 0                              | -      |
| Precinct VBM           | 0                              | -      |
| MB Precinct            | 0                              | -      |
| Provisional            | 0                              | -      |
| Total                  | 0                              | -      |
| 95U204.01-MB 95U204.01 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 0                              | -      |
| Precinct VBM           | 0                              | -      |
| MB Precinct            | 201                            | 33.78% |
| Provisional            | 0                              | -      |
| Total                  | 201                            | 33.78% |
| 95U301.05-MB 95U301.05 |                                |        |
| Precinct TSx           | -                              | -      |
| Precinct OS            | -                              | -      |
| Precinct VBM           | -                              | -      |
| MB Precinct            | -                              | -      |
| Provisional            | -                              | -      |
| Total                  | -                              | -      |
| 95U302.04-MB 95U302.04 |                                |        |
| Precinct TSx           | -                              | -      |
| Precinct OS            | -                              | -      |
| Precinct VBM           | -                              | -      |
| MB Precinct            | -                              | -      |
| Provisional            | -                              | -      |
| Total                  | -                              | -      |
| 95U302.05-MB 95U302.05 |                                |        |
| Precinct TSx           | -                              | -      |
| Precinct OS            | -                              | -      |
| Precinct VBM           | -                              | -      |
| MB Precinct            | -                              | -      |
| Provisional            | -                              | -      |
| Total                  | -                              | -      |
| 95U303.03-MB 95U303.03 |                                |        |
| Precinct TSx           | -                              | -      |
| Precinct OS            | -                              | -      |
| Precinct VBM           | -                              | -      |
| MB Precinct            | -                              | -      |
| Provisional            | -                              | -      |
| Total                  | -                              | -      |

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|                        | County Supervisor 2nd<br>District |
|------------------------|-----------------------------------|
|                        | DAVE PRATT                        |
| 95U303.05-MB 95U303.05 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 95U304.05-MB 95U304.05 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 95U305.05-MB 95U305.05 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 95U401.08-MB 95U401.08 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 95U402.07-MB 95U402.07 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 95U402.08-MB 95U402.08 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 95U403.08-MB 95U403.08 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 95U404.06-MB 95U404.06 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 95U404.08-MB 95U404.08 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |

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|                               | County Supervisor 2nd District |        |
|-------------------------------|--------------------------------|--------|
|                               | DAVE PRATT                     |        |
| Provisional                   | -                              | -      |
| Total                         | -                              | -      |
| 95U501.09-MB 95U501.09        |                                |        |
| Precinct TSx                  | -                              | -      |
| Precinct OS                   | -                              | -      |
| Precinct VBM                  | -                              | -      |
| MB Precinct                   | -                              | -      |
| Provisional                   | -                              | -      |
| Total                         | -                              | -      |
| 95U501.10-MB 95U501.10        |                                |        |
| Precinct TSx                  | -                              | -      |
| Precinct OS                   | -                              | -      |
| Precinct VBM                  | -                              | -      |
| MB Precinct                   | -                              | -      |
| Provisional                   | -                              | -      |
| Total                         | -                              | -      |
| 95U598.10-MB 95U598.10        |                                |        |
| Precinct TSx                  | -                              | -      |
| Precinct OS                   | -                              | -      |
| Precinct VBM                  | -                              | -      |
| MB Precinct                   | -                              | -      |
| Provisional                   | -                              | -      |
| Total                         | -                              | -      |
| 95U599.10-MB 95U599.10        |                                |        |
| Precinct TSx                  | -                              | -      |
| Precinct OS                   | -                              | -      |
| Precinct VBM                  | -                              | -      |
| MB Precinct                   | -                              | -      |
| Provisional                   | -                              | -      |
| Total                         | -                              | -      |
| 96U101.11-MB 96U101.11        |                                |        |
| Precinct TSx                  | -                              | -      |
| Precinct OS                   | -                              | -      |
| Precinct VBM                  | -                              | -      |
| MB Precinct                   | -                              | -      |
| Provisional                   | -                              | -      |
| Total                         | -                              | -      |
| 96U401.14-MB 96U401.14        |                                |        |
| Precinct TSx                  | -                              | -      |
| Precinct OS                   | -                              | -      |
| Precinct VBM                  | -                              | -      |
| MB Precinct                   | -                              | -      |
| Provisional                   | -                              | -      |
| Total                         | -                              | -      |
| Total                         | 3                              | 75.00% |
| Precinct OS                   | 872                            | 39.03% |
| Precinct VBM                  | 3316                           | 37.94% |
| MB Precinct                   | 532                            | 36.89% |
| Provisional                   | 100                            | 39.37% |
| Total                         | 4823                           | 38.06% |
| STATE BOARD OF EQUALIZATION   |                                |        |
| STATE BOARD OF EQUALIZATION 1 |                                |        |
| Precinct TSx                  | 3                              | 75.00% |
| Precinct OS                   | 872                            | 39.03% |
| Precinct VBM                  | 3316                           | 37.94% |
| MB Precinct                   | 532                            | 36.89% |
| Provisional                   | 100                            | 39.37% |
| Total                         | 4823                           | 38.06% |
| Total                         |                                |        |

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|                                     | County Supervisor 2nd District |               |
|-------------------------------------|--------------------------------|---------------|
|                                     | DAVE PRATT                     |               |
| Precinct TSx                        | 3                              | 75.00%        |
| Precinct OS                         | 872                            | 39.03%        |
| Precinct VBM                        | 3316                           | 37.94%        |
| MB Precinct                         | 532                            | 36.89%        |
| Provisional                         | 100                            | 39.37%        |
| <b>Total</b>                        | <b>4823</b>                    | <b>38.06%</b> |
| <b>UNITED STATES SENATE</b>         |                                |               |
| <b>UNITED STATES SENATE</b>         |                                |               |
| Precinct TSx                        | 3                              | 75.00%        |
| Precinct OS                         | 872                            | 39.03%        |
| Precinct VBM                        | 3316                           | 37.94%        |
| MB Precinct                         | 532                            | 36.89%        |
| Provisional                         | 100                            | 39.37%        |
| <b>Total</b>                        | <b>4823</b>                    | <b>38.06%</b> |
| <b>Total</b>                        |                                |               |
| Precinct TSx                        | 3                              | 75.00%        |
| Precinct OS                         | 872                            | 39.03%        |
| Precinct VBM                        | 3316                           | 37.94%        |
| MB Precinct                         | 532                            | 36.89%        |
| Provisional                         | 100                            | 39.37%        |
| <b>Total</b>                        | <b>4823</b>                    | <b>38.06%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |                                |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                                |               |
| Precinct TSx                        | 3                              | 75.00%        |
| Precinct OS                         | 872                            | 39.03%        |
| Precinct VBM                        | 3316                           | 37.94%        |
| MB Precinct                         | 532                            | 36.89%        |
| Provisional                         | 100                            | 39.37%        |
| <b>Total</b>                        | <b>4823</b>                    | <b>38.06%</b> |
| <b>Total</b>                        |                                |               |
| Precinct TSx                        | 3                              | 75.00%        |
| Precinct OS                         | 872                            | 39.03%        |
| Precinct VBM                        | 3316                           | 37.94%        |
| MB Precinct                         | 532                            | 36.89%        |
| Provisional                         | 100                            | 39.37%        |
| <b>Total</b>                        | <b>4823</b>                    | <b>38.06%</b> |
| <b>STATE SENATE</b>                 |                                |               |
| <b>1ST STATE SENATE DISTRICT</b>    |                                |               |
| Precinct TSx                        | 3                              | 75.00%        |
| Precinct OS                         | 872                            | 39.03%        |
| Precinct VBM                        | 3316                           | 37.94%        |
| MB Precinct                         | 532                            | 36.89%        |
| Provisional                         | 100                            | 39.37%        |
| <b>Total</b>                        | <b>4823</b>                    | <b>38.06%</b> |
| <b>Total</b>                        |                                |               |
| Precinct TSx                        | 3                              | 75.00%        |
| Precinct OS                         | 872                            | 39.03%        |
| Precinct VBM                        | 3316                           | 37.94%        |
| MB Precinct                         | 532                            | 36.89%        |
| Provisional                         | 100                            | 39.37%        |
| <b>Total</b>                        | <b>4823</b>                    | <b>38.06%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                                |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |                                |               |
| Precinct TSx                        | 0                              | -             |
| Precinct OS                         | 406                            | 44.03%        |
| Precinct VBM                        | 1707                           | 42.23%        |
| MB Precinct                         | 532                            | 36.89%        |

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|                                   | County Supervisor 2nd District |        |
|-----------------------------------|--------------------------------|--------|
|                                   | DAVE PRATT                     |        |
| Provisional                       | 41                             | 43.16% |
| Total                             | 2686                           | 41.32% |
| <b>6TH STATE ASSEMBLY DIST</b>    |                                |        |
| Precinct TSx                      | 3                              | 75.00% |
| Precinct OS                       | 466                            | 35.52% |
| Precinct VBM                      | 1609                           | 34.26% |
| MB Precinct                       | 0                              | -      |
| Provisional                       | 59                             | 37.11% |
| Total                             | 2137                           | 34.62% |
| Total                             |                                |        |
| Precinct TSx                      | 3                              | 75.00% |
| Precinct OS                       | 872                            | 39.03% |
| Precinct VBM                      | 3316                           | 37.94% |
| MB Precinct                       | 532                            | 36.89% |
| Provisional                       | 100                            | 39.37% |
| Total                             | 4823                           | 38.06% |
| <b>SUPERVISORIAL DISTRICTS</b>    |                                |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |                                |        |
| Precinct TSx                      | 0                              | -      |
| Precinct OS                       | 0                              | -      |
| Precinct VBM                      | 0                              | -      |
| MB Precinct                       | 0                              | -      |
| Provisional                       | 0                              | -      |
| Total                             | 0                              | -      |
| <b>2ND SUPERVISORIAL DISTRICT</b> |                                |        |
| Precinct TSx                      | 3                              | 75.00% |
| Precinct OS                       | 872                            | 39.03% |
| Precinct VBM                      | 3316                           | 37.94% |
| MB Precinct                       | 532                            | 36.89% |
| Provisional                       | 100                            | 39.37% |
| Total                             | 4823                           | 38.06% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |                                |        |
| Precinct TSx                      | 0                              | -      |
| Precinct OS                       | 0                              | -      |
| Precinct VBM                      | 0                              | -      |
| MB Precinct                       | 0                              | -      |
| Provisional                       | 0                              | -      |
| Total                             | 0                              | -      |
| <b>4TH SUPERVISORIAL DISTRICT</b> |                                |        |
| Precinct TSx                      | 0                              | -      |
| Precinct OS                       | 0                              | -      |
| Precinct VBM                      | 0                              | -      |
| MB Precinct                       | 0                              | -      |
| Provisional                       | 0                              | -      |
| Total                             | 0                              | -      |
| <b>5TH SUPERVISORIAL DISTRICT</b> |                                |        |
| Precinct TSx                      | 0                              | -      |
| Precinct OS                       | 0                              | -      |
| Precinct VBM                      | 0                              | -      |
| MB Precinct                       | 0                              | -      |
| Provisional                       | 0                              | -      |
| Total                             | 0                              | -      |
| Total                             |                                |        |
| Precinct TSx                      | 3                              | 75.00% |
| Precinct OS                       | 872                            | 39.03% |
| Precinct VBM                      | 3316                           | 37.94% |
| MB Precinct                       | 532                            | 36.89% |
| Provisional                       | 100                            | 39.37% |
| Total                             | 4823                           | 38.06% |

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|                                 | County Supervisor 2nd<br>District |        |
|---------------------------------|-----------------------------------|--------|
|                                 | DAVE PRATT                        |        |
| <b>CITIES</b>                   |                                   |        |
| <b>CITY OF PLACERVILLE</b>      |                                   |        |
| Precinct TSx                    | 0                                 | -      |
| Precinct OS                     | 0                                 | -      |
| Precinct VBM                    | 0                                 | -      |
| MB Precinct                     | 0                                 | -      |
| Provisional                     | 0                                 | -      |
| Total                           | 0                                 | -      |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                                   |        |
| Precinct TSx                    | 0                                 | -      |
| Precinct OS                     | 0                                 | -      |
| Precinct VBM                    | 0                                 | -      |
| MB Precinct                     | 0                                 | -      |
| Provisional                     | 0                                 | -      |
| Total                           | 0                                 | -      |
| <b>UNINCORPORATED AREA</b>      |                                   |        |
| Precinct TSx                    | 3                                 | 75.00% |
| Precinct OS                     | 872                               | 39.03% |
| Precinct VBM                    | 3316                              | 37.94% |
| MB Precinct                     | 532                               | 36.89% |
| Provisional                     | 100                               | 39.37% |
| Total                           | 4823                              | 38.06% |
| <b>Total</b>                    |                                   |        |
| Precinct TSx                    | 3                                 | 75.00% |
| Precinct OS                     | 872                               | 39.03% |
| Precinct VBM                    | 3316                              | 37.94% |
| MB Precinct                     | 532                               | 36.89% |
| Provisional                     | 100                               | 39.37% |
| Total                           | 4823                              | 38.06% |



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|                        | County Supervisor 3rd District |          |               |             |                      |
|------------------------|--------------------------------|----------|---------------|-------------|----------------------|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | BRIAN K.<br>VEERKAMP |
| Jurisdiction Wide      |                                |          |               |             |                      |
| 100001.11-CP 100001.11 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 100002.11-CP 100002.11 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 100003.11-CP 100003.11 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 100004.11-CP 100004.11 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 100005.11-CP 100005.11 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 100006.11-CP 100006.11 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 100008.11-CP 100008.11 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 100009.11-CP 100009.11 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 100012.11-CP 100012.11 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |

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|------------------------|--------------------------------|----------|---------------|-------------|----------------------|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | BRIAN K.<br>VEERKAMP |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 100013.11-CP 100013.11 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 100014.11-CP 100014.11 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 100022.11-CP 100022.11 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 100023.11-CP 100023.11 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 200003.12-CP 200003.12 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 200004.12-CP 200004.12 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 200005.02-CP 200005.02 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 200006.12-CP 200006.12 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 200008.12-CP 200008.12 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |

**COUNTY OF EL DORADO**  
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|------------------------|--------------------------------|----------|---------------|-------------|----------------------|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | BRIAN K.<br>VEERKAMP |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 200010.12-CP 200010.12 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 200012.12-CP 200012.12 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 200013.12-CP 200013.12 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 200014.12-CP 200014.12 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 200015.02-CP 200015.02 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 200016.12-CP 200016.12 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 200016.13-CP 200016.13 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 200017.02-CP 200017.02 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |

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|------------------------|--------------------------------|----------|---------------|-------------|----------------------|--------|---|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | BRIAN K.<br>VEERKAMP |        |   |
| 200018.02-CP 200018.02 |                                |          |               |             |                      |        |   |
| Precinct TSx           | -                              | -        | -             | -           | -                    | -      | - |
| Precinct OS            | -                              | -        | -             | -           | -                    | -      | - |
| Precinct VBM           | -                              | -        | -             | -           | -                    | -      | - |
| MB Precinct            | -                              | -        | -             | -           | -                    | -      | - |
| Provisional            | -                              | -        | -             | -           | -                    | -      | - |
| Total                  | -                              | -        | -             | -           | -                    | -      | - |
| 200020.02-CP 200020.02 |                                |          |               |             |                      |        |   |
| Precinct TSx           | -                              | -        | -             | -           | -                    | -      | - |
| Precinct OS            | -                              | -        | -             | -           | -                    | -      | - |
| Precinct VBM           | -                              | -        | -             | -           | -                    | -      | - |
| MB Precinct            | -                              | -        | -             | -           | -                    | -      | - |
| Provisional            | -                              | -        | -             | -           | -                    | -      | - |
| Total                  | -                              | -        | -             | -           | -                    | -      | - |
| 200021.01-CP 200021.01 |                                |          |               |             |                      |        |   |
| Precinct TSx           | -                              | -        | -             | -           | -                    | -      | - |
| Precinct OS            | -                              | -        | -             | -           | -                    | -      | - |
| Precinct VBM           | -                              | -        | -             | -           | -                    | -      | - |
| MB Precinct            | -                              | -        | -             | -           | -                    | -      | - |
| Provisional            | -                              | -        | -             | -           | -                    | -      | - |
| Total                  | -                              | -        | -             | -           | -                    | -      | - |
| 200021.02-CP 200021.02 |                                |          |               |             |                      |        |   |
| Precinct TSx           | -                              | -        | -             | -           | -                    | -      | - |
| Precinct OS            | -                              | -        | -             | -           | -                    | -      | - |
| Precinct VBM           | -                              | -        | -             | -           | -                    | -      | - |
| MB Precinct            | -                              | -        | -             | -           | -                    | -      | - |
| Provisional            | -                              | -        | -             | -           | -                    | -      | - |
| Total                  | -                              | -        | -             | -           | -                    | -      | - |
| 200024.02-CP 200024.02 |                                |          |               |             |                      |        |   |
| Precinct TSx           | -                              | -        | -             | -           | -                    | -      | - |
| Precinct OS            | -                              | -        | -             | -           | -                    | -      | - |
| Precinct VBM           | -                              | -        | -             | -           | -                    | -      | - |
| MB Precinct            | -                              | -        | -             | -           | -                    | -      | - |
| Provisional            | -                              | -        | -             | -           | -                    | -      | - |
| Total                  | -                              | -        | -             | -           | -                    | -      | - |
| 300001.04-CP 300001.04 |                                |          |               |             |                      |        |   |
| Precinct TSx           | 259                            | 1        | 0             | 0           | 0                    | -      | - |
| Precinct OS            | 259                            | 1        | 12            | 10          | 9                    | 90.00% | - |
| Precinct VBM           | 259                            | 1        | 100           | 93          | 69                   | 74.19% | - |
| MB Precinct            | 259                            | 1        | 0             | 0           | 0                    | -      | - |
| Provisional            | 259                            | 1        | 0             | 0           | 0                    | -      | - |
| Total                  | 259                            | 5        | 112           | 103         | 78                   | 75.73% | - |
| 300001.05-CP 300001.05 |                                |          |               |             |                      |        |   |
| Precinct TSx           | 731                            | 1        | 0             | 0           | 0                    | -      | - |
| Precinct OS            | 731                            | 1        | 76            | 67          | 51                   | 76.12% | - |
| Precinct VBM           | 731                            | 1        | 330           | 293         | 225                  | 76.79% | - |
| MB Precinct            | 731                            | 1        | 0             | 0           | 0                    | -      | - |
| Provisional            | 731                            | 1        | 10            | 8           | 4                    | 50.00% | - |
| Total                  | 731                            | 5        | 416           | 368         | 280                  | 76.09% | - |
| 300003.05-CP 300003.05 |                                |          |               |             |                      |        |   |
| Precinct TSx           | 1932                           | 1        | 0             | 0           | 0                    | -      | - |
| Precinct OS            | 1932                           | 1        | 205           | 179         | 126                  | 70.39% | - |
| Precinct VBM           | 1932                           | 1        | 924           | 824         | 598                  | 72.57% | - |
| MB Precinct            | 1932                           | 1        | 0             | 0           | 0                    | -      | - |
| Provisional            | 1932                           | 1        | 27            | 22          | 14                   | 63.64% | - |
| Total                  | 1932                           | 5        | 1156          | 1025        | 738                  | 72.00% | - |
| 300004.05-CP 300004.05 |                                |          |               |             |                      |        |   |
| Precinct TSx           | 1273                           | 1        | 0             | 0           | 0                    | -      | - |
| Precinct OS            | 1273                           | 1        | 175           | 150         | 96                   | 64.00% | - |
| Precinct VBM           | 1273                           | 1        | 576           | 513         | 354                  | 69.01% | - |
| MB Precinct            | 1273                           | 1        | 0             | 0           | 0                    | -      | - |

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|------------------------|--------------------------------|----------|---------------|-------------|----------------------|--------|--|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | BRIAN K.<br>VEERKAMP |        |  |
| Provisional            | 1273                           | 1        | 17            | 12          | 6                    | 50.00% |  |
| Total                  | 1273                           | 5        | 768           | 675         | 456                  | 67.56% |  |
| 300005.05-CP 300005.05 |                                |          |               |             |                      |        |  |
| Precinct TSx           | 1374                           | 1        | 0             | 0           | 0                    | -      |  |
| Precinct OS            | 1374                           | 1        | 163           | 138         | 99                   | 71.74% |  |
| Precinct VBM           | 1374                           | 1        | 660           | 605         | 456                  | 75.37% |  |
| MB Precinct            | 1374                           | 1        | 0             | 0           | 0                    | -      |  |
| Provisional            | 1374                           | 1        | 26            | 20          | 10                   | 50.00% |  |
| Total                  | 1374                           | 5        | 849           | 763         | 565                  | 74.05% |  |
| 300008.05-CP 300008.05 |                                |          |               |             |                      |        |  |
| Precinct TSx           | 1366                           | 1        | 0             | 0           | 0                    | -      |  |
| Precinct OS            | 1366                           | 1        | 139           | 130         | 87                   | 66.92% |  |
| Precinct VBM           | 1366                           | 1        | 740           | 674         | 496                  | 73.59% |  |
| MB Precinct            | 1366                           | 1        | 0             | 0           | 0                    | -      |  |
| Provisional            | 1366                           | 1        | 14            | 6           | 4                    | 66.67% |  |
| Total                  | 1366                           | 5        | 893           | 810         | 587                  | 72.47% |  |
| 300009.04-CP 300009.04 |                                |          |               |             |                      |        |  |
| Precinct TSx           | 873                            | 1        | 0             | 0           | 0                    | -      |  |
| Precinct OS            | 873                            | 1        | 121           | 110         | 73                   | 66.36% |  |
| Precinct VBM           | 873                            | 1        | 395           | 355         | 246                  | 69.30% |  |
| MB Precinct            | 873                            | 1        | 0             | 0           | 0                    | -      |  |
| Provisional            | 873                            | 1        | 13            | 10          | 7                    | 70.00% |  |
| Total                  | 873                            | 5        | 529           | 475         | 326                  | 68.63% |  |
| 300010.04-CP 300010.04 |                                |          |               |             |                      |        |  |
| Precinct TSx           | 1340                           | 1        | 0             | 0           | 0                    | -      |  |
| Precinct OS            | 1340                           | 1        | 171           | 153         | 103                  | 67.32% |  |
| Precinct VBM           | 1340                           | 1        | 656           | 597         | 434                  | 72.70% |  |
| MB Precinct            | 1340                           | 1        | 0             | 0           | 0                    | -      |  |
| Provisional            | 1340                           | 1        | 11            | 11          | 7                    | 63.64% |  |
| Total                  | 1340                           | 5        | 838           | 761         | 544                  | 71.48% |  |
| 300011.04-CP 300011.04 |                                |          |               |             |                      |        |  |
| Precinct TSx           | 1491                           | 1        | 0             | 0           | 0                    | -      |  |
| Precinct OS            | 1491                           | 1        | 142           | 127         | 81                   | 63.78% |  |
| Precinct VBM           | 1491                           | 1        | 711           | 643         | 423                  | 65.79% |  |
| MB Precinct            | 1491                           | 1        | 0             | 0           | 0                    | -      |  |
| Provisional            | 1491                           | 1        | 22            | 15          | 11                   | 73.33% |  |
| Total                  | 1491                           | 5        | 875           | 785         | 515                  | 65.61% |  |
| 300013.05-CP 300013.05 |                                |          |               |             |                      |        |  |
| Precinct TSx           | 1490                           | 1        | 3             | 3           | 1                    | 33.33% |  |
| Precinct OS            | 1490                           | 1        | 128           | 112         | 71                   | 63.39% |  |
| Precinct VBM           | 1490                           | 1        | 721           | 673         | 465                  | 69.09% |  |
| MB Precinct            | 1490                           | 1        | 0             | 0           | 0                    | -      |  |
| Provisional            | 1490                           | 1        | 15            | 13          | 8                    | 61.54% |  |
| Total                  | 1490                           | 5        | 867           | 801         | 545                  | 68.04% |  |
| 300015.04-CP 300015.04 |                                |          |               |             |                      |        |  |
| Precinct TSx           | 119                            | 1        | 0             | 0           | 0                    | -      |  |
| Precinct OS            | 119                            | 1        | 10            | 8           | 7                    | 87.50% |  |
| Precinct VBM           | 119                            | 1        | 63            | 55          | 48                   | 87.27% |  |
| MB Precinct            | 119                            | 1        | 0             | 0           | 0                    | -      |  |
| Provisional            | 119                            | 1        | 4             | 4           | 3                    | 75.00% |  |
| Total                  | 119                            | 5        | 77            | 67          | 58                   | 86.57% |  |
| 300015.05-CP 300015.05 |                                |          |               |             |                      |        |  |
| Precinct TSx           | 503                            | 1        | 0             | 0           | 0                    | -      |  |
| Precinct OS            | 503                            | 1        | 66            | 60          | 37                   | 61.67% |  |
| Precinct VBM           | 503                            | 1        | 266           | 242         | 172                  | 71.07% |  |
| MB Precinct            | 503                            | 1        | 0             | 0           | 0                    | -      |  |
| Provisional            | 503                            | 1        | 2             | 2           | 1                    | 50.00% |  |
| Total                  | 503                            | 5        | 334           | 304         | 210                  | 69.08% |  |
| 300017.05-CP 300017.05 |                                |          |               |             |                      |        |  |
| Precinct TSx           | 1240                           | 1        | 0             | 0           | 0                    | -      |  |
| Precinct OS            | 1240                           | 1        | 117           | 107         | 75                   | 70.09% |  |

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|                                | Reg. Voters | Vote For | Times Counted | Total Votes | BRIAN K. VEERKAMP |        |
| Precinct VBM                   | 1240        | 1        | 655           | 600         | 452               | 75.33% |
| MB Precinct                    | 1240        | 1        | 0             | 0           | 0                 | -      |
| Provisional                    | 1240        | 1        | 9             | 7           | 6                 | 85.71% |
| Total                          | 1240        | 5        | 781           | 714         | 533               | 74.65% |
| 300018.04-CP 300018.04         |             |          |               |             |                   |        |
| Precinct TSx                   | 1306        | 1        | 0             | 0           | 0                 | -      |
| Precinct OS                    | 1306        | 1        | 148           | 132         | 95                | 71.97% |
| Precinct VBM                   | 1306        | 1        | 505           | 465         | 327               | 70.32% |
| MB Precinct                    | 1306        | 1        | 0             | 0           | 0                 | -      |
| Provisional                    | 1306        | 1        | 31            | 25          | 17                | 68.00% |
| Total                          | 1306        | 5        | 684           | 622         | 439               | 70.58% |
| 300020.04-CP 300020.04         |             |          |               |             |                   |        |
| Precinct TSx                   | 1401        | 1        | 0             | 0           | 0                 | -      |
| Precinct OS                    | 1401        | 1        | 168           | 152         | 94                | 61.84% |
| Precinct VBM                   | 1401        | 1        | 632           | 565         | 397               | 70.27% |
| MB Precinct                    | 1401        | 1        | 0             | 0           | 0                 | -      |
| Provisional                    | 1401        | 1        | 14            | 9           | 2                 | 22.22% |
| Total                          | 1401        | 5        | 814           | 726         | 493               | 67.91% |
| 300021.03-CP 300021.03         |             |          |               |             |                   |        |
| Precinct TSx                   | 1234        | 1        | 0             | 0           | 0                 | -      |
| Precinct OS                    | 1234        | 1        | 153           | 139         | 94                | 67.63% |
| Precinct VBM                   | 1234        | 1        | 614           | 572         | 430               | 75.17% |
| MB Precinct                    | 1234        | 1        | 0             | 0           | 0                 | -      |
| Provisional                    | 1234        | 1        | 13            | 9           | 6                 | 66.67% |
| Total                          | 1234        | 5        | 780           | 720         | 530               | 73.61% |
| 300022.05-CP 300022.05         |             |          |               |             |                   |        |
| Precinct TSx                   | 872         | 1        | 0             | 0           | 0                 | -      |
| Precinct OS                    | 872         | 1        | 101           | 90          | 64                | 71.11% |
| Precinct VBM                   | 872         | 1        | 356           | 318         | 222               | 69.81% |
| MB Precinct                    | 872         | 1        | 0             | 0           | 0                 | -      |
| Provisional                    | 872         | 1        | 12            | 8           | 5                 | 62.50% |
| Total                          | 872         | 5        | 469           | 416         | 291               | 69.95% |
| 300023.03-CP 300023.03         |             |          |               |             |                   |        |
| Precinct TSx                   | 1819        | 1        | 0             | 0           | 0                 | -      |
| Precinct OS                    | 1819        | 1        | 228           | 208         | 150               | 72.12% |
| Precinct VBM                   | 1819        | 1        | 924           | 867         | 633               | 73.01% |
| MB Precinct                    | 1819        | 1        | 0             | 0           | 0                 | -      |
| Provisional                    | 1819        | 1        | 26            | 21          | 15                | 71.43% |
| Total                          | 1819        | 5        | 1178          | 1096        | 798               | 72.81% |
| 300023.05-CP 300023.05         |             |          |               |             |                   |        |
| Precinct TSx                   | 386         | 1        | 0             | 0           | 0                 | -      |
| Precinct OS                    | 386         | 1        | 45            | 40          | 23                | 57.50% |
| Precinct VBM                   | 386         | 1        | 193           | 180         | 132               | 73.33% |
| MB Precinct                    | 386         | 1        | 0             | 0           | 0                 | -      |
| Provisional                    | 386         | 1        | 8             | 5           | 2                 | 40.00% |
| Total                          | 386         | 5        | 246           | 225         | 157               | 69.78% |
| 400001.14-CP 400001.14         |             |          |               |             |                   |        |
| Precinct TSx                   | -           | -        | -             | -           | -                 | -      |
| Precinct OS                    | -           | -        | -             | -           | -                 | -      |
| Precinct VBM                   | -           | -        | -             | -           | -                 | -      |
| MB Precinct                    | -           | -        | -             | -           | -                 | -      |
| Provisional                    | -           | -        | -             | -           | -                 | -      |
| Total                          | -           | -        | -             | -           | -                 | -      |
| 400002.08-CP 400002.08         |             |          |               |             |                   |        |
| Precinct TSx                   | -           | -        | -             | -           | -                 | -      |
| Precinct OS                    | -           | -        | -             | -           | -                 | -      |
| Precinct VBM                   | -           | -        | -             | -           | -                 | -      |
| MB Precinct                    | -           | -        | -             | -           | -                 | -      |
| Provisional                    | -           | -        | -             | -           | -                 | -      |
| Total                          | -           | -        | -             | -           | -                 | -      |
| 400003.08-CP 400003.08         |             |          |               |             |                   |        |

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|------------------------|--------------------------------|----------|---------------|-------------|----------------------|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | BRIAN K.<br>VEERKAMP |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 400004.14-CP 400004.14 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 400005.08-CP 400005.08 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 400006.08-CP 400006.08 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 400007.08-CP 400007.08 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 400010.08-CP 400010.08 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 400012.08-CP 400012.08 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 400013.08-CP 400013.08 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 400014.08-CP 400014.08 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |

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|                        | County Supervisor 3rd District |          |               |             |                      |
|------------------------|--------------------------------|----------|---------------|-------------|----------------------|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | BRIAN K.<br>VEERKAMP |
| Total                  | -                              | -        | -             | -           | -                    |
| 400017.08-CP 400017.08 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 400018.08-CP 400018.08 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 400020.08-CP 400020.08 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 400021.07-CP 400021.07 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 500001.10-CP 500001.10 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 500002.10-CP 500002.10 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 500003.10-CP 500003.10 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 500004.10-CP 500004.10 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 500005.10-CP 500005.10 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |



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|                        | County Supervisor 3rd District |          |               |             |                      |
|------------------------|--------------------------------|----------|---------------|-------------|----------------------|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes | BRIAN K.<br>VEERKAMP |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 500006.10-CP 500006.10 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 500007.10-CP 500007.10 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 500009.10-CP 500009.10 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 500011.10-CP 500011.10 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 500012.10-CP 500012.10 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 500015.10-CP 500015.10 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 500020.10-CP 500020.10 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 500021.10-CP 500021.10 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |
| Precinct OS            | -                              | -        | -             | -           | -                    |
| Precinct VBM           | -                              | -        | -             | -           | -                    |
| MB Precinct            | -                              | -        | -             | -           | -                    |
| Provisional            | -                              | -        | -             | -           | -                    |
| Total                  | -                              | -        | -             | -           | -                    |
| 95U201.01-MB 95U201.01 |                                |          |               |             |                      |
| Precinct TSx           | -                              | -        | -             | -           | -                    |

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|                        | County Supervisor 3rd District |          |               |             |     | BRIAN K.<br>VEERKAMP |   |
|------------------------|--------------------------------|----------|---------------|-------------|-----|----------------------|---|
|                        | Reg. Voters                    | Vote For | Times Counted | Total Votes |     |                      |   |
| Precinct OS            | -                              | -        | -             | -           | -   | -                    | - |
| Precinct VBM           | -                              | -        | -             | -           | -   | -                    | - |
| MB Precinct            | -                              | -        | -             | -           | -   | -                    | - |
| Provisional            | -                              | -        | -             | -           | -   | -                    | - |
| Total                  | -                              | -        | -             | -           | -   | -                    | - |
| 95U201.02-MB 95U201.02 |                                |          |               |             |     |                      |   |
| Precinct TSx           | -                              | -        | -             | -           | -   | -                    | - |
| Precinct OS            | -                              | -        | -             | -           | -   | -                    | - |
| Precinct VBM           | -                              | -        | -             | -           | -   | -                    | - |
| MB Precinct            | -                              | -        | -             | -           | -   | -                    | - |
| Provisional            | -                              | -        | -             | -           | -   | -                    | - |
| Total                  | -                              | -        | -             | -           | -   | -                    | - |
| 95U202.02-MB 95U202.02 |                                |          |               |             |     |                      |   |
| Precinct TSx           | -                              | -        | -             | -           | -   | -                    | - |
| Precinct OS            | -                              | -        | -             | -           | -   | -                    | - |
| Precinct VBM           | -                              | -        | -             | -           | -   | -                    | - |
| MB Precinct            | -                              | -        | -             | -           | -   | -                    | - |
| Provisional            | -                              | -        | -             | -           | -   | -                    | - |
| Total                  | -                              | -        | -             | -           | -   | -                    | - |
| 95U203.02-MB 95U203.02 |                                |          |               |             |     |                      |   |
| Precinct TSx           | -                              | -        | -             | -           | -   | -                    | - |
| Precinct OS            | -                              | -        | -             | -           | -   | -                    | - |
| Precinct VBM           | -                              | -        | -             | -           | -   | -                    | - |
| MB Precinct            | -                              | -        | -             | -           | -   | -                    | - |
| Provisional            | -                              | -        | -             | -           | -   | -                    | - |
| Total                  | -                              | -        | -             | -           | -   | -                    | - |
| 95U204.01-MB 95U204.01 |                                |          |               |             |     |                      |   |
| Precinct TSx           | -                              | -        | -             | -           | -   | -                    | - |
| Precinct OS            | -                              | -        | -             | -           | -   | -                    | - |
| Precinct VBM           | -                              | -        | -             | -           | -   | -                    | - |
| MB Precinct            | -                              | -        | -             | -           | -   | -                    | - |
| Provisional            | -                              | -        | -             | -           | -   | -                    | - |
| Total                  | -                              | -        | -             | -           | -   | -                    | - |
| 95U301.05-MB 95U301.05 |                                |          |               |             |     |                      |   |
| Precinct TSx           | 244                            | 1        | 0             | 0           | 0   | 0                    | - |
| Precinct OS            | 244                            | 1        | 0             | 0           | 0   | 0                    | - |
| Precinct VBM           | 244                            | 1        | 0             | 0           | 0   | 0                    | - |
| MB Precinct            | 244                            | 1        | 151           | 139         | 112 | 80.58%               |   |
| Provisional            | 244                            | 1        | 2             | 1           | 1   | 100.00%              |   |
| Total                  | 244                            | 5        | 153           | 140         | 113 | 80.71%               |   |
| 95U302.04-MB 95U302.04 |                                |          |               |             |     |                      |   |
| Precinct TSx           | 0                              | 1        | 0             | 0           | 0   | 0                    | - |
| Precinct OS            | 0                              | 1        | 0             | 0           | 0   | 0                    | - |
| Precinct VBM           | 0                              | 1        | 0             | 0           | 0   | 0                    | - |
| MB Precinct            | 0                              | 1        | 1             | 1           | 0   | 0.00%                |   |
| Provisional            | 0                              | 1        | 0             | 0           | 0   | 0                    | - |
| Total                  | 0                              | 5        | 1             | 1           | 0   | 0.00%                |   |
| 95U302.05-MB 95U302.05 |                                |          |               |             |     |                      |   |
| Precinct TSx           | 128                            | 1        | 0             | 0           | 0   | 0                    | - |
| Precinct OS            | 128                            | 1        | 0             | 0           | 0   | 0                    | - |
| Precinct VBM           | 128                            | 1        | 0             | 0           | 0   | 0                    | - |
| MB Precinct            | 128                            | 1        | 86            | 80          | 59  | 73.75%               |   |
| Provisional            | 128                            | 1        | 0             | 0           | 0   | 0                    | - |
| Total                  | 128                            | 5        | 86            | 80          | 59  | 73.75%               |   |
| 95U303.03-MB 95U303.03 |                                |          |               |             |     |                      |   |
| Precinct TSx           | 144                            | 1        | 0             | 0           | 0   | 0                    | - |
| Precinct OS            | 144                            | 1        | 0             | 0           | 0   | 0                    | - |
| Precinct VBM           | 144                            | 1        | 0             | 0           | 0   | 0                    | - |
| MB Precinct            | 144                            | 1        | 84            | 78          | 59  | 75.64%               |   |
| Provisional            | 144                            | 1        | 0             | 0           | 0   | 0                    | - |
| Total                  | 144                            | 5        | 84            | 78          | 59  | 75.64%               |   |



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|                                      | Reg. Voters                    | Vote For   | Times Counted | Total Votes  | BRIAN K.<br>VEERKAMP |               |   |
| Provisional                          | -                              | -          | -             | -            | -                    | -             | - |
| Total                                | -                              | -          | -             | -            | -                    | -             | - |
| 95U501.09-MB 95U501.09               |                                |            |               |              |                      |               |   |
| Precinct TSx                         | -                              | -          | -             | -            | -                    | -             | - |
| Precinct OS                          | -                              | -          | -             | -            | -                    | -             | - |
| Precinct VBM                         | -                              | -          | -             | -            | -                    | -             | - |
| MB Precinct                          | -                              | -          | -             | -            | -                    | -             | - |
| Provisional                          | -                              | -          | -             | -            | -                    | -             | - |
| Total                                | -                              | -          | -             | -            | -                    | -             | - |
| 95U501.10-MB 95U501.10               |                                |            |               |              |                      |               |   |
| Precinct TSx                         | -                              | -          | -             | -            | -                    | -             | - |
| Precinct OS                          | -                              | -          | -             | -            | -                    | -             | - |
| Precinct VBM                         | -                              | -          | -             | -            | -                    | -             | - |
| MB Precinct                          | -                              | -          | -             | -            | -                    | -             | - |
| Provisional                          | -                              | -          | -             | -            | -                    | -             | - |
| Total                                | -                              | -          | -             | -            | -                    | -             | - |
| 95U598.10-MB 95U598.10               |                                |            |               |              |                      |               |   |
| Precinct TSx                         | -                              | -          | -             | -            | -                    | -             | - |
| Precinct OS                          | -                              | -          | -             | -            | -                    | -             | - |
| Precinct VBM                         | -                              | -          | -             | -            | -                    | -             | - |
| MB Precinct                          | -                              | -          | -             | -            | -                    | -             | - |
| Provisional                          | -                              | -          | -             | -            | -                    | -             | - |
| Total                                | -                              | -          | -             | -            | -                    | -             | - |
| 95U599.10-MB 95U599.10               |                                |            |               |              |                      |               |   |
| Precinct TSx                         | -                              | -          | -             | -            | -                    | -             | - |
| Precinct OS                          | -                              | -          | -             | -            | -                    | -             | - |
| Precinct VBM                         | -                              | -          | -             | -            | -                    | -             | - |
| MB Precinct                          | -                              | -          | -             | -            | -                    | -             | - |
| Provisional                          | -                              | -          | -             | -            | -                    | -             | - |
| Total                                | -                              | -          | -             | -            | -                    | -             | - |
| 96U101.11-MB 96U101.11               |                                |            |               |              |                      |               |   |
| Precinct TSx                         | -                              | -          | -             | -            | -                    | -             | - |
| Precinct OS                          | -                              | -          | -             | -            | -                    | -             | - |
| Precinct VBM                         | -                              | -          | -             | -            | -                    | -             | - |
| MB Precinct                          | -                              | -          | -             | -            | -                    | -             | - |
| Provisional                          | -                              | -          | -             | -            | -                    | -             | - |
| Total                                | -                              | -          | -             | -            | -                    | -             | - |
| 96U401.14-MB 96U401.14               |                                |            |               |              |                      |               |   |
| Precinct TSx                         | -                              | -          | -             | -            | -                    | -             | - |
| Precinct OS                          | -                              | -          | -             | -            | -                    | -             | - |
| Precinct VBM                         | -                              | -          | -             | -            | -                    | -             | - |
| MB Precinct                          | -                              | -          | -             | -            | -                    | -             | - |
| Provisional                          | -                              | -          | -             | -            | -                    | -             | - |
| Total                                | -                              | -          | -             | -            | -                    | -             | - |
| <b>Total</b>                         |                                |            |               |              |                      |               |   |
| Precinct TSx                         | 21691                          | 26         | 3             | 3            | 1                    | 33.33%        |   |
| Precinct OS                          | 21691                          | 26         | 2368          | 2112         | 1435                 | 67.95%        |   |
| Precinct VBM                         | 21691                          | 26         | 10021         | 9134         | 6579                 | 72.03%        |   |
| MB Precinct                          | 21691                          | 26         | 440           | 403          | 309                  | 76.67%        |   |
| Provisional                          | 21691                          | 26         | 277           | 208          | 129                  | 62.02%        |   |
| <b>Total</b>                         | <b>21691</b>                   | <b>130</b> | <b>13109</b>  | <b>11860</b> | <b>8453</b>          | <b>71.27%</b> |   |
| <b>STATE BOARD OF EQUALIZATION</b>   |                                |            |               |              |                      |               |   |
| <b>STATE BOARD OF EQUALIZATION 1</b> |                                |            |               |              |                      |               |   |
| Precinct TSx                         | 21691                          | 26         | 3             | 3            | 1                    | 33.33%        |   |
| Precinct OS                          | 21691                          | 26         | 2368          | 2112         | 1435                 | 67.95%        |   |
| Precinct VBM                         | 21691                          | 26         | 10021         | 9134         | 6579                 | 72.03%        |   |
| MB Precinct                          | 21691                          | 26         | 440           | 403          | 309                  | 76.67%        |   |
| Provisional                          | 21691                          | 26         | 277           | 208          | 129                  | 62.02%        |   |
| <b>Total</b>                         | <b>21691</b>                   | <b>130</b> | <b>13109</b>  | <b>11860</b> | <b>8453</b>          | <b>71.27%</b> |   |
| <b>Total</b>                         |                                |            |               |              |                      |               |   |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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| County Supervisor 3rd District      |              |            |               |              |                   |               |
|-------------------------------------|--------------|------------|---------------|--------------|-------------------|---------------|
|                                     | Reg. Voters  | Vote For   | Times Counted | Total Votes  | BRIAN K. VEERKAMP |               |
| Precinct TSx                        | 21691        | 26         | 3             | 3            | 1                 | 33.33%        |
| Precinct OS                         | 21691        | 26         | 2368          | 2112         | 1435              | 67.95%        |
| Precinct VBM                        | 21691        | 26         | 10021         | 9134         | 6579              | 72.03%        |
| MB Precinct                         | 21691        | 26         | 440           | 403          | 309               | 76.67%        |
| Provisional                         | 21691        | 26         | 277           | 208          | 129               | 62.02%        |
| <b>Total</b>                        | <b>21691</b> | <b>130</b> | <b>13109</b>  | <b>11860</b> | <b>8453</b>       | <b>71.27%</b> |
| <b>UNITED STATES SENATE</b>         |              |            |               |              |                   |               |
| <b>UNITED STATES SENATE</b>         |              |            |               |              |                   |               |
| Precinct TSx                        | 21691        | 26         | 3             | 3            | 1                 | 33.33%        |
| Precinct OS                         | 21691        | 26         | 2368          | 2112         | 1435              | 67.95%        |
| Precinct VBM                        | 21691        | 26         | 10021         | 9134         | 6579              | 72.03%        |
| MB Precinct                         | 21691        | 26         | 440           | 403          | 309               | 76.67%        |
| Provisional                         | 21691        | 26         | 277           | 208          | 129               | 62.02%        |
| <b>Total</b>                        | <b>21691</b> | <b>130</b> | <b>13109</b>  | <b>11860</b> | <b>8453</b>       | <b>71.27%</b> |
| <b>Total</b>                        |              |            |               |              |                   |               |
| Precinct TSx                        | 21691        | 26         | 3             | 3            | 1                 | 33.33%        |
| Precinct OS                         | 21691        | 26         | 2368          | 2112         | 1435              | 67.95%        |
| Precinct VBM                        | 21691        | 26         | 10021         | 9134         | 6579              | 72.03%        |
| MB Precinct                         | 21691        | 26         | 440           | 403          | 309               | 76.67%        |
| Provisional                         | 21691        | 26         | 277           | 208          | 129               | 62.02%        |
| <b>Total</b>                        | <b>21691</b> | <b>130</b> | <b>13109</b>  | <b>11860</b> | <b>8453</b>       | <b>71.27%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |              |            |               |              |                   |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |              |            |               |              |                   |               |
| Precinct TSx                        | 21691        | 26         | 3             | 3            | 1                 | 33.33%        |
| Precinct OS                         | 21691        | 26         | 2368          | 2112         | 1435              | 67.95%        |
| Precinct VBM                        | 21691        | 26         | 10021         | 9134         | 6579              | 72.03%        |
| MB Precinct                         | 21691        | 26         | 440           | 403          | 309               | 76.67%        |
| Provisional                         | 21691        | 26         | 277           | 208          | 129               | 62.02%        |
| <b>Total</b>                        | <b>21691</b> | <b>130</b> | <b>13109</b>  | <b>11860</b> | <b>8453</b>       | <b>71.27%</b> |
| <b>Total</b>                        |              |            |               |              |                   |               |
| Precinct TSx                        | 21691        | 26         | 3             | 3            | 1                 | 33.33%        |
| Precinct OS                         | 21691        | 26         | 2368          | 2112         | 1435              | 67.95%        |
| Precinct VBM                        | 21691        | 26         | 10021         | 9134         | 6579              | 72.03%        |
| MB Precinct                         | 21691        | 26         | 440           | 403          | 309               | 76.67%        |
| Provisional                         | 21691        | 26         | 277           | 208          | 129               | 62.02%        |
| <b>Total</b>                        | <b>21691</b> | <b>130</b> | <b>13109</b>  | <b>11860</b> | <b>8453</b>       | <b>71.27%</b> |
| <b>STATE SENATE</b>                 |              |            |               |              |                   |               |
| <b>1ST STATE SENATE DISTRICT</b>    |              |            |               |              |                   |               |
| Precinct TSx                        | 21691        | 26         | 3             | 3            | 1                 | 33.33%        |
| Precinct OS                         | 21691        | 26         | 2368          | 2112         | 1435              | 67.95%        |
| Precinct VBM                        | 21691        | 26         | 10021         | 9134         | 6579              | 72.03%        |
| MB Precinct                         | 21691        | 26         | 440           | 403          | 309               | 76.67%        |
| Provisional                         | 21691        | 26         | 277           | 208          | 129               | 62.02%        |
| <b>Total</b>                        | <b>21691</b> | <b>130</b> | <b>13109</b>  | <b>11860</b> | <b>8453</b>       | <b>71.27%</b> |
| <b>Total</b>                        |              |            |               |              |                   |               |
| Precinct TSx                        | 21691        | 26         | 3             | 3            | 1                 | 33.33%        |
| Precinct OS                         | 21691        | 26         | 2368          | 2112         | 1435              | 67.95%        |
| Precinct VBM                        | 21691        | 26         | 10021         | 9134         | 6579              | 72.03%        |
| MB Precinct                         | 21691        | 26         | 440           | 403          | 309               | 76.67%        |
| Provisional                         | 21691        | 26         | 277           | 208          | 129               | 62.02%        |
| <b>Total</b>                        | <b>21691</b> | <b>130</b> | <b>13109</b>  | <b>11860</b> | <b>8453</b>       | <b>71.27%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |              |            |               |              |                   |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |              |            |               |              |                   |               |
| Precinct TSx                        | 21691        | 26         | 3             | 3            | 1                 | 33.33%        |
| Precinct OS                         | 21691        | 26         | 2368          | 2112         | 1435              | 67.95%        |
| Precinct VBM                        | 21691        | 26         | 10021         | 9134         | 6579              | 72.03%        |
| MB Precinct                         | 21691        | 26         | 440           | 403          | 309               | 76.67%        |

**COUNTY OF EL DORADO**  
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| County Supervisor 3rd District    |             |          |               |             |                   |        |
|-----------------------------------|-------------|----------|---------------|-------------|-------------------|--------|
|                                   | Reg. Voters | Vote For | Times Counted | Total Votes | BRIAN K. VEERKAMP |        |
| Provisional                       | 21691       | 26       | 277           | 208         | 129               | 62.02% |
| Total                             | 21691       | 130      | 13109         | 11860       | 8453              | 71.27% |
| <b>6TH STATE ASSEMBLY DIST</b>    |             |          |               |             |                   |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0                 | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0                 | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Total                             | 0           | 0        | 0             | 0           | 0                 | -      |
| Total                             |             |          |               |             |                   |        |
| Precinct TSx                      | 21691       | 26       | 3             | 3           | 1                 | 33.33% |
| Precinct OS                       | 21691       | 26       | 2368          | 2112        | 1435              | 67.95% |
| Precinct VBM                      | 21691       | 26       | 10021         | 9134        | 6579              | 72.03% |
| MB Precinct                       | 21691       | 26       | 440           | 403         | 309               | 76.67% |
| Provisional                       | 21691       | 26       | 277           | 208         | 129               | 62.02% |
| Total                             | 21691       | 130      | 13109         | 11860       | 8453              | 71.27% |
| <b>SUPERVISORIAL DISTRICTS</b>    |             |          |               |             |                   |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |             |          |               |             |                   |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0                 | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0                 | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Total                             | 0           | 0        | 0             | 0           | 0                 | -      |
| <b>2ND SUPERVISORIAL DISTRICT</b> |             |          |               |             |                   |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0                 | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0                 | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Total                             | 0           | 0        | 0             | 0           | 0                 | -      |
| <b>3RD SUPERVISORIAL DISTRICT</b> |             |          |               |             |                   |        |
| Precinct TSx                      | 21691       | 26       | 3             | 3           | 1                 | 33.33% |
| Precinct OS                       | 21691       | 26       | 2368          | 2112        | 1435              | 67.95% |
| Precinct VBM                      | 21691       | 26       | 10021         | 9134        | 6579              | 72.03% |
| MB Precinct                       | 21691       | 26       | 440           | 403         | 309               | 76.67% |
| Provisional                       | 21691       | 26       | 277           | 208         | 129               | 62.02% |
| Total                             | 21691       | 130      | 13109         | 11860       | 8453              | 71.27% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |                   |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0                 | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0                 | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Total                             | 0           | 0        | 0             | 0           | 0                 | -      |
| <b>5TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |                   |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0                 | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0                 | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0                 | -      |
| Total                             | 0           | 0        | 0             | 0           | 0                 | -      |
| Total                             |             |          |               |             |                   |        |
| Precinct TSx                      | 21691       | 26       | 3             | 3           | 1                 | 33.33% |
| Precinct OS                       | 21691       | 26       | 2368          | 2112        | 1435              | 67.95% |
| Precinct VBM                      | 21691       | 26       | 10021         | 9134        | 6579              | 72.03% |
| MB Precinct                       | 21691       | 26       | 440           | 403         | 309               | 76.67% |
| Provisional                       | 21691       | 26       | 277           | 208         | 129               | 62.02% |
| Total                             | 21691       | 130      | 13109         | 11860       | 8453              | 71.27% |

**COUNTY OF EL DORADO**  
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| County Supervisor 3rd District  |              |            |               |              |                      |               |
|---------------------------------|--------------|------------|---------------|--------------|----------------------|---------------|
|                                 | Reg. Voters  | Vote For   | Times Counted | Total Votes  | BRIAN K.<br>VEERKAMP |               |
| <b>CITIES</b>                   |              |            |               |              |                      |               |
| <b>CITY OF PLACERVILLE</b>      |              |            |               |              |                      |               |
| Precinct TSx                    | 5569         | 5          | 0             | 0            | 0                    | -             |
| Precinct OS                     | 5569         | 5          | 631           | 544          | 381                  | 70.04%        |
| Precinct VBM                    | 5569         | 5          | 2590          | 2328         | 1702                 | 73.11%        |
| MB Precinct                     | 5569         | 5          | 0             | 0            | 0                    | -             |
| Provisional                     | 5569         | 5          | 80            | 62           | 34                   | 54.84%        |
| <b>Total</b>                    | <b>5569</b>  | <b>25</b>  | <b>3301</b>   | <b>2934</b>  | <b>2117</b>          | <b>72.15%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |              |            |               |              |                      |               |
| Precinct TSx                    | 0            | 0          | 0             | 0            | 0                    | -             |
| Precinct OS                     | 0            | 0          | 0             | 0            | 0                    | -             |
| Precinct VBM                    | 0            | 0          | 0             | 0            | 0                    | -             |
| MB Precinct                     | 0            | 0          | 0             | 0            | 0                    | -             |
| Provisional                     | 0            | 0          | 0             | 0            | 0                    | -             |
| <b>Total</b>                    | <b>0</b>     | <b>0</b>   | <b>0</b>      | <b>0</b>     | <b>0</b>             | <b>-</b>      |
| <b>UNINCORPORATED AREA</b>      |              |            |               |              |                      |               |
| Precinct TSx                    | 16122        | 21         | 3             | 3            | 1                    | 33.33%        |
| Precinct OS                     | 16122        | 21         | 1737          | 1568         | 1054                 | 67.22%        |
| Precinct VBM                    | 16122        | 21         | 7431          | 6806         | 4877                 | 71.66%        |
| MB Precinct                     | 16122        | 21         | 440           | 403          | 309                  | 76.67%        |
| Provisional                     | 16122        | 21         | 197           | 146          | 95                   | 65.07%        |
| <b>Total</b>                    | <b>16122</b> | <b>105</b> | <b>9808</b>   | <b>8926</b>  | <b>6336</b>          | <b>70.98%</b> |
| <b>Total</b>                    |              |            |               |              |                      |               |
| Precinct TSx                    | 21691        | 26         | 3             | 3            | 1                    | 33.33%        |
| Precinct OS                     | 21691        | 26         | 2368          | 2112         | 1435                 | 67.95%        |
| Precinct VBM                    | 21691        | 26         | 10021         | 9134         | 6579                 | 72.03%        |
| MB Precinct                     | 21691        | 26         | 440           | 403          | 309                  | 76.67%        |
| Provisional                     | 21691        | 26         | 277           | 208          | 129                  | 62.02%        |
| <b>Total</b>                    | <b>21691</b> | <b>130</b> | <b>13109</b>  | <b>11860</b> | <b>8453</b>          | <b>71.27%</b> |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                        | County Supervisor 3rd District |
|------------------------|--------------------------------|
|                        | C.J. LOUIS                     |
| Jurisdiction Wide      |                                |
| 100001.11-CP 100001.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100002.11-CP 100002.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100003.11-CP 100003.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100004.11-CP 100004.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100005.11-CP 100005.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100006.11-CP 100006.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100008.11-CP 100008.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100009.11-CP 100009.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100012.11-CP 100012.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |



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|                        | County Supervisor 3rd District |
|------------------------|--------------------------------|
|                        | C.J. LOUIS                     |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100013.11-CP 100013.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100014.11-CP 100014.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100022.11-CP 100022.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 100023.11-CP 100023.11 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 200003.12-CP 200003.12 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 200004.12-CP 200004.12 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 200005.02-CP 200005.02 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 200006.12-CP 200006.12 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 200008.12-CP 200008.12 |                                |
| Precinct TSx           | -                              |

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|                        | County Supervisor 3rd District |
|------------------------|--------------------------------|
|                        | C.J. LOUIS                     |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 200010.12-CP 200010.12 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 200012.12-CP 200012.12 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 200013.12-CP 200013.12 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 200014.12-CP 200014.12 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 200015.02-CP 200015.02 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 200016.12-CP 200016.12 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 200016.13-CP 200016.13 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 200017.02-CP 200017.02 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |

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|                        | County Supervisor 3rd<br>District |        |  |
|------------------------|-----------------------------------|--------|--|
|                        | C.J. LOUIS                        |        |  |
| 200018.02-CP 200018.02 |                                   |        |  |
| Precinct TSx           | -                                 | -      |  |
| Precinct OS            | -                                 | -      |  |
| Precinct VBM           | -                                 | -      |  |
| MB Precinct            | -                                 | -      |  |
| Provisional            | -                                 | -      |  |
| Total                  | -                                 | -      |  |
| 200020.02-CP 200020.02 |                                   |        |  |
| Precinct TSx           | -                                 | -      |  |
| Precinct OS            | -                                 | -      |  |
| Precinct VBM           | -                                 | -      |  |
| MB Precinct            | -                                 | -      |  |
| Provisional            | -                                 | -      |  |
| Total                  | -                                 | -      |  |
| 200021.01-CP 200021.01 |                                   |        |  |
| Precinct TSx           | -                                 | -      |  |
| Precinct OS            | -                                 | -      |  |
| Precinct VBM           | -                                 | -      |  |
| MB Precinct            | -                                 | -      |  |
| Provisional            | -                                 | -      |  |
| Total                  | -                                 | -      |  |
| 200021.02-CP 200021.02 |                                   |        |  |
| Precinct TSx           | -                                 | -      |  |
| Precinct OS            | -                                 | -      |  |
| Precinct VBM           | -                                 | -      |  |
| MB Precinct            | -                                 | -      |  |
| Provisional            | -                                 | -      |  |
| Total                  | -                                 | -      |  |
| 200024.02-CP 200024.02 |                                   |        |  |
| Precinct TSx           | -                                 | -      |  |
| Precinct OS            | -                                 | -      |  |
| Precinct VBM           | -                                 | -      |  |
| MB Precinct            | -                                 | -      |  |
| Provisional            | -                                 | -      |  |
| Total                  | -                                 | -      |  |
| 300001.04-CP 300001.04 |                                   |        |  |
| Precinct TSx           | 0                                 | -      |  |
| Precinct OS            | 1                                 | 10.00% |  |
| Precinct VBM           | 23                                | 24.73% |  |
| MB Precinct            | 0                                 | -      |  |
| Provisional            | 0                                 | -      |  |
| Total                  | 24                                | 23.30% |  |
| 300001.05-CP 300001.05 |                                   |        |  |
| Precinct TSx           | 0                                 | -      |  |
| Precinct OS            | 16                                | 23.88% |  |
| Precinct VBM           | 66                                | 22.53% |  |
| MB Precinct            | 0                                 | -      |  |
| Provisional            | 4                                 | 50.00% |  |
| Total                  | 86                                | 23.37% |  |
| 300003.05-CP 300003.05 |                                   |        |  |
| Precinct TSx           | 0                                 | -      |  |
| Precinct OS            | 53                                | 29.61% |  |
| Precinct VBM           | 223                               | 27.06% |  |
| MB Precinct            | 0                                 | -      |  |
| Provisional            | 8                                 | 36.36% |  |
| Total                  | 284                               | 27.71% |  |
| 300004.05-CP 300004.05 |                                   |        |  |
| Precinct TSx           | 0                                 | -      |  |
| Precinct OS            | 53                                | 35.33% |  |
| Precinct VBM           | 155                               | 30.21% |  |
| MB Precinct            | 0                                 | -      |  |

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|                        | C.J. LOUIS                        |
| Provisional            | 5 41.67%                          |
| Total                  | 213 31.56%                        |
| 300005.05-CP 300005.05 |                                   |
| Precinct TSx           | 0 -                               |
| Precinct OS            | 39 28.26%                         |
| Precinct VBM           | 144 23.80%                        |
| MB Precinct            | 0 -                               |
| Provisional            | 10 50.00%                         |
| Total                  | 193 25.29%                        |
| 300008.05-CP 300008.05 |                                   |
| Precinct TSx           | 0 -                               |
| Precinct OS            | 43 33.08%                         |
| Precinct VBM           | 173 25.67%                        |
| MB Precinct            | 0 -                               |
| Provisional            | 1 16.67%                          |
| Total                  | 217 26.79%                        |
| 300009.04-CP 300009.04 |                                   |
| Precinct TSx           | 0 -                               |
| Precinct OS            | 35 31.82%                         |
| Precinct VBM           | 107 30.14%                        |
| MB Precinct            | 0 -                               |
| Provisional            | 3 30.00%                          |
| Total                  | 145 30.53%                        |
| 300010.04-CP 300010.04 |                                   |
| Precinct TSx           | 0 -                               |
| Precinct OS            | 50 32.68%                         |
| Precinct VBM           | 161 26.97%                        |
| MB Precinct            | 0 -                               |
| Provisional            | 4 36.36%                          |
| Total                  | 215 28.25%                        |
| 300011.04-CP 300011.04 |                                   |
| Precinct TSx           | 0 -                               |
| Precinct OS            | 43 33.86%                         |
| Precinct VBM           | 211 32.81%                        |
| MB Precinct            | 0 -                               |
| Provisional            | 4 26.67%                          |
| Total                  | 258 32.87%                        |
| 300013.05-CP 300013.05 |                                   |
| Precinct TSx           | 2 66.67%                          |
| Precinct OS            | 41 36.61%                         |
| Precinct VBM           | 206 30.61%                        |
| MB Precinct            | 0 -                               |
| Provisional            | 5 38.46%                          |
| Total                  | 254 31.71%                        |
| 300015.04-CP 300015.04 |                                   |
| Precinct TSx           | 0 -                               |
| Precinct OS            | 1 12.50%                          |
| Precinct VBM           | 7 12.73%                          |
| MB Precinct            | 0 -                               |
| Provisional            | 1 25.00%                          |
| Total                  | 9 13.43%                          |
| 300015.05-CP 300015.05 |                                   |
| Precinct TSx           | 0 -                               |
| Precinct OS            | 23 38.33%                         |
| Precinct VBM           | 68 28.10%                         |
| MB Precinct            | 0 -                               |
| Provisional            | 1 50.00%                          |
| Total                  | 92 30.26%                         |
| 300017.05-CP 300017.05 |                                   |
| Precinct TSx           | 0 -                               |
| Precinct OS            | 32 29.91%                         |

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|                        | County Supervisor 3rd District |        |
|------------------------|--------------------------------|--------|
|                        | C.J. LOUIS                     |        |
| Precinct VBM           | 147                            | 24.50% |
| MB Precinct            | 0                              | -      |
| Provisional            | 1                              | 14.29% |
| Total                  | 180                            | 25.21% |
| 300018.04-CP 300018.04 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 34                             | 25.76% |
| Precinct VBM           | 134                            | 28.82% |
| MB Precinct            | 0                              | -      |
| Provisional            | 7                              | 28.00% |
| Total                  | 175                            | 28.14% |
| 300020.04-CP 300020.04 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 56                             | 36.84% |
| Precinct VBM           | 166                            | 29.38% |
| MB Precinct            | 0                              | -      |
| Provisional            | 7                              | 77.78% |
| Total                  | 229                            | 31.54% |
| 300021.03-CP 300021.03 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 45                             | 32.37% |
| Precinct VBM           | 142                            | 24.83% |
| MB Precinct            | 0                              | -      |
| Provisional            | 3                              | 33.33% |
| Total                  | 190                            | 26.39% |
| 300022.05-CP 300022.05 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 24                             | 26.67% |
| Precinct VBM           | 93                             | 29.25% |
| MB Precinct            | 0                              | -      |
| Provisional            | 3                              | 37.50% |
| Total                  | 120                            | 28.85% |
| 300023.03-CP 300023.03 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 57                             | 27.40% |
| Precinct VBM           | 232                            | 26.76% |
| MB Precinct            | 0                              | -      |
| Provisional            | 6                              | 28.57% |
| Total                  | 295                            | 26.92% |
| 300023.05-CP 300023.05 |                                |        |
| Precinct TSx           | 0                              | -      |
| Precinct OS            | 17                             | 42.50% |
| Precinct VBM           | 48                             | 26.67% |
| MB Precinct            | 0                              | -      |
| Provisional            | 3                              | 60.00% |
| Total                  | 68                             | 30.22% |
| 400001.14-CP 400001.14 |                                |        |
| Precinct TSx           | -                              | -      |
| Precinct OS            | -                              | -      |
| Precinct VBM           | -                              | -      |
| MB Precinct            | -                              | -      |
| Provisional            | -                              | -      |
| Total                  | -                              | -      |
| 400002.08-CP 400002.08 |                                |        |
| Precinct TSx           | -                              | -      |
| Precinct OS            | -                              | -      |
| Precinct VBM           | -                              | -      |
| MB Precinct            | -                              | -      |
| Provisional            | -                              | -      |
| Total                  | -                              | -      |
| 400003.08-CP 400003.08 |                                |        |

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|------------------------|--------------------------------|
|                        | C.J. LOUIS                     |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400004.14-CP 400004.14 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400005.08-CP 400005.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400006.08-CP 400006.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400007.08-CP 400007.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400010.08-CP 400010.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400012.08-CP 400012.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400013.08-CP 400013.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400014.08-CP 400014.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |

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|                        | C.J. LOUIS                     |
| Total                  | -                              |
| 400017.08-CP 400017.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400018.08-CP 400018.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400020.08-CP 400020.08 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 400021.07-CP 400021.07 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 500001.10-CP 500001.10 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 500002.10-CP 500002.10 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 500003.10-CP 500003.10 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 500004.10-CP 500004.10 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |
| MB Precinct            | -                              |
| Provisional            | -                              |
| Total                  | -                              |
| 500005.10-CP 500005.10 |                                |
| Precinct TSx           | -                              |
| Precinct OS            | -                              |
| Precinct VBM           | -                              |

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|                        | C.J. LOUIS                        |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 500006.10-CP 500006.10 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 500007.10-CP 500007.10 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 500009.10-CP 500009.10 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 500011.10-CP 500011.10 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 500012.10-CP 500012.10 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 500015.10-CP 500015.10 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 500020.10-CP 500020.10 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 500021.10-CP 500021.10 |                                   |
| Precinct TSx           | -                                 |
| Precinct OS            | -                                 |
| Precinct VBM           | -                                 |
| MB Precinct            | -                                 |
| Provisional            | -                                 |
| Total                  | -                                 |
| 95U201.01-MB 95U201.01 |                                   |
| Precinct TSx           | -                                 |



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|                        | C.J. LOUIS                     |         |
| Precinct OS            | -                              |         |
| Precinct VBM           | -                              |         |
| MB Precinct            | -                              |         |
| Provisional            | -                              |         |
| Total                  | -                              |         |
| 95U201.02-MB 95U201.02 |                                |         |
| Precinct TSx           | -                              |         |
| Precinct OS            | -                              |         |
| Precinct VBM           | -                              |         |
| MB Precinct            | -                              |         |
| Provisional            | -                              |         |
| Total                  | -                              |         |
| 95U202.02-MB 95U202.02 |                                |         |
| Precinct TSx           | -                              |         |
| Precinct OS            | -                              |         |
| Precinct VBM           | -                              |         |
| MB Precinct            | -                              |         |
| Provisional            | -                              |         |
| Total                  | -                              |         |
| 95U203.02-MB 95U203.02 |                                |         |
| Precinct TSx           | -                              |         |
| Precinct OS            | -                              |         |
| Precinct VBM           | -                              |         |
| MB Precinct            | -                              |         |
| Provisional            | -                              |         |
| Total                  | -                              |         |
| 95U204.01-MB 95U204.01 |                                |         |
| Precinct TSx           | -                              |         |
| Precinct OS            | -                              |         |
| Precinct VBM           | -                              |         |
| MB Precinct            | -                              |         |
| Provisional            | -                              |         |
| Total                  | -                              |         |
| 95U301.05-MB 95U301.05 |                                |         |
| Precinct TSx           | 0                              |         |
| Precinct OS            | 0                              |         |
| Precinct VBM           | 0                              |         |
| MB Precinct            | 27                             | 19.42%  |
| Provisional            | 0                              | 0.00%   |
| Total                  | 27                             | 19.29%  |
| 95U302.04-MB 95U302.04 |                                |         |
| Precinct TSx           | 0                              |         |
| Precinct OS            | 0                              |         |
| Precinct VBM           | 0                              |         |
| MB Precinct            | 1                              | 100.00% |
| Provisional            | 0                              |         |
| Total                  | 1                              | 100.00% |
| 95U302.05-MB 95U302.05 |                                |         |
| Precinct TSx           | 0                              |         |
| Precinct OS            | 0                              |         |
| Precinct VBM           | 0                              |         |
| MB Precinct            | 20                             | 25.00%  |
| Provisional            | 0                              |         |
| Total                  | 20                             | 25.00%  |
| 95U303.03-MB 95U303.03 |                                |         |
| Precinct TSx           | 0                              |         |
| Precinct OS            | 0                              |         |
| Precinct VBM           | 0                              |         |
| MB Precinct            | 19                             | 24.36%  |
| Provisional            | 0                              |         |
| Total                  | 19                             | 24.36%  |

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|                        | County Supervisor 3rd<br>District |        |
|------------------------|-----------------------------------|--------|
|                        | C.J. LOUIS                        |        |
| 95U303.05-MB 95U303.05 |                                   |        |
| Precinct TSx           | 0                                 | -      |
| Precinct OS            | 0                                 | -      |
| Precinct VBM           | 0                                 | -      |
| MB Precinct            | 23                                | 23.47% |
| Provisional            | 0                                 | -      |
| Total                  | 23                                | 23.47% |
| 95U304.05-MB 95U304.05 |                                   |        |
| Precinct TSx           | 0                                 | -      |
| Precinct OS            | 0                                 | -      |
| Precinct VBM           | 0                                 | -      |
| MB Precinct            | 0                                 | -      |
| Provisional            | 0                                 | -      |
| Total                  | 0                                 | -      |
| 95U305.05-MB 95U305.05 |                                   |        |
| Precinct TSx           | 0                                 | -      |
| Precinct OS            | 0                                 | -      |
| Precinct VBM           | 0                                 | -      |
| MB Precinct            | 3                                 | 42.86% |
| Provisional            | 0                                 | -      |
| Total                  | 3                                 | 42.86% |
| 95U401.08-MB 95U401.08 |                                   |        |
| Precinct TSx           | -                                 | -      |
| Precinct OS            | -                                 | -      |
| Precinct VBM           | -                                 | -      |
| MB Precinct            | -                                 | -      |
| Provisional            | -                                 | -      |
| Total                  | -                                 | -      |
| 95U402.07-MB 95U402.07 |                                   |        |
| Precinct TSx           | -                                 | -      |
| Precinct OS            | -                                 | -      |
| Precinct VBM           | -                                 | -      |
| MB Precinct            | -                                 | -      |
| Provisional            | -                                 | -      |
| Total                  | -                                 | -      |
| 95U402.08-MB 95U402.08 |                                   |        |
| Precinct TSx           | -                                 | -      |
| Precinct OS            | -                                 | -      |
| Precinct VBM           | -                                 | -      |
| MB Precinct            | -                                 | -      |
| Provisional            | -                                 | -      |
| Total                  | -                                 | -      |
| 95U403.08-MB 95U403.08 |                                   |        |
| Precinct TSx           | -                                 | -      |
| Precinct OS            | -                                 | -      |
| Precinct VBM           | -                                 | -      |
| MB Precinct            | -                                 | -      |
| Provisional            | -                                 | -      |
| Total                  | -                                 | -      |
| 95U404.06-MB 95U404.06 |                                   |        |
| Precinct TSx           | -                                 | -      |
| Precinct OS            | -                                 | -      |
| Precinct VBM           | -                                 | -      |
| MB Precinct            | -                                 | -      |
| Provisional            | -                                 | -      |
| Total                  | -                                 | -      |
| 95U404.08-MB 95U404.08 |                                   |        |
| Precinct TSx           | -                                 | -      |
| Precinct OS            | -                                 | -      |
| Precinct VBM           | -                                 | -      |
| MB Precinct            | -                                 | -      |

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|                               | County Supervisor 3rd District |        |
|-------------------------------|--------------------------------|--------|
|                               | C.J. LOUIS                     |        |
| Provisional                   | -                              | -      |
| Total                         | -                              | -      |
| 95U501.09-MB 95U501.09        |                                |        |
| Precinct TSx                  | -                              | -      |
| Precinct OS                   | -                              | -      |
| Precinct VBM                  | -                              | -      |
| MB Precinct                   | -                              | -      |
| Provisional                   | -                              | -      |
| Total                         | -                              | -      |
| 95U501.10-MB 95U501.10        |                                |        |
| Precinct TSx                  | -                              | -      |
| Precinct OS                   | -                              | -      |
| Precinct VBM                  | -                              | -      |
| MB Precinct                   | -                              | -      |
| Provisional                   | -                              | -      |
| Total                         | -                              | -      |
| 95U598.10-MB 95U598.10        |                                |        |
| Precinct TSx                  | -                              | -      |
| Precinct OS                   | -                              | -      |
| Precinct VBM                  | -                              | -      |
| MB Precinct                   | -                              | -      |
| Provisional                   | -                              | -      |
| Total                         | -                              | -      |
| 95U599.10-MB 95U599.10        |                                |        |
| Precinct TSx                  | -                              | -      |
| Precinct OS                   | -                              | -      |
| Precinct VBM                  | -                              | -      |
| MB Precinct                   | -                              | -      |
| Provisional                   | -                              | -      |
| Total                         | -                              | -      |
| 96U101.11-MB 96U101.11        |                                |        |
| Precinct TSx                  | -                              | -      |
| Precinct OS                   | -                              | -      |
| Precinct VBM                  | -                              | -      |
| MB Precinct                   | -                              | -      |
| Provisional                   | -                              | -      |
| Total                         | -                              | -      |
| 96U401.14-MB 96U401.14        |                                |        |
| Precinct TSx                  | -                              | -      |
| Precinct OS                   | -                              | -      |
| Precinct VBM                  | -                              | -      |
| MB Precinct                   | -                              | -      |
| Provisional                   | -                              | -      |
| Total                         | -                              | -      |
| Total                         | -                              | -      |
| Precinct TSx                  | 2                              | 66.67% |
| Precinct OS                   | 663                            | 31.39% |
| Precinct VBM                  | 2506                           | 27.44% |
| MB Precinct                   | 93                             | 23.08% |
| Provisional                   | 76                             | 36.54% |
| Total                         | 3340                           | 28.16% |
| STATE BOARD OF EQUALIZATION   |                                |        |
| STATE BOARD OF EQUALIZATION 1 |                                |        |
| Precinct TSx                  | 2                              | 66.67% |
| Precinct OS                   | 663                            | 31.39% |
| Precinct VBM                  | 2506                           | 27.44% |
| MB Precinct                   | 93                             | 23.08% |
| Provisional                   | 76                             | 36.54% |
| Total                         | 3340                           | 28.16% |
| Total                         |                                |        |

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|                                     | County Supervisor 3rd District |               |
|-------------------------------------|--------------------------------|---------------|
|                                     | C.J. LOUIS                     |               |
| Precinct TSx                        | 2                              | 66.67%        |
| Precinct OS                         | 663                            | 31.39%        |
| Precinct VBM                        | 2506                           | 27.44%        |
| MB Precinct                         | 93                             | 23.08%        |
| Provisional                         | 76                             | 36.54%        |
| <b>Total</b>                        | <b>3340</b>                    | <b>28.16%</b> |
| <b>UNITED STATES SENATE</b>         |                                |               |
| <b>UNITED STATES SENATE</b>         |                                |               |
| Precinct TSx                        | 2                              | 66.67%        |
| Precinct OS                         | 663                            | 31.39%        |
| Precinct VBM                        | 2506                           | 27.44%        |
| MB Precinct                         | 93                             | 23.08%        |
| Provisional                         | 76                             | 36.54%        |
| <b>Total</b>                        | <b>3340</b>                    | <b>28.16%</b> |
| <b>Total</b>                        |                                |               |
| Precinct TSx                        | 2                              | 66.67%        |
| Precinct OS                         | 663                            | 31.39%        |
| Precinct VBM                        | 2506                           | 27.44%        |
| MB Precinct                         | 93                             | 23.08%        |
| Provisional                         | 76                             | 36.54%        |
| <b>Total</b>                        | <b>3340</b>                    | <b>28.16%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |                                |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                                |               |
| Precinct TSx                        | 2                              | 66.67%        |
| Precinct OS                         | 663                            | 31.39%        |
| Precinct VBM                        | 2506                           | 27.44%        |
| MB Precinct                         | 93                             | 23.08%        |
| Provisional                         | 76                             | 36.54%        |
| <b>Total</b>                        | <b>3340</b>                    | <b>28.16%</b> |
| <b>Total</b>                        |                                |               |
| Precinct TSx                        | 2                              | 66.67%        |
| Precinct OS                         | 663                            | 31.39%        |
| Precinct VBM                        | 2506                           | 27.44%        |
| MB Precinct                         | 93                             | 23.08%        |
| Provisional                         | 76                             | 36.54%        |
| <b>Total</b>                        | <b>3340</b>                    | <b>28.16%</b> |
| <b>STATE SENATE</b>                 |                                |               |
| <b>1ST STATE SENATE DISTRICT</b>    |                                |               |
| Precinct TSx                        | 2                              | 66.67%        |
| Precinct OS                         | 663                            | 31.39%        |
| Precinct VBM                        | 2506                           | 27.44%        |
| MB Precinct                         | 93                             | 23.08%        |
| Provisional                         | 76                             | 36.54%        |
| <b>Total</b>                        | <b>3340</b>                    | <b>28.16%</b> |
| <b>Total</b>                        |                                |               |
| Precinct TSx                        | 2                              | 66.67%        |
| Precinct OS                         | 663                            | 31.39%        |
| Precinct VBM                        | 2506                           | 27.44%        |
| MB Precinct                         | 93                             | 23.08%        |
| Provisional                         | 76                             | 36.54%        |
| <b>Total</b>                        | <b>3340</b>                    | <b>28.16%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                                |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |                                |               |
| Precinct TSx                        | 2                              | 66.67%        |
| Precinct OS                         | 663                            | 31.39%        |
| Precinct VBM                        | 2506                           | 27.44%        |
| MB Precinct                         | 93                             | 23.08%        |

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|                                   |  | County Supervisor 3rd District |        |
|-----------------------------------|--|--------------------------------|--------|
|                                   |  | C.J. LOUIS                     |        |
| Provisional                       |  | 76                             | 36.54% |
| Total                             |  | 3340                           | 28.16% |
| <b>6TH STATE ASSEMBLY DIST</b>    |  |                                |        |
| Precinct TSx                      |  | 0                              | -      |
| Precinct OS                       |  | 0                              | -      |
| Precinct VBM                      |  | 0                              | -      |
| MB Precinct                       |  | 0                              | -      |
| Provisional                       |  | 0                              | -      |
| Total                             |  | 0                              | -      |
| <b>Total</b>                      |  |                                |        |
| Precinct TSx                      |  | 2                              | 66.67% |
| Precinct OS                       |  | 663                            | 31.39% |
| Precinct VBM                      |  | 2506                           | 27.44% |
| MB Precinct                       |  | 93                             | 23.08% |
| Provisional                       |  | 76                             | 36.54% |
| Total                             |  | 3340                           | 28.16% |
| <b>SUPERVISORIAL DISTRICTS</b>    |  |                                |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |  |                                |        |
| Precinct TSx                      |  | 0                              | -      |
| Precinct OS                       |  | 0                              | -      |
| Precinct VBM                      |  | 0                              | -      |
| MB Precinct                       |  | 0                              | -      |
| Provisional                       |  | 0                              | -      |
| Total                             |  | 0                              | -      |
| <b>2ND SUPERVISORIAL DISTRICT</b> |  |                                |        |
| Precinct TSx                      |  | 0                              | -      |
| Precinct OS                       |  | 0                              | -      |
| Precinct VBM                      |  | 0                              | -      |
| MB Precinct                       |  | 0                              | -      |
| Provisional                       |  | 0                              | -      |
| Total                             |  | 0                              | -      |
| <b>3RD SUPERVISORIAL DISTRICT</b> |  |                                |        |
| Precinct TSx                      |  | 2                              | 66.67% |
| Precinct OS                       |  | 663                            | 31.39% |
| Precinct VBM                      |  | 2506                           | 27.44% |
| MB Precinct                       |  | 93                             | 23.08% |
| Provisional                       |  | 76                             | 36.54% |
| Total                             |  | 3340                           | 28.16% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |  |                                |        |
| Precinct TSx                      |  | 0                              | -      |
| Precinct OS                       |  | 0                              | -      |
| Precinct VBM                      |  | 0                              | -      |
| MB Precinct                       |  | 0                              | -      |
| Provisional                       |  | 0                              | -      |
| Total                             |  | 0                              | -      |
| <b>5TH SUPERVISORIAL DISTRICT</b> |  |                                |        |
| Precinct TSx                      |  | 0                              | -      |
| Precinct OS                       |  | 0                              | -      |
| Precinct VBM                      |  | 0                              | -      |
| MB Precinct                       |  | 0                              | -      |
| Provisional                       |  | 0                              | -      |
| Total                             |  | 0                              | -      |
| <b>Total</b>                      |  |                                |        |
| Precinct TSx                      |  | 2                              | 66.67% |
| Precinct OS                       |  | 663                            | 31.39% |
| Precinct VBM                      |  | 2506                           | 27.44% |
| MB Precinct                       |  | 93                             | 23.08% |
| Provisional                       |  | 76                             | 36.54% |
| Total                             |  | 3340                           | 28.16% |

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|                                 | County Supervisor 3rd<br>District |               |
|---------------------------------|-----------------------------------|---------------|
|                                 | C.J. LOUIS                        |               |
| <b>CITIES</b>                   |                                   |               |
| <b>CITY OF PLACERVILLE</b>      |                                   |               |
| Precinct TSx                    | 0                                 | -             |
| Precinct OS                     | 162                               | 29.78%        |
| Precinct VBM                    | 611                               | 26.25%        |
| MB Precinct                     | 0                                 | -             |
| Provisional                     | 27                                | 43.55%        |
| <b>Total</b>                    | <b>800</b>                        | <b>27.27%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                                   |               |
| Precinct TSx                    | 0                                 | -             |
| Precinct OS                     | 0                                 | -             |
| Precinct VBM                    | 0                                 | -             |
| MB Precinct                     | 0                                 | -             |
| Provisional                     | 0                                 | -             |
| <b>Total</b>                    | <b>0</b>                          | <b>-</b>      |
| <b>UNINCORPORATED AREA</b>      |                                   |               |
| Precinct TSx                    | 2                                 | 66.67%        |
| Precinct OS                     | 501                               | 31.95%        |
| Precinct VBM                    | 1895                              | 27.84%        |
| MB Precinct                     | 93                                | 23.08%        |
| Provisional                     | 49                                | 33.56%        |
| <b>Total</b>                    | <b>2540</b>                       | <b>28.46%</b> |
| <b>Total</b>                    |                                   |               |
| Precinct TSx                    | 2                                 | 66.67%        |
| Precinct OS                     | 663                               | 31.39%        |
| Precinct VBM                    | 2506                              | 27.44%        |
| MB Precinct                     | 93                                | 23.08%        |
| Provisional                     | 76                                | 36.54%        |
| <b>Total</b>                    | <b>3340</b>                       | <b>28.16%</b> |

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|                          | Reg. Voters | Vote For | Times Counted | Total Votes | YES |         |
|--------------------------|-------------|----------|---------------|-------------|-----|---------|
| <b>Jurisdiction Wide</b> |             |          |               |             |     |         |
| 100001.11-CP 100001.11   |             |          |               |             |     |         |
| Precinct TSx             | 1615        | 1        | 0             | 0           | 0   | -       |
| Precinct OS              | 1615        | 1        | 190           | 163         | 125 | 76.69%  |
| Precinct VBM             | 1615        | 1        | 781           | 711         | 532 | 74.82%  |
| MB Precinct              | 1615        | 1        | 0             | 0           | 0   | -       |
| Provisional              | 1615        | 1        | 17            | 13          | 10  | 76.92%  |
| Total                    | 1615        | 5        | 988           | 887         | 667 | 75.20%  |
| 100002.11-CP 100002.11   |             |          |               |             |     |         |
| Precinct TSx             | 1000        | 1        | 0             | 0           | 0   | -       |
| Precinct OS              | 1000        | 1        | 89            | 79          | 61  | 77.22%  |
| Precinct VBM             | 1000        | 1        | 476           | 433         | 316 | 72.98%  |
| MB Precinct              | 1000        | 1        | 0             | 0           | 0   | -       |
| Provisional              | 1000        | 1        | 20            | 18          | 14  | 77.78%  |
| Total                    | 1000        | 5        | 585           | 530         | 391 | 73.77%  |
| 100003.11-CP 100003.11   |             |          |               |             |     |         |
| Precinct TSx             | 1269        | 1        | 0             | 0           | 0   | -       |
| Precinct OS              | 1269        | 1        | 123           | 113         | 80  | 70.80%  |
| Precinct VBM             | 1269        | 1        | 773           | 700         | 543 | 77.57%  |
| MB Precinct              | 1269        | 1        | 0             | 0           | 0   | -       |
| Provisional              | 1269        | 1        | 24            | 21          | 18  | 85.71%  |
| Total                    | 1269        | 5        | 920           | 834         | 641 | 76.86%  |
| 100004.11-CP 100004.11   |             |          |               |             |     |         |
| Precinct TSx             | 1633        | 1        | 0             | 0           | 0   | -       |
| Precinct OS              | 1633        | 1        | 254           | 232         | 179 | 77.16%  |
| Precinct VBM             | 1633        | 1        | 738           | 665         | 518 | 77.89%  |
| MB Precinct              | 1633        | 1        | 0             | 0           | 0   | -       |
| Provisional              | 1633        | 1        | 4             | 2           | 2   | 100.00% |
| Total                    | 1633        | 5        | 996           | 899         | 699 | 77.75%  |
| 100005.11-CP 100005.11   |             |          |               |             |     |         |
| Precinct TSx             | 1586        | 1        | 0             | 0           | 0   | -       |
| Precinct OS              | 1586        | 1        | 146           | 132         | 94  | 71.21%  |
| Precinct VBM             | 1586        | 1        | 895           | 825         | 567 | 68.73%  |
| MB Precinct              | 1586        | 1        | 0             | 0           | 0   | -       |
| Provisional              | 1586        | 1        | 23            | 21          | 14  | 66.67%  |
| Total                    | 1586        | 5        | 1064          | 978         | 675 | 69.02%  |
| 100006.11-CP 100006.11   |             |          |               |             |     |         |
| Precinct TSx             | 1928        | 1        | 0             | 0           | 0   | -       |
| Precinct OS              | 1928        | 1        | 252           | 222         | 158 | 71.17%  |
| Precinct VBM             | 1928        | 1        | 944           | 847         | 652 | 76.98%  |
| MB Precinct              | 1928        | 1        | 0             | 0           | 0   | -       |
| Provisional              | 1928        | 1        | 31            | 22          | 17  | 77.27%  |
| Total                    | 1928        | 5        | 1227          | 1091        | 827 | 75.80%  |
| 100008.11-CP 100008.11   |             |          |               |             |     |         |
| Precinct TSx             | 1671        | 1        | 0             | 0           | 0   | -       |
| Precinct OS              | 1671        | 1        | 197           | 165         | 132 | 80.00%  |
| Precinct VBM             | 1671        | 1        | 740           | 674         | 514 | 76.26%  |
| MB Precinct              | 1671        | 1        | 0             | 0           | 0   | -       |
| Provisional              | 1671        | 1        | 17            | 13          | 13  | 100.00% |
| Total                    | 1671        | 5        | 954           | 852         | 659 | 77.35%  |
| 100009.11-CP 100009.11   |             |          |               |             |     |         |
| Precinct TSx             | 1838        | 1        | 0             | 0           | 0   | -       |
| Precinct OS              | 1838        | 1        | 169           | 151         | 119 | 78.81%  |
| Precinct VBM             | 1838        | 1        | 751           | 672         | 541 | 80.51%  |
| MB Precinct              | 1838        | 1        | 0             | 0           | 0   | -       |
| Provisional              | 1838        | 1        | 26            | 18          | 15  | 83.33%  |
| Total                    | 1838        | 5        | 946           | 841         | 675 | 80.26%  |
| 100012.11-CP 100012.11   |             |          |               |             |     |         |
| Precinct TSx             | 2108        | 1        | 0             | 0           | 0   | -       |
| Precinct OS              | 2108        | 1        | 255           | 230         | 180 | 78.26%  |
| Precinct VBM             | 2108        | 1        | 968           | 855         | 656 | 76.73%  |

**COUNTY OF EL DORADO**  
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|                        | Reg. Voters | Vote For | Times Counted | Total Votes | YES |        |  |
|------------------------|-------------|----------|---------------|-------------|-----|--------|--|
| MB Precinct            | 2108        | 1        | 0             | 0           | 0   | -      |  |
| Provisional            | 2108        | 1        | 29            | 28          | 23  | 82.14% |  |
| Total                  | 2108        | 5        | 1252          | 1113        | 859 | 77.18% |  |
| 100013.11-CP 100013.11 |             |          |               |             |     |        |  |
| Precinct TSx           | 2087        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS            | 2087        | 1        | 250           | 226         | 170 | 75.22% |  |
| Precinct VBM           | 2087        | 1        | 908           | 830         | 616 | 74.22% |  |
| MB Precinct            | 2087        | 1        | 0             | 0           | 0   | -      |  |
| Provisional            | 2087        | 1        | 28            | 26          | 24  | 92.31% |  |
| Total                  | 2087        | 5        | 1186          | 1082        | 810 | 74.86% |  |
| 100014.11-CP 100014.11 |             |          |               |             |     |        |  |
| Precinct TSx           | 1659        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS            | 1659        | 1        | 211           | 185         | 131 | 70.81% |  |
| Precinct VBM           | 1659        | 1        | 706           | 638         | 479 | 75.08% |  |
| MB Precinct            | 1659        | 1        | 0             | 0           | 0   | -      |  |
| Provisional            | 1659        | 1        | 30            | 24          | 18  | 75.00% |  |
| Total                  | 1659        | 5        | 947           | 847         | 628 | 74.14% |  |
| 100022.11-CP 100022.11 |             |          |               |             |     |        |  |
| Precinct TSx           | 1432        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS            | 1432        | 1        | 177           | 162         | 111 | 68.52% |  |
| Precinct VBM           | 1432        | 1        | 678           | 605         | 450 | 74.38% |  |
| MB Precinct            | 1432        | 1        | 0             | 0           | 0   | -      |  |
| Provisional            | 1432        | 1        | 13            | 11          | 10  | 90.91% |  |
| Total                  | 1432        | 5        | 868           | 778         | 571 | 73.39% |  |
| 100023.11-CP 100023.11 |             |          |               |             |     |        |  |
| Precinct TSx           | 2135        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS            | 2135        | 1        | 217           | 187         | 135 | 72.19% |  |
| Precinct VBM           | 2135        | 1        | 1053          | 973         | 727 | 74.72% |  |
| MB Precinct            | 2135        | 1        | 0             | 0           | 0   | -      |  |
| Provisional            | 2135        | 1        | 33            | 27          | 22  | 81.48% |  |
| Total                  | 2135        | 5        | 1303          | 1187        | 884 | 74.47% |  |
| 200003.12-CP 200003.12 |             |          |               |             |     |        |  |
| Precinct TSx           | 1362        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS            | 1362        | 1        | 166           | 146         | 114 | 78.08% |  |
| Precinct VBM           | 1362        | 1        | 558           | 499         | 394 | 78.96% |  |
| MB Precinct            | 1362        | 1        | 0             | 0           | 0   | -      |  |
| Provisional            | 1362        | 1        | 31            | 26          | 19  | 73.08% |  |
| Total                  | 1362        | 5        | 755           | 671         | 527 | 78.54% |  |
| 200004.12-CP 200004.12 |             |          |               |             |     |        |  |
| Precinct TSx           | 1507        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS            | 1507        | 1        | 182           | 167         | 130 | 77.84% |  |
| Precinct VBM           | 1507        | 1        | 662           | 602         | 455 | 75.58% |  |
| MB Precinct            | 1507        | 1        | 0             | 0           | 0   | -      |  |
| Provisional            | 1507        | 1        | 14            | 12          | 11  | 91.67% |  |
| Total                  | 1507        | 5        | 858           | 781         | 596 | 76.31% |  |
| 200005.02-CP 200005.02 |             |          |               |             |     |        |  |
| Precinct TSx           | 867         | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS            | 867         | 1        | 94            | 91          | 69  | 75.82% |  |
| Precinct VBM           | 867         | 1        | 452           | 414         | 311 | 75.12% |  |
| MB Precinct            | 867         | 1        | 0             | 0           | 0   | -      |  |
| Provisional            | 867         | 1        | 21            | 19          | 16  | 84.21% |  |
| Total                  | 867         | 5        | 567           | 524         | 396 | 75.57% |  |
| 200006.12-CP 200006.12 |             |          |               |             |     |        |  |
| Precinct TSx           | 1086        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS            | 1086        | 1        | 117           | 109         | 80  | 73.39% |  |
| Precinct VBM           | 1086        | 1        | 532           | 498         | 335 | 67.27% |  |
| MB Precinct            | 1086        | 1        | 0             | 0           | 0   | -      |  |
| Provisional            | 1086        | 1        | 29            | 28          | 24  | 85.71% |  |
| Total                  | 1086        | 5        | 678           | 635         | 439 | 69.13% |  |
| 200008.12-CP 200008.12 |             |          |               |             |     |        |  |
| Precinct TSx           | 999         | 1        | 0             | 0           | 0   | -      |  |



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|                        | Reg. Voters | Vote For | Times Counted | Total Votes | YES |         |
|------------------------|-------------|----------|---------------|-------------|-----|---------|
| Precinct OS            | 999         | 1        | 155           | 132         | 99  | 75.00%  |
| Precinct VBM           | 999         | 1        | 379           | 337         | 256 | 75.96%  |
| MB Precinct            | 999         | 1        | 0             | 0           | 0   | -       |
| Provisional            | 999         | 1        | 10            | 9           | 7   | 77.78%  |
| Total                  | 999         | 5        | 544           | 478         | 362 | 75.73%  |
| 200010.12-CP 200010.12 |             |          |               |             |     |         |
| Precinct TSx           | 1976        | 1        | 4             | 4           | 4   | 100.00% |
| Precinct OS            | 1976        | 1        | 197           | 174         | 135 | 77.59%  |
| Precinct VBM           | 1976        | 1        | 859           | 785         | 618 | 78.73%  |
| MB Precinct            | 1976        | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1976        | 1        | 34            | 29          | 27  | 93.10%  |
| Total                  | 1976        | 5        | 1094          | 992         | 784 | 79.03%  |
| 200012.12-CP 200012.12 |             |          |               |             |     |         |
| Precinct TSx           | 1064        | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1064        | 1        | 145           | 132         | 108 | 81.82%  |
| Precinct VBM           | 1064        | 1        | 464           | 417         | 306 | 73.38%  |
| MB Precinct            | 1064        | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1064        | 1        | 14            | 11          | 7   | 63.64%  |
| Total                  | 1064        | 5        | 623           | 560         | 421 | 75.18%  |
| 200013.12-CP 200013.12 |             |          |               |             |     |         |
| Precinct TSx           | 1574        | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1574        | 1        | 211           | 193         | 151 | 78.24%  |
| Precinct VBM           | 1574        | 1        | 701           | 641         | 476 | 74.26%  |
| MB Precinct            | 1574        | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1574        | 1        | 23            | 20          | 17  | 85.00%  |
| Total                  | 1574        | 5        | 935           | 854         | 644 | 75.41%  |
| 200014.12-CP 200014.12 |             |          |               |             |     |         |
| Precinct TSx           | 1438        | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1438        | 1        | 201           | 182         | 135 | 74.18%  |
| Precinct VBM           | 1438        | 1        | 613           | 545         | 408 | 74.86%  |
| MB Precinct            | 1438        | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1438        | 1        | 30            | 26          | 23  | 88.46%  |
| Total                  | 1438        | 5        | 844           | 753         | 566 | 75.17%  |
| 200015.02-CP 200015.02 |             |          |               |             |     |         |
| Precinct TSx           | 1832        | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1832        | 1        | 218           | 196         | 147 | 75.00%  |
| Precinct VBM           | 1832        | 1        | 886           | 806         | 641 | 79.53%  |
| MB Precinct            | 1832        | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1832        | 1        | 22            | 19          | 15  | 78.95%  |
| Total                  | 1832        | 5        | 1126          | 1021        | 803 | 78.65%  |
| 200016.12-CP 200016.12 |             |          |               |             |     |         |
| Precinct TSx           | 301         | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 301         | 1        | 26            | 24          | 20  | 83.33%  |
| Precinct VBM           | 301         | 1        | 139           | 131         | 90  | 68.70%  |
| MB Precinct            | 301         | 1        | 0             | 0           | 0   | -       |
| Provisional            | 301         | 1        | 6             | 5           | 3   | 60.00%  |
| Total                  | 301         | 5        | 171           | 160         | 113 | 70.63%  |
| 200016.13-CP 200016.13 |             |          |               |             |     |         |
| Precinct TSx           | 565         | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 565         | 1        | 80            | 73          | 45  | 61.64%  |
| Precinct VBM           | 565         | 1        | 338           | 303         | 204 | 67.33%  |
| MB Precinct            | 565         | 1        | 0             | 0           | 0   | -       |
| Provisional            | 565         | 1        | 6             | 5           | 1   | 20.00%  |
| Total                  | 565         | 5        | 424           | 381         | 250 | 65.62%  |
| 200017.02-CP 200017.02 |             |          |               |             |     |         |
| Precinct TSx           | 1286        | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1286        | 1        | 146           | 131         | 102 | 77.86%  |
| Precinct VBM           | 1286        | 1        | 594           | 533         | 401 | 75.23%  |
| MB Precinct            | 1286        | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1286        | 1        | 12            | 11          | 9   | 81.82%  |
| Total                  | 1286        | 5        | 752           | 675         | 512 | 75.85%  |

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|                        | Reg. Voters          | Vote For | Times Counted | Total Votes | YES |        |
| 200018.02-CP 200018.02 |                      |          |               |             |     |        |
| Precinct TSx           | 1454                 | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1454                 | 1        | 169           | 160         | 122 | 76.25% |
| Precinct VBM           | 1454                 | 1        | 704           | 638         | 466 | 73.04% |
| MB Precinct            | 1454                 | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1454                 | 1        | 13            | 12          | 8   | 66.67% |
| Total                  | 1454                 | 5        | 886           | 810         | 596 | 73.58% |
| 200020.02-CP 200020.02 |                      |          |               |             |     |        |
| Precinct TSx           | 1213                 | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1213                 | 1        | 139           | 127         | 92  | 72.44% |
| Precinct VBM           | 1213                 | 1        | 526           | 477         | 369 | 77.36% |
| MB Precinct            | 1213                 | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1213                 | 1        | 19            | 16          | 12  | 75.00% |
| Total                  | 1213                 | 5        | 684           | 620         | 473 | 76.29% |
| 200021.01-CP 200021.01 |                      |          |               |             |     |        |
| Precinct TSx           | 436                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 436                  | 1        | 38            | 31          | 25  | 80.65% |
| Precinct VBM           | 436                  | 1        | 206           | 184         | 126 | 68.48% |
| MB Precinct            | 436                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 436                  | 1        | 7             | 6           | 4   | 66.67% |
| Total                  | 436                  | 5        | 251           | 221         | 155 | 70.14% |
| 200021.02-CP 200021.02 |                      |          |               |             |     |        |
| Precinct TSx           | 1296                 | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1296                 | 1        | 170           | 160         | 110 | 68.75% |
| Precinct VBM           | 1296                 | 1        | 752           | 645         | 490 | 75.97% |
| MB Precinct            | 1296                 | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1296                 | 1        | 20            | 16          | 14  | 87.50% |
| Total                  | 1296                 | 5        | 942           | 821         | 614 | 74.79% |
| 200024.02-CP 200024.02 |                      |          |               |             |     |        |
| Precinct TSx           | 555                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 555                  | 1        | 55            | 51          | 38  | 74.51% |
| Precinct VBM           | 555                  | 1        | 282           | 262         | 200 | 76.34% |
| MB Precinct            | 555                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 555                  | 1        | 9             | 7           | 6   | 85.71% |
| Total                  | 555                  | 5        | 346           | 320         | 244 | 76.25% |
| 300001.04-CP 300001.04 |                      |          |               |             |     |        |
| Precinct TSx           | 259                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 259                  | 1        | 12            | 12          | 8   | 66.67% |
| Precinct VBM           | 259                  | 1        | 100           | 92          | 69  | 75.00% |
| MB Precinct            | 259                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 259                  | 1        | 0             | 0           | 0   | -      |
| Total                  | 259                  | 5        | 112           | 104         | 77  | 74.04% |
| 300001.05-CP 300001.05 |                      |          |               |             |     |        |
| Precinct TSx           | 731                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 731                  | 1        | 76            | 69          | 55  | 79.71% |
| Precinct VBM           | 731                  | 1        | 330           | 294         | 227 | 77.21% |
| MB Precinct            | 731                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 731                  | 1        | 10            | 10          | 8   | 80.00% |
| Total                  | 731                  | 5        | 416           | 373         | 290 | 77.75% |
| 300003.05-CP 300003.05 |                      |          |               |             |     |        |
| Precinct TSx           | 1932                 | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1932                 | 1        | 205           | 182         | 129 | 70.88% |
| Precinct VBM           | 1932                 | 1        | 924           | 823         | 663 | 80.56% |
| MB Precinct            | 1932                 | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1932                 | 1        | 27            | 21          | 17  | 80.95% |
| Total                  | 1932                 | 5        | 1156          | 1026        | 809 | 78.85% |
| 300004.05-CP 300004.05 |                      |          |               |             |     |        |
| Precinct TSx           | 1273                 | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1273                 | 1        | 175           | 144         | 108 | 75.00% |
| Precinct VBM           | 1273                 | 1        | 576           | 519         | 392 | 75.53% |
| MB Precinct            | 1273                 | 1        | 0             | 0           | 0   | -      |

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|                        | Reg. Voters          | Vote For | Times Counted | Total Votes | YES |         |
| Provisional            | 1273                 | 1        | 17            | 13          | 11  | 84.62%  |
| Total                  | 1273                 | 5        | 768           | 676         | 511 | 75.59%  |
| 300005.05-CP 300005.05 |                      |          |               |             |     |         |
| Precinct TSx           | 1374                 | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1374                 | 1        | 163           | 144         | 118 | 81.94%  |
| Precinct VBM           | 1374                 | 1        | 660           | 595         | 460 | 77.31%  |
| MB Precinct            | 1374                 | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1374                 | 1        | 26            | 18          | 10  | 55.56%  |
| Total                  | 1374                 | 5        | 849           | 757         | 588 | 77.68%  |
| 300008.05-CP 300008.05 |                      |          |               |             |     |         |
| Precinct TSx           | 1366                 | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1366                 | 1        | 139           | 128         | 81  | 63.28%  |
| Precinct VBM           | 1366                 | 1        | 740           | 698         | 514 | 73.64%  |
| MB Precinct            | 1366                 | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1366                 | 1        | 14            | 14          | 11  | 78.57%  |
| Total                  | 1366                 | 5        | 893           | 840         | 606 | 72.14%  |
| 300009.04-CP 300009.04 |                      |          |               |             |     |         |
| Precinct TSx           | 873                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 873                  | 1        | 121           | 105         | 76  | 72.38%  |
| Precinct VBM           | 873                  | 1        | 395           | 362         | 271 | 74.86%  |
| MB Precinct            | 873                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 873                  | 1        | 13            | 10          | 9   | 90.00%  |
| Total                  | 873                  | 5        | 529           | 477         | 356 | 74.63%  |
| 300010.04-CP 300010.04 |                      |          |               |             |     |         |
| Precinct TSx           | 1340                 | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1340                 | 1        | 171           | 158         | 118 | 74.68%  |
| Precinct VBM           | 1340                 | 1        | 656           | 589         | 462 | 78.44%  |
| MB Precinct            | 1340                 | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1340                 | 1        | 11            | 10          | 10  | 100.00% |
| Total                  | 1340                 | 5        | 838           | 757         | 590 | 77.94%  |
| 300011.04-CP 300011.04 |                      |          |               |             |     |         |
| Precinct TSx           | 1491                 | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1491                 | 1        | 142           | 134         | 97  | 72.39%  |
| Precinct VBM           | 1491                 | 1        | 711           | 642         | 477 | 74.30%  |
| MB Precinct            | 1491                 | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1491                 | 1        | 22            | 17          | 12  | 70.59%  |
| Total                  | 1491                 | 5        | 875           | 793         | 586 | 73.90%  |
| 300013.05-CP 300013.05 |                      |          |               |             |     |         |
| Precinct TSx           | 1490                 | 1        | 3             | 2           | 2   | 100.00% |
| Precinct OS            | 1490                 | 1        | 128           | 122         | 86  | 70.49%  |
| Precinct VBM           | 1490                 | 1        | 721           | 661         | 496 | 75.04%  |
| MB Precinct            | 1490                 | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1490                 | 1        | 15            | 13          | 10  | 76.92%  |
| Total                  | 1490                 | 5        | 867           | 798         | 594 | 74.44%  |
| 300015.04-CP 300015.04 |                      |          |               |             |     |         |
| Precinct TSx           | 119                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 119                  | 1        | 10            | 10          | 6   | 60.00%  |
| Precinct VBM           | 119                  | 1        | 63            | 53          | 41  | 77.36%  |
| MB Precinct            | 119                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 119                  | 1        | 4             | 3           | 3   | 100.00% |
| Total                  | 119                  | 5        | 77            | 66          | 50  | 75.76%  |
| 300015.05-CP 300015.05 |                      |          |               |             |     |         |
| Precinct TSx           | 503                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 503                  | 1        | 66            | 63          | 46  | 73.02%  |
| Precinct VBM           | 503                  | 1        | 266           | 231         | 185 | 80.09%  |
| MB Precinct            | 503                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 503                  | 1        | 2             | 1           | 1   | 100.00% |
| Total                  | 503                  | 5        | 334           | 295         | 232 | 78.64%  |
| 300017.05-CP 300017.05 |                      |          |               |             |     |         |
| Precinct TSx           | 1240                 | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1240                 | 1        | 117           | 106         | 64  | 60.38%  |

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|                        | Reg. Voters          | Vote For | Times Counted | Total Votes | YES |        |
| Precinct VBM           | 1240                 | 1        | 655           | 604         | 443 | 73.34% |
| MB Precinct            | 1240                 | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1240                 | 1        | 9             | 8           | 6   | 75.00% |
| Total                  | 1240                 | 5        | 781           | 718         | 513 | 71.45% |
| 300018.04-CP 300018.04 |                      |          |               |             |     |        |
| Precinct TSx           | 1306                 | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1306                 | 1        | 148           | 130         | 94  | 72.31% |
| Precinct VBM           | 1306                 | 1        | 505           | 458         | 334 | 72.93% |
| MB Precinct            | 1306                 | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1306                 | 1        | 31            | 25          | 22  | 88.00% |
| Total                  | 1306                 | 5        | 684           | 613         | 450 | 73.41% |
| 300020.04-CP 300020.04 |                      |          |               |             |     |        |
| Precinct TSx           | 1401                 | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1401                 | 1        | 168           | 151         | 115 | 76.16% |
| Precinct VBM           | 1401                 | 1        | 632           | 566         | 426 | 75.27% |
| MB Precinct            | 1401                 | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1401                 | 1        | 14            | 10          | 7   | 70.00% |
| Total                  | 1401                 | 5        | 814           | 727         | 548 | 75.38% |
| 300021.03-CP 300021.03 |                      |          |               |             |     |        |
| Precinct TSx           | 1234                 | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1234                 | 1        | 153           | 134         | 108 | 80.60% |
| Precinct VBM           | 1234                 | 1        | 614           | 546         | 416 | 76.19% |
| MB Precinct            | 1234                 | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1234                 | 1        | 13            | 12          | 9   | 75.00% |
| Total                  | 1234                 | 5        | 780           | 692         | 533 | 77.02% |
| 300022.05-CP 300022.05 |                      |          |               |             |     |        |
| Precinct TSx           | 872                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 872                  | 1        | 101           | 88          | 68  | 77.27% |
| Precinct VBM           | 872                  | 1        | 356           | 319         | 244 | 76.49% |
| MB Precinct            | 872                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 872                  | 1        | 12            | 11          | 9   | 81.82% |
| Total                  | 872                  | 5        | 469           | 418         | 321 | 76.79% |
| 300023.03-CP 300023.03 |                      |          |               |             |     |        |
| Precinct TSx           | 1819                 | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1819                 | 1        | 228           | 208         | 166 | 79.81% |
| Precinct VBM           | 1819                 | 1        | 924           | 846         | 661 | 78.13% |
| MB Precinct            | 1819                 | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1819                 | 1        | 26            | 21          | 16  | 76.19% |
| Total                  | 1819                 | 5        | 1178          | 1075        | 843 | 78.42% |
| 300023.05-CP 300023.05 |                      |          |               |             |     |        |
| Precinct TSx           | 386                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 386                  | 1        | 45            | 39          | 34  | 87.18% |
| Precinct VBM           | 386                  | 1        | 193           | 174         | 132 | 75.86% |
| MB Precinct            | 386                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 386                  | 1        | 8             | 7           | 6   | 85.71% |
| Total                  | 386                  | 5        | 246           | 220         | 172 | 78.18% |
| 400001.14-CP 400001.14 |                      |          |               |             |     |        |
| Precinct TSx           | 1957                 | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1957                 | 1        | 178           | 158         | 113 | 71.52% |
| Precinct VBM           | 1957                 | 1        | 992           | 895         | 658 | 73.52% |
| MB Precinct            | 1957                 | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1957                 | 1        | 23            | 21          | 18  | 85.71% |
| Total                  | 1957                 | 5        | 1193          | 1074        | 789 | 73.46% |
| 400002.08-CP 400002.08 |                      |          |               |             |     |        |
| Precinct TSx           | 1508                 | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1508                 | 1        | 200           | 178         | 134 | 75.28% |
| Precinct VBM           | 1508                 | 1        | 730           | 654         | 477 | 72.94% |
| MB Precinct            | 1508                 | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1508                 | 1        | 15            | 15          | 13  | 86.67% |
| Total                  | 1508                 | 5        | 945           | 847         | 624 | 73.67% |
| 400003.08-CP 400003.08 |                      |          |               |             |     |        |

**COUNTY OF EL DORADO**  
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|                        | Reg. Voters          | Vote For | Times Counted | Total Votes | YES |         |
| Precinct TSx           | 1800                 | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1800                 | 1        | 245           | 225         | 166 | 73.78%  |
| Precinct VBM           | 1800                 | 1        | 773           | 701         | 524 | 74.75%  |
| MB Precinct            | 1800                 | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1800                 | 1        | 35            | 27          | 20  | 74.07%  |
| Total                  | 1800                 | 5        | 1053          | 953         | 710 | 74.50%  |
| 400004.14-CP 400004.14 |                      |          |               |             |     |         |
| Precinct TSx           | 1536                 | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1536                 | 1        | 194           | 168         | 121 | 72.02%  |
| Precinct VBM           | 1536                 | 1        | 662           | 597         | 442 | 74.04%  |
| MB Precinct            | 1536                 | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1536                 | 1        | 19            | 17          | 17  | 100.00% |
| Total                  | 1536                 | 5        | 875           | 782         | 580 | 74.17%  |
| 400005.08-CP 400005.08 |                      |          |               |             |     |         |
| Precinct TSx           | 655                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 655                  | 1        | 79            | 68          | 41  | 60.29%  |
| Precinct VBM           | 655                  | 1        | 262           | 234         | 180 | 76.92%  |
| MB Precinct            | 655                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 655                  | 1        | 11            | 11          | 6   | 54.55%  |
| Total                  | 655                  | 5        | 352           | 313         | 227 | 72.52%  |
| 400006.08-CP 400006.08 |                      |          |               |             |     |         |
| Precinct TSx           | 1537                 | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1537                 | 1        | 228           | 210         | 160 | 76.19%  |
| Precinct VBM           | 1537                 | 1        | 722           | 646         | 462 | 71.52%  |
| MB Precinct            | 1537                 | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1537                 | 1        | 19            | 15          | 11  | 73.33%  |
| Total                  | 1537                 | 5        | 969           | 871         | 633 | 72.68%  |
| 400007.08-CP 400007.08 |                      |          |               |             |     |         |
| Precinct TSx           | 1766                 | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1766                 | 1        | 228           | 213         | 147 | 69.01%  |
| Precinct VBM           | 1766                 | 1        | 765           | 706         | 480 | 67.99%  |
| MB Precinct            | 1766                 | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1766                 | 1        | 22            | 19          | 16  | 84.21%  |
| Total                  | 1766                 | 5        | 1015          | 938         | 643 | 68.55%  |
| 400010.08-CP 400010.08 |                      |          |               |             |     |         |
| Precinct TSx           | 1875                 | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1875                 | 1        | 244           | 225         | 163 | 72.44%  |
| Precinct VBM           | 1875                 | 1        | 869           | 797         | 563 | 70.64%  |
| MB Precinct            | 1875                 | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1875                 | 1        | 31            | 23          | 17  | 73.91%  |
| Total                  | 1875                 | 5        | 1144          | 1045        | 743 | 71.10%  |
| 400012.08-CP 400012.08 |                      |          |               |             |     |         |
| Precinct TSx           | 877                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 877                  | 1        | 113           | 106         | 78  | 73.58%  |
| Precinct VBM           | 877                  | 1        | 434           | 382         | 277 | 72.51%  |
| MB Precinct            | 877                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 877                  | 1        | 22            | 17          | 10  | 58.82%  |
| Total                  | 877                  | 5        | 569           | 505         | 365 | 72.28%  |
| 400013.08-CP 400013.08 |                      |          |               |             |     |         |
| Precinct TSx           | 758                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 758                  | 1        | 127           | 113         | 79  | 69.91%  |
| Precinct VBM           | 758                  | 1        | 315           | 287         | 223 | 77.70%  |
| MB Precinct            | 758                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 758                  | 1        | 4             | 4           | 3   | 75.00%  |
| Total                  | 758                  | 5        | 446           | 404         | 305 | 75.50%  |
| 400014.08-CP 400014.08 |                      |          |               |             |     |         |
| Precinct TSx           | 1526                 | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1526                 | 1        | 230           | 205         | 137 | 66.83%  |
| Precinct VBM           | 1526                 | 1        | 736           | 676         | 545 | 80.62%  |
| MB Precinct            | 1526                 | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1526                 | 1        | 36            | 35          | 26  | 74.29%  |

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|------------------------|-------------|----------|---------------|-------------|-----|--------|
| Total                  | 1526        | 5        | 1002          | 916         | 708 | 77.29% |
| 400017.08-CP 400017.08 |             |          |               |             |     |        |
| Precinct TSx           | 1495        | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1495        | 1        | 175           | 160         | 119 | 74.38% |
| Precinct VBM           | 1495        | 1        | 752           | 688         | 501 | 72.82% |
| MB Precinct            | 1495        | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1495        | 1        | 28            | 23          | 20  | 86.96% |
| Total                  | 1495        | 5        | 955           | 871         | 640 | 73.48% |
| 400018.08-CP 400018.08 |             |          |               |             |     |        |
| Precinct TSx           | 597         | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 597         | 1        | 86            | 72          | 49  | 68.06% |
| Precinct VBM           | 597         | 1        | 276           | 252         | 186 | 73.81% |
| MB Precinct            | 597         | 1        | 0             | 0           | 0   | -      |
| Provisional            | 597         | 1        | 7             | 5           | 4   | 80.00% |
| Total                  | 597         | 5        | 369           | 329         | 239 | 72.64% |
| 400020.08-CP 400020.08 |             |          |               |             |     |        |
| Precinct TSx           | 724         | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 724         | 1        | 114           | 105         | 77  | 73.33% |
| Precinct VBM           | 724         | 1        | 343           | 316         | 237 | 75.00% |
| MB Precinct            | 724         | 1        | 0             | 0           | 0   | -      |
| Provisional            | 724         | 1        | 2             | 2           | 1   | 50.00% |
| Total                  | 724         | 5        | 459           | 423         | 315 | 74.47% |
| 400021.07-CP 400021.07 |             |          |               |             |     |        |
| Precinct TSx           | 1332        | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1332        | 1        | 127           | 119         | 89  | 74.79% |
| Precinct VBM           | 1332        | 1        | 767           | 693         | 512 | 73.88% |
| MB Precinct            | 1332        | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1332        | 1        | 18            | 15          | 9   | 60.00% |
| Total                  | 1332        | 5        | 912           | 827         | 610 | 73.76% |
| 500001.10-CP 500001.10 |             |          |               |             |     |        |
| Precinct TSx           | 1717        | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1717        | 1        | 210           | 176         | 128 | 72.73% |
| Precinct VBM           | 1717        | 1        | 617           | 536         | 421 | 78.54% |
| MB Precinct            | 1717        | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1717        | 1        | 19            | 15          | 12  | 80.00% |
| Total                  | 1717        | 5        | 846           | 727         | 561 | 77.17% |
| 500002.10-CP 500002.10 |             |          |               |             |     |        |
| Precinct TSx           | 1381        | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1381        | 1        | 137           | 117         | 92  | 78.63% |
| Precinct VBM           | 1381        | 1        | 412           | 364         | 292 | 80.22% |
| MB Precinct            | 1381        | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1381        | 1        | 31            | 26          | 20  | 76.92% |
| Total                  | 1381        | 5        | 580           | 507         | 404 | 79.68% |
| 500003.10-CP 500003.10 |             |          |               |             |     |        |
| Precinct TSx           | 1065        | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1065        | 1        | 131           | 119         | 99  | 83.19% |
| Precinct VBM           | 1065        | 1        | 346           | 296         | 226 | 76.35% |
| MB Precinct            | 1065        | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1065        | 1        | 11            | 6           | 5   | 83.33% |
| Total                  | 1065        | 5        | 488           | 421         | 330 | 78.38% |
| 500004.10-CP 500004.10 |             |          |               |             |     |        |
| Precinct TSx           | 981         | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 981         | 1        | 122           | 110         | 85  | 77.27% |
| Precinct VBM           | 981         | 1        | 264           | 223         | 164 | 73.54% |
| MB Precinct            | 981         | 1        | 0             | 0           | 0   | -      |
| Provisional            | 981         | 1        | 21            | 18          | 15  | 83.33% |
| Total                  | 981         | 5        | 407           | 351         | 264 | 75.21% |
| 500005.10-CP 500005.10 |             |          |               |             |     |        |
| Precinct TSx           | 946         | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 946         | 1        | 107           | 95          | 71  | 74.74% |
| Precinct VBM           | 946         | 1        | 250           | 213         | 169 | 79.34% |

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|                        | Reg. Voters | Vote For | Times Counted | Total Votes | YES |         |   |
|------------------------|-------------|----------|---------------|-------------|-----|---------|---|
| MB Precinct            | 946         | 1        | 0             | 0           | 0   | 0       | - |
| Provisional            | 946         | 1        | 16            | 13          | 8   | 61.54%  |   |
| Total                  | 946         | 5        | 373           | 321         | 248 | 77.26%  |   |
| 500006.10-CP 500006.10 |             |          |               |             |     |         |   |
| Precinct TSx           | 1661        | 1        | 0             | 0           | 0   | -       |   |
| Precinct OS            | 1661        | 1        | 190           | 163         | 132 | 80.98%  |   |
| Precinct VBM           | 1661        | 1        | 498           | 425         | 326 | 76.71%  |   |
| MB Precinct            | 1661        | 1        | 0             | 0           | 0   | -       |   |
| Provisional            | 1661        | 1        | 58            | 46          | 37  | 80.43%  |   |
| Total                  | 1661        | 5        | 746           | 634         | 495 | 78.08%  |   |
| 500007.10-CP 500007.10 |             |          |               |             |     |         |   |
| Precinct TSx           | 1788        | 1        | 0             | 0           | 0   | -       |   |
| Precinct OS            | 1788        | 1        | 208           | 186         | 128 | 68.82%  |   |
| Precinct VBM           | 1788        | 1        | 688           | 592         | 419 | 70.78%  |   |
| MB Precinct            | 1788        | 1        | 0             | 0           | 0   | -       |   |
| Provisional            | 1788        | 1        | 20            | 17          | 14  | 82.35%  |   |
| Total                  | 1788        | 5        | 916           | 795         | 561 | 70.57%  |   |
| 500009.10-CP 500009.10 |             |          |               |             |     |         |   |
| Precinct TSx           | 1073        | 1        | 0             | 0           | 0   | -       |   |
| Precinct OS            | 1073        | 1        | 121           | 105         | 85  | 80.95%  |   |
| Precinct VBM           | 1073        | 1        | 304           | 254         | 195 | 76.77%  |   |
| MB Precinct            | 1073        | 1        | 0             | 0           | 0   | -       |   |
| Provisional            | 1073        | 1        | 38            | 28          | 24  | 85.71%  |   |
| Total                  | 1073        | 5        | 463           | 387         | 304 | 78.55%  |   |
| 500011.10-CP 500011.10 |             |          |               |             |     |         |   |
| Precinct TSx           | 1635        | 1        | 0             | 0           | 0   | -       |   |
| Precinct OS            | 1635        | 1        | 178           | 156         | 114 | 73.08%  |   |
| Precinct VBM           | 1635        | 1        | 380           | 328         | 262 | 79.88%  |   |
| MB Precinct            | 1635        | 1        | 0             | 0           | 0   | -       |   |
| Provisional            | 1635        | 1        | 40            | 30          | 25  | 83.33%  |   |
| Total                  | 1635        | 5        | 598           | 514         | 401 | 78.02%  |   |
| 500012.10-CP 500012.10 |             |          |               |             |     |         |   |
| Precinct TSx           | 1574        | 1        | 1             | 1           | 1   | 100.00% |   |
| Precinct OS            | 1574        | 1        | 195           | 164         | 119 | 72.56%  |   |
| Precinct VBM           | 1574        | 1        | 601           | 536         | 400 | 74.63%  |   |
| MB Precinct            | 1574        | 1        | 0             | 0           | 0   | -       |   |
| Provisional            | 1574        | 1        | 37            | 28          | 23  | 82.14%  |   |
| Total                  | 1574        | 5        | 834           | 729         | 543 | 74.49%  |   |
| 500015.10-CP 500015.10 |             |          |               |             |     |         |   |
| Precinct TSx           | 978         | 1        | 0             | 0           | 0   | -       |   |
| Precinct OS            | 978         | 1        | 105           | 96          | 72  | 75.00%  |   |
| Precinct VBM           | 978         | 1        | 407           | 357         | 286 | 80.11%  |   |
| MB Precinct            | 978         | 1        | 0             | 0           | 0   | -       |   |
| Provisional            | 978         | 1        | 13            | 12          | 10  | 83.33%  |   |
| Total                  | 978         | 5        | 525           | 465         | 368 | 79.14%  |   |
| 500020.10-CP 500020.10 |             |          |               |             |     |         |   |
| Precinct TSx           | 1530        | 1        | 0             | 0           | 0   | -       |   |
| Precinct OS            | 1530        | 1        | 161           | 149         | 116 | 77.85%  |   |
| Precinct VBM           | 1530        | 1        | 659           | 617         | 479 | 77.63%  |   |
| MB Precinct            | 1530        | 1        | 0             | 0           | 0   | -       |   |
| Provisional            | 1530        | 1        | 27            | 27          | 22  | 81.48%  |   |
| Total                  | 1530        | 5        | 847           | 793         | 617 | 77.81%  |   |
| 500021.10-CP 500021.10 |             |          |               |             |     |         |   |
| Precinct TSx           | 1404        | 1        | 0             | 0           | 0   | -       |   |
| Precinct OS            | 1404        | 1        | 169           | 157         | 123 | 78.34%  |   |
| Precinct VBM           | 1404        | 1        | 632           | 580         | 430 | 74.14%  |   |
| MB Precinct            | 1404        | 1        | 0             | 0           | 0   | -       |   |
| Provisional            | 1404        | 1        | 28            | 24          | 20  | 83.33%  |   |
| Total                  | 1404        | 5        | 829           | 761         | 573 | 75.30%  |   |
| 95U201.01-MB 95U201.01 |             |          |               |             |     |         |   |
| Precinct TSx           | 8           | 1        | 0             | 0           | 0   | -       |   |

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|------------------------|----------------------|----------|---------------|-------------|-----|---------|
|                        | Reg. Voters          | Vote For | Times Counted | Total Votes | YES |         |
| Precinct OS            | 8                    | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 8                    | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 8                    | 1        | 7             | 7           | 6   | 85.71%  |
| Provisional            | 8                    | 1        | 0             | 0           | 0   | -       |
| Total                  | 8                    | 5        | 7             | 7           | 6   | 85.71%  |
| 95U201.02-MB 95U201.02 |                      |          |               |             |     |         |
| Precinct TSx           | 541                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 541                  | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 541                  | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 541                  | 1        | 358           | 321         | 231 | 71.96%  |
| Provisional            | 541                  | 1        | 1             | 1           | 0   | 0.00%   |
| Total                  | 541                  | 5        | 359           | 322         | 231 | 71.74%  |
| 95U202.02-MB 95U202.02 |                      |          |               |             |     |         |
| Precinct TSx           | 859                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 859                  | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 859                  | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 859                  | 1        | 574           | 517         | 394 | 76.21%  |
| Provisional            | 859                  | 1        | 4             | 3           | 3   | 100.00% |
| Total                  | 859                  | 5        | 578           | 520         | 397 | 76.35%  |
| 95U203.02-MB 95U203.02 |                      |          |               |             |     |         |
| Precinct TSx           | 0                    | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                    | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                    | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                    | 1        | 0             | 0           | 0   | -       |
| Provisional            | 0                    | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                    | 5        | 0             | 0           | 0   | -       |
| 95U204.01-MB 95U204.01 |                      |          |               |             |     |         |
| Precinct TSx           | 895                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 895                  | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 895                  | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 895                  | 1        | 651           | 603         | 439 | 72.80%  |
| Provisional            | 895                  | 1        | 2             | 2           | 2   | 100.00% |
| Total                  | 895                  | 5        | 653           | 605         | 441 | 72.89%  |
| 95U301.05-MB 95U301.05 |                      |          |               |             |     |         |
| Precinct TSx           | 244                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 244                  | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 244                  | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 244                  | 1        | 151           | 141         | 110 | 78.01%  |
| Provisional            | 244                  | 1        | 2             | 1           | 0   | 0.00%   |
| Total                  | 244                  | 5        | 153           | 142         | 110 | 77.46%  |
| 95U302.04-MB 95U302.04 |                      |          |               |             |     |         |
| Precinct TSx           | 0                    | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                    | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                    | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                    | 1        | 1             | 1           | 1   | 100.00% |
| Provisional            | 0                    | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                    | 5        | 1             | 1           | 1   | 100.00% |
| 95U302.05-MB 95U302.05 |                      |          |               |             |     |         |
| Precinct TSx           | 128                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 128                  | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 128                  | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 128                  | 1        | 86            | 80          | 59  | 73.75%  |
| Provisional            | 128                  | 1        | 0             | 0           | 0   | -       |
| Total                  | 128                  | 5        | 86            | 80          | 59  | 73.75%  |
| 95U303.03-MB 95U303.03 |                      |          |               |             |     |         |
| Precinct TSx           | 144                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 144                  | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 144                  | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 144                  | 1        | 84            | 77          | 58  | 75.32%  |
| Provisional            | 144                  | 1        | 0             | 0           | 0   | -       |
| Total                  | 144                  | 5        | 84            | 77          | 58  | 75.32%  |



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|                        | State Proposition 50 |          |               |             |     |         |
|------------------------|----------------------|----------|---------------|-------------|-----|---------|
|                        | Reg. Voters          | Vote For | Times Counted | Total Votes | YES |         |
| 95U303.05-MB 95U303.05 |                      |          |               |             |     |         |
| Precinct TSx           | 159                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 159                  | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 159                  | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 159                  | 1        | 111           | 95          | 69  | 72.63%  |
| Provisional            | 159                  | 1        | 1             | 1           | 0   | 0.00%   |
| Total                  | 159                  | 5        | 112           | 96          | 69  | 71.88%  |
| 95U304.05-MB 95U304.05 |                      |          |               |             |     |         |
| Precinct TSx           | 0                    | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                    | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                    | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                    | 1        | 0             | 0           | 0   | -       |
| Provisional            | 0                    | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                    | 5        | 0             | 0           | 0   | -       |
| 95U305.05-MB 95U305.05 |                      |          |               |             |     |         |
| Precinct TSx           | 7                    | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 7                    | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 7                    | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 7                    | 1        | 7             | 7           | 5   | 71.43%  |
| Provisional            | 7                    | 1        | 0             | 0           | 0   | -       |
| Total                  | 7                    | 5        | 7             | 7           | 5   | 71.43%  |
| 95U401.08-MB 95U401.08 |                      |          |               |             |     |         |
| Precinct TSx           | 39                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 39                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 39                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 39                   | 1        | 16            | 11          | 11  | 100.00% |
| Provisional            | 39                   | 1        | 0             | 0           | 0   | -       |
| Total                  | 39                   | 5        | 16            | 11          | 11  | 100.00% |
| 95U402.07-MB 95U402.07 |                      |          |               |             |     |         |
| Precinct TSx           | 413                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 413                  | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 413                  | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 413                  | 1        | 256           | 232         | 159 | 68.53%  |
| Provisional            | 413                  | 1        | 1             | 1           | 1   | 100.00% |
| Total                  | 413                  | 5        | 257           | 233         | 160 | 68.67%  |
| 95U402.08-MB 95U402.08 |                      |          |               |             |     |         |
| Precinct TSx           | 873                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 873                  | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 873                  | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 873                  | 1        | 566           | 513         | 374 | 72.90%  |
| Provisional            | 873                  | 1        | 3             | 2           | 1   | 50.00%  |
| Total                  | 873                  | 5        | 569           | 515         | 375 | 72.82%  |
| 95U403.08-MB 95U403.08 |                      |          |               |             |     |         |
| Precinct TSx           | 1591                 | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1591                 | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 1591                 | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 1591                 | 1        | 1042          | 937         | 704 | 75.13%  |
| Provisional            | 1591                 | 1        | 13            | 11          | 10  | 90.91%  |
| Total                  | 1591                 | 5        | 1055          | 948         | 714 | 75.32%  |
| 95U404.06-MB 95U404.06 |                      |          |               |             |     |         |
| Precinct TSx           | 0                    | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                    | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                    | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                    | 1        | 0             | 0           | 0   | -       |
| Provisional            | 0                    | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                    | 5        | 0             | 0           | 0   | -       |
| 95U404.08-MB 95U404.08 |                      |          |               |             |     |         |
| Precinct TSx           | 0                    | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                    | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                    | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                    | 1        | 0             | 0           | 0   | -       |

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|                               | Reg. Voters | Vote For | Times Counted | Total Votes | YES   |         |
|-------------------------------|-------------|----------|---------------|-------------|-------|---------|
| Provisional                   | 0           | 1        | 0             | 0           | 0     | -       |
| Total                         | 0           | 5        | 0             | 0           | 0     | -       |
| 95U501.09-MB 95U501.09        |             |          |               |             |       |         |
| Precinct TSx                  | 0           | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 0           | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 0           | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 0           | 1        | 2             | 2           | 2     | 100.00% |
| Provisional                   | 0           | 1        | 0             | 0           | 0     | -       |
| Total                         | 0           | 5        | 2             | 2           | 2     | 100.00% |
| 95U501.10-MB 95U501.10        |             |          |               |             |       |         |
| Precinct TSx                  | 286         | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 286         | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 286         | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 286         | 1        | 172           | 156         | 119   | 76.28%  |
| Provisional                   | 286         | 1        | 2             | 1           | 0     | 0.00%   |
| Total                         | 286         | 5        | 174           | 157         | 119   | 75.80%  |
| 95U598.10-MB 95U598.10        |             |          |               |             |       |         |
| Precinct TSx                  | 568         | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 568         | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 568         | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 568         | 1        | 239           | 199         | 158   | 79.40%  |
| Provisional                   | 568         | 1        | 3             | 1           | 1     | 100.00% |
| Total                         | 568         | 5        | 242           | 200         | 159   | 79.50%  |
| 95U599.10-MB 95U599.10        |             |          |               |             |       |         |
| Precinct TSx                  | 75          | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 75          | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 75          | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 75          | 1        | 38            | 35          | 26    | 74.29%  |
| Provisional                   | 75          | 1        | 0             | 0           | 0     | -       |
| Total                         | 75          | 5        | 38            | 35          | 26    | 74.29%  |
| 96U101.11-MB 96U101.11        |             |          |               |             |       |         |
| Precinct TSx                  | 626         | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 626         | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 626         | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 626         | 1        | 423           | 377         | 287   | 76.13%  |
| Provisional                   | 626         | 1        | 6             | 6           | 6     | 100.00% |
| Total                         | 626         | 5        | 429           | 383         | 293   | 76.50%  |
| 96U401.14-MB 96U401.14        |             |          |               |             |       |         |
| Precinct TSx                  | 566         | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 566         | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 566         | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 566         | 1        | 359           | 313         | 224   | 71.57%  |
| Provisional                   | 566         | 1        | 6             | 6           | 4     | 66.67%  |
| Total                         | 566         | 5        | 365           | 319         | 228   | 71.47%  |
| Total                         |             |          |               |             |       |         |
| Precinct TSx                  | 109479      | 102      | 8             | 7           | 7     | 100.00% |
| Precinct OS                   | 109479      | 102      | 12009         | 10771       | 8011  | 74.38%  |
| Precinct VBM                  | 109479      | 102      | 45535         | 41062       | 30906 | 75.27%  |
| MB Precinct                   | 109479      | 102      | 5143          | 4624        | 3436  | 74.31%  |
| Provisional                   | 109479      | 102      | 1584          | 1320        | 1054  | 79.85%  |
| Total                         | 109479      | 510      | 64279         | 57784       | 43414 | 75.13%  |
| STATE BOARD OF EQUALIZATION   |             |          |               |             |       |         |
| STATE BOARD OF EQUALIZATION 1 |             |          |               |             |       |         |
| Precinct TSx                  | 109479      | 102      | 8             | 7           | 7     | 100.00% |
| Precinct OS                   | 109479      | 102      | 12009         | 10771       | 8011  | 74.38%  |
| Precinct VBM                  | 109479      | 102      | 45535         | 41062       | 30906 | 75.27%  |
| MB Precinct                   | 109479      | 102      | 5143          | 4624        | 3436  | 74.31%  |
| Provisional                   | 109479      | 102      | 1584          | 1320        | 1054  | 79.85%  |
| Total                         | 109479      | 510      | 64279         | 57784       | 43414 | 75.13%  |
| Total                         |             |          |               |             |       |         |

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|                                     | Reg. Voters   | Vote For   | Times Counted | Total Votes  | YES          |               |
|-------------------------------------|---------------|------------|---------------|--------------|--------------|---------------|
| Precinct TSx                        | 109479        | 102        | 8             | 7            | 7            | 100.00%       |
| Precinct OS                         | 109479        | 102        | 12009         | 10771        | 8011         | 74.38%        |
| Precinct VBM                        | 109479        | 102        | 45535         | 41062        | 30906        | 75.27%        |
| MB Precinct                         | 109479        | 102        | 5143          | 4624         | 3436         | 74.31%        |
| Provisional                         | 109479        | 102        | 1584          | 1320         | 1054         | 79.85%        |
| <b>Total</b>                        | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>57784</b> | <b>43414</b> | <b>75.13%</b> |
| <b>UNITED STATES SENATE</b>         |               |            |               |              |              |               |
| <b>UNITED STATES SENATE</b>         |               |            |               |              |              |               |
| Precinct TSx                        | 109479        | 102        | 8             | 7            | 7            | 100.00%       |
| Precinct OS                         | 109479        | 102        | 12009         | 10771        | 8011         | 74.38%        |
| Precinct VBM                        | 109479        | 102        | 45535         | 41062        | 30906        | 75.27%        |
| MB Precinct                         | 109479        | 102        | 5143          | 4624         | 3436         | 74.31%        |
| Provisional                         | 109479        | 102        | 1584          | 1320         | 1054         | 79.85%        |
| <b>Total</b>                        | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>57784</b> | <b>43414</b> | <b>75.13%</b> |
| <b>Total</b>                        |               |            |               |              |              |               |
| Precinct TSx                        | 109479        | 102        | 8             | 7            | 7            | 100.00%       |
| Precinct OS                         | 109479        | 102        | 12009         | 10771        | 8011         | 74.38%        |
| Precinct VBM                        | 109479        | 102        | 45535         | 41062        | 30906        | 75.27%        |
| MB Precinct                         | 109479        | 102        | 5143          | 4624         | 3436         | 74.31%        |
| Provisional                         | 109479        | 102        | 1584          | 1320         | 1054         | 79.85%        |
| <b>Total</b>                        | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>57784</b> | <b>43414</b> | <b>75.13%</b> |
| <b>Total</b>                        |               |            |               |              |              |               |
| <b>UNITED STATES REPRESENTATIVE</b> |               |            |               |              |              |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |               |            |               |              |              |               |
| Precinct TSx                        | 109479        | 102        | 8             | 7            | 7            | 100.00%       |
| Precinct OS                         | 109479        | 102        | 12009         | 10771        | 8011         | 74.38%        |
| Precinct VBM                        | 109479        | 102        | 45535         | 41062        | 30906        | 75.27%        |
| MB Precinct                         | 109479        | 102        | 5143          | 4624         | 3436         | 74.31%        |
| Provisional                         | 109479        | 102        | 1584          | 1320         | 1054         | 79.85%        |
| <b>Total</b>                        | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>57784</b> | <b>43414</b> | <b>75.13%</b> |
| <b>Total</b>                        |               |            |               |              |              |               |
| Precinct TSx                        | 109479        | 102        | 8             | 7            | 7            | 100.00%       |
| Precinct OS                         | 109479        | 102        | 12009         | 10771        | 8011         | 74.38%        |
| Precinct VBM                        | 109479        | 102        | 45535         | 41062        | 30906        | 75.27%        |
| MB Precinct                         | 109479        | 102        | 5143          | 4624         | 3436         | 74.31%        |
| Provisional                         | 109479        | 102        | 1584          | 1320         | 1054         | 79.85%        |
| <b>Total</b>                        | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>57784</b> | <b>43414</b> | <b>75.13%</b> |
| <b>Total</b>                        |               |            |               |              |              |               |
| <b>STATE SENATE</b>                 |               |            |               |              |              |               |
| <b>1ST STATE SENATE DISTRICT</b>    |               |            |               |              |              |               |
| Precinct TSx                        | 109479        | 102        | 8             | 7            | 7            | 100.00%       |
| Precinct OS                         | 109479        | 102        | 12009         | 10771        | 8011         | 74.38%        |
| Precinct VBM                        | 109479        | 102        | 45535         | 41062        | 30906        | 75.27%        |
| MB Precinct                         | 109479        | 102        | 5143          | 4624         | 3436         | 74.31%        |
| Provisional                         | 109479        | 102        | 1584          | 1320         | 1054         | 79.85%        |
| <b>Total</b>                        | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>57784</b> | <b>43414</b> | <b>75.13%</b> |
| <b>Total</b>                        |               |            |               |              |              |               |
| Precinct TSx                        | 109479        | 102        | 8             | 7            | 7            | 100.00%       |
| Precinct OS                         | 109479        | 102        | 12009         | 10771        | 8011         | 74.38%        |
| Precinct VBM                        | 109479        | 102        | 45535         | 41062        | 30906        | 75.27%        |
| MB Precinct                         | 109479        | 102        | 5143          | 4624         | 3436         | 74.31%        |
| Provisional                         | 109479        | 102        | 1584          | 1320         | 1054         | 79.85%        |
| <b>Total</b>                        | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>57784</b> | <b>43414</b> | <b>75.13%</b> |
| <b>Total</b>                        |               |            |               |              |              |               |
| <b>STATE ASSEMBLY DISTRICTS</b>     |               |            |               |              |              |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |               |            |               |              |              |               |
| Precinct TSx                        | 70961         | 75         | 4             | 3            | 3            | 100.00%       |
| Precinct OS                         | 70961         | 75         | 7627          | 6866         | 5085         | 74.06%        |
| Precinct VBM                        | 70961         | 75         | 28225         | 25384        | 19153        | 75.45%        |
| MB Precinct                         | 70961         | 75         | 4361          | 3934         | 2925         | 74.35%        |

**COUNTY OF EL DORADO**  
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|                                   | Reg. Voters | Vote For | Times Counted | Total Votes | YES   |         |
|-----------------------------------|-------------|----------|---------------|-------------|-------|---------|
| Provisional                       | 70961       | 75       | 1038          | 855         | 670   | 78.36%  |
| Total                             | 70961       | 375      | 41255         | 37042       | 27836 | 75.15%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |             |          |               |             |       |         |
| Precinct TSx                      | 38518       | 27       | 4             | 4           | 4     | 100.00% |
| Precinct OS                       | 38518       | 27       | 4382          | 3905        | 2926  | 74.93%  |
| Precinct VBM                      | 38518       | 27       | 17310         | 15678       | 11753 | 74.96%  |
| MB Precinct                       | 38518       | 27       | 782           | 690         | 511   | 74.06%  |
| Provisional                       | 38518       | 27       | 546           | 465         | 384   | 82.58%  |
| Total                             | 38518       | 135      | 23024         | 20742       | 15578 | 75.10%  |
| <b>Total</b>                      |             |          |               |             |       |         |
| Precinct TSx                      | 109479      | 102      | 8             | 7           | 7     | 100.00% |
| Precinct OS                       | 109479      | 102      | 12009         | 10771       | 8011  | 74.38%  |
| Precinct VBM                      | 109479      | 102      | 45535         | 41062       | 30906 | 75.27%  |
| MB Precinct                       | 109479      | 102      | 5143          | 4624        | 3436  | 74.31%  |
| Provisional                       | 109479      | 102      | 1584          | 1320        | 1054  | 79.85%  |
| Total                             | 109479      | 510      | 64279         | 57784       | 43414 | 75.13%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |             |          |               |             |       |         |
| <b>1ST SUPERVISORIAL DISTRICT</b> |             |          |               |             |       |         |
| Precinct TSx                      | 22587       | 14       | 0             | 0           | 0     | -       |
| Precinct OS                       | 22587       | 14       | 2530          | 2247        | 1675  | 74.54%  |
| Precinct VBM                      | 22587       | 14       | 10411         | 9428        | 7111  | 75.42%  |
| MB Precinct                       | 22587       | 14       | 423           | 377         | 287   | 76.13%  |
| Provisional                       | 22587       | 14       | 301           | 250         | 206   | 82.40%  |
| Total                             | 22587       | 70       | 13665         | 12302       | 9279  | 75.43%  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |             |          |               |             |       |         |
| Precinct TSx                      | 23114       | 23       | 4             | 4           | 4     | 100.00% |
| Precinct OS                       | 23114       | 23       | 2509          | 2279        | 1722  | 75.56%  |
| Precinct VBM                      | 23114       | 23       | 9647          | 8717        | 6546  | 75.09%  |
| MB Precinct                       | 23114       | 23       | 1590          | 1448        | 1070  | 73.90%  |
| Provisional                       | 23114       | 23       | 327           | 283         | 228   | 80.57%  |
| Total                             | 23114       | 115      | 14077         | 12731       | 9570  | 75.17%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |             |          |               |             |       |         |
| Precinct TSx                      | 21691       | 26       | 3             | 2           | 2     | 100.00% |
| Precinct OS                       | 21691       | 26       | 2368          | 2127        | 1577  | 74.14%  |
| Precinct VBM                      | 21691       | 26       | 10021         | 9072        | 6913  | 76.20%  |
| MB Precinct                       | 21691       | 26       | 440           | 401         | 302   | 75.31%  |
| Provisional                       | 21691       | 26       | 277           | 226         | 177   | 78.32%  |
| Total                             | 21691       | 130      | 13109         | 11828       | 8971  | 75.85%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |       |         |
| Precinct TSx                      | 23425       | 22       | 0             | 0           | 0     | -       |
| Precinct OS                       | 23425       | 22       | 2568          | 2325        | 1673  | 71.96%  |
| Precinct VBM                      | 23425       | 22       | 9398          | 8524        | 6267  | 73.52%  |
| MB Precinct                       | 23425       | 22       | 2239          | 2006        | 1472  | 73.38%  |
| Provisional                       | 23425       | 22       | 315           | 269         | 207   | 76.95%  |
| Total                             | 23425       | 110      | 14520         | 13124       | 9619  | 73.29%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |       |         |
| Precinct TSx                      | 18662       | 17       | 1             | 1           | 1     | 100.00% |
| Precinct OS                       | 18662       | 17       | 2034          | 1793        | 1364  | 76.07%  |
| Precinct VBM                      | 18662       | 17       | 6058          | 5321        | 4069  | 76.47%  |
| MB Precinct                       | 18662       | 17       | 451           | 392         | 305   | 77.81%  |
| Provisional                       | 18662       | 17       | 364           | 292         | 236   | 80.82%  |
| Total                             | 18662       | 85       | 8908          | 7799        | 5975  | 76.61%  |
| <b>Total</b>                      |             |          |               |             |       |         |
| Precinct TSx                      | 109479      | 102      | 8             | 7           | 7     | 100.00% |
| Precinct OS                       | 109479      | 102      | 12009         | 10771       | 8011  | 74.38%  |
| Precinct VBM                      | 109479      | 102      | 45535         | 41062       | 30906 | 75.27%  |
| MB Precinct                       | 109479      | 102      | 5143          | 4624        | 3436  | 74.31%  |
| Provisional                       | 109479      | 102      | 1584          | 1320        | 1054  | 79.85%  |
| Total                             | 109479      | 510      | 64279         | 57784       | 43414 | 75.13%  |

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|                                 | Reg. Voters | Vote For | Times Counted | Total Votes | YES   |         |  |
|---------------------------------|-------------|----------|---------------|-------------|-------|---------|--|
| <b>CITIES</b>                   |             |          |               |             |       |         |  |
| <b>CITY OF PLACERVILLE</b>      |             |          |               |             |       |         |  |
| Precinct TSx                    | 5569        | 5        | 0             | 0           | 0     | -       |  |
| Precinct OS                     | 5569        | 5        | 631           | 551         | 418   | 75.86%  |  |
| Precinct VBM                    | 5569        | 5        | 2590          | 2323        | 1811  | 77.96%  |  |
| MB Precinct                     | 5569        | 5        | 0             | 0           | 0     | -       |  |
| Provisional                     | 5569        | 5        | 80            | 62          | 46    | 74.19%  |  |
| Total                           | 5569        | 25       | 3301          | 2936        | 2275  | 77.49%  |  |
| <b>CITY OF SOUTH LAKE TAHOE</b> |             |          |               |             |       |         |  |
| Precinct TSx                    | 9465        | 7        | 0             | 0           | 0     | -       |  |
| Precinct OS                     | 9465        | 7        | 1063          | 932         | 707   | 75.86%  |  |
| Precinct VBM                    | 9465        | 7        | 2796          | 2399        | 1827  | 76.16%  |  |
| MB Precinct                     | 9465        | 7        | 0             | 0           | 0     | -       |  |
| Provisional                     | 9465        | 7        | 224           | 178         | 143   | 80.34%  |  |
| Total                           | 9465        | 35       | 4083          | 3509        | 2677  | 76.29%  |  |
| <b>UNINCORPORATED AREA</b>      |             |          |               |             |       |         |  |
| Precinct TSx                    | 94445       | 90       | 8             | 7           | 7     | 100.00% |  |
| Precinct OS                     | 94445       | 90       | 10315         | 9288        | 6886  | 74.14%  |  |
| Precinct VBM                    | 94445       | 90       | 40149         | 36340       | 27268 | 75.04%  |  |
| MB Precinct                     | 94445       | 90       | 5143          | 4624        | 3436  | 74.31%  |  |
| Provisional                     | 94445       | 90       | 1280          | 1080        | 865   | 80.09%  |  |
| Total                           | 94445       | 450      | 56895         | 51339       | 38462 | 74.92%  |  |
| <b>Total</b>                    |             |          |               |             |       |         |  |
| Precinct TSx                    | 109479      | 102      | 8             | 7           | 7     | 100.00% |  |
| Precinct OS                     | 109479      | 102      | 12009         | 10771       | 8011  | 74.38%  |  |
| Precinct VBM                    | 109479      | 102      | 45535         | 41062       | 30906 | 75.27%  |  |
| MB Precinct                     | 109479      | 102      | 5143          | 4624        | 3436  | 74.31%  |  |
| Provisional                     | 109479      | 102      | 1584          | 1320        | 1054  | 79.85%  |  |
| Total                           | 109479      | 510      | 64279         | 57784       | 43414 | 75.13%  |  |

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|                        | NO                   |        |
| Jurisdiction Wide      |                      |        |
| 100001.11-CP 100001.11 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 38                   | 23.31% |
| Precinct VBM           | 179                  | 25.18% |
| MB Precinct            | 0                    | -      |
| Provisional            | 3                    | 23.08% |
| Total                  | 220                  | 24.80% |
| 100002.11-CP 100002.11 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 18                   | 22.78% |
| Precinct VBM           | 117                  | 27.02% |
| MB Precinct            | 0                    | -      |
| Provisional            | 4                    | 22.22% |
| Total                  | 139                  | 26.23% |
| 100003.11-CP 100003.11 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 33                   | 29.20% |
| Precinct VBM           | 157                  | 22.43% |
| MB Precinct            | 0                    | -      |
| Provisional            | 3                    | 14.29% |
| Total                  | 193                  | 23.14% |
| 100004.11-CP 100004.11 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 53                   | 22.84% |
| Precinct VBM           | 147                  | 22.11% |
| MB Precinct            | 0                    | -      |
| Provisional            | 0                    | 0.00%  |
| Total                  | 200                  | 22.25% |
| 100005.11-CP 100005.11 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 38                   | 28.79% |
| Precinct VBM           | 258                  | 31.27% |
| MB Precinct            | 0                    | -      |
| Provisional            | 7                    | 33.33% |
| Total                  | 303                  | 30.98% |
| 100006.11-CP 100006.11 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 64                   | 28.83% |
| Precinct VBM           | 195                  | 23.02% |
| MB Precinct            | 0                    | -      |
| Provisional            | 5                    | 22.73% |
| Total                  | 264                  | 24.20% |
| 100008.11-CP 100008.11 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 33                   | 20.00% |
| Precinct VBM           | 160                  | 23.74% |
| MB Precinct            | 0                    | -      |
| Provisional            | 0                    | 0.00%  |
| Total                  | 193                  | 22.65% |
| 100009.11-CP 100009.11 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 32                   | 21.19% |
| Precinct VBM           | 131                  | 19.49% |
| MB Precinct            | 0                    | -      |
| Provisional            | 3                    | 16.67% |
| Total                  | 166                  | 19.74% |
| 100012.11-CP 100012.11 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 50                   | 21.74% |
| Precinct VBM           | 199                  | 23.27% |

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| MB Precinct            | 0                    | -      |
| Provisional            | 5                    | 17.86% |
| Total                  | 254                  | 22.82% |
| 100013.11-CP 100013.11 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 56                   | 24.78% |
| Precinct VBM           | 214                  | 25.78% |
| MB Precinct            | 0                    | -      |
| Provisional            | 2                    | 7.69%  |
| Total                  | 272                  | 25.14% |
| 100014.11-CP 100014.11 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 54                   | 29.19% |
| Precinct VBM           | 159                  | 24.92% |
| MB Precinct            | 0                    | -      |
| Provisional            | 6                    | 25.00% |
| Total                  | 219                  | 25.86% |
| 100022.11-CP 100022.11 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 51                   | 31.48% |
| Precinct VBM           | 155                  | 25.62% |
| MB Precinct            | 0                    | -      |
| Provisional            | 1                    | 9.09%  |
| Total                  | 207                  | 26.61% |
| 100023.11-CP 100023.11 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 52                   | 27.81% |
| Precinct VBM           | 246                  | 25.28% |
| MB Precinct            | 0                    | -      |
| Provisional            | 5                    | 18.52% |
| Total                  | 303                  | 25.53% |
| 200003.12-CP 200003.12 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 32                   | 21.92% |
| Precinct VBM           | 105                  | 21.04% |
| MB Precinct            | 0                    | -      |
| Provisional            | 7                    | 26.92% |
| Total                  | 144                  | 21.46% |
| 200004.12-CP 200004.12 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 37                   | 22.16% |
| Precinct VBM           | 147                  | 24.42% |
| MB Precinct            | 0                    | -      |
| Provisional            | 1                    | 8.33%  |
| Total                  | 185                  | 23.69% |
| 200005.02-CP 200005.02 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 22                   | 24.18% |
| Precinct VBM           | 103                  | 24.88% |
| MB Precinct            | 0                    | -      |
| Provisional            | 3                    | 15.79% |
| Total                  | 128                  | 24.43% |
| 200006.12-CP 200006.12 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 29                   | 26.61% |
| Precinct VBM           | 163                  | 32.73% |
| MB Precinct            | 0                    | -      |
| Provisional            | 4                    | 14.29% |
| Total                  | 196                  | 30.87% |
| 200008.12-CP 200008.12 |                      |        |
| Precinct TSx           | 0                    | -      |

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| Precinct OS            | 33                   | 25.00% |
| Precinct VBM           | 81                   | 24.04% |
| MB Precinct            | 0                    | -      |
| Provisional            | 2                    | 22.22% |
| Total                  | 116                  | 24.27% |
| 200010.12-CP 200010.12 |                      |        |
| Precinct TSx           | 0                    | 0.00%  |
| Precinct OS            | 39                   | 22.41% |
| Precinct VBM           | 167                  | 21.27% |
| MB Precinct            | 0                    | -      |
| Provisional            | 2                    | 6.90%  |
| Total                  | 208                  | 20.97% |
| 200012.12-CP 200012.12 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 24                   | 18.18% |
| Precinct VBM           | 111                  | 26.62% |
| MB Precinct            | 0                    | -      |
| Provisional            | 4                    | 36.36% |
| Total                  | 139                  | 24.82% |
| 200013.12-CP 200013.12 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 42                   | 21.76% |
| Precinct VBM           | 165                  | 25.74% |
| MB Precinct            | 0                    | -      |
| Provisional            | 3                    | 15.00% |
| Total                  | 210                  | 24.59% |
| 200014.12-CP 200014.12 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 47                   | 25.82% |
| Precinct VBM           | 137                  | 25.14% |
| MB Precinct            | 0                    | -      |
| Provisional            | 3                    | 11.54% |
| Total                  | 187                  | 24.83% |
| 200015.02-CP 200015.02 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 49                   | 25.00% |
| Precinct VBM           | 165                  | 20.47% |
| MB Precinct            | 0                    | -      |
| Provisional            | 4                    | 21.05% |
| Total                  | 218                  | 21.35% |
| 200016.12-CP 200016.12 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 4                    | 16.67% |
| Precinct VBM           | 41                   | 31.30% |
| MB Precinct            | 0                    | -      |
| Provisional            | 2                    | 40.00% |
| Total                  | 47                   | 29.38% |
| 200016.13-CP 200016.13 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 28                   | 38.36% |
| Precinct VBM           | 99                   | 32.67% |
| MB Precinct            | 0                    | -      |
| Provisional            | 4                    | 80.00% |
| Total                  | 131                  | 34.38% |
| 200017.02-CP 200017.02 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 29                   | 22.14% |
| Precinct VBM           | 132                  | 24.77% |
| MB Precinct            | 0                    | -      |
| Provisional            | 2                    | 18.18% |
| Total                  | 163                  | 24.15% |



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| 200018.02-CP 200018.02 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 38                   | 23.75% |
| Precinct VBM           | 172                  | 26.96% |
| MB Precinct            | 0                    | -      |
| Provisional            | 4                    | 33.33% |
| Total                  | 214                  | 26.42% |
| 200020.02-CP 200020.02 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 35                   | 27.56% |
| Precinct VBM           | 108                  | 22.64% |
| MB Precinct            | 0                    | -      |
| Provisional            | 4                    | 25.00% |
| Total                  | 147                  | 23.71% |
| 200021.01-CP 200021.01 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 6                    | 19.35% |
| Precinct VBM           | 58                   | 31.52% |
| MB Precinct            | 0                    | -      |
| Provisional            | 2                    | 33.33% |
| Total                  | 66                   | 29.86% |
| 200021.02-CP 200021.02 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 50                   | 31.25% |
| Precinct VBM           | 155                  | 24.03% |
| MB Precinct            | 0                    | -      |
| Provisional            | 2                    | 12.50% |
| Total                  | 207                  | 25.21% |
| 200024.02-CP 200024.02 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 13                   | 25.49% |
| Precinct VBM           | 62                   | 23.66% |
| MB Precinct            | 0                    | -      |
| Provisional            | 1                    | 14.29% |
| Total                  | 76                   | 23.75% |
| 300001.04-CP 300001.04 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 4                    | 33.33% |
| Precinct VBM           | 23                   | 25.00% |
| MB Precinct            | 0                    | -      |
| Provisional            | 0                    | -      |
| Total                  | 27                   | 25.96% |
| 300001.05-CP 300001.05 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 14                   | 20.29% |
| Precinct VBM           | 67                   | 22.79% |
| MB Precinct            | 0                    | -      |
| Provisional            | 2                    | 20.00% |
| Total                  | 83                   | 22.25% |
| 300003.05-CP 300003.05 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 53                   | 29.12% |
| Precinct VBM           | 160                  | 19.44% |
| MB Precinct            | 0                    | -      |
| Provisional            | 4                    | 19.05% |
| Total                  | 217                  | 21.15% |
| 300004.05-CP 300004.05 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 36                   | 25.00% |
| Precinct VBM           | 127                  | 24.47% |
| MB Precinct            | 0                    | -      |

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| Provisional            | 2                    | 15.38% |
| Total                  | 165                  | 24.41% |
| 300005.05-CP 300005.05 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 26                   | 18.06% |
| Precinct VBM           | 135                  | 22.69% |
| MB Precinct            | 0                    | -      |
| Provisional            | 8                    | 44.44% |
| Total                  | 169                  | 22.32% |
| 300008.05-CP 300008.05 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 47                   | 36.72% |
| Precinct VBM           | 184                  | 26.36% |
| MB Precinct            | 0                    | -      |
| Provisional            | 3                    | 21.43% |
| Total                  | 234                  | 27.86% |
| 300009.04-CP 300009.04 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 29                   | 27.62% |
| Precinct VBM           | 91                   | 25.14% |
| MB Precinct            | 0                    | -      |
| Provisional            | 1                    | 10.00% |
| Total                  | 121                  | 25.37% |
| 300010.04-CP 300010.04 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 40                   | 25.32% |
| Precinct VBM           | 127                  | 21.56% |
| MB Precinct            | 0                    | -      |
| Provisional            | 0                    | 0.00%  |
| Total                  | 167                  | 22.06% |
| 300011.04-CP 300011.04 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 37                   | 27.61% |
| Precinct VBM           | 165                  | 25.70% |
| MB Precinct            | 0                    | -      |
| Provisional            | 5                    | 29.41% |
| Total                  | 207                  | 26.10% |
| 300013.05-CP 300013.05 |                      |        |
| Precinct TSx           | 0                    | 0.00%  |
| Precinct OS            | 36                   | 29.51% |
| Precinct VBM           | 165                  | 24.96% |
| MB Precinct            | 0                    | -      |
| Provisional            | 3                    | 23.08% |
| Total                  | 204                  | 25.56% |
| 300015.04-CP 300015.04 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 4                    | 40.00% |
| Precinct VBM           | 12                   | 22.64% |
| MB Precinct            | 0                    | -      |
| Provisional            | 0                    | 0.00%  |
| Total                  | 16                   | 24.24% |
| 300015.05-CP 300015.05 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 17                   | 26.98% |
| Precinct VBM           | 46                   | 19.91% |
| MB Precinct            | 0                    | -      |
| Provisional            | 0                    | 0.00%  |
| Total                  | 63                   | 21.36% |
| 300017.05-CP 300017.05 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 42                   | 39.62% |

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| Precinct VBM           |  | 161                  | 26.66% |
| MB Precinct            |  | 0                    | -      |
| Provisional            |  | 2                    | 25.00% |
| Total                  |  | 205                  | 28.55% |
| 300018.04-CP 300018.04 |  |                      |        |
| Precinct TSx           |  | 0                    | -      |
| Precinct OS            |  | 36                   | 27.69% |
| Precinct VBM           |  | 124                  | 27.07% |
| MB Precinct            |  | 0                    | -      |
| Provisional            |  | 3                    | 12.00% |
| Total                  |  | 163                  | 26.59% |
| 300020.04-CP 300020.04 |  |                      |        |
| Precinct TSx           |  | 0                    | -      |
| Precinct OS            |  | 36                   | 23.84% |
| Precinct VBM           |  | 140                  | 24.73% |
| MB Precinct            |  | 0                    | -      |
| Provisional            |  | 3                    | 30.00% |
| Total                  |  | 179                  | 24.62% |
| 300021.03-CP 300021.03 |  |                      |        |
| Precinct TSx           |  | 0                    | -      |
| Precinct OS            |  | 26                   | 19.40% |
| Precinct VBM           |  | 130                  | 23.81% |
| MB Precinct            |  | 0                    | -      |
| Provisional            |  | 3                    | 25.00% |
| Total                  |  | 159                  | 22.98% |
| 300022.05-CP 300022.05 |  |                      |        |
| Precinct TSx           |  | 0                    | -      |
| Precinct OS            |  | 20                   | 22.73% |
| Precinct VBM           |  | 75                   | 23.51% |
| MB Precinct            |  | 0                    | -      |
| Provisional            |  | 2                    | 18.18% |
| Total                  |  | 97                   | 23.21% |
| 300023.03-CP 300023.03 |  |                      |        |
| Precinct TSx           |  | 0                    | -      |
| Precinct OS            |  | 42                   | 20.19% |
| Precinct VBM           |  | 185                  | 21.87% |
| MB Precinct            |  | 0                    | -      |
| Provisional            |  | 5                    | 23.81% |
| Total                  |  | 232                  | 21.58% |
| 300023.05-CP 300023.05 |  |                      |        |
| Precinct TSx           |  | 0                    | -      |
| Precinct OS            |  | 5                    | 12.82% |
| Precinct VBM           |  | 42                   | 24.14% |
| MB Precinct            |  | 0                    | -      |
| Provisional            |  | 1                    | 14.29% |
| Total                  |  | 48                   | 21.82% |
| 400001.14-CP 400001.14 |  |                      |        |
| Precinct TSx           |  | 0                    | -      |
| Precinct OS            |  | 45                   | 28.48% |
| Precinct VBM           |  | 237                  | 26.48% |
| MB Precinct            |  | 0                    | -      |
| Provisional            |  | 3                    | 14.29% |
| Total                  |  | 285                  | 26.54% |
| 400002.08-CP 400002.08 |  |                      |        |
| Precinct TSx           |  | 0                    | -      |
| Precinct OS            |  | 44                   | 24.72% |
| Precinct VBM           |  | 177                  | 27.06% |
| MB Precinct            |  | 0                    | -      |
| Provisional            |  | 2                    | 13.33% |
| Total                  |  | 223                  | 26.33% |
| 400003.08-CP 400003.08 |  |                      |        |

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| Precinct TSx           | 0                    | -      |
| Precinct OS            | 59                   | 26.22% |
| Precinct VBM           | 177                  | 25.25% |
| MB Precinct            | 0                    | -      |
| Provisional            | 7                    | 25.93% |
| Total                  | 243                  | 25.50% |
| 400004.14-CP 400004.14 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 47                   | 27.98% |
| Precinct VBM           | 155                  | 25.96% |
| MB Precinct            | 0                    | -      |
| Provisional            | 0                    | 0.00%  |
| Total                  | 202                  | 25.83% |
| 400005.08-CP 400005.08 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 27                   | 39.71% |
| Precinct VBM           | 54                   | 23.08% |
| MB Precinct            | 0                    | -      |
| Provisional            | 5                    | 45.45% |
| Total                  | 86                   | 27.48% |
| 400006.08-CP 400006.08 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 50                   | 23.81% |
| Precinct VBM           | 184                  | 28.48% |
| MB Precinct            | 0                    | -      |
| Provisional            | 4                    | 26.67% |
| Total                  | 238                  | 27.32% |
| 400007.08-CP 400007.08 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 66                   | 30.99% |
| Precinct VBM           | 226                  | 32.01% |
| MB Precinct            | 0                    | -      |
| Provisional            | 3                    | 15.79% |
| Total                  | 295                  | 31.45% |
| 400010.08-CP 400010.08 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 62                   | 27.56% |
| Precinct VBM           | 234                  | 29.36% |
| MB Precinct            | 0                    | -      |
| Provisional            | 6                    | 26.09% |
| Total                  | 302                  | 28.90% |
| 400012.08-CP 400012.08 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 28                   | 26.42% |
| Precinct VBM           | 105                  | 27.49% |
| MB Precinct            | 0                    | -      |
| Provisional            | 7                    | 41.18% |
| Total                  | 140                  | 27.72% |
| 400013.08-CP 400013.08 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 34                   | 30.09% |
| Precinct VBM           | 64                   | 22.30% |
| MB Precinct            | 0                    | -      |
| Provisional            | 1                    | 25.00% |
| Total                  | 99                   | 24.50% |
| 400014.08-CP 400014.08 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 68                   | 33.17% |
| Precinct VBM           | 131                  | 19.38% |
| MB Precinct            | 0                    | -      |
| Provisional            | 9                    | 25.71% |

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| Total                  | 208                  | 22.71% |
| 400017.08-CP 400017.08 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 41                   | 25.63% |
| Precinct VBM           | 187                  | 27.18% |
| MB Precinct            | 0                    | -      |
| Provisional            | 3                    | 13.04% |
| Total                  | 231                  | 26.52% |
| 400018.08-CP 400018.08 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 23                   | 31.94% |
| Precinct VBM           | 66                   | 26.19% |
| MB Precinct            | 0                    | -      |
| Provisional            | 1                    | 20.00% |
| Total                  | 90                   | 27.36% |
| 400020.08-CP 400020.08 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 28                   | 26.67% |
| Precinct VBM           | 79                   | 25.00% |
| MB Precinct            | 0                    | -      |
| Provisional            | 1                    | 50.00% |
| Total                  | 108                  | 25.53% |
| 400021.07-CP 400021.07 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 30                   | 25.21% |
| Precinct VBM           | 181                  | 26.12% |
| MB Precinct            | 0                    | -      |
| Provisional            | 6                    | 40.00% |
| Total                  | 217                  | 26.24% |
| 500001.10-CP 500001.10 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 48                   | 27.27% |
| Precinct VBM           | 115                  | 21.46% |
| MB Precinct            | 0                    | -      |
| Provisional            | 3                    | 20.00% |
| Total                  | 166                  | 22.83% |
| 500002.10-CP 500002.10 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 25                   | 21.37% |
| Precinct VBM           | 72                   | 19.78% |
| MB Precinct            | 0                    | -      |
| Provisional            | 6                    | 23.08% |
| Total                  | 103                  | 20.32% |
| 500003.10-CP 500003.10 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 20                   | 16.81% |
| Precinct VBM           | 70                   | 23.65% |
| MB Precinct            | 0                    | -      |
| Provisional            | 1                    | 16.67% |
| Total                  | 91                   | 21.62% |
| 500004.10-CP 500004.10 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 25                   | 22.73% |
| Precinct VBM           | 59                   | 26.46% |
| MB Precinct            | 0                    | -      |
| Provisional            | 3                    | 16.67% |
| Total                  | 87                   | 24.79% |
| 500005.10-CP 500005.10 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 24                   | 25.26% |
| Precinct VBM           | 44                   | 20.66% |

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| MB Precinct            | 0                    | -      |
| Provisional            | 5                    | 38.46% |
| Total                  | 73                   | 22.74% |
| 500006.10-CP 500006.10 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 31                   | 19.02% |
| Precinct VBM           | 99                   | 23.29% |
| MB Precinct            | 0                    | -      |
| Provisional            | 9                    | 19.57% |
| Total                  | 139                  | 21.92% |
| 500007.10-CP 500007.10 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 58                   | 31.18% |
| Precinct VBM           | 173                  | 29.22% |
| MB Precinct            | 0                    | -      |
| Provisional            | 3                    | 17.65% |
| Total                  | 234                  | 29.43% |
| 500009.10-CP 500009.10 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 20                   | 19.05% |
| Precinct VBM           | 59                   | 23.23% |
| MB Precinct            | 0                    | -      |
| Provisional            | 4                    | 14.29% |
| Total                  | 83                   | 21.45% |
| 500011.10-CP 500011.10 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 42                   | 26.92% |
| Precinct VBM           | 66                   | 20.12% |
| MB Precinct            | 0                    | -      |
| Provisional            | 5                    | 16.67% |
| Total                  | 113                  | 21.98% |
| 500012.10-CP 500012.10 |                      |        |
| Precinct TSx           | 0                    | 0.00%  |
| Precinct OS            | 45                   | 27.44% |
| Precinct VBM           | 136                  | 25.37% |
| MB Precinct            | 0                    | -      |
| Provisional            | 5                    | 17.86% |
| Total                  | 186                  | 25.51% |
| 500015.10-CP 500015.10 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 24                   | 25.00% |
| Precinct VBM           | 71                   | 19.89% |
| MB Precinct            | 0                    | -      |
| Provisional            | 2                    | 16.67% |
| Total                  | 97                   | 20.86% |
| 500020.10-CP 500020.10 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 33                   | 22.15% |
| Precinct VBM           | 138                  | 22.37% |
| MB Precinct            | 0                    | -      |
| Provisional            | 5                    | 18.52% |
| Total                  | 176                  | 22.19% |
| 500021.10-CP 500021.10 |                      |        |
| Precinct TSx           | 0                    | -      |
| Precinct OS            | 34                   | 21.66% |
| Precinct VBM           | 150                  | 25.86% |
| MB Precinct            | 0                    | -      |
| Provisional            | 4                    | 16.67% |
| Total                  | 188                  | 24.70% |
| 95U201.01-MB 95U201.01 |                      |        |
| Precinct TSx           | 0                    | -      |

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|                        | NO                   |         |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 1                    | 14.29%  |
| Provisional            | 0                    | -       |
| Total                  | 1                    | 14.29%  |
| 95U201.02-MB 95U201.02 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 90                   | 28.04%  |
| Provisional            | 1                    | 100.00% |
| Total                  | 91                   | 28.26%  |
| 95U202.02-MB 95U202.02 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 123                  | 23.79%  |
| Provisional            | 0                    | 0.00%   |
| Total                  | 123                  | 23.65%  |
| 95U203.02-MB 95U203.02 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 0                    | -       |
| Provisional            | 0                    | -       |
| Total                  | 0                    | -       |
| 95U204.01-MB 95U204.01 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 164                  | 27.20%  |
| Provisional            | 0                    | 0.00%   |
| Total                  | 164                  | 27.11%  |
| 95U301.05-MB 95U301.05 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 31                   | 21.99%  |
| Provisional            | 1                    | 100.00% |
| Total                  | 32                   | 22.54%  |
| 95U302.04-MB 95U302.04 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 0                    | 0.00%   |
| Provisional            | 0                    | -       |
| Total                  | 0                    | 0.00%   |
| 95U302.05-MB 95U302.05 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 21                   | 26.25%  |
| Provisional            | 0                    | -       |
| Total                  | 21                   | 26.25%  |
| 95U303.03-MB 95U303.03 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 19                   | 24.68%  |
| Provisional            | 0                    | -       |
| Total                  | 19                   | 24.68%  |

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| 95U303.05-MB 95U303.05 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 26                   | 27.37%  |
| Provisional            | 1                    | 100.00% |
| Total                  | 27                   | 28.13%  |
| 95U304.05-MB 95U304.05 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 0                    | -       |
| Provisional            | 0                    | -       |
| Total                  | 0                    | -       |
| 95U305.05-MB 95U305.05 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 2                    | 28.57%  |
| Provisional            | 0                    | -       |
| Total                  | 2                    | 28.57%  |
| 95U401.08-MB 95U401.08 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 0                    | 0.00%   |
| Provisional            | 0                    | -       |
| Total                  | 0                    | 0.00%   |
| 95U402.07-MB 95U402.07 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 73                   | 31.47%  |
| Provisional            | 0                    | 0.00%   |
| Total                  | 73                   | 31.33%  |
| 95U402.08-MB 95U402.08 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 139                  | 27.10%  |
| Provisional            | 1                    | 50.00%  |
| Total                  | 140                  | 27.18%  |
| 95U403.08-MB 95U403.08 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 233                  | 24.87%  |
| Provisional            | 1                    | 9.09%   |
| Total                  | 234                  | 24.68%  |
| 95U404.06-MB 95U404.06 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 0                    | -       |
| Provisional            | 0                    | -       |
| Total                  | 0                    | -       |
| 95U404.08-MB 95U404.08 |                      |         |
| Precinct TSx           | 0                    | -       |
| Precinct OS            | 0                    | -       |
| Precinct VBM           | 0                    | -       |
| MB Precinct            | 0                    | -       |



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| Provisional                   | 0                    | -       |
| Total                         | 0                    | -       |
| 95U501.09-MB 95U501.09        |                      |         |
| Precinct TSx                  | 0                    | -       |
| Precinct OS                   | 0                    | -       |
| Precinct VBM                  | 0                    | -       |
| MB Precinct                   | 0                    | 0.00%   |
| Provisional                   | 0                    | -       |
| Total                         | 0                    | 0.00%   |
| 95U501.10-MB 95U501.10        |                      |         |
| Precinct TSx                  | 0                    | -       |
| Precinct OS                   | 0                    | -       |
| Precinct VBM                  | 0                    | -       |
| MB Precinct                   | 37                   | 23.72%  |
| Provisional                   | 1                    | 100.00% |
| Total                         | 38                   | 24.20%  |
| 95U598.10-MB 95U598.10        |                      |         |
| Precinct TSx                  | 0                    | -       |
| Precinct OS                   | 0                    | -       |
| Precinct VBM                  | 0                    | -       |
| MB Precinct                   | 41                   | 20.60%  |
| Provisional                   | 0                    | 0.00%   |
| Total                         | 41                   | 20.50%  |
| 95U599.10-MB 95U599.10        |                      |         |
| Precinct TSx                  | 0                    | -       |
| Precinct OS                   | 0                    | -       |
| Precinct VBM                  | 0                    | -       |
| MB Precinct                   | 9                    | 25.71%  |
| Provisional                   | 0                    | -       |
| Total                         | 9                    | 25.71%  |
| 96U101.11-MB 96U101.11        |                      |         |
| Precinct TSx                  | 0                    | -       |
| Precinct OS                   | 0                    | -       |
| Precinct VBM                  | 0                    | -       |
| MB Precinct                   | 90                   | 23.87%  |
| Provisional                   | 0                    | 0.00%   |
| Total                         | 90                   | 23.50%  |
| 96U401.14-MB 96U401.14        |                      |         |
| Precinct TSx                  | 0                    | -       |
| Precinct OS                   | 0                    | -       |
| Precinct VBM                  | 0                    | -       |
| MB Precinct                   | 89                   | 28.43%  |
| Provisional                   | 2                    | 33.33%  |
| Total                         | 91                   | 28.53%  |
| Total                         |                      |         |
| Precinct TSx                  | 0                    | 0.00%   |
| Precinct OS                   | 2760                 | 25.62%  |
| Precinct VBM                  | 10156                | 24.73%  |
| MB Precinct                   | 1188                 | 25.69%  |
| Provisional                   | 266                  | 20.15%  |
| Total                         | 14370                | 24.87%  |
| STATE BOARD OF EQUALIZATION   |                      |         |
| STATE BOARD OF EQUALIZATION 1 |                      |         |
| Precinct TSx                  | 0                    | 0.00%   |
| Precinct OS                   | 2760                 | 25.62%  |
| Precinct VBM                  | 10156                | 24.73%  |
| MB Precinct                   | 1188                 | 25.69%  |
| Provisional                   | 266                  | 20.15%  |
| Total                         | 14370                | 24.87%  |
| Total                         |                      |         |

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| Precinct TSx                        | 0                    | 0.00%         |
| Precinct OS                         | 2760                 | 25.62%        |
| Precinct VBM                        | 10156                | 24.73%        |
| MB Precinct                         | 1188                 | 25.69%        |
| Provisional                         | 266                  | 20.15%        |
| <b>Total</b>                        | <b>14370</b>         | <b>24.87%</b> |
| <b>UNITED STATES SENATE</b>         |                      |               |
| <b>UNITED STATES SENATE</b>         |                      |               |
| Precinct TSx                        | 0                    | 0.00%         |
| Precinct OS                         | 2760                 | 25.62%        |
| Precinct VBM                        | 10156                | 24.73%        |
| MB Precinct                         | 1188                 | 25.69%        |
| Provisional                         | 266                  | 20.15%        |
| <b>Total</b>                        | <b>14370</b>         | <b>24.87%</b> |
| <b>Total</b>                        |                      |               |
| Precinct TSx                        | 0                    | 0.00%         |
| Precinct OS                         | 2760                 | 25.62%        |
| Precinct VBM                        | 10156                | 24.73%        |
| MB Precinct                         | 1188                 | 25.69%        |
| Provisional                         | 266                  | 20.15%        |
| <b>Total</b>                        | <b>14370</b>         | <b>24.87%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |                      |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                      |               |
| Precinct TSx                        | 0                    | 0.00%         |
| Precinct OS                         | 2760                 | 25.62%        |
| Precinct VBM                        | 10156                | 24.73%        |
| MB Precinct                         | 1188                 | 25.69%        |
| Provisional                         | 266                  | 20.15%        |
| <b>Total</b>                        | <b>14370</b>         | <b>24.87%</b> |
| <b>Total</b>                        |                      |               |
| Precinct TSx                        | 0                    | 0.00%         |
| Precinct OS                         | 2760                 | 25.62%        |
| Precinct VBM                        | 10156                | 24.73%        |
| MB Precinct                         | 1188                 | 25.69%        |
| Provisional                         | 266                  | 20.15%        |
| <b>Total</b>                        | <b>14370</b>         | <b>24.87%</b> |
| <b>STATE SENATE</b>                 |                      |               |
| <b>1ST STATE SENATE DISTRICT</b>    |                      |               |
| Precinct TSx                        | 0                    | 0.00%         |
| Precinct OS                         | 2760                 | 25.62%        |
| Precinct VBM                        | 10156                | 24.73%        |
| MB Precinct                         | 1188                 | 25.69%        |
| Provisional                         | 266                  | 20.15%        |
| <b>Total</b>                        | <b>14370</b>         | <b>24.87%</b> |
| <b>Total</b>                        |                      |               |
| Precinct TSx                        | 0                    | 0.00%         |
| Precinct OS                         | 2760                 | 25.62%        |
| Precinct VBM                        | 10156                | 24.73%        |
| MB Precinct                         | 1188                 | 25.69%        |
| Provisional                         | 266                  | 20.15%        |
| <b>Total</b>                        | <b>14370</b>         | <b>24.87%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                      |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |                      |               |
| Precinct TSx                        | 0                    | 0.00%         |
| Precinct OS                         | 1781                 | 25.94%        |
| Precinct VBM                        | 6231                 | 24.55%        |
| MB Precinct                         | 1009                 | 25.65%        |

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| Provisional                       |  | 185                  | 21.64% |
| Total                             |  | 9206                 | 24.85% |
| <b>6TH STATE ASSEMBLY DIST</b>    |  |                      |        |
| Precinct TSx                      |  | 0                    | 0.00%  |
| Precinct OS                       |  | 979                  | 25.07% |
| Precinct VBM                      |  | 3925                 | 25.04% |
| MB Precinct                       |  | 179                  | 25.94% |
| Provisional                       |  | 81                   | 17.42% |
| Total                             |  | 5164                 | 24.90% |
| <b>Total</b>                      |  |                      |        |
| Precinct TSx                      |  | 0                    | 0.00%  |
| Precinct OS                       |  | 2760                 | 25.62% |
| Precinct VBM                      |  | 10156                | 24.73% |
| MB Precinct                       |  | 1188                 | 25.69% |
| Provisional                       |  | 266                  | 20.15% |
| Total                             |  | 14370                | 24.87% |
| <b>SUPERVISORIAL DISTRICTS</b>    |  |                      |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |  |                      |        |
| Precinct TSx                      |  | 0                    | -      |
| Precinct OS                       |  | 572                  | 25.46% |
| Precinct VBM                      |  | 2317                 | 24.58% |
| MB Precinct                       |  | 90                   | 23.87% |
| Provisional                       |  | 44                   | 17.60% |
| Total                             |  | 3023                 | 24.57% |
| <b>2ND SUPERVISORIAL DISTRICT</b> |  |                      |        |
| Precinct TSx                      |  | 0                    | 0.00%  |
| Precinct OS                       |  | 557                  | 24.44% |
| Precinct VBM                      |  | 2171                 | 24.91% |
| MB Precinct                       |  | 378                  | 26.10% |
| Provisional                       |  | 55                   | 19.43% |
| Total                             |  | 3161                 | 24.83% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |  |                      |        |
| Precinct TSx                      |  | 0                    | 0.00%  |
| Precinct OS                       |  | 550                  | 25.86% |
| Precinct VBM                      |  | 2159                 | 23.80% |
| MB Precinct                       |  | 99                   | 24.69% |
| Provisional                       |  | 49                   | 21.68% |
| Total                             |  | 2857                 | 24.15% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |  |                      |        |
| Precinct TSx                      |  | 0                    | -      |
| Precinct OS                       |  | 652                  | 28.04% |
| Precinct VBM                      |  | 2257                 | 26.48% |
| MB Precinct                       |  | 534                  | 26.62% |
| Provisional                       |  | 62                   | 23.05% |
| Total                             |  | 3505                 | 26.71% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |  |                      |        |
| Precinct TSx                      |  | 0                    | 0.00%  |
| Precinct OS                       |  | 429                  | 23.93% |
| Precinct VBM                      |  | 1252                 | 23.53% |
| MB Precinct                       |  | 87                   | 22.19% |
| Provisional                       |  | 56                   | 19.18% |
| Total                             |  | 1824                 | 23.39% |
| <b>Total</b>                      |  |                      |        |
| Precinct TSx                      |  | 0                    | 0.00%  |
| Precinct OS                       |  | 2760                 | 25.62% |
| Precinct VBM                      |  | 10156                | 24.73% |
| MB Precinct                       |  | 1188                 | 25.69% |
| Provisional                       |  | 266                  | 20.15% |
| Total                             |  | 14370                | 24.87% |

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|                                 | State Proposition 50 |               |
|---------------------------------|----------------------|---------------|
|                                 | NO                   |               |
| <b>CITIES</b>                   |                      |               |
| <b>CITY OF PLACERVILLE</b>      |                      |               |
| Precinct TSx                    | 0                    | -             |
| Precinct OS                     | 133                  | 24.14%        |
| Precinct VBM                    | 512                  | 22.04%        |
| MB Precinct                     | 0                    | -             |
| Provisional                     | 16                   | 25.81%        |
| <b>Total</b>                    | <b>661</b>           | <b>22.51%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                      |               |
| Precinct TSx                    | 0                    | -             |
| Precinct OS                     | 225                  | 24.14%        |
| Precinct VBM                    | 572                  | 23.84%        |
| MB Precinct                     | 0                    | -             |
| Provisional                     | 35                   | 19.66%        |
| <b>Total</b>                    | <b>832</b>           | <b>23.71%</b> |
| <b>UNINCORPORATED AREA</b>      |                      |               |
| Precinct TSx                    | 0                    | 0.00%         |
| Precinct OS                     | 2402                 | 25.86%        |
| Precinct VBM                    | 9072                 | 24.96%        |
| MB Precinct                     | 1188                 | 25.69%        |
| Provisional                     | 215                  | 19.91%        |
| <b>Total</b>                    | <b>12877</b>         | <b>25.08%</b> |
| <b>Total</b>                    |                      |               |
| Precinct TSx                    | 0                    | 0.00%         |
| Precinct OS                     | 2760                 | 25.62%        |
| Precinct VBM                    | 10156                | 24.73%        |
| MB Precinct                     | 1188                 | 25.69%        |
| Provisional                     | 266                  | 20.15%        |
| <b>Total</b>                    | <b>14370</b>         | <b>24.87%</b> |

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Measure H - Camino USD (55% to pass)

|                        | Reg. Voters | Vote For | Times Counted | Total Votes | YES | NO |
|------------------------|-------------|----------|---------------|-------------|-----|----|
| Jurisdiction Wide      |             |          |               |             |     |    |
| 100001.11-CP 100001.11 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 100002.11-CP 100002.11 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 100003.11-CP 100003.11 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 100004.11-CP 100004.11 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 100005.11-CP 100005.11 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 100006.11-CP 100006.11 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 100008.11-CP 100008.11 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 100009.11-CP 100009.11 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 100012.11-CP 100012.11 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |

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|                         | Reg. Voters | Vote For | Times Counted | Total Votes | YES | NO |
|-------------------------|-------------|----------|---------------|-------------|-----|----|
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 100013.11-CP 100013.11  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 100014.11-CP 100014.11  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 100022.11-CP 100022.11  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 100023.11-CP 100023.11  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 200003.12-CP 200003.12  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 200004.12-CP 200004.12  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 200005.02-CP 200005.02  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 200006.12-CP 200006.12  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 200008.12-CP 200008.12  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |

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|                        | Reg. Voters | Vote For | Times Counted | Total Votes | YES | NO |
|------------------------|-------------|----------|---------------|-------------|-----|----|
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 200010.12-CP 200010.12 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 200012.12-CP 200012.12 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 200013.12-CP 200013.12 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 200014.12-CP 200014.12 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 200015.02-CP 200015.02 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 200016.12-CP 200016.12 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 200016.13-CP 200016.13 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 200017.02-CP 200017.02 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |

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|                        | Reg. Voters | Vote For | Times Counted | Total Votes | YES | NO |
|------------------------|-------------|----------|---------------|-------------|-----|----|
| 200018.02-CP 200018.02 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 200020.02-CP 200020.02 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 200021.01-CP 200021.01 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 200021.02-CP 200021.02 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 200024.02-CP 200024.02 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 300001.04-CP 300001.04 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 300001.05-CP 300001.05 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 300003.05-CP 300003.05 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 300004.05-CP 300004.05 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |



**COUNTY OF EL DORADO**  
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Measure H - Camino USD (55% to pass)

|                        | Reg. Voters | Vote For | Times Counted | Total Votes | YES | NO |
|------------------------|-------------|----------|---------------|-------------|-----|----|
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 300005.05-CP 300005.05 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 300008.05-CP 300008.05 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 300009.04-CP 300009.04 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 300010.04-CP 300010.04 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 300011.04-CP 300011.04 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 300013.05-CP 300013.05 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 300015.04-CP 300015.04 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 300015.05-CP 300015.05 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 300017.05-CP 300017.05 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |



**COUNTY OF EL DORADO**  
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Measure H - Camino USD (55% to pass)

|                        | Reg. Voters | Vote For | Times Counted | Total Votes | YES | NO |
|------------------------|-------------|----------|---------------|-------------|-----|----|
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 400004.14-CP 400004.14 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 400005.08-CP 400005.08 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 400006.08-CP 400006.08 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 400007.08-CP 400007.08 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 400010.08-CP 400010.08 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 400012.08-CP 400012.08 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 400013.08-CP 400013.08 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 400014.08-CP 400014.08 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |

**COUNTY OF EL DORADO**  
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Measure H - Camino USD (55% to pass)

|                        | Reg. Voters | Vote For | Times Counted | Total Votes | YES | NO |
|------------------------|-------------|----------|---------------|-------------|-----|----|
| Total                  | -           | -        | -             | -           | -   | -  |
| 400017.08-CP 400017.08 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 400018.08-CP 400018.08 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 400020.08-CP 400020.08 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 400021.07-CP 400021.07 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 500001.10-CP 500001.10 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 500002.10-CP 500002.10 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 500003.10-CP 500003.10 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 500004.10-CP 500004.10 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |
| MB Precinct            | -           | -        | -             | -           | -   | -  |
| Provisional            | -           | -        | -             | -           | -   | -  |
| Total                  | -           | -        | -             | -           | -   | -  |
| 500005.10-CP 500005.10 |             |          |               |             |     |    |
| Precinct TSx           | -           | -        | -             | -           | -   | -  |
| Precinct OS            | -           | -        | -             | -           | -   | -  |
| Precinct VBM           | -           | -        | -             | -           | -   | -  |

**COUNTY OF EL DORADO**  
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Measure H - Camino USD (55% to pass)

|                         | Reg. Voters | Vote For | Times Counted | Total Votes | YES | NO |
|-------------------------|-------------|----------|---------------|-------------|-----|----|
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 500006.10-CP 500006.10  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 500007.10-CP 500007.10  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 500009.10-CP 500009.10  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 500011.10-CP 500011.10  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 500012.10-CP 500012.10  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 500015.10-CP 500015.10  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 500020.10-CP 500020.10  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 500021.10-CP 500021.10  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |
| Precinct OS             | -           | -        | -             | -           | -   | -  |
| Precinct VBM            | -           | -        | -             | -           | -   | -  |
| MB Precinct Provisional | -           | -        | -             | -           | -   | -  |
| Total                   | -           | -        | -             | -           | -   | -  |
| 95U201.01-MB 95U201.01  |             |          |               |             |     |    |
| Precinct TSx            | -           | -        | -             | -           | -   | -  |

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Measure H - Camino USD (55% to pass)

|                        | Reg. Voters | Vote For | Times Counted | Total Votes | YES |        | NO |        |
|------------------------|-------------|----------|---------------|-------------|-----|--------|----|--------|
| Precinct OS            | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct VBM           | -           | -        | -             | -           | -   | -      | -  | -      |
| MB Precinct            | -           | -        | -             | -           | -   | -      | -  | -      |
| Provisional            | -           | -        | -             | -           | -   | -      | -  | -      |
| Total                  | -           | -        | -             | -           | -   | -      | -  | -      |
| 95U201.02-MB 95U201.02 |             |          |               |             |     |        |    |        |
| Precinct TSx           | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct OS            | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct VBM           | -           | -        | -             | -           | -   | -      | -  | -      |
| MB Precinct            | -           | -        | -             | -           | -   | -      | -  | -      |
| Provisional            | -           | -        | -             | -           | -   | -      | -  | -      |
| Total                  | -           | -        | -             | -           | -   | -      | -  | -      |
| 95U202.02-MB 95U202.02 |             |          |               |             |     |        |    |        |
| Precinct TSx           | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct OS            | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct VBM           | -           | -        | -             | -           | -   | -      | -  | -      |
| MB Precinct            | -           | -        | -             | -           | -   | -      | -  | -      |
| Provisional            | -           | -        | -             | -           | -   | -      | -  | -      |
| Total                  | -           | -        | -             | -           | -   | -      | -  | -      |
| 95U203.02-MB 95U203.02 |             |          |               |             |     |        |    |        |
| Precinct TSx           | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct OS            | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct VBM           | -           | -        | -             | -           | -   | -      | -  | -      |
| MB Precinct            | -           | -        | -             | -           | -   | -      | -  | -      |
| Provisional            | -           | -        | -             | -           | -   | -      | -  | -      |
| Total                  | -           | -        | -             | -           | -   | -      | -  | -      |
| 95U204.01-MB 95U204.01 |             |          |               |             |     |        |    |        |
| Precinct TSx           | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct OS            | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct VBM           | -           | -        | -             | -           | -   | -      | -  | -      |
| MB Precinct            | -           | -        | -             | -           | -   | -      | -  | -      |
| Provisional            | -           | -        | -             | -           | -   | -      | -  | -      |
| Total                  | -           | -        | -             | -           | -   | -      | -  | -      |
| 95U301.05-MB 95U301.05 |             |          |               |             |     |        |    |        |
| Precinct TSx           | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct OS            | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct VBM           | -           | -        | -             | -           | -   | -      | -  | -      |
| MB Precinct            | -           | -        | -             | -           | -   | -      | -  | -      |
| Provisional            | -           | -        | -             | -           | -   | -      | -  | -      |
| Total                  | -           | -        | -             | -           | -   | -      | -  | -      |
| 95U302.04-MB 95U302.04 |             |          |               |             |     |        |    |        |
| Precinct TSx           | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct OS            | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct VBM           | -           | -        | -             | -           | -   | -      | -  | -      |
| MB Precinct            | -           | -        | -             | -           | -   | -      | -  | -      |
| Provisional            | -           | -        | -             | -           | -   | -      | -  | -      |
| Total                  | -           | -        | -             | -           | -   | -      | -  | -      |
| 95U302.05-MB 95U302.05 |             |          |               |             |     |        |    |        |
| Precinct TSx           | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct OS            | -           | -        | -             | -           | -   | -      | -  | -      |
| Precinct VBM           | -           | -        | -             | -           | -   | -      | -  | -      |
| MB Precinct            | -           | -        | -             | -           | -   | -      | -  | -      |
| Provisional            | -           | -        | -             | -           | -   | -      | -  | -      |
| Total                  | -           | -        | -             | -           | -   | -      | -  | -      |
| 95U303.03-MB 95U303.03 |             |          |               |             |     |        |    |        |
| Precinct TSx           | 144         | 1        | 0             | 0           | 0   | -      | 0  | -      |
| Precinct OS            | 144         | 1        | 0             | 0           | 0   | -      | 0  | -      |
| Precinct VBM           | 144         | 1        | 0             | 0           | 0   | -      | 0  | -      |
| MB Precinct            | 144         | 1        | 84            | 84          | 56  | 66.67% | 28 | 33.33% |
| Provisional            | 144         | 1        | 0             | 0           | 0   | -      | 0  | -      |
| Total                  | 144         | 5        | 84            | 84          | 56  | 66.67% | 28 | 33.33% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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Measure H - Camino USD (55% to pass)

|                        | Reg. Voters | Vote For | Times Counted | Total Votes | YES | NO |   |
|------------------------|-------------|----------|---------------|-------------|-----|----|---|
| 95U303.05-MB 95U303.05 |             |          |               |             |     |    |   |
| Precinct TSx           | -           | -        | -             | -           | -   | -  | - |
| Precinct OS            | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM           | -           | -        | -             | -           | -   | -  | - |
| MB Precinct            | -           | -        | -             | -           | -   | -  | - |
| Provisional            | -           | -        | -             | -           | -   | -  | - |
| Total                  | -           | -        | -             | -           | -   | -  | - |
| 95U304.05-MB 95U304.05 |             |          |               |             |     |    |   |
| Precinct TSx           | -           | -        | -             | -           | -   | -  | - |
| Precinct OS            | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM           | -           | -        | -             | -           | -   | -  | - |
| MB Precinct            | -           | -        | -             | -           | -   | -  | - |
| Provisional            | -           | -        | -             | -           | -   | -  | - |
| Total                  | -           | -        | -             | -           | -   | -  | - |
| 95U305.05-MB 95U305.05 |             |          |               |             |     |    |   |
| Precinct TSx           | -           | -        | -             | -           | -   | -  | - |
| Precinct OS            | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM           | -           | -        | -             | -           | -   | -  | - |
| MB Precinct            | -           | -        | -             | -           | -   | -  | - |
| Provisional            | -           | -        | -             | -           | -   | -  | - |
| Total                  | -           | -        | -             | -           | -   | -  | - |
| 95U401.08-MB 95U401.08 |             |          |               |             |     |    |   |
| Precinct TSx           | -           | -        | -             | -           | -   | -  | - |
| Precinct OS            | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM           | -           | -        | -             | -           | -   | -  | - |
| MB Precinct            | -           | -        | -             | -           | -   | -  | - |
| Provisional            | -           | -        | -             | -           | -   | -  | - |
| Total                  | -           | -        | -             | -           | -   | -  | - |
| 95U402.07-MB 95U402.07 |             |          |               |             |     |    |   |
| Precinct TSx           | -           | -        | -             | -           | -   | -  | - |
| Precinct OS            | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM           | -           | -        | -             | -           | -   | -  | - |
| MB Precinct            | -           | -        | -             | -           | -   | -  | - |
| Provisional            | -           | -        | -             | -           | -   | -  | - |
| Total                  | -           | -        | -             | -           | -   | -  | - |
| 95U402.08-MB 95U402.08 |             |          |               |             |     |    |   |
| Precinct TSx           | -           | -        | -             | -           | -   | -  | - |
| Precinct OS            | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM           | -           | -        | -             | -           | -   | -  | - |
| MB Precinct            | -           | -        | -             | -           | -   | -  | - |
| Provisional            | -           | -        | -             | -           | -   | -  | - |
| Total                  | -           | -        | -             | -           | -   | -  | - |
| 95U403.08-MB 95U403.08 |             |          |               |             |     |    |   |
| Precinct TSx           | -           | -        | -             | -           | -   | -  | - |
| Precinct OS            | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM           | -           | -        | -             | -           | -   | -  | - |
| MB Precinct            | -           | -        | -             | -           | -   | -  | - |
| Provisional            | -           | -        | -             | -           | -   | -  | - |
| Total                  | -           | -        | -             | -           | -   | -  | - |
| 95U404.06-MB 95U404.06 |             |          |               |             |     |    |   |
| Precinct TSx           | 0           | 1        | 0             | 0           | 0   | -  | 0 |
| Precinct OS            | 0           | 1        | 0             | 0           | 0   | -  | 0 |
| Precinct VBM           | 0           | 1        | 0             | 0           | 0   | -  | 0 |
| MB Precinct            | 0           | 1        | 0             | 0           | 0   | -  | 0 |
| Provisional            | 0           | 1        | 0             | 0           | 0   | -  | 0 |
| Total                  | 0           | 5        | 0             | 0           | 0   | -  | 0 |
| 95U404.08-MB 95U404.08 |             |          |               |             |     |    |   |
| Precinct TSx           | -           | -        | -             | -           | -   | -  | - |
| Precinct OS            | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM           | -           | -        | -             | -           | -   | -  | - |
| MB Precinct            | -           | -        | -             | -           | -   | -  | - |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                               |              | Measure H - Camino USD (55% to pass) |          |               |             |      |         |            |
|-------------------------------|--------------|--------------------------------------|----------|---------------|-------------|------|---------|------------|
|                               |              | Reg. Voters                          | Vote For | Times Counted | Total Votes | YES  | NO      |            |
| Provisional                   |              | -                                    | -        | -             | -           | -    | -       | -          |
| Total                         |              | -                                    | -        | -             | -           | -    | -       | -          |
| 95U501.09-MB 95U501.09        |              |                                      |          |               |             |      |         |            |
|                               | Precinct TSx | 0                                    | 1        | 0             | 0           | 0    | -       | 0          |
|                               | Precinct OS  | 0                                    | 1        | 0             | 0           | 0    | -       | 0          |
|                               | Precinct VBM | 0                                    | 1        | 0             | 0           | 0    | -       | 0          |
|                               | MB Precinct  | 0                                    | 1        | 2             | 2           | 2    | 100.00% | 0 0.00%    |
|                               | Provisional  | 0                                    | 1        | 0             | 0           | 0    | -       | 0          |
| Total                         |              | 0                                    | 5        | 2             | 2           | 2    | 100.00% | 0 0.00%    |
| 95U501.10-MB 95U501.10        |              |                                      |          |               |             |      |         |            |
|                               | Precinct TSx | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Precinct OS  | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Precinct VBM | -                                    | -        | -             | -           | -    | -       | -          |
|                               | MB Precinct  | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Provisional  | -                                    | -        | -             | -           | -    | -       | -          |
| Total                         |              | -                                    | -        | -             | -           | -    | -       | -          |
| 95U598.10-MB 95U598.10        |              |                                      |          |               |             |      |         |            |
|                               | Precinct TSx | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Precinct OS  | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Precinct VBM | -                                    | -        | -             | -           | -    | -       | -          |
|                               | MB Precinct  | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Provisional  | -                                    | -        | -             | -           | -    | -       | -          |
| Total                         |              | -                                    | -        | -             | -           | -    | -       | -          |
| 95U599.10-MB 95U599.10        |              |                                      |          |               |             |      |         |            |
|                               | Precinct TSx | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Precinct OS  | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Precinct VBM | -                                    | -        | -             | -           | -    | -       | -          |
|                               | MB Precinct  | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Provisional  | -                                    | -        | -             | -           | -    | -       | -          |
| Total                         |              | -                                    | -        | -             | -           | -    | -       | -          |
| 96U101.11-MB 96U101.11        |              |                                      |          |               |             |      |         |            |
|                               | Precinct TSx | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Precinct OS  | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Precinct VBM | -                                    | -        | -             | -           | -    | -       | -          |
|                               | MB Precinct  | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Provisional  | -                                    | -        | -             | -           | -    | -       | -          |
| Total                         |              | -                                    | -        | -             | -           | -    | -       | -          |
| 96U401.14-MB 96U401.14        |              |                                      |          |               |             |      |         |            |
|                               | Precinct TSx | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Precinct OS  | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Precinct VBM | -                                    | -        | -             | -           | -    | -       | -          |
|                               | MB Precinct  | -                                    | -        | -             | -           | -    | -       | -          |
|                               | Provisional  | -                                    | -        | -             | -           | -    | -       | -          |
| Total                         |              | -                                    | -        | -             | -           | -    | -       | -          |
| Total                         |              | 3197                                 | 5        | 0             | 0           | 0    | -       | 0          |
|                               | Precinct OS  | 3197                                 | 5        | 381           | 371         | 266  | 71.70%  | 105 28.30% |
|                               | Precinct VBM | 3197                                 | 5        | 1538          | 1478        | 1057 | 71.52%  | 421 28.48% |
|                               | MB Precinct  | 3197                                 | 5        | 86            | 86          | 58   | 67.44%  | 28 32.56%  |
|                               | Provisional  | 3197                                 | 5        | 39            | 34          | 23   | 67.65%  | 11 32.35%  |
| Total                         |              | 3197                                 | 25       | 2044          | 1969        | 1404 | 71.31%  | 565 28.69% |
| STATE BOARD OF EQUALIZATION   |              |                                      |          |               |             |      |         |            |
| STATE BOARD OF EQUALIZATION 1 |              |                                      |          |               |             |      |         |            |
|                               | Precinct TSx | 3197                                 | 5        | 0             | 0           | 0    | -       | 0          |
|                               | Precinct OS  | 3197                                 | 5        | 381           | 371         | 266  | 71.70%  | 105 28.30% |
|                               | Precinct VBM | 3197                                 | 5        | 1538          | 1478        | 1057 | 71.52%  | 421 28.48% |
|                               | MB Precinct  | 3197                                 | 5        | 86            | 86          | 58   | 67.44%  | 28 32.56%  |
|                               | Provisional  | 3197                                 | 5        | 39            | 34          | 23   | 67.65%  | 11 32.35%  |
| Total                         |              | 3197                                 | 25       | 2044          | 1969        | 1404 | 71.31%  | 565 28.69% |
| Total                         |              |                                      |          |               |             |      |         |            |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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Measure H - Camino USD (55% to pass)

|                                     | Reg. Voters | Vote For  | Times Counted | Total Votes | YES         |               | NO         |               |
|-------------------------------------|-------------|-----------|---------------|-------------|-------------|---------------|------------|---------------|
| Precinct TSx                        | 3197        | 5         | 0             | 0           | 0           | -             | 0          | -             |
| Precinct OS                         | 3197        | 5         | 381           | 371         | 266         | 71.70%        | 105        | 28.30%        |
| Precinct VBM                        | 3197        | 5         | 1538          | 1478        | 1057        | 71.52%        | 421        | 28.48%        |
| MB Precinct                         | 3197        | 5         | 86            | 86          | 58          | 67.44%        | 28         | 32.56%        |
| Provisional                         | 3197        | 5         | 39            | 34          | 23          | 67.65%        | 11         | 32.35%        |
| <b>Total</b>                        | <b>3197</b> | <b>25</b> | <b>2044</b>   | <b>1969</b> | <b>1404</b> | <b>71.31%</b> | <b>565</b> | <b>28.69%</b> |
| <b>UNITED STATES SENATE</b>         |             |           |               |             |             |               |            |               |
| <b>UNITED STATES SENATE</b>         |             |           |               |             |             |               |            |               |
| Precinct TSx                        | 3197        | 5         | 0             | 0           | 0           | -             | 0          | -             |
| Precinct OS                         | 3197        | 5         | 381           | 371         | 266         | 71.70%        | 105        | 28.30%        |
| Precinct VBM                        | 3197        | 5         | 1538          | 1478        | 1057        | 71.52%        | 421        | 28.48%        |
| MB Precinct                         | 3197        | 5         | 86            | 86          | 58          | 67.44%        | 28         | 32.56%        |
| Provisional                         | 3197        | 5         | 39            | 34          | 23          | 67.65%        | 11         | 32.35%        |
| <b>Total</b>                        | <b>3197</b> | <b>25</b> | <b>2044</b>   | <b>1969</b> | <b>1404</b> | <b>71.31%</b> | <b>565</b> | <b>28.69%</b> |
| <b>Total</b>                        |             |           |               |             |             |               |            |               |
| Precinct TSx                        | 3197        | 5         | 0             | 0           | 0           | -             | 0          | -             |
| Precinct OS                         | 3197        | 5         | 381           | 371         | 266         | 71.70%        | 105        | 28.30%        |
| Precinct VBM                        | 3197        | 5         | 1538          | 1478        | 1057        | 71.52%        | 421        | 28.48%        |
| MB Precinct                         | 3197        | 5         | 86            | 86          | 58          | 67.44%        | 28         | 32.56%        |
| Provisional                         | 3197        | 5         | 39            | 34          | 23          | 67.65%        | 11         | 32.35%        |
| <b>Total</b>                        | <b>3197</b> | <b>25</b> | <b>2044</b>   | <b>1969</b> | <b>1404</b> | <b>71.31%</b> | <b>565</b> | <b>28.69%</b> |
| <b>Total</b>                        |             |           |               |             |             |               |            |               |
| <b>UNITED STATES REPRESENTATIVE</b> |             |           |               |             |             |               |            |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |             |           |               |             |             |               |            |               |
| Precinct TSx                        | 3197        | 5         | 0             | 0           | 0           | -             | 0          | -             |
| Precinct OS                         | 3197        | 5         | 381           | 371         | 266         | 71.70%        | 105        | 28.30%        |
| Precinct VBM                        | 3197        | 5         | 1538          | 1478        | 1057        | 71.52%        | 421        | 28.48%        |
| MB Precinct                         | 3197        | 5         | 86            | 86          | 58          | 67.44%        | 28         | 32.56%        |
| Provisional                         | 3197        | 5         | 39            | 34          | 23          | 67.65%        | 11         | 32.35%        |
| <b>Total</b>                        | <b>3197</b> | <b>25</b> | <b>2044</b>   | <b>1969</b> | <b>1404</b> | <b>71.31%</b> | <b>565</b> | <b>28.69%</b> |
| <b>Total</b>                        |             |           |               |             |             |               |            |               |
| Precinct TSx                        | 3197        | 5         | 0             | 0           | 0           | -             | 0          | -             |
| Precinct OS                         | 3197        | 5         | 381           | 371         | 266         | 71.70%        | 105        | 28.30%        |
| Precinct VBM                        | 3197        | 5         | 1538          | 1478        | 1057        | 71.52%        | 421        | 28.48%        |
| MB Precinct                         | 3197        | 5         | 86            | 86          | 58          | 67.44%        | 28         | 32.56%        |
| Provisional                         | 3197        | 5         | 39            | 34          | 23          | 67.65%        | 11         | 32.35%        |
| <b>Total</b>                        | <b>3197</b> | <b>25</b> | <b>2044</b>   | <b>1969</b> | <b>1404</b> | <b>71.31%</b> | <b>565</b> | <b>28.69%</b> |
| <b>Total</b>                        |             |           |               |             |             |               |            |               |
| <b>STATE SENATE</b>                 |             |           |               |             |             |               |            |               |
| <b>1ST STATE SENATE DISTRICT</b>    |             |           |               |             |             |               |            |               |
| Precinct TSx                        | 3197        | 5         | 0             | 0           | 0           | -             | 0          | -             |
| Precinct OS                         | 3197        | 5         | 381           | 371         | 266         | 71.70%        | 105        | 28.30%        |
| Precinct VBM                        | 3197        | 5         | 1538          | 1478        | 1057        | 71.52%        | 421        | 28.48%        |
| MB Precinct                         | 3197        | 5         | 86            | 86          | 58          | 67.44%        | 28         | 32.56%        |
| Provisional                         | 3197        | 5         | 39            | 34          | 23          | 67.65%        | 11         | 32.35%        |
| <b>Total</b>                        | <b>3197</b> | <b>25</b> | <b>2044</b>   | <b>1969</b> | <b>1404</b> | <b>71.31%</b> | <b>565</b> | <b>28.69%</b> |
| <b>Total</b>                        |             |           |               |             |             |               |            |               |
| Precinct TSx                        | 3197        | 5         | 0             | 0           | 0           | -             | 0          | -             |
| Precinct OS                         | 3197        | 5         | 381           | 371         | 266         | 71.70%        | 105        | 28.30%        |
| Precinct VBM                        | 3197        | 5         | 1538          | 1478        | 1057        | 71.52%        | 421        | 28.48%        |
| MB Precinct                         | 3197        | 5         | 86            | 86          | 58          | 67.44%        | 28         | 32.56%        |
| Provisional                         | 3197        | 5         | 39            | 34          | 23          | 67.65%        | 11         | 32.35%        |
| <b>Total</b>                        | <b>3197</b> | <b>25</b> | <b>2044</b>   | <b>1969</b> | <b>1404</b> | <b>71.31%</b> | <b>565</b> | <b>28.69%</b> |
| <b>Total</b>                        |             |           |               |             |             |               |            |               |
| <b>STATE ASSEMBLY DISTRICTS</b>     |             |           |               |             |             |               |            |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |             |           |               |             |             |               |            |               |
| Precinct TSx                        | 3197        | 5         | 0             | 0           | 0           | -             | 0          | -             |
| Precinct OS                         | 3197        | 5         | 381           | 371         | 266         | 71.70%        | 105        | 28.30%        |
| Precinct VBM                        | 3197        | 5         | 1538          | 1478        | 1057        | 71.52%        | 421        | 28.48%        |
| MB Precinct                         | 3197        | 5         | 86            | 86          | 58          | 67.44%        | 28         | 32.56%        |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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Measure H - Camino USD (55% to pass)

|                                   | Reg. Voters | Vote For | Times Counted | Total Votes | YES  |         | NO  |        |
|-----------------------------------|-------------|----------|---------------|-------------|------|---------|-----|--------|
| Provisional                       | 3197        | 5        | 39            | 34          | 23   | 67.65%  | 11  | 32.35% |
| Total                             | 3197        | 25       | 2044          | 1969        | 1404 | 71.31%  | 565 | 28.69% |
| <b>6TH STATE ASSEMBLY DIST</b>    |             |          |               |             |      |         |     |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| Total                             | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| Total                             | 3197        | 5        | 0             | 0           | 0    | -       | 0   | -      |
| Precinct OS                       | 3197        | 5        | 381           | 371         | 266  | 71.70%  | 105 | 28.30% |
| Precinct VBM                      | 3197        | 5        | 1538          | 1478        | 1057 | 71.52%  | 421 | 28.48% |
| MB Precinct                       | 3197        | 5        | 86            | 86          | 58   | 67.44%  | 28  | 32.56% |
| Provisional                       | 3197        | 5        | 39            | 34          | 23   | 67.65%  | 11  | 32.35% |
| Total                             | 3197        | 25       | 2044          | 1969        | 1404 | 71.31%  | 565 | 28.69% |
| <b>SUPERVISORIAL DISTRICTS</b>    |             |          |               |             |      |         |     |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |             |          |               |             |      |         |     |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| Total                             | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| <b>2ND SUPERVISORIAL DISTRICT</b> |             |          |               |             |      |         |     |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| Total                             | 0           | 0        | 0             | 0           | 0    | -       | 0   | -      |
| <b>3RD SUPERVISORIAL DISTRICT</b> |             |          |               |             |      |         |     |        |
| Precinct TSx                      | 3197        | 3        | 0             | 0           | 0    | -       | 0   | -      |
| Precinct OS                       | 3197        | 3        | 381           | 371         | 266  | 71.70%  | 105 | 28.30% |
| Precinct VBM                      | 3197        | 3        | 1538          | 1478        | 1057 | 71.52%  | 421 | 28.48% |
| MB Precinct                       | 3197        | 3        | 84            | 84          | 56   | 66.67%  | 28  | 33.33% |
| Provisional                       | 3197        | 3        | 39            | 34          | 23   | 67.65%  | 11  | 32.35% |
| Total                             | 3197        | 15       | 2042          | 1967        | 1402 | 71.28%  | 565 | 28.72% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |      |         |     |        |
| Precinct TSx                      | 0           | 1        | 0             | 0           | 0    | -       | 0   | -      |
| Precinct OS                       | 0           | 1        | 0             | 0           | 0    | -       | 0   | -      |
| Precinct VBM                      | 0           | 1        | 0             | 0           | 0    | -       | 0   | -      |
| MB Precinct                       | 0           | 1        | 0             | 0           | 0    | -       | 0   | -      |
| Provisional                       | 0           | 1        | 0             | 0           | 0    | -       | 0   | -      |
| Total                             | 0           | 5        | 0             | 0           | 0    | -       | 0   | -      |
| <b>5TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |      |         |     |        |
| Precinct TSx                      | 0           | 1        | 0             | 0           | 0    | -       | 0   | -      |
| Precinct OS                       | 0           | 1        | 0             | 0           | 0    | -       | 0   | -      |
| Precinct VBM                      | 0           | 1        | 0             | 0           | 0    | -       | 0   | -      |
| MB Precinct                       | 0           | 1        | 2             | 2           | 2    | 100.00% | 0   | 0.00%  |
| Provisional                       | 0           | 1        | 0             | 0           | 0    | -       | 0   | -      |
| Total                             | 0           | 5        | 2             | 2           | 2    | 100.00% | 0   | 0.00%  |
| Total                             | 3197        | 5        | 0             | 0           | 0    | -       | 0   | -      |
| Precinct OS                       | 3197        | 5        | 381           | 371         | 266  | 71.70%  | 105 | 28.30% |
| Precinct VBM                      | 3197        | 5        | 1538          | 1478        | 1057 | 71.52%  | 421 | 28.48% |
| MB Precinct                       | 3197        | 5        | 86            | 86          | 58   | 67.44%  | 28  | 32.56% |
| Provisional                       | 3197        | 5        | 39            | 34          | 23   | 67.65%  | 11  | 32.35% |
| Total                             | 3197        | 25       | 2044          | 1969        | 1404 | 71.31%  | 565 | 28.69% |

**COUNTY OF EL DORADO**  
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|                                 |  | Measure H - Camino USD (55% to pass) |           |                  |             |             |               |                   |
|---------------------------------|--|--------------------------------------|-----------|------------------|-------------|-------------|---------------|-------------------|
|                                 |  | Reg. Voters                          | Vote For  | Times<br>Counted | Total Votes | YES         | NO            |                   |
| <b>CITIES</b>                   |  |                                      |           |                  |             |             |               |                   |
| <b>CITY OF PLACERVILLE</b>      |  |                                      |           |                  |             |             |               |                   |
| Precinct TSx                    |  | 0                                    | 0         | 0                | 0           | 0           | -             | 0                 |
| Precinct OS                     |  | 0                                    | 0         | 0                | 0           | 0           | -             | 0                 |
| Precinct VBM                    |  | 0                                    | 0         | 0                | 0           | 0           | -             | 0                 |
| MB Precinct                     |  | 0                                    | 0         | 0                | 0           | 0           | -             | 0                 |
| Provisional                     |  | 0                                    | 0         | 0                | 0           | 0           | -             | 0                 |
| <b>Total</b>                    |  | <b>0</b>                             | <b>0</b>  | <b>0</b>         | <b>0</b>    | <b>0</b>    | <b>-</b>      | <b>0</b>          |
| <b>CITY OF SOUTH LAKE TAHOE</b> |  |                                      |           |                  |             |             |               |                   |
| Precinct TSx                    |  | 0                                    | 0         | 0                | 0           | 0           | -             | 0                 |
| Precinct OS                     |  | 0                                    | 0         | 0                | 0           | 0           | -             | 0                 |
| Precinct VBM                    |  | 0                                    | 0         | 0                | 0           | 0           | -             | 0                 |
| MB Precinct                     |  | 0                                    | 0         | 0                | 0           | 0           | -             | 0                 |
| Provisional                     |  | 0                                    | 0         | 0                | 0           | 0           | -             | 0                 |
| <b>Total</b>                    |  | <b>0</b>                             | <b>0</b>  | <b>0</b>         | <b>0</b>    | <b>0</b>    | <b>-</b>      | <b>0</b>          |
| <b>UNINCORPORATED AREA</b>      |  |                                      |           |                  |             |             |               |                   |
| Precinct TSx                    |  | 3197                                 | 5         | 0                | 0           | 0           | -             | 0                 |
| Precinct OS                     |  | 3197                                 | 5         | 381              | 371         | 266         | 71.70%        | 105 28.30%        |
| Precinct VBM                    |  | 3197                                 | 5         | 1538             | 1478        | 1057        | 71.52%        | 421 28.48%        |
| MB Precinct                     |  | 3197                                 | 5         | 86               | 86          | 58          | 67.44%        | 28 32.56%         |
| Provisional                     |  | 3197                                 | 5         | 39               | 34          | 23          | 67.65%        | 11 32.35%         |
| <b>Total</b>                    |  | <b>3197</b>                          | <b>25</b> | <b>2044</b>      | <b>1969</b> | <b>1404</b> | <b>71.31%</b> | <b>565 28.69%</b> |
| <b>Total</b>                    |  |                                      |           |                  |             |             |               |                   |
| Precinct TSx                    |  | 3197                                 | 5         | 0                | 0           | 0           | -             | 0                 |
| Precinct OS                     |  | 3197                                 | 5         | 381              | 371         | 266         | 71.70%        | 105 28.30%        |
| Precinct VBM                    |  | 3197                                 | 5         | 1538             | 1478        | 1057        | 71.52%        | 421 28.48%        |
| MB Precinct                     |  | 3197                                 | 5         | 86               | 86          | 58          | 67.44%        | 28 32.56%         |
| Provisional                     |  | 3197                                 | 5         | 39               | 34          | 23          | 67.65%        | 11 32.35%         |
| <b>Total</b>                    |  | <b>3197</b>                          | <b>25</b> | <b>2044</b>      | <b>1969</b> | <b>1404</b> | <b>71.31%</b> | <b>565 28.69%</b> |









**COUNTY OF EL DORADO**  
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| Measure C - Mother Lode USD (55% to pass) |             |          |               |             |     |         |            |
|---|-------------|----------|---------------|-------------|-----|---------|------------|
|   | Reg. Voters | Vote For | Times Counted | Total Votes | YES |         | NO         |
| Provisional                               | -           | -        | -             | -           | -   | -       | -          |
| Total                                     | -           | -        | -             | -           | -   | -       | -          |
| 300005.05-CP 300005.05                    |             |          |               |             |     |         |            |
| Precinct TSx                              | -           | -        | -             | -           | -   | -       | -          |
| Precinct OS                               | -           | -        | -             | -           | -   | -       | -          |
| Precinct VBM                              | -           | -        | -             | -           | -   | -       | -          |
| MB Precinct                               | -           | -        | -             | -           | -   | -       | -          |
| Provisional                               | -           | -        | -             | -           | -   | -       | -          |
| Total                                     | -           | -        | -             | -           | -   | -       | -          |
| 300008.05-CP 300008.05                    |             |          |               |             |     |         |            |
| Precinct TSx                              | -           | -        | -             | -           | -   | -       | -          |
| Precinct OS                               | -           | -        | -             | -           | -   | -       | -          |
| Precinct VBM                              | -           | -        | -             | -           | -   | -       | -          |
| MB Precinct                               | -           | -        | -             | -           | -   | -       | -          |
| Provisional                               | -           | -        | -             | -           | -   | -       | -          |
| Total                                     | -           | -        | -             | -           | -   | -       | -          |
| 300009.04-CP 300009.04                    |             |          |               |             |     |         |            |
| Precinct TSx                              | 873         | 1        | 0             | 0           | 0   | -       | 0          |
| Precinct OS                               | 873         | 1        | 121           | 108         | 55  | 50.93%  | 53 49.07%  |
| Precinct VBM                              | 873         | 1        | 395           | 371         | 196 | 52.83%  | 175 47.17% |
| MB Precinct                               | 873         | 1        | 0             | 0           | 0   | -       | 0          |
| Provisional                               | 873         | 1        | 13            | 11          | 9   | 81.82%  | 2 18.18%   |
| Total                                     | 873         | 5        | 529           | 490         | 260 | 53.06%  | 230 46.94% |
| 300010.04-CP 300010.04                    |             |          |               |             |     |         |            |
| Precinct TSx                              | 1340        | 1        | 0             | 0           | 0   | -       | 0          |
| Precinct OS                               | 1340        | 1        | 171           | 165         | 95  | 57.58%  | 70 42.42%  |
| Precinct VBM                              | 1340        | 1        | 656           | 601         | 369 | 61.40%  | 232 38.60% |
| MB Precinct                               | 1340        | 1        | 0             | 0           | 0   | -       | 0          |
| Provisional                               | 1340        | 1        | 11            | 10          | 5   | 50.00%  | 5 50.00%   |
| Total                                     | 1340        | 5        | 838           | 776         | 469 | 60.44%  | 307 39.56% |
| 300011.04-CP 300011.04                    |             |          |               |             |     |         |            |
| Precinct TSx                              | 1491        | 1        | 0             | 0           | 0   | -       | 0          |
| Precinct OS                               | 1491        | 1        | 142           | 137         | 72  | 52.55%  | 65 47.45%  |
| Precinct VBM                              | 1491        | 1        | 711           | 662         | 379 | 57.25%  | 283 42.75% |
| MB Precinct                               | 1491        | 1        | 0             | 0           | 0   | -       | 0          |
| Provisional                               | 1491        | 1        | 22            | 18          | 12  | 66.67%  | 6 33.33%   |
| Total                                     | 1491        | 5        | 875           | 817         | 463 | 56.67%  | 354 43.33% |
| 300013.05-CP 300013.05                    |             |          |               |             |     |         |            |
| Precinct TSx                              | -           | -        | -             | -           | -   | -       | -          |
| Precinct OS                               | -           | -        | -             | -           | -   | -       | -          |
| Precinct VBM                              | -           | -        | -             | -           | -   | -       | -          |
| MB Precinct                               | -           | -        | -             | -           | -   | -       | -          |
| Provisional                               | -           | -        | -             | -           | -   | -       | -          |
| Total                                     | -           | -        | -             | -           | -   | -       | -          |
| 300015.04-CP 300015.04                    |             |          |               |             |     |         |            |
| Precinct TSx                              | 119         | 1        | 0             | 0           | 0   | -       | 0          |
| Precinct OS                               | 119         | 1        | 10            | 10          | 4   | 40.00%  | 6 60.00%   |
| Precinct VBM                              | 119         | 1        | 63            | 53          | 40  | 75.47%  | 13 24.53%  |
| MB Precinct                               | 119         | 1        | 0             | 0           | 0   | -       | 0          |
| Provisional                               | 119         | 1        | 4             | 4           | 4   | 100.00% | 0 0.00%    |
| Total                                     | 119         | 5        | 77            | 67          | 48  | 71.64%  | 19 28.36%  |
| 300015.05-CP 300015.05                    |             |          |               |             |     |         |            |
| Precinct TSx                              | -           | -        | -             | -           | -   | -       | -          |
| Precinct OS                               | -           | -        | -             | -           | -   | -       | -          |
| Precinct VBM                              | -           | -        | -             | -           | -   | -       | -          |
| MB Precinct                               | -           | -        | -             | -           | -   | -       | -          |
| Provisional                               | -           | -        | -             | -           | -   | -       | -          |
| Total                                     | -           | -        | -             | -           | -   | -       | -          |
| 300017.05-CP 300017.05                    |             |          |               |             |     |         |            |
| Precinct TSx                              | -           | -        | -             | -           | -   | -       | -          |
| Precinct OS                               | -           | -        | -             | -           | -   | -       | -          |









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| Measure C - Mother Lode USD (55% to pass) |             |          |               |             |     |    |   |
|---|-------------|----------|---------------|-------------|-----|----|---|
|   | Reg. Voters | Vote For | Times Counted | Total Votes | YES | NO |   |
| MB Precinct Provisional                   | -           | -        | -             | -           | -   | -  | - |
| Total                                     | -           | -        | -             | -           | -   | -  | - |
| 500006.10-CP 500006.10                    |             |          |               |             |     |    |   |
| Precinct TSx                              | -           | -        | -             | -           | -   | -  | - |
| Precinct OS                               | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM                              | -           | -        | -             | -           | -   | -  | - |
| MB Precinct Provisional                   | -           | -        | -             | -           | -   | -  | - |
| Total                                     | -           | -        | -             | -           | -   | -  | - |
| 500007.10-CP 500007.10                    |             |          |               |             |     |    |   |
| Precinct TSx                              | -           | -        | -             | -           | -   | -  | - |
| Precinct OS                               | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM                              | -           | -        | -             | -           | -   | -  | - |
| MB Precinct Provisional                   | -           | -        | -             | -           | -   | -  | - |
| Total                                     | -           | -        | -             | -           | -   | -  | - |
| 500009.10-CP 500009.10                    |             |          |               |             |     |    |   |
| Precinct TSx                              | -           | -        | -             | -           | -   | -  | - |
| Precinct OS                               | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM                              | -           | -        | -             | -           | -   | -  | - |
| MB Precinct Provisional                   | -           | -        | -             | -           | -   | -  | - |
| Total                                     | -           | -        | -             | -           | -   | -  | - |
| 500011.10-CP 500011.10                    |             |          |               |             |     |    |   |
| Precinct TSx                              | -           | -        | -             | -           | -   | -  | - |
| Precinct OS                               | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM                              | -           | -        | -             | -           | -   | -  | - |
| MB Precinct Provisional                   | -           | -        | -             | -           | -   | -  | - |
| Total                                     | -           | -        | -             | -           | -   | -  | - |
| 500012.10-CP 500012.10                    |             |          |               |             |     |    |   |
| Precinct TSx                              | -           | -        | -             | -           | -   | -  | - |
| Precinct OS                               | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM                              | -           | -        | -             | -           | -   | -  | - |
| MB Precinct Provisional                   | -           | -        | -             | -           | -   | -  | - |
| Total                                     | -           | -        | -             | -           | -   | -  | - |
| 500015.10-CP 500015.10                    |             |          |               |             |     |    |   |
| Precinct TSx                              | -           | -        | -             | -           | -   | -  | - |
| Precinct OS                               | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM                              | -           | -        | -             | -           | -   | -  | - |
| MB Precinct Provisional                   | -           | -        | -             | -           | -   | -  | - |
| Total                                     | -           | -        | -             | -           | -   | -  | - |
| 500020.10-CP 500020.10                    |             |          |               |             |     |    |   |
| Precinct TSx                              | -           | -        | -             | -           | -   | -  | - |
| Precinct OS                               | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM                              | -           | -        | -             | -           | -   | -  | - |
| MB Precinct Provisional                   | -           | -        | -             | -           | -   | -  | - |
| Total                                     | -           | -        | -             | -           | -   | -  | - |
| 500021.10-CP 500021.10                    |             |          |               |             |     |    |   |
| Precinct TSx                              | -           | -        | -             | -           | -   | -  | - |
| Precinct OS                               | -           | -        | -             | -           | -   | -  | - |
| Precinct VBM                              | -           | -        | -             | -           | -   | -  | - |
| MB Precinct Provisional                   | -           | -        | -             | -           | -   | -  | - |
| Total                                     | -           | -        | -             | -           | -   | -  | - |
| 95U201.01-MB 95U201.01                    |             |          |               |             |     |    |   |
| Precinct TSx                              | 8           | 1        | 0             | 0           | 0   | -  | 0 |







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| Measure C - Mother Lode USD (55% to pass) |             |           |               |             |             |               |             |               |
|---|-------------|-----------|---------------|-------------|-------------|---------------|-------------|---------------|
|   | Reg. Voters | Vote For  | Times Counted | Total Votes | YES         |               | NO          |               |
| Precinct TSx                              | 9873        | 13        | 0             | 0           | 0           | -             | 0           | -             |
| Precinct OS                               | 9873        | 13        | 937           | 883         | 502         | 56.85%        | 381         | 43.15%        |
| Precinct VBM                              | 9873        | 13        | 4035          | 3749        | 2204        | 58.79%        | 1545        | 41.21%        |
| MB Precinct                               | 9873        | 13        | 915           | 866         | 482         | 55.66%        | 384         | 44.34%        |
| Provisional                               | 9873        | 13        | 123           | 96          | 62          | 64.58%        | 34          | 35.42%        |
| <b>Total</b>                              | <b>9873</b> | <b>65</b> | <b>6010</b>   | <b>5594</b> | <b>3250</b> | <b>58.10%</b> | <b>2344</b> | <b>41.90%</b> |
| <b>UNITED STATES SENATE</b>               |             |           |               |             |             |               |             |               |
| <b>UNITED STATES SENATE</b>               |             |           |               |             |             |               |             |               |
| Precinct TSx                              | 9873        | 13        | 0             | 0           | 0           | -             | 0           | -             |
| Precinct OS                               | 9873        | 13        | 937           | 883         | 502         | 56.85%        | 381         | 43.15%        |
| Precinct VBM                              | 9873        | 13        | 4035          | 3749        | 2204        | 58.79%        | 1545        | 41.21%        |
| MB Precinct                               | 9873        | 13        | 915           | 866         | 482         | 55.66%        | 384         | 44.34%        |
| Provisional                               | 9873        | 13        | 123           | 96          | 62          | 64.58%        | 34          | 35.42%        |
| <b>Total</b>                              | <b>9873</b> | <b>65</b> | <b>6010</b>   | <b>5594</b> | <b>3250</b> | <b>58.10%</b> | <b>2344</b> | <b>41.90%</b> |
| <b>Total</b>                              |             |           |               |             |             |               |             |               |
| Precinct TSx                              | 9873        | 13        | 0             | 0           | 0           | -             | 0           | -             |
| Precinct OS                               | 9873        | 13        | 937           | 883         | 502         | 56.85%        | 381         | 43.15%        |
| Precinct VBM                              | 9873        | 13        | 4035          | 3749        | 2204        | 58.79%        | 1545        | 41.21%        |
| MB Precinct                               | 9873        | 13        | 915           | 866         | 482         | 55.66%        | 384         | 44.34%        |
| Provisional                               | 9873        | 13        | 123           | 96          | 62          | 64.58%        | 34          | 35.42%        |
| <b>Total</b>                              | <b>9873</b> | <b>65</b> | <b>6010</b>   | <b>5594</b> | <b>3250</b> | <b>58.10%</b> | <b>2344</b> | <b>41.90%</b> |
| <b>Total</b>                              |             |           |               |             |             |               |             |               |
| <b>UNITED STATES REPRESENTATIVE</b>       |             |           |               |             |             |               |             |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>         |             |           |               |             |             |               |             |               |
| Precinct TSx                              | 9873        | 13        | 0             | 0           | 0           | -             | 0           | -             |
| Precinct OS                               | 9873        | 13        | 937           | 883         | 502         | 56.85%        | 381         | 43.15%        |
| Precinct VBM                              | 9873        | 13        | 4035          | 3749        | 2204        | 58.79%        | 1545        | 41.21%        |
| MB Precinct                               | 9873        | 13        | 915           | 866         | 482         | 55.66%        | 384         | 44.34%        |
| Provisional                               | 9873        | 13        | 123           | 96          | 62          | 64.58%        | 34          | 35.42%        |
| <b>Total</b>                              | <b>9873</b> | <b>65</b> | <b>6010</b>   | <b>5594</b> | <b>3250</b> | <b>58.10%</b> | <b>2344</b> | <b>41.90%</b> |
| <b>Total</b>                              |             |           |               |             |             |               |             |               |
| Precinct TSx                              | 9873        | 13        | 0             | 0           | 0           | -             | 0           | -             |
| Precinct OS                               | 9873        | 13        | 937           | 883         | 502         | 56.85%        | 381         | 43.15%        |
| Precinct VBM                              | 9873        | 13        | 4035          | 3749        | 2204        | 58.79%        | 1545        | 41.21%        |
| MB Precinct                               | 9873        | 13        | 915           | 866         | 482         | 55.66%        | 384         | 44.34%        |
| Provisional                               | 9873        | 13        | 123           | 96          | 62          | 64.58%        | 34          | 35.42%        |
| <b>Total</b>                              | <b>9873</b> | <b>65</b> | <b>6010</b>   | <b>5594</b> | <b>3250</b> | <b>58.10%</b> | <b>2344</b> | <b>41.90%</b> |
| <b>Total</b>                              |             |           |               |             |             |               |             |               |
| <b>STATE SENATE</b>                       |             |           |               |             |             |               |             |               |
| <b>1ST STATE SENATE DISTRICT</b>          |             |           |               |             |             |               |             |               |
| Precinct TSx                              | 9873        | 13        | 0             | 0           | 0           | -             | 0           | -             |
| Precinct OS                               | 9873        | 13        | 937           | 883         | 502         | 56.85%        | 381         | 43.15%        |
| Precinct VBM                              | 9873        | 13        | 4035          | 3749        | 2204        | 58.79%        | 1545        | 41.21%        |
| MB Precinct                               | 9873        | 13        | 915           | 866         | 482         | 55.66%        | 384         | 44.34%        |
| Provisional                               | 9873        | 13        | 123           | 96          | 62          | 64.58%        | 34          | 35.42%        |
| <b>Total</b>                              | <b>9873</b> | <b>65</b> | <b>6010</b>   | <b>5594</b> | <b>3250</b> | <b>58.10%</b> | <b>2344</b> | <b>41.90%</b> |
| <b>Total</b>                              |             |           |               |             |             |               |             |               |
| Precinct TSx                              | 9873        | 13        | 0             | 0           | 0           | -             | 0           | -             |
| Precinct OS                               | 9873        | 13        | 937           | 883         | 502         | 56.85%        | 381         | 43.15%        |
| Precinct VBM                              | 9873        | 13        | 4035          | 3749        | 2204        | 58.79%        | 1545        | 41.21%        |
| MB Precinct                               | 9873        | 13        | 915           | 866         | 482         | 55.66%        | 384         | 44.34%        |
| Provisional                               | 9873        | 13        | 123           | 96          | 62          | 64.58%        | 34          | 35.42%        |
| <b>Total</b>                              | <b>9873</b> | <b>65</b> | <b>6010</b>   | <b>5594</b> | <b>3250</b> | <b>58.10%</b> | <b>2344</b> | <b>41.90%</b> |
| <b>Total</b>                              |             |           |               |             |             |               |             |               |
| <b>STATE ASSEMBLY DISTRICTS</b>           |             |           |               |             |             |               |             |               |
| <b>5TH STATE ASSEMBLY DIST</b>            |             |           |               |             |             |               |             |               |
| Precinct TSx                              | 9873        | 13        | 0             | 0           | 0           | -             | 0           | -             |
| Precinct OS                               | 9873        | 13        | 937           | 883         | 502         | 56.85%        | 381         | 43.15%        |
| Precinct VBM                              | 9873        | 13        | 4035          | 3749        | 2204        | 58.79%        | 1545        | 41.21%        |
| MB Precinct                               | 9873        | 13        | 915           | 866         | 482         | 55.66%        | 384         | 44.34%        |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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Measure C - Mother Lode USD (55% to pass)

|                                   | Reg. Voters | Vote For | Times Counted | Total Votes | YES  | NO      |      |        |
|-----------------------------------|-------------|----------|---------------|-------------|------|---------|------|--------|
| Provisional                       | 9873        | 13       | 123           | 96          | 62   | 64.58%  | 34   | 35.42% |
| Total                             | 9873        | 65       | 6010          | 5594        | 3250 | 58.10%  | 2344 | 41.90% |
| <b>6TH STATE ASSEMBLY DIST</b>    |             |          |               |             |      |         |      |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| Total                             | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| <b>Total</b>                      |             |          |               |             |      |         |      |        |
| Precinct TSx                      | 9873        | 13       | 0             | 0           | 0    | -       | 0    | -      |
| Precinct OS                       | 9873        | 13       | 937           | 883         | 502  | 56.85%  | 381  | 43.15% |
| Precinct VBM                      | 9873        | 13       | 4035          | 3749        | 2204 | 58.79%  | 1545 | 41.21% |
| MB Precinct                       | 9873        | 13       | 915           | 866         | 482  | 55.66%  | 384  | 44.34% |
| Provisional                       | 9873        | 13       | 123           | 96          | 62   | 64.58%  | 34   | 35.42% |
| Total                             | 9873        | 65       | 6010          | 5594        | 3250 | 58.10%  | 2344 | 41.90% |
| <b>SUPERVISORIAL DISTRICTS</b>    |             |          |               |             |      |         |      |        |
| <b>1ST SUPERVISORIAL DISTRICT</b> |             |          |               |             |      |         |      |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| Total                             | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| <b>2ND SUPERVISORIAL DISTRICT</b> |             |          |               |             |      |         |      |        |
| Precinct TSx                      | 1339        | 3        | 0             | 0           | 0    | -       | 0    | -      |
| Precinct OS                       | 1339        | 3        | 38            | 34          | 21   | 61.76%  | 13   | 38.24% |
| Precinct VBM                      | 1339        | 3        | 206           | 192         | 110  | 57.29%  | 82   | 42.71% |
| MB Precinct                       | 1339        | 3        | 658           | 628         | 356  | 56.69%  | 272  | 43.31% |
| Provisional                       | 1339        | 3        | 9             | 7           | 3    | 42.86%  | 4    | 57.14% |
| Total                             | 1339        | 15       | 911           | 861         | 490  | 56.91%  | 371  | 43.09% |
| <b>3RD SUPERVISORIAL DISTRICT</b> |             |          |               |             |      |         |      |        |
| Precinct TSx                      | 6789        | 8        | 0             | 0           | 0    | -       | 0    | -      |
| Precinct OS                       | 6789        | 8        | 772           | 726         | 401  | 55.23%  | 325  | 44.77% |
| Precinct VBM                      | 6789        | 8        | 3062          | 2834        | 1684 | 59.42%  | 1150 | 40.58% |
| MB Precinct                       | 6789        | 8        | 1             | 1           | 1    | 100.00% | 0    | 0.00%  |
| Provisional                       | 6789        | 8        | 95            | 76          | 50   | 65.79%  | 26   | 34.21% |
| Total                             | 6789        | 40       | 3930          | 3637        | 2136 | 58.73%  | 1501 | 41.27% |
| <b>4TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |      |         |      |        |
| Precinct TSx                      | 1745        | 2        | 0             | 0           | 0    | -       | 0    | -      |
| Precinct OS                       | 1745        | 2        | 127           | 123         | 80   | 65.04%  | 43   | 34.96% |
| Precinct VBM                      | 1745        | 2        | 767           | 723         | 410  | 56.71%  | 313  | 43.29% |
| MB Precinct                       | 1745        | 2        | 256           | 237         | 125  | 52.74%  | 112  | 47.26% |
| Provisional                       | 1745        | 2        | 19            | 13          | 9    | 69.23%  | 4    | 30.77% |
| Total                             | 1745        | 10       | 1169          | 1096        | 624  | 56.93%  | 472  | 43.07% |
| <b>5TH SUPERVISORIAL DISTRICT</b> |             |          |               |             |      |         |      |        |
| Precinct TSx                      | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| Precinct OS                       | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| Precinct VBM                      | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| MB Precinct                       | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| Provisional                       | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| Total                             | 0           | 0        | 0             | 0           | 0    | -       | 0    | -      |
| <b>Total</b>                      |             |          |               |             |      |         |      |        |
| Precinct TSx                      | 9873        | 13       | 0             | 0           | 0    | -       | 0    | -      |
| Precinct OS                       | 9873        | 13       | 937           | 883         | 502  | 56.85%  | 381  | 43.15% |
| Precinct VBM                      | 9873        | 13       | 4035          | 3749        | 2204 | 58.79%  | 1545 | 41.21% |
| MB Precinct                       | 9873        | 13       | 915           | 866         | 482  | 55.66%  | 384  | 44.34% |
| Provisional                       | 9873        | 13       | 123           | 96          | 62   | 64.58%  | 34   | 35.42% |
| Total                             | 9873        | 65       | 6010          | 5594        | 3250 | 58.10%  | 2344 | 41.90% |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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Measure C - Mother Lode USD (55% to pass)

|                                 | Reg. Voters | Vote For  | Times Counted | Total Votes | YES         |               | NO          |               |
|---------------------------------|-------------|-----------|---------------|-------------|-------------|---------------|-------------|---------------|
| <b>CITIES</b>                   |             |           |               |             |             |               |             |               |
| <b>CITY OF PLACERVILLE</b>      |             |           |               |             |             |               |             |               |
| Precinct TSx                    | 259         | 1         | 0             | 0           | 0           | -             | 0           | -             |
| Precinct OS                     | 259         | 1         | 12            | 9           | 2           | 22.22%        | 7           | 77.78%        |
| Precinct VBM                    | 259         | 1         | 100           | 88          | 69          | 78.41%        | 19          | 21.59%        |
| MB Precinct                     | 259         | 1         | 0             | 0           | 0           | -             | 0           | -             |
| Provisional                     | 259         | 1         | 0             | 0           | 0           | -             | 0           | -             |
| <b>Total</b>                    | <b>259</b>  | <b>5</b>  | <b>112</b>    | <b>97</b>   | <b>71</b>   | <b>73.20%</b> | <b>26</b>   | <b>26.80%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |             |           |               |             |             |               |             |               |
| Precinct TSx                    | 0           | 0         | 0             | 0           | 0           | -             | 0           | -             |
| Precinct OS                     | 0           | 0         | 0             | 0           | 0           | -             | 0           | -             |
| Precinct VBM                    | 0           | 0         | 0             | 0           | 0           | -             | 0           | -             |
| MB Precinct                     | 0           | 0         | 0             | 0           | 0           | -             | 0           | -             |
| Provisional                     | 0           | 0         | 0             | 0           | 0           | -             | 0           | -             |
| <b>Total</b>                    | <b>0</b>    | <b>0</b>  | <b>0</b>      | <b>0</b>    | <b>0</b>    | <b>-</b>      | <b>0</b>    | <b>-</b>      |
| <b>UNINCORPORATED AREA</b>      |             |           |               |             |             |               |             |               |
| Precinct TSx                    | 9614        | 12        | 0             | 0           | 0           | -             | 0           | -             |
| Precinct OS                     | 9614        | 12        | 925           | 874         | 500         | 57.21%        | 374         | 42.79%        |
| Precinct VBM                    | 9614        | 12        | 3935          | 3661        | 2135        | 58.32%        | 1526        | 41.68%        |
| MB Precinct                     | 9614        | 12        | 915           | 866         | 482         | 55.66%        | 384         | 44.34%        |
| Provisional                     | 9614        | 12        | 123           | 96          | 62          | 64.58%        | 34          | 35.42%        |
| <b>Total</b>                    | <b>9614</b> | <b>60</b> | <b>5898</b>   | <b>5497</b> | <b>3179</b> | <b>57.83%</b> | <b>2318</b> | <b>42.17%</b> |
| <b>Total</b>                    |             |           |               |             |             |               |             |               |
| Precinct TSx                    | 9873        | 13        | 0             | 0           | 0           | -             | 0           | -             |
| Precinct OS                     | 9873        | 13        | 937           | 883         | 502         | 56.85%        | 381         | 43.15%        |
| Precinct VBM                    | 9873        | 13        | 4035          | 3749        | 2204        | 58.79%        | 1545        | 41.21%        |
| MB Precinct                     | 9873        | 13        | 915           | 866         | 482         | 55.66%        | 384         | 44.34%        |
| Provisional                     | 9873        | 13        | 123           | 96          | 62          | 64.58%        | 34          | 35.42%        |
| <b>Total</b>                    | <b>9873</b> | <b>65</b> | <b>6010</b>   | <b>5594</b> | <b>3250</b> | <b>58.10%</b> | <b>2344</b> | <b>41.90%</b> |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
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|                          | Measure A - County (majority to pass) |          |               |             |            |
|--------------------------|---------------------------------------|----------|---------------|-------------|------------|
|                          | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES        |
| <b>Jurisdiction Wide</b> |                                       |          |               |             |            |
| 100001.11-CP 100001.11   |                                       |          |               |             |            |
| Precinct TSx             | 1615                                  | 1        | 0             | 0           | 0          |
| Precinct OS              | 1615                                  | 1        | 190           | 162         | 89 54.94%  |
| Precinct VBM             | 1615                                  | 1        | 781           | 654         | 409 62.54% |
| MB Precinct              | 1615                                  | 1        | 0             | 0           | 0          |
| Provisional              | 1615                                  | 1        | 17            | 11          | 5 45.45%   |
| Total                    | 1615                                  | 5        | 988           | 827         | 503 60.82% |
| 100002.11-CP 100002.11   |                                       |          |               |             |            |
| Precinct TSx             | 1000                                  | 1        | 0             | 0           | 0          |
| Precinct OS              | 1000                                  | 1        | 89            | 74          | 43 58.11%  |
| Precinct VBM             | 1000                                  | 1        | 476           | 409         | 211 51.59% |
| MB Precinct              | 1000                                  | 1        | 0             | 0           | 0          |
| Provisional              | 1000                                  | 1        | 20            | 18          | 9 50.00%   |
| Total                    | 1000                                  | 5        | 585           | 501         | 263 52.50% |
| 100003.11-CP 100003.11   |                                       |          |               |             |            |
| Precinct TSx             | 1269                                  | 1        | 0             | 0           | 0          |
| Precinct OS              | 1269                                  | 1        | 123           | 106         | 52 49.06%  |
| Precinct VBM             | 1269                                  | 1        | 773           | 651         | 432 66.36% |
| MB Precinct              | 1269                                  | 1        | 0             | 0           | 0          |
| Provisional              | 1269                                  | 1        | 24            | 19          | 9 47.37%   |
| Total                    | 1269                                  | 5        | 920           | 776         | 493 63.53% |
| 100004.11-CP 100004.11   |                                       |          |               |             |            |
| Precinct TSx             | 1633                                  | 1        | 0             | 0           | 0          |
| Precinct OS              | 1633                                  | 1        | 254           | 215         | 112 52.09% |
| Precinct VBM             | 1633                                  | 1        | 738           | 628         | 378 60.19% |
| MB Precinct              | 1633                                  | 1        | 0             | 0           | 0          |
| Provisional              | 1633                                  | 1        | 4             | 2           | 0 0.00%    |
| Total                    | 1633                                  | 5        | 996           | 845         | 490 57.99% |
| 100005.11-CP 100005.11   |                                       |          |               |             |            |
| Precinct TSx             | 1586                                  | 1        | 0             | 0           | 0          |
| Precinct OS              | 1586                                  | 1        | 146           | 125         | 74 59.20%  |
| Precinct VBM             | 1586                                  | 1        | 895           | 771         | 514 66.67% |
| MB Precinct              | 1586                                  | 1        | 0             | 0           | 0          |
| Provisional              | 1586                                  | 1        | 23            | 20          | 11 55.00%  |
| Total                    | 1586                                  | 5        | 1064          | 916         | 599 65.39% |
| 100006.11-CP 100006.11   |                                       |          |               |             |            |
| Precinct TSx             | 1928                                  | 1        | 0             | 0           | 0          |
| Precinct OS              | 1928                                  | 1        | 252           | 199         | 112 56.28% |
| Precinct VBM             | 1928                                  | 1        | 944           | 779         | 476 61.10% |
| MB Precinct              | 1928                                  | 1        | 0             | 0           | 0          |
| Provisional              | 1928                                  | 1        | 31            | 20          | 11 55.00%  |
| Total                    | 1928                                  | 5        | 1227          | 998         | 599 60.02% |
| 100008.11-CP 100008.11   |                                       |          |               |             |            |
| Precinct TSx             | 1671                                  | 1        | 0             | 0           | 0          |
| Precinct OS              | 1671                                  | 1        | 197           | 151         | 80 52.98%  |
| Precinct VBM             | 1671                                  | 1        | 740           | 611         | 387 63.34% |
| MB Precinct              | 1671                                  | 1        | 0             | 0           | 0          |
| Provisional              | 1671                                  | 1        | 17            | 12          | 9 75.00%   |
| Total                    | 1671                                  | 5        | 954           | 774         | 476 61.50% |
| 100009.11-CP 100009.11   |                                       |          |               |             |            |
| Precinct TSx             | 1838                                  | 1        | 0             | 0           | 0          |
| Precinct OS              | 1838                                  | 1        | 169           | 138         | 78 56.52%  |
| Precinct VBM             | 1838                                  | 1        | 751           | 636         | 373 58.65% |
| MB Precinct              | 1838                                  | 1        | 0             | 0           | 0          |
| Provisional              | 1838                                  | 1        | 26            | 16          | 12 75.00%  |
| Total                    | 1838                                  | 5        | 946           | 790         | 463 58.61% |
| 100012.11-CP 100012.11   |                                       |          |               |             |            |
| Precinct TSx             | 2108                                  | 1        | 0             | 0           | 0          |
| Precinct OS              | 2108                                  | 1        | 255           | 206         | 98 47.57%  |
| Precinct VBM             | 2108                                  | 1        | 968           | 818         | 491 60.02% |

**COUNTY OF EL DORADO**  
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|                        | Measure A - County (majority to pass) |          |               |             |     |        |
|------------------------|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| MB Precinct            | 2108                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 2108                                  | 1        | 29            | 24          | 14  | 58.33% |
| Total                  | 2108                                  | 5        | 1252          | 1048        | 603 | 57.54% |
| 100013.11-CP 100013.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 2087                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 2087                                  | 1        | 250           | 204         | 114 | 55.88% |
| Precinct VBM           | 2087                                  | 1        | 908           | 773         | 434 | 56.14% |
| MB Precinct            | 2087                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 2087                                  | 1        | 28            | 23          | 10  | 43.48% |
| Total                  | 2087                                  | 5        | 1186          | 1000        | 558 | 55.80% |
| 100014.11-CP 100014.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1659                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1659                                  | 1        | 211           | 162         | 80  | 49.38% |
| Precinct VBM           | 1659                                  | 1        | 706           | 596         | 338 | 56.71% |
| MB Precinct            | 1659                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1659                                  | 1        | 30            | 22          | 11  | 50.00% |
| Total                  | 1659                                  | 5        | 947           | 780         | 429 | 55.00% |
| 100022.11-CP 100022.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1432                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1432                                  | 1        | 177           | 156         | 95  | 60.90% |
| Precinct VBM           | 1432                                  | 1        | 678           | 576         | 332 | 57.64% |
| MB Precinct            | 1432                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1432                                  | 1        | 13            | 9           | 5   | 55.56% |
| Total                  | 1432                                  | 5        | 868           | 741         | 432 | 58.30% |
| 100023.11-CP 100023.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 2135                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 2135                                  | 1        | 217           | 171         | 78  | 45.61% |
| Precinct VBM           | 2135                                  | 1        | 1053          | 918         | 547 | 59.59% |
| MB Precinct            | 2135                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 2135                                  | 1        | 33            | 22          | 13  | 59.09% |
| Total                  | 2135                                  | 5        | 1303          | 1111        | 638 | 57.43% |
| 200003.12-CP 200003.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1362                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1362                                  | 1        | 166           | 140         | 70  | 50.00% |
| Precinct VBM           | 1362                                  | 1        | 558           | 467         | 252 | 53.96% |
| MB Precinct            | 1362                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1362                                  | 1        | 31            | 26          | 12  | 46.15% |
| Total                  | 1362                                  | 5        | 755           | 633         | 334 | 52.76% |
| 200004.12-CP 200004.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1507                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1507                                  | 1        | 182           | 151         | 80  | 52.98% |
| Precinct VBM           | 1507                                  | 1        | 662           | 564         | 343 | 60.82% |
| MB Precinct            | 1507                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1507                                  | 1        | 14            | 13          | 8   | 61.54% |
| Total                  | 1507                                  | 5        | 858           | 728         | 431 | 59.20% |
| 200005.02-CP 200005.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 867                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 867                                   | 1        | 94            | 82          | 39  | 47.56% |
| Precinct VBM           | 867                                   | 1        | 452           | 391         | 177 | 45.27% |
| MB Precinct            | 867                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 867                                   | 1        | 21            | 16          | 7   | 43.75% |
| Total                  | 867                                   | 5        | 567           | 489         | 223 | 45.60% |
| 200006.12-CP 200006.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1086                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1086                                  | 1        | 117           | 98          | 46  | 46.94% |
| Precinct VBM           | 1086                                  | 1        | 532           | 462         | 255 | 55.19% |
| MB Precinct            | 1086                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1086                                  | 1        | 29            | 24          | 9   | 37.50% |
| Total                  | 1086                                  | 5        | 678           | 584         | 310 | 53.08% |
| 200008.12-CP 200008.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 999                                   | 1        | 0             | 0           | 0   | -      |

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|------------------------|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Precinct OS            | 999                                   | 1        | 155           | 120         | 58  | 48.33% |
| Precinct VBM           | 999                                   | 1        | 379           | 319         | 169 | 52.98% |
| MB Precinct            | 999                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 999                                   | 1        | 10            | 8           | 5   | 62.50% |
| Total                  | 999                                   | 5        | 544           | 447         | 232 | 51.90% |
| 200010.12-CP 200010.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1976                                  | 1        | 4             | 4           | 3   | 75.00% |
| Precinct OS            | 1976                                  | 1        | 197           | 161         | 75  | 46.58% |
| Precinct VBM           | 1976                                  | 1        | 859           | 731         | 416 | 56.91% |
| MB Precinct            | 1976                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1976                                  | 1        | 34            | 26          | 14  | 53.85% |
| Total                  | 1976                                  | 5        | 1094          | 922         | 508 | 55.10% |
| 200012.12-CP 200012.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1064                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1064                                  | 1        | 145           | 121         | 54  | 44.63% |
| Precinct VBM           | 1064                                  | 1        | 464           | 396         | 229 | 57.83% |
| MB Precinct            | 1064                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1064                                  | 1        | 14            | 11          | 4   | 36.36% |
| Total                  | 1064                                  | 5        | 623           | 528         | 287 | 54.36% |
| 200013.12-CP 200013.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1574                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1574                                  | 1        | 211           | 181         | 94  | 51.93% |
| Precinct VBM           | 1574                                  | 1        | 701           | 594         | 325 | 54.71% |
| MB Precinct            | 1574                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1574                                  | 1        | 23            | 20          | 10  | 50.00% |
| Total                  | 1574                                  | 5        | 935           | 795         | 429 | 53.96% |
| 200014.12-CP 200014.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1438                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1438                                  | 1        | 201           | 162         | 98  | 60.49% |
| Precinct VBM           | 1438                                  | 1        | 613           | 503         | 284 | 56.46% |
| MB Precinct            | 1438                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1438                                  | 1        | 30            | 23          | 15  | 65.22% |
| Total                  | 1438                                  | 5        | 844           | 688         | 397 | 57.70% |
| 200015.02-CP 200015.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1832                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1832                                  | 1        | 218           | 179         | 72  | 40.22% |
| Precinct VBM           | 1832                                  | 1        | 886           | 743         | 380 | 51.14% |
| MB Precinct            | 1832                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1832                                  | 1        | 22            | 19          | 7   | 36.84% |
| Total                  | 1832                                  | 5        | 1126          | 941         | 459 | 48.78% |
| 200016.12-CP 200016.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 301                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 301                                   | 1        | 26            | 23          | 12  | 52.17% |
| Precinct VBM           | 301                                   | 1        | 139           | 117         | 58  | 49.57% |
| MB Precinct            | 301                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 301                                   | 1        | 6             | 4           | 2   | 50.00% |
| Total                  | 301                                   | 5        | 171           | 144         | 72  | 50.00% |
| 200016.13-CP 200016.13 |                                       |          |               |             |     |        |
| Precinct TSx           | 565                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 565                                   | 1        | 80            | 65          | 34  | 52.31% |
| Precinct VBM           | 565                                   | 1        | 338           | 280         | 141 | 50.36% |
| MB Precinct            | 565                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 565                                   | 1        | 6             | 5           | 3   | 60.00% |
| Total                  | 565                                   | 5        | 424           | 350         | 178 | 50.86% |
| 200017.02-CP 200017.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1286                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1286                                  | 1        | 146           | 121         | 57  | 47.11% |
| Precinct VBM           | 1286                                  | 1        | 594           | 498         | 252 | 50.60% |
| MB Precinct            | 1286                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1286                                  | 1        | 12            | 10          | 5   | 50.00% |
| Total                  | 1286                                  | 5        | 752           | 629         | 314 | 49.92% |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| 200018.02-CP 200018.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1454                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1454                                  | 1        | 169           | 142         | 65  | 45.77% |
| Precinct VBM           | 1454                                  | 1        | 704           | 600         | 317 | 52.83% |
| MB Precinct            | 1454                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1454                                  | 1        | 13            | 9           | 3   | 33.33% |
| Total                  | 1454                                  | 5        | 886           | 751         | 385 | 51.26% |
| 200020.02-CP 200020.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1213                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1213                                  | 1        | 139           | 116         | 59  | 50.86% |
| Precinct VBM           | 1213                                  | 1        | 526           | 450         | 245 | 54.44% |
| MB Precinct            | 1213                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1213                                  | 1        | 19            | 16          | 7   | 43.75% |
| Total                  | 1213                                  | 5        | 684           | 582         | 311 | 53.44% |
| 200021.01-CP 200021.01 |                                       |          |               |             |     |        |
| Precinct TSx           | 436                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 436                                   | 1        | 38            | 30          | 13  | 43.33% |
| Precinct VBM           | 436                                   | 1        | 206           | 179         | 102 | 56.98% |
| MB Precinct            | 436                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 436                                   | 1        | 7             | 5           | 0   | 0.00%  |
| Total                  | 436                                   | 5        | 251           | 214         | 115 | 53.74% |
| 200021.02-CP 200021.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1296                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1296                                  | 1        | 170           | 145         | 80  | 55.17% |
| Precinct VBM           | 1296                                  | 1        | 752           | 593         | 300 | 50.59% |
| MB Precinct            | 1296                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1296                                  | 1        | 20            | 13          | 3   | 23.08% |
| Total                  | 1296                                  | 5        | 942           | 751         | 383 | 51.00% |
| 200024.02-CP 200024.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 555                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 555                                   | 1        | 55            | 48          | 19  | 39.58% |
| Precinct VBM           | 555                                   | 1        | 282           | 246         | 111 | 45.12% |
| MB Precinct            | 555                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 555                                   | 1        | 9             | 5           | 2   | 40.00% |
| Total                  | 555                                   | 5        | 346           | 299         | 132 | 44.15% |
| 300001.04-CP 300001.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 259                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 259                                   | 1        | 12            | 10          | 4   | 40.00% |
| Precinct VBM           | 259                                   | 1        | 100           | 90          | 46  | 51.11% |
| MB Precinct            | 259                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 259                                   | 1        | 0             | 0           | 0   | -      |
| Total                  | 259                                   | 5        | 112           | 100         | 50  | 50.00% |
| 300001.05-CP 300001.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 731                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 731                                   | 1        | 76            | 62          | 37  | 59.68% |
| Precinct VBM           | 731                                   | 1        | 330           | 270         | 142 | 52.59% |
| MB Precinct            | 731                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 731                                   | 1        | 10            | 9           | 5   | 55.56% |
| Total                  | 731                                   | 5        | 416           | 341         | 184 | 53.96% |
| 300003.05-CP 300003.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1932                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1932                                  | 1        | 205           | 171         | 75  | 43.86% |
| Precinct VBM           | 1932                                  | 1        | 924           | 769         | 425 | 55.27% |
| MB Precinct            | 1932                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1932                                  | 1        | 27            | 21          | 10  | 47.62% |
| Total                  | 1932                                  | 5        | 1156          | 961         | 510 | 53.07% |
| 300004.05-CP 300004.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1273                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1273                                  | 1        | 175           | 140         | 73  | 52.14% |
| Precinct VBM           | 1273                                  | 1        | 576           | 477         | 266 | 55.77% |
| MB Precinct            | 1273                                  | 1        | 0             | 0           | 0   | -      |

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|---------------------------------------|-------------|----------|---------------|-------------|-----|--------|--|
|                                       | Reg. Voters | Vote For | Times Counted | Total Votes | YES |        |  |
| Provisional                           | 1273        | 1        | 17            | 12          | 3   | 25.00% |  |
| Total                                 | 1273        | 5        | 768           | 629         | 342 | 54.37% |  |
| 300005.05-CP 300005.05                |             |          |               |             |     |        |  |
| Precinct TSx                          | 1374        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 1374        | 1        | 163           | 133         | 70  | 52.63% |  |
| Precinct VBM                          | 1374        | 1        | 660           | 560         | 313 | 55.89% |  |
| MB Precinct                           | 1374        | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 1374        | 1        | 26            | 18          | 7   | 38.89% |  |
| Total                                 | 1374        | 5        | 849           | 711         | 390 | 54.85% |  |
| 300008.05-CP 300008.05                |             |          |               |             |     |        |  |
| Precinct TSx                          | 1366        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 1366        | 1        | 139           | 120         | 52  | 43.33% |  |
| Precinct VBM                          | 1366        | 1        | 740           | 643         | 345 | 53.65% |  |
| MB Precinct                           | 1366        | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 1366        | 1        | 14            | 10          | 4   | 40.00% |  |
| Total                                 | 1366        | 5        | 893           | 773         | 401 | 51.88% |  |
| 300009.04-CP 300009.04                |             |          |               |             |     |        |  |
| Precinct TSx                          | 873         | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 873         | 1        | 121           | 100         | 42  | 42.00% |  |
| Precinct VBM                          | 873         | 1        | 395           | 323         | 161 | 49.85% |  |
| MB Precinct                           | 873         | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 873         | 1        | 13            | 10          | 5   | 50.00% |  |
| Total                                 | 873         | 5        | 529           | 433         | 208 | 48.04% |  |
| 300010.04-CP 300010.04                |             |          |               |             |     |        |  |
| Precinct TSx                          | 1340        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 1340        | 1        | 171           | 147         | 58  | 39.46% |  |
| Precinct VBM                          | 1340        | 1        | 656           | 562         | 287 | 51.07% |  |
| MB Precinct                           | 1340        | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 1340        | 1        | 11            | 10          | 2   | 20.00% |  |
| Total                                 | 1340        | 5        | 838           | 719         | 347 | 48.26% |  |
| 300011.04-CP 300011.04                |             |          |               |             |     |        |  |
| Precinct TSx                          | 1491        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 1491        | 1        | 142           | 128         | 60  | 46.88% |  |
| Precinct VBM                          | 1491        | 1        | 711           | 601         | 290 | 48.25% |  |
| MB Precinct                           | 1491        | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 1491        | 1        | 22            | 16          | 6   | 37.50% |  |
| Total                                 | 1491        | 5        | 875           | 745         | 356 | 47.79% |  |
| 300013.05-CP 300013.05                |             |          |               |             |     |        |  |
| Precinct TSx                          | 1490        | 1        | 3             | 2           | 1   | 50.00% |  |
| Precinct OS                           | 1490        | 1        | 128           | 113         | 54  | 47.79% |  |
| Precinct VBM                          | 1490        | 1        | 721           | 641         | 346 | 53.98% |  |
| MB Precinct                           | 1490        | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 1490        | 1        | 15            | 9           | 6   | 66.67% |  |
| Total                                 | 1490        | 5        | 867           | 765         | 407 | 53.20% |  |
| 300015.04-CP 300015.04                |             |          |               |             |     |        |  |
| Precinct TSx                          | 119         | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 119         | 1        | 10            | 9           | 2   | 22.22% |  |
| Precinct VBM                          | 119         | 1        | 63            | 43          | 21  | 48.84% |  |
| MB Precinct                           | 119         | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 119         | 1        | 4             | 4           | 2   | 50.00% |  |
| Total                                 | 119         | 5        | 77            | 56          | 25  | 44.64% |  |
| 300015.05-CP 300015.05                |             |          |               |             |     |        |  |
| Precinct TSx                          | 503         | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 503         | 1        | 66            | 54          | 24  | 44.44% |  |
| Precinct VBM                          | 503         | 1        | 266           | 218         | 116 | 53.21% |  |
| MB Precinct                           | 503         | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 503         | 1        | 2             | 1           | 0   | 0.00%  |  |
| Total                                 | 503         | 5        | 334           | 273         | 140 | 51.28% |  |
| 300017.05-CP 300017.05                |             |          |               |             |     |        |  |
| Precinct TSx                          | 1240        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 1240        | 1        | 117           | 95          | 40  | 42.11% |  |

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|------------------------|---------------------------------------|----------|---------------|-------------|------------|---------------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES        |               |
| Precinct VBM           | 1240                                  | 1        | 655           | 558         | 328        | 58.78%        |
| MB Precinct            | 1240                                  | 1        | 0             | 0           | 0          | -             |
| Provisional            | 1240                                  | 1        | 9             | 7           | 6          | 85.71%        |
| <b>Total</b>           | <b>1240</b>                           | <b>5</b> | <b>781</b>    | <b>660</b>  | <b>374</b> | <b>56.67%</b> |
| 300018.04-CP 300018.04 |                                       |          |               |             |            |               |
| Precinct TSx           | 1306                                  | 1        | 0             | 0           | 0          | -             |
| Precinct OS            | 1306                                  | 1        | 148           | 129         | 52         | 40.31%        |
| Precinct VBM           | 1306                                  | 1        | 505           | 430         | 180        | 41.86%        |
| MB Precinct            | 1306                                  | 1        | 0             | 0           | 0          | -             |
| Provisional            | 1306                                  | 1        | 31            | 22          | 12         | 54.55%        |
| <b>Total</b>           | <b>1306</b>                           | <b>5</b> | <b>684</b>    | <b>581</b>  | <b>244</b> | <b>42.00%</b> |
| 300020.04-CP 300020.04 |                                       |          |               |             |            |               |
| Precinct TSx           | 1401                                  | 1        | 0             | 0           | 0          | -             |
| Precinct OS            | 1401                                  | 1        | 168           | 145         | 58         | 40.00%        |
| Precinct VBM           | 1401                                  | 1        | 632           | 524         | 264        | 50.38%        |
| MB Precinct            | 1401                                  | 1        | 0             | 0           | 0          | -             |
| Provisional            | 1401                                  | 1        | 14            | 10          | 6          | 60.00%        |
| <b>Total</b>           | <b>1401</b>                           | <b>5</b> | <b>814</b>    | <b>679</b>  | <b>328</b> | <b>48.31%</b> |
| 300021.03-CP 300021.03 |                                       |          |               |             |            |               |
| Precinct TSx           | 1234                                  | 1        | 0             | 0           | 0          | -             |
| Precinct OS            | 1234                                  | 1        | 153           | 123         | 61         | 49.59%        |
| Precinct VBM           | 1234                                  | 1        | 614           | 519         | 282        | 54.34%        |
| MB Precinct            | 1234                                  | 1        | 0             | 0           | 0          | -             |
| Provisional            | 1234                                  | 1        | 13            | 10          | 3          | 30.00%        |
| <b>Total</b>           | <b>1234</b>                           | <b>5</b> | <b>780</b>    | <b>652</b>  | <b>346</b> | <b>53.07%</b> |
| 300022.05-CP 300022.05 |                                       |          |               |             |            |               |
| Precinct TSx           | 872                                   | 1        | 0             | 0           | 0          | -             |
| Precinct OS            | 872                                   | 1        | 101           | 84          | 25         | 29.76%        |
| Precinct VBM           | 872                                   | 1        | 356           | 303         | 155        | 51.16%        |
| MB Precinct            | 872                                   | 1        | 0             | 0           | 0          | -             |
| Provisional            | 872                                   | 1        | 12            | 10          | 6          | 60.00%        |
| <b>Total</b>           | <b>872</b>                            | <b>5</b> | <b>469</b>    | <b>397</b>  | <b>186</b> | <b>46.85%</b> |
| 300023.03-CP 300023.03 |                                       |          |               |             |            |               |
| Precinct TSx           | 1819                                  | 1        | 0             | 0           | 0          | -             |
| Precinct OS            | 1819                                  | 1        | 228           | 200         | 97         | 48.50%        |
| Precinct VBM           | 1819                                  | 1        | 924           | 779         | 401        | 51.48%        |
| MB Precinct            | 1819                                  | 1        | 0             | 0           | 0          | -             |
| Provisional            | 1819                                  | 1        | 26            | 19          | 9          | 47.37%        |
| <b>Total</b>           | <b>1819</b>                           | <b>5</b> | <b>1178</b>   | <b>998</b>  | <b>507</b> | <b>50.80%</b> |
| 300023.05-CP 300023.05 |                                       |          |               |             |            |               |
| Precinct TSx           | 386                                   | 1        | 0             | 0           | 0          | -             |
| Precinct OS            | 386                                   | 1        | 45            | 38          | 27         | 71.05%        |
| Precinct VBM           | 386                                   | 1        | 193           | 167         | 89         | 53.29%        |
| MB Precinct            | 386                                   | 1        | 0             | 0           | 0          | -             |
| Provisional            | 386                                   | 1        | 8             | 5           | 3          | 60.00%        |
| <b>Total</b>           | <b>386</b>                            | <b>5</b> | <b>246</b>    | <b>210</b>  | <b>119</b> | <b>56.67%</b> |
| 400001.14-CP 400001.14 |                                       |          |               |             |            |               |
| Precinct TSx           | 1957                                  | 1        | 0             | 0           | 0          | -             |
| Precinct OS            | 1957                                  | 1        | 178           | 141         | 64         | 45.39%        |
| Precinct VBM           | 1957                                  | 1        | 992           | 825         | 465        | 56.36%        |
| MB Precinct            | 1957                                  | 1        | 0             | 0           | 0          | -             |
| Provisional            | 1957                                  | 1        | 23            | 20          | 8          | 40.00%        |
| <b>Total</b>           | <b>1957</b>                           | <b>5</b> | <b>1193</b>   | <b>986</b>  | <b>537</b> | <b>54.46%</b> |
| 400002.08-CP 400002.08 |                                       |          |               |             |            |               |
| Precinct TSx           | 1508                                  | 1        | 0             | 0           | 0          | -             |
| Precinct OS            | 1508                                  | 1        | 200           | 164         | 68         | 41.46%        |
| Precinct VBM           | 1508                                  | 1        | 730           | 609         | 314        | 51.56%        |
| MB Precinct            | 1508                                  | 1        | 0             | 0           | 0          | -             |
| Provisional            | 1508                                  | 1        | 15            | 12          | 5          | 41.67%        |
| <b>Total</b>           | <b>1508</b>                           | <b>5</b> | <b>945</b>    | <b>785</b>  | <b>387</b> | <b>49.30%</b> |
| 400003.08-CP 400003.08 |                                       |          |               |             |            |               |



**COUNTY OF EL DORADO**  
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|                        | Measure A - County (majority to pass) |          |               |             |     |        |
|------------------------|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Precinct TSx           | 1800                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1800                                  | 1        | 245           | 204         | 79  | 38.73% |
| Precinct VBM           | 1800                                  | 1        | 773           | 654         | 354 | 54.13% |
| MB Precinct            | 1800                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1800                                  | 1        | 35            | 23          | 9   | 39.13% |
| Total                  | 1800                                  | 5        | 1053          | 881         | 442 | 50.17% |
| 400004.14-CP 400004.14 |                                       |          |               |             |     |        |
| Precinct TSx           | 1536                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1536                                  | 1        | 194           | 158         | 79  | 50.00% |
| Precinct VBM           | 1536                                  | 1        | 662           | 561         | 318 | 56.68% |
| MB Precinct            | 1536                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1536                                  | 1        | 19            | 14          | 8   | 57.14% |
| Total                  | 1536                                  | 5        | 875           | 733         | 405 | 55.25% |
| 400005.08-CP 400005.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 655                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 655                                   | 1        | 79            | 66          | 25  | 37.88% |
| Precinct VBM           | 655                                   | 1        | 262           | 214         | 117 | 54.67% |
| MB Precinct            | 655                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 655                                   | 1        | 11            | 9           | 3   | 33.33% |
| Total                  | 655                                   | 5        | 352           | 289         | 145 | 50.17% |
| 400006.08-CP 400006.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1537                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1537                                  | 1        | 228           | 192         | 99  | 51.56% |
| Precinct VBM           | 1537                                  | 1        | 722           | 588         | 302 | 51.36% |
| MB Precinct            | 1537                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1537                                  | 1        | 19            | 15          | 6   | 40.00% |
| Total                  | 1537                                  | 5        | 969           | 795         | 407 | 51.19% |
| 400007.08-CP 400007.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1766                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1766                                  | 1        | 228           | 205         | 89  | 43.41% |
| Precinct VBM           | 1766                                  | 1        | 765           | 661         | 327 | 49.47% |
| MB Precinct            | 1766                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1766                                  | 1        | 22            | 18          | 10  | 55.56% |
| Total                  | 1766                                  | 5        | 1015          | 884         | 426 | 48.19% |
| 400010.08-CP 400010.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1875                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1875                                  | 1        | 244           | 211         | 81  | 38.39% |
| Precinct VBM           | 1875                                  | 1        | 869           | 745         | 330 | 44.30% |
| MB Precinct            | 1875                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1875                                  | 1        | 31            | 21          | 11  | 52.38% |
| Total                  | 1875                                  | 5        | 1144          | 977         | 422 | 43.19% |
| 400012.08-CP 400012.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 877                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 877                                   | 1        | 113           | 97          | 44  | 45.36% |
| Precinct VBM           | 877                                   | 1        | 434           | 359         | 177 | 49.30% |
| MB Precinct            | 877                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 877                                   | 1        | 22            | 16          | 7   | 43.75% |
| Total                  | 877                                   | 5        | 569           | 472         | 228 | 48.31% |
| 400013.08-CP 400013.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 758                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 758                                   | 1        | 127           | 108         | 39  | 36.11% |
| Precinct VBM           | 758                                   | 1        | 315           | 259         | 139 | 53.67% |
| MB Precinct            | 758                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 758                                   | 1        | 4             | 3           | 2   | 66.67% |
| Total                  | 758                                   | 5        | 446           | 370         | 180 | 48.65% |
| 400014.08-CP 400014.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1526                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1526                                  | 1        | 230           | 186         | 91  | 48.92% |
| Precinct VBM           | 1526                                  | 1        | 736           | 622         | 340 | 54.66% |
| MB Precinct            | 1526                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1526                                  | 1        | 36            | 31          | 14  | 45.16% |

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|------------------------|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Total                  | 1526                                  | 5        | 1002          | 839         | 445 | 53.04% |
| 400017.08-CP 400017.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1495                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1495                                  | 1        | 175           | 135         | 61  | 45.19% |
| Precinct VBM           | 1495                                  | 1        | 752           | 648         | 326 | 50.31% |
| MB Precinct            | 1495                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1495                                  | 1        | 28            | 23          | 11  | 47.83% |
| Total                  | 1495                                  | 5        | 955           | 806         | 398 | 49.38% |
| 400018.08-CP 400018.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 597                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 597                                   | 1        | 86            | 70          | 35  | 50.00% |
| Precinct VBM           | 597                                   | 1        | 276           | 241         | 140 | 58.09% |
| MB Precinct            | 597                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 597                                   | 1        | 7             | 4           | 1   | 25.00% |
| Total                  | 597                                   | 5        | 369           | 315         | 176 | 55.87% |
| 400020.08-CP 400020.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 724                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 724                                   | 1        | 114           | 97          | 40  | 41.24% |
| Precinct VBM           | 724                                   | 1        | 343           | 287         | 140 | 48.78% |
| MB Precinct            | 724                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 724                                   | 1        | 2             | 2           | 1   | 50.00% |
| Total                  | 724                                   | 5        | 459           | 386         | 181 | 46.89% |
| 400021.07-CP 400021.07 |                                       |          |               |             |     |        |
| Precinct TSx           | 1332                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1332                                  | 1        | 127           | 112         | 53  | 47.32% |
| Precinct VBM           | 1332                                  | 1        | 767           | 657         | 328 | 49.92% |
| MB Precinct            | 1332                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1332                                  | 1        | 18            | 13          | 6   | 46.15% |
| Total                  | 1332                                  | 5        | 912           | 782         | 387 | 49.49% |
| 500001.10-CP 500001.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1717                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1717                                  | 1        | 210           | 162         | 86  | 53.09% |
| Precinct VBM           | 1717                                  | 1        | 617           | 491         | 264 | 53.77% |
| MB Precinct            | 1717                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1717                                  | 1        | 19            | 13          | 6   | 46.15% |
| Total                  | 1717                                  | 5        | 846           | 666         | 356 | 53.45% |
| 500002.10-CP 500002.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1381                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1381                                  | 1        | 137           | 104         | 49  | 47.12% |
| Precinct VBM           | 1381                                  | 1        | 412           | 322         | 166 | 51.55% |
| MB Precinct            | 1381                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1381                                  | 1        | 31            | 23          | 10  | 43.48% |
| Total                  | 1381                                  | 5        | 580           | 449         | 225 | 50.11% |
| 500003.10-CP 500003.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1065                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1065                                  | 1        | 131           | 99          | 57  | 57.58% |
| Precinct VBM           | 1065                                  | 1        | 346           | 261         | 145 | 55.56% |
| MB Precinct            | 1065                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1065                                  | 1        | 11            | 4           | 0   | 0.00%  |
| Total                  | 1065                                  | 5        | 488           | 364         | 202 | 55.49% |
| 500004.10-CP 500004.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 981                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 981                                   | 1        | 122           | 106         | 50  | 47.17% |
| Precinct VBM           | 981                                   | 1        | 264           | 208         | 107 | 51.44% |
| MB Precinct            | 981                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 981                                   | 1        | 21            | 17          | 10  | 58.82% |
| Total                  | 981                                   | 5        | 407           | 331         | 167 | 50.45% |
| 500005.10-CP 500005.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 946                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 946                                   | 1        | 107           | 85          | 43  | 50.59% |
| Precinct VBM           | 946                                   | 1        | 250           | 202         | 96  | 47.52% |

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|---------------------------------------|-------------|----------|---------------|-------------|-----|--------|
|                                       | Reg. Voters | Vote For | Times Counted | Total Votes | YES |        |
| MB Precinct                           | 946         | 1        | 0             | 0           | 0   | -      |
| Provisional                           | 946         | 1        | 16            | 11          | 6   | 54.55% |
| Total                                 | 946         | 5        | 373           | 298         | 145 | 48.66% |
| 500006.10-CP 500006.10                |             |          |               |             |     |        |
| Precinct TSx                          | 1661        | 1        | 0             | 0           | 0   | -      |
| Precinct OS                           | 1661        | 1        | 190           | 140         | 73  | 52.14% |
| Precinct VBM                          | 1661        | 1        | 498           | 387         | 212 | 54.78% |
| MB Precinct                           | 1661        | 1        | 0             | 0           | 0   | -      |
| Provisional                           | 1661        | 1        | 58            | 36          | 22  | 61.11% |
| Total                                 | 1661        | 5        | 746           | 563         | 307 | 54.53% |
| 500007.10-CP 500007.10                |             |          |               |             |     |        |
| Precinct TSx                          | 1788        | 1        | 0             | 0           | 0   | -      |
| Precinct OS                           | 1788        | 1        | 208           | 168         | 96  | 57.14% |
| Precinct VBM                          | 1788        | 1        | 688           | 551         | 309 | 56.08% |
| MB Precinct                           | 1788        | 1        | 0             | 0           | 0   | -      |
| Provisional                           | 1788        | 1        | 20            | 14          | 7   | 50.00% |
| Total                                 | 1788        | 5        | 916           | 733         | 412 | 56.21% |
| 500009.10-CP 500009.10                |             |          |               |             |     |        |
| Precinct TSx                          | 1073        | 1        | 0             | 0           | 0   | -      |
| Precinct OS                           | 1073        | 1        | 121           | 94          | 55  | 58.51% |
| Precinct VBM                          | 1073        | 1        | 304           | 240         | 140 | 58.33% |
| MB Precinct                           | 1073        | 1        | 0             | 0           | 0   | -      |
| Provisional                           | 1073        | 1        | 38            | 23          | 14  | 60.87% |
| Total                                 | 1073        | 5        | 463           | 357         | 209 | 58.54% |
| 500011.10-CP 500011.10                |             |          |               |             |     |        |
| Precinct TSx                          | 1635        | 1        | 0             | 0           | 0   | -      |
| Precinct OS                           | 1635        | 1        | 178           | 146         | 73  | 50.00% |
| Precinct VBM                          | 1635        | 1        | 380           | 308         | 173 | 56.17% |
| MB Precinct                           | 1635        | 1        | 0             | 0           | 0   | -      |
| Provisional                           | 1635        | 1        | 40            | 30          | 17  | 56.67% |
| Total                                 | 1635        | 5        | 598           | 484         | 263 | 54.34% |
| 500012.10-CP 500012.10                |             |          |               |             |     |        |
| Precinct TSx                          | 1574        | 1        | 1             | 1           | 0   | 0.00%  |
| Precinct OS                           | 1574        | 1        | 195           | 138         | 62  | 44.93% |
| Precinct VBM                          | 1574        | 1        | 601           | 510         | 267 | 52.35% |
| MB Precinct                           | 1574        | 1        | 0             | 0           | 0   | -      |
| Provisional                           | 1574        | 1        | 37            | 26          | 18  | 69.23% |
| Total                                 | 1574        | 5        | 834           | 675         | 347 | 51.41% |
| 500015.10-CP 500015.10                |             |          |               |             |     |        |
| Precinct TSx                          | 978         | 1        | 0             | 0           | 0   | -      |
| Precinct OS                           | 978         | 1        | 105           | 87          | 45  | 51.72% |
| Precinct VBM                          | 978         | 1        | 407           | 328         | 200 | 60.98% |
| MB Precinct                           | 978         | 1        | 0             | 0           | 0   | -      |
| Provisional                           | 978         | 1        | 13            | 12          | 8   | 66.67% |
| Total                                 | 978         | 5        | 525           | 427         | 253 | 59.25% |
| 500020.10-CP 500020.10                |             |          |               |             |     |        |
| Precinct TSx                          | 1530        | 1        | 0             | 0           | 0   | -      |
| Precinct OS                           | 1530        | 1        | 161           | 142         | 67  | 47.18% |
| Precinct VBM                          | 1530        | 1        | 659           | 578         | 298 | 51.56% |
| MB Precinct                           | 1530        | 1        | 0             | 0           | 0   | -      |
| Provisional                           | 1530        | 1        | 27            | 26          | 18  | 69.23% |
| Total                                 | 1530        | 5        | 847           | 746         | 383 | 51.34% |
| 500021.10-CP 500021.10                |             |          |               |             |     |        |
| Precinct TSx                          | 1404        | 1        | 0             | 0           | 0   | -      |
| Precinct OS                           | 1404        | 1        | 169           | 134         | 57  | 42.54% |
| Precinct VBM                          | 1404        | 1        | 632           | 561         | 273 | 48.66% |
| MB Precinct                           | 1404        | 1        | 0             | 0           | 0   | -      |
| Provisional                           | 1404        | 1        | 28            | 22          | 9   | 40.91% |
| Total                                 | 1404        | 5        | 829           | 717         | 339 | 47.28% |
| 95U201.01-MB 95U201.01                |             |          |               |             |     |        |
| Precinct TSx                          | 8           | 1        | 0             | 0           | 0   | -      |

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|------------------------|---------------------------------------|----------|---------------|-------------|-----|---------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |         |
| Precinct OS            | 8                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 8                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 8                                     | 1        | 7             | 7           | 7   | 100.00% |
| Provisional            | 8                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 8                                     | 5        | 7             | 7           | 7   | 100.00% |
| 95U201.02-MB 95U201.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 541                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 541                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 541                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 541                                   | 1        | 358           | 285         | 149 | 52.28%  |
| Provisional            | 541                                   | 1        | 1             | 0           | 0   | -       |
| Total                  | 541                                   | 5        | 359           | 285         | 149 | 52.28%  |
| 95U202.02-MB 95U202.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 859                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 859                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 859                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 859                                   | 1        | 574           | 489         | 229 | 46.83%  |
| Provisional            | 859                                   | 1        | 4             | 3           | 0   | 0.00%   |
| Total                  | 859                                   | 5        | 578           | 492         | 229 | 46.54%  |
| 95U203.02-MB 95U203.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                                     | 5        | 0             | 0           | 0   | -       |
| 95U204.01-MB 95U204.01 |                                       |          |               |             |     |         |
| Precinct TSx           | 895                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 895                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 895                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 895                                   | 1        | 651           | 576         | 282 | 48.96%  |
| Provisional            | 895                                   | 1        | 2             | 0           | 0   | -       |
| Total                  | 895                                   | 5        | 653           | 576         | 282 | 48.96%  |
| 95U301.05-MB 95U301.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 244                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 244                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 244                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 244                                   | 1        | 151           | 132         | 59  | 44.70%  |
| Provisional            | 244                                   | 1        | 2             | 1           | 0   | 0.00%   |
| Total                  | 244                                   | 5        | 153           | 133         | 59  | 44.36%  |
| 95U302.04-MB 95U302.04 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 1             | 1           | 1   | 100.00% |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                                     | 5        | 1             | 1           | 1   | 100.00% |
| 95U302.05-MB 95U302.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 128                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 128                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 128                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 128                                   | 1        | 86            | 75          | 36  | 48.00%  |
| Provisional            | 128                                   | 1        | 0             | 0           | 0   | -       |
| Total                  | 128                                   | 5        | 86            | 75          | 36  | 48.00%  |
| 95U303.03-MB 95U303.03 |                                       |          |               |             |     |         |
| Precinct TSx           | 144                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 144                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 144                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 144                                   | 1        | 84            | 74          | 32  | 43.24%  |
| Provisional            | 144                                   | 1        | 0             | 0           | 0   | -       |
| Total                  | 144                                   | 5        | 84            | 74          | 32  | 43.24%  |

**COUNTY OF EL DORADO**  
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|                        | Measure A - County (majority to pass) |          |               |             |     |        |
|------------------------|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| 95U303.05-MB 95U303.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 159                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 159                                   | 1        | 0             | 0           | 0   | -      |
| Precinct VBM           | 159                                   | 1        | 0             | 0           | 0   | -      |
| MB Precinct            | 159                                   | 1        | 111           | 90          | 40  | 44.44% |
| Provisional            | 159                                   | 1        | 1             | 1           | 0   | 0.00%  |
| Total                  | 159                                   | 5        | 112           | 91          | 40  | 43.96% |
| 95U304.05-MB 95U304.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -      |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -      |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -      |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -      |
| Total                  | 0                                     | 5        | 0             | 0           | 0   | -      |
| 95U305.05-MB 95U305.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 7                                     | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 7                                     | 1        | 0             | 0           | 0   | -      |
| Precinct VBM           | 7                                     | 1        | 0             | 0           | 0   | -      |
| MB Precinct            | 7                                     | 1        | 7             | 4           | 2   | 50.00% |
| Provisional            | 7                                     | 1        | 0             | 0           | 0   | -      |
| Total                  | 7                                     | 5        | 7             | 4           | 2   | 50.00% |
| 95U401.08-MB 95U401.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 39                                    | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 39                                    | 1        | 0             | 0           | 0   | -      |
| Precinct VBM           | 39                                    | 1        | 0             | 0           | 0   | -      |
| MB Precinct            | 39                                    | 1        | 16            | 10          | 4   | 40.00% |
| Provisional            | 39                                    | 1        | 0             | 0           | 0   | -      |
| Total                  | 39                                    | 5        | 16            | 10          | 4   | 40.00% |
| 95U402.07-MB 95U402.07 |                                       |          |               |             |     |        |
| Precinct TSx           | 413                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 413                                   | 1        | 0             | 0           | 0   | -      |
| Precinct VBM           | 413                                   | 1        | 0             | 0           | 0   | -      |
| MB Precinct            | 413                                   | 1        | 256           | 209         | 103 | 49.28% |
| Provisional            | 413                                   | 1        | 1             | 1           | 0   | 0.00%  |
| Total                  | 413                                   | 5        | 257           | 210         | 103 | 49.05% |
| 95U402.08-MB 95U402.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 873                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 873                                   | 1        | 0             | 0           | 0   | -      |
| Precinct VBM           | 873                                   | 1        | 0             | 0           | 0   | -      |
| MB Precinct            | 873                                   | 1        | 566           | 478         | 262 | 54.81% |
| Provisional            | 873                                   | 1        | 3             | 1           | 0   | 0.00%  |
| Total                  | 873                                   | 5        | 569           | 479         | 262 | 54.70% |
| 95U403.08-MB 95U403.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1591                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1591                                  | 1        | 0             | 0           | 0   | -      |
| Precinct VBM           | 1591                                  | 1        | 0             | 0           | 0   | -      |
| MB Precinct            | 1591                                  | 1        | 1042          | 891         | 425 | 47.70% |
| Provisional            | 1591                                  | 1        | 13            | 10          | 6   | 60.00% |
| Total                  | 1591                                  | 5        | 1055          | 901         | 431 | 47.84% |
| 95U404.06-MB 95U404.06 |                                       |          |               |             |     |        |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -      |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -      |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -      |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -      |
| Total                  | 0                                     | 5        | 0             | 0           | 0   | -      |
| 95U404.08-MB 95U404.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -      |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -      |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -      |

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|                               | Measure A - County (majority to pass) |          |               |             |       |         |
|-------------------------------|---------------------------------------|----------|---------------|-------------|-------|---------|
|                               | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES   |         |
| Provisional                   | 0                                     | 1        | 0             | 0           | 0     | -       |
| Total                         | 0                                     | 5        | 0             | 0           | 0     | -       |
| 95U501.09-MB 95U501.09        |                                       |          |               |             |       |         |
| Precinct TSx                  | 0                                     | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 0                                     | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 0                                     | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 0                                     | 1        | 2             | 2           | 2     | 100.00% |
| Provisional                   | 0                                     | 1        | 0             | 0           | 0     | -       |
| Total                         | 0                                     | 5        | 2             | 2           | 2     | 100.00% |
| 95U501.10-MB 95U501.10        |                                       |          |               |             |       |         |
| Precinct TSx                  | 286                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 286                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 286                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 286                                   | 1        | 172           | 153         | 71    | 46.41%  |
| Provisional                   | 286                                   | 1        | 2             | 2           | 0     | 0.00%   |
| Total                         | 286                                   | 5        | 174           | 155         | 71    | 45.81%  |
| 95U598.10-MB 95U598.10        |                                       |          |               |             |       |         |
| Precinct TSx                  | 568                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 568                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 568                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 568                                   | 1        | 239           | 179         | 101   | 56.42%  |
| Provisional                   | 568                                   | 1        | 3             | 1           | 1     | 100.00% |
| Total                         | 568                                   | 5        | 242           | 180         | 102   | 56.67%  |
| 95U599.10-MB 95U599.10        |                                       |          |               |             |       |         |
| Precinct TSx                  | 75                                    | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 75                                    | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 75                                    | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 75                                    | 1        | 38            | 29          | 15    | 51.72%  |
| Provisional                   | 75                                    | 1        | 0             | 0           | 0     | -       |
| Total                         | 75                                    | 5        | 38            | 29          | 15    | 51.72%  |
| 96U101.11-MB 96U101.11        |                                       |          |               |             |       |         |
| Precinct TSx                  | 626                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 626                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 626                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 626                                   | 1        | 423           | 343         | 202   | 58.89%  |
| Provisional                   | 626                                   | 1        | 6             | 4           | 0     | 0.00%   |
| Total                         | 626                                   | 5        | 429           | 347         | 202   | 58.21%  |
| 96U401.14-MB 96U401.14        |                                       |          |               |             |       |         |
| Precinct TSx                  | 566                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 566                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 566                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 566                                   | 1        | 359           | 282         | 139   | 49.29%  |
| Provisional                   | 566                                   | 1        | 6             | 6           | 2     | 33.33%  |
| Total                         | 566                                   | 5        | 365           | 288         | 141   | 48.96%  |
| Total                         |                                       |          |               |             |       |         |
| Precinct TSx                  | 109479                                | 102      | 8             | 7           | 4     | 57.14%  |
| Precinct OS                   | 109479                                | 102      | 12009         | 9906        | 4801  | 48.47%  |
| Precinct VBM                  | 109479                                | 102      | 45535         | 38307       | 20902 | 54.56%  |
| MB Precinct                   | 109479                                | 102      | 5143          | 4309        | 2161  | 50.15%  |
| Provisional                   | 109479                                | 102      | 1584          | 1185        | 586   | 49.45%  |
| Total                         | 109479                                | 510      | 64279         | 53714       | 28454 | 52.97%  |
| STATE BOARD OF EQUALIZATION   |                                       |          |               |             |       |         |
| STATE BOARD OF EQUALIZATION 1 |                                       |          |               |             |       |         |
| Precinct TSx                  | 109479                                | 102      | 8             | 7           | 4     | 57.14%  |
| Precinct OS                   | 109479                                | 102      | 12009         | 9906        | 4801  | 48.47%  |
| Precinct VBM                  | 109479                                | 102      | 45535         | 38307       | 20902 | 54.56%  |
| MB Precinct                   | 109479                                | 102      | 5143          | 4309        | 2161  | 50.15%  |
| Provisional                   | 109479                                | 102      | 1584          | 1185        | 586   | 49.45%  |
| Total                         | 109479                                | 510      | 64279         | 53714       | 28454 | 52.97%  |
| Total                         |                                       |          |               |             |       |         |

**COUNTY OF EL DORADO**  
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|                                     |  | Measure A - County (majority to pass) |            |               |              |              |               |  |
|-------------------------------------|--|---------------------------------------|------------|---------------|--------------|--------------|---------------|--|
|                                     |  | Reg. Voters                           | Vote For   | Times Counted | Total Votes  | YES          |               |  |
| Precinct TSx                        |  | 109479                                | 102        | 8             | 7            | 4            | 57.14%        |  |
| Precinct OS                         |  | 109479                                | 102        | 12009         | 9906         | 4801         | 48.47%        |  |
| Precinct VBM                        |  | 109479                                | 102        | 45535         | 38307        | 20902        | 54.56%        |  |
| MB Precinct                         |  | 109479                                | 102        | 5143          | 4309         | 2161         | 50.15%        |  |
| Provisional                         |  | 109479                                | 102        | 1584          | 1185         | 586          | 49.45%        |  |
| <b>Total</b>                        |  | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53714</b> | <b>28454</b> | <b>52.97%</b> |  |
| <b>UNITED STATES SENATE</b>         |  |                                       |            |               |              |              |               |  |
| <b>UNITED STATES SENATE</b>         |  |                                       |            |               |              |              |               |  |
| Precinct TSx                        |  | 109479                                | 102        | 8             | 7            | 4            | 57.14%        |  |
| Precinct OS                         |  | 109479                                | 102        | 12009         | 9906         | 4801         | 48.47%        |  |
| Precinct VBM                        |  | 109479                                | 102        | 45535         | 38307        | 20902        | 54.56%        |  |
| MB Precinct                         |  | 109479                                | 102        | 5143          | 4309         | 2161         | 50.15%        |  |
| Provisional                         |  | 109479                                | 102        | 1584          | 1185         | 586          | 49.45%        |  |
| <b>Total</b>                        |  | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53714</b> | <b>28454</b> | <b>52.97%</b> |  |
| <b>Total</b>                        |  |                                       |            |               |              |              |               |  |
| Precinct TSx                        |  | 109479                                | 102        | 8             | 7            | 4            | 57.14%        |  |
| Precinct OS                         |  | 109479                                | 102        | 12009         | 9906         | 4801         | 48.47%        |  |
| Precinct VBM                        |  | 109479                                | 102        | 45535         | 38307        | 20902        | 54.56%        |  |
| MB Precinct                         |  | 109479                                | 102        | 5143          | 4309         | 2161         | 50.15%        |  |
| Provisional                         |  | 109479                                | 102        | 1584          | 1185         | 586          | 49.45%        |  |
| <b>Total</b>                        |  | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53714</b> | <b>28454</b> | <b>52.97%</b> |  |
| <b>UNITED STATES REPRESENTATIVE</b> |  |                                       |            |               |              |              |               |  |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |  |                                       |            |               |              |              |               |  |
| Precinct TSx                        |  | 109479                                | 102        | 8             | 7            | 4            | 57.14%        |  |
| Precinct OS                         |  | 109479                                | 102        | 12009         | 9906         | 4801         | 48.47%        |  |
| Precinct VBM                        |  | 109479                                | 102        | 45535         | 38307        | 20902        | 54.56%        |  |
| MB Precinct                         |  | 109479                                | 102        | 5143          | 4309         | 2161         | 50.15%        |  |
| Provisional                         |  | 109479                                | 102        | 1584          | 1185         | 586          | 49.45%        |  |
| <b>Total</b>                        |  | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53714</b> | <b>28454</b> | <b>52.97%</b> |  |
| <b>Total</b>                        |  |                                       |            |               |              |              |               |  |
| Precinct TSx                        |  | 109479                                | 102        | 8             | 7            | 4            | 57.14%        |  |
| Precinct OS                         |  | 109479                                | 102        | 12009         | 9906         | 4801         | 48.47%        |  |
| Precinct VBM                        |  | 109479                                | 102        | 45535         | 38307        | 20902        | 54.56%        |  |
| MB Precinct                         |  | 109479                                | 102        | 5143          | 4309         | 2161         | 50.15%        |  |
| Provisional                         |  | 109479                                | 102        | 1584          | 1185         | 586          | 49.45%        |  |
| <b>Total</b>                        |  | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53714</b> | <b>28454</b> | <b>52.97%</b> |  |
| <b>STATE SENATE</b>                 |  |                                       |            |               |              |              |               |  |
| <b>1ST STATE SENATE DISTRICT</b>    |  |                                       |            |               |              |              |               |  |
| Precinct TSx                        |  | 109479                                | 102        | 8             | 7            | 4            | 57.14%        |  |
| Precinct OS                         |  | 109479                                | 102        | 12009         | 9906         | 4801         | 48.47%        |  |
| Precinct VBM                        |  | 109479                                | 102        | 45535         | 38307        | 20902        | 54.56%        |  |
| MB Precinct                         |  | 109479                                | 102        | 5143          | 4309         | 2161         | 50.15%        |  |
| Provisional                         |  | 109479                                | 102        | 1584          | 1185         | 586          | 49.45%        |  |
| <b>Total</b>                        |  | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53714</b> | <b>28454</b> | <b>52.97%</b> |  |
| <b>Total</b>                        |  |                                       |            |               |              |              |               |  |
| Precinct TSx                        |  | 109479                                | 102        | 8             | 7            | 4            | 57.14%        |  |
| Precinct OS                         |  | 109479                                | 102        | 12009         | 9906         | 4801         | 48.47%        |  |
| Precinct VBM                        |  | 109479                                | 102        | 45535         | 38307        | 20902        | 54.56%        |  |
| MB Precinct                         |  | 109479                                | 102        | 5143          | 4309         | 2161         | 50.15%        |  |
| Provisional                         |  | 109479                                | 102        | 1584          | 1185         | 586          | 49.45%        |  |
| <b>Total</b>                        |  | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53714</b> | <b>28454</b> | <b>52.97%</b> |  |
| <b>STATE ASSEMBLY DISTRICTS</b>     |  |                                       |            |               |              |              |               |  |
| <b>5TH STATE ASSEMBLY DIST</b>      |  |                                       |            |               |              |              |               |  |
| Precinct TSx                        |  | 70961                                 | 75         | 4             | 3            | 1            | 33.33%        |  |
| Precinct OS                         |  | 70961                                 | 75         | 7627          | 6316         | 2932         | 46.42%        |  |
| Precinct VBM                        |  | 70961                                 | 75         | 28225         | 23668        | 12325        | 52.07%        |  |
| MB Precinct                         |  | 70961                                 | 75         | 4361          | 3684         | 1820         | 49.40%        |  |

**COUNTY OF EL DORADO**  
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| Measure A - County (majority to pass) |             |          |               |             |       |        |
|---------------------------------------|-------------|----------|---------------|-------------|-------|--------|
|                                       | Reg. Voters | Vote For | Times Counted | Total Votes | YES   |        |
| Provisional                           | 70961       | 75       | 1038          | 763         | 367   | 48.10% |
| Total                                 | 70961       | 375      | 41255         | 34434       | 17445 | 50.66% |
| <b>6TH STATE ASSEMBLY DIST</b>        |             |          |               |             |       |        |
| Precinct TSx                          | 38518       | 27       | 4             | 4           | 3     | 75.00% |
| Precinct OS                           | 38518       | 27       | 4382          | 3590        | 1869  | 52.06% |
| Precinct VBM                          | 38518       | 27       | 17310         | 14639       | 8577  | 58.59% |
| MB Precinct                           | 38518       | 27       | 782           | 625         | 341   | 54.56% |
| Provisional                           | 38518       | 27       | 546           | 422         | 219   | 51.90% |
| Total                                 | 38518       | 135      | 23024         | 19280       | 11009 | 57.10% |
| <b>Total</b>                          |             |          |               |             |       |        |
| Precinct TSx                          | 109479      | 102      | 8             | 7           | 4     | 57.14% |
| Precinct OS                           | 109479      | 102      | 12009         | 9906        | 4801  | 48.47% |
| Precinct VBM                          | 109479      | 102      | 45535         | 38307       | 20902 | 54.56% |
| MB Precinct                           | 109479      | 102      | 5143          | 4309        | 2161  | 50.15% |
| Provisional                           | 109479      | 102      | 1584          | 1185        | 586   | 49.45% |
| Total                                 | 109479      | 510      | 64279         | 53714       | 28454 | 52.97% |
| <b>SUPERVISORIAL DISTRICTS</b>        |             |          |               |             |       |        |
| <b>1ST SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 22587       | 14       | 0             | 0           | 0     | -      |
| Precinct OS                           | 22587       | 14       | 2530          | 2069        | 1105  | 53.41% |
| Precinct VBM                          | 22587       | 14       | 10411         | 8820        | 5322  | 60.34% |
| MB Precinct                           | 22587       | 14       | 423           | 343         | 202   | 58.89% |
| Provisional                           | 22587       | 14       | 301           | 222         | 119   | 53.60% |
| Total                                 | 22587       | 70       | 13665         | 11454       | 6748  | 58.91% |
| <b>2ND SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 23114       | 23       | 4             | 4           | 3     | 75.00% |
| Precinct OS                           | 23114       | 23       | 2509          | 2085        | 1025  | 49.16% |
| Precinct VBM                          | 23114       | 23       | 9647          | 8133        | 4356  | 53.56% |
| MB Precinct                           | 23114       | 23       | 1590          | 1357        | 667   | 49.15% |
| Provisional                           | 23114       | 23       | 327           | 256         | 116   | 45.31% |
| Total                                 | 23114       | 115      | 14077         | 11835       | 6167  | 52.11% |
| <b>3RD SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 21691       | 26       | 3             | 2           | 1     | 50.00% |
| Precinct OS                           | 21691       | 26       | 2368          | 2001        | 911   | 45.53% |
| Precinct VBM                          | 21691       | 26       | 10021         | 8477        | 4457  | 52.58% |
| MB Precinct                           | 21691       | 26       | 440           | 376         | 170   | 45.21% |
| Provisional                           | 21691       | 26       | 277           | 205         | 95    | 46.34% |
| Total                                 | 21691       | 130      | 13109         | 11061       | 5634  | 50.94% |
| <b>4TH SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 23425       | 22       | 0             | 0           | 0     | -      |
| Precinct OS                           | 23425       | 22       | 2568          | 2146        | 947   | 44.13% |
| Precinct VBM                          | 23425       | 22       | 9398          | 7930        | 4117  | 51.92% |
| MB Precinct                           | 23425       | 22       | 2239          | 1870        | 933   | 49.89% |
| Provisional                           | 23425       | 22       | 315           | 242         | 110   | 45.45% |
| Total                                 | 23425       | 110      | 14520         | 12188       | 6107  | 50.11% |
| <b>5TH SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 18662       | 17       | 1             | 1           | 0     | 0.00%  |
| Precinct OS                           | 18662       | 17       | 2034          | 1605        | 813   | 50.65% |
| Precinct VBM                          | 18662       | 17       | 6058          | 4947        | 2650  | 53.57% |
| MB Precinct                           | 18662       | 17       | 451           | 363         | 189   | 52.07% |
| Provisional                           | 18662       | 17       | 364           | 260         | 146   | 56.15% |
| Total                                 | 18662       | 85       | 8908          | 7176        | 3798  | 52.93% |
| <b>Total</b>                          |             |          |               |             |       |        |
| Precinct TSx                          | 109479      | 102      | 8             | 7           | 4     | 57.14% |
| Precinct OS                           | 109479      | 102      | 12009         | 9906        | 4801  | 48.47% |
| Precinct VBM                          | 109479      | 102      | 45535         | 38307       | 20902 | 54.56% |
| MB Precinct                           | 109479      | 102      | 5143          | 4309        | 2161  | 50.15% |
| Provisional                           | 109479      | 102      | 1584          | 1185        | 586   | 49.45% |
| Total                                 | 109479      | 510      | 64279         | 53714       | 28454 | 52.97% |



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|                                 |  | Measure A - County (majority to pass) |            |               |              |              |  |               |
|---------------------------------|--|---------------------------------------|------------|---------------|--------------|--------------|--|---------------|
|                                 |  | Reg. Voters                           | Vote For   | Times Counted | Total Votes  | YES          |  |               |
| <b>CITIES</b>                   |  |                                       |            |               |              |              |  |               |
| <b>CITY OF PLACERVILLE</b>      |  |                                       |            |               |              |              |  |               |
| Precinct TSx                    |  | 5569                                  | 5          | 0             | 0            | 0            |  | -             |
| Precinct OS                     |  | 5569                                  | 5          | 631           | 516          | 259          |  | 50.19%        |
| Precinct VBM                    |  | 5569                                  | 5          | 2590          | 2166         | 1192         |  | 55.03%        |
| MB Precinct                     |  | 5569                                  | 5          | 0             | 0            | 0            |  | -             |
| Provisional                     |  | 5569                                  | 5          | 80            | 60           | 25           |  | 41.67%        |
| <b>Total</b>                    |  | <b>5569</b>                           | <b>25</b>  | <b>3301</b>   | <b>2742</b>  | <b>1476</b>  |  | <b>53.83%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |  |                                       |            |               |              |              |  |               |
| Precinct TSx                    |  | 9465                                  | 7          | 0             | 0            | 0            |  | -             |
| Precinct OS                     |  | 9465                                  | 7          | 1063          | 843          | 439          |  | 52.08%        |
| Precinct VBM                    |  | 9465                                  | 7          | 2796          | 2218         | 1203         |  | 54.24%        |
| MB Precinct                     |  | 9465                                  | 7          | 0             | 0            | 0            |  | -             |
| Provisional                     |  | 9465                                  | 7          | 224           | 154          | 86           |  | 55.84%        |
| <b>Total</b>                    |  | <b>9465</b>                           | <b>35</b>  | <b>4083</b>   | <b>3215</b>  | <b>1728</b>  |  | <b>53.75%</b> |
| <b>UNINCORPORATED AREA</b>      |  |                                       |            |               |              |              |  |               |
| Precinct TSx                    |  | 94445                                 | 90         | 8             | 7            | 4            |  | 57.14%        |
| Precinct OS                     |  | 94445                                 | 90         | 10315         | 8547         | 4103         |  | 48.01%        |
| Precinct VBM                    |  | 94445                                 | 90         | 40149         | 33923        | 18507        |  | 54.56%        |
| MB Precinct                     |  | 94445                                 | 90         | 5143          | 4309         | 2161         |  | 50.15%        |
| Provisional                     |  | 94445                                 | 90         | 1280          | 971          | 475          |  | 48.92%        |
| <b>Total</b>                    |  | <b>94445</b>                          | <b>450</b> | <b>56895</b>  | <b>47757</b> | <b>25250</b> |  | <b>52.87%</b> |
| <b>Total</b>                    |  |                                       |            |               |              |              |  |               |
| Precinct TSx                    |  | 109479                                | 102        | 8             | 7            | 4            |  | 57.14%        |
| Precinct OS                     |  | 109479                                | 102        | 12009         | 9906         | 4801         |  | 48.47%        |
| Precinct VBM                    |  | 109479                                | 102        | 45535         | 38307        | 20902        |  | 54.56%        |
| MB Precinct                     |  | 109479                                | 102        | 5143          | 4309         | 2161         |  | 50.15%        |
| Provisional                     |  | 109479                                | 102        | 1584          | 1185         | 586          |  | 49.45%        |
| <b>Total</b>                    |  | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53714</b> | <b>28454</b> |  | <b>52.97%</b> |

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|--------------------------|--|
|                          | NO                                       |
| <b>Jurisdiction Wide</b> |  |
| 100001.11-CP 100001.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 73 45.06%                                |
| Precinct VBM             | 245 37.46%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 6 54.55%                                 |
| Total                    | 324 39.18%                               |
| 100002.11-CP 100002.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 31 41.89%                                |
| Precinct VBM             | 198 48.41%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 9 50.00%                                 |
| Total                    | 238 47.50%                               |
| 100003.11-CP 100003.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 54 50.94%                                |
| Precinct VBM             | 219 33.64%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 10 52.63%                                |
| Total                    | 283 36.47%                               |
| 100004.11-CP 100004.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 103 47.91%                               |
| Precinct VBM             | 250 39.81%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 2 100.00%                                |
| Total                    | 355 42.01%                               |
| 100005.11-CP 100005.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 51 40.80%                                |
| Precinct VBM             | 257 33.33%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 9 45.00%                                 |
| Total                    | 317 34.61%                               |
| 100006.11-CP 100006.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 87 43.72%                                |
| Precinct VBM             | 303 38.90%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 9 45.00%                                 |
| Total                    | 399 39.98%                               |
| 100008.11-CP 100008.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 71 47.02%                                |
| Precinct VBM             | 224 36.66%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 3 25.00%                                 |
| Total                    | 298 38.50%                               |
| 100009.11-CP 100009.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 60 43.48%                                |
| Precinct VBM             | 263 41.35%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 4 25.00%                                 |
| Total                    | 327 41.39%                               |
| 100012.11-CP 100012.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 108 52.43%                               |
| Precinct VBM             | 327 39.98%                               |

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|                        | Measure A - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| MB Precinct            | 0 -                                      |
| Provisional            | 10 41.67%                                |
| Total                  | 445 42.46%                               |
| 100013.11-CP 100013.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 90 44.12%                                |
| Precinct VBM           | 339 43.86%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 13 56.52%                                |
| Total                  | 442 44.20%                               |
| 100014.11-CP 100014.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 82 50.62%                                |
| Precinct VBM           | 258 43.29%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 11 50.00%                                |
| Total                  | 351 45.00%                               |
| 100022.11-CP 100022.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 61 39.10%                                |
| Precinct VBM           | 244 42.36%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 44.44%                                 |
| Total                  | 309 41.70%                               |
| 100023.11-CP 100023.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 93 54.39%                                |
| Precinct VBM           | 371 40.41%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 40.91%                                 |
| Total                  | 473 42.57%                               |
| 200003.12-CP 200003.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 70 50.00%                                |
| Precinct VBM           | 215 46.04%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 14 53.85%                                |
| Total                  | 299 47.24%                               |
| 200004.12-CP 200004.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 71 47.02%                                |
| Precinct VBM           | 221 39.18%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 38.46%                                 |
| Total                  | 297 40.80%                               |
| 200005.02-CP 200005.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 43 52.44%                                |
| Precinct VBM           | 214 54.73%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 56.25%                                 |
| Total                  | 266 54.40%                               |
| 200006.12-CP 200006.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 52 53.06%                                |
| Precinct VBM           | 207 44.81%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 15 62.50%                                |
| Total                  | 274 46.92%                               |
| 200008.12-CP 200008.12 |  |
| Precinct TSx           | 0 -                                      |

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|                        | Measure A - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| Precinct OS            | 62 51.67%                                |
| Precinct VBM           | 150 47.02%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 37.50%                                 |
| Total                  | 215 48.10%                               |
| 200010.12-CP 200010.12 |  |
| Precinct TSx           | 1 25.00%                                 |
| Precinct OS            | 86 53.42%                                |
| Precinct VBM           | 315 43.09%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 12 46.15%                                |
| Total                  | 414 44.90%                               |
| 200012.12-CP 200012.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 67 55.37%                                |
| Precinct VBM           | 167 42.17%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 63.64%                                 |
| Total                  | 241 45.64%                               |
| 200013.12-CP 200013.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 87 48.07%                                |
| Precinct VBM           | 269 45.29%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 10 50.00%                                |
| Total                  | 366 46.04%                               |
| 200014.12-CP 200014.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 64 39.51%                                |
| Precinct VBM           | 219 43.54%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 34.78%                                 |
| Total                  | 291 42.30%                               |
| 200015.02-CP 200015.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 107 59.78%                               |
| Precinct VBM           | 363 48.86%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 12 63.16%                                |
| Total                  | 482 51.22%                               |
| 200016.12-CP 200016.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 11 47.83%                                |
| Precinct VBM           | 59 50.43%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 2 50.00%                                 |
| Total                  | 72 50.00%                                |
| 200016.13-CP 200016.13 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 31 47.69%                                |
| Precinct VBM           | 139 49.64%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 2 40.00%                                 |
| Total                  | 172 49.14%                               |
| 200017.02-CP 200017.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 64 52.89%                                |
| Precinct VBM           | 246 49.40%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 50.00%                                 |
| Total                  | 315 50.08%                               |

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|                        | NO                                       |
| 200018.02-CP 200018.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 77 54.23%                                |
| Precinct VBM           | 283 47.17%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 66.67%                                 |
| Total                  | 366 48.74%                               |
| 200020.02-CP 200020.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 57 49.14%                                |
| Precinct VBM           | 205 45.56%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 56.25%                                 |
| Total                  | 271 46.56%                               |
| 200021.01-CP 200021.01 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 17 56.67%                                |
| Precinct VBM           | 77 43.02%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 100.00%                                |
| Total                  | 99 46.26%                                |
| 200021.02-CP 200021.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 65 44.83%                                |
| Precinct VBM           | 293 49.41%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 10 76.92%                                |
| Total                  | 368 49.00%                               |
| 200024.02-CP 200024.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 29 60.42%                                |
| Precinct VBM           | 135 54.88%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 60.00%                                 |
| Total                  | 167 55.85%                               |
| 300001.04-CP 300001.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 6 60.00%                                 |
| Precinct VBM           | 44 48.89%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 -                                      |
| Total                  | 50 50.00%                                |
| 300001.05-CP 300001.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 25 40.32%                                |
| Precinct VBM           | 128 47.41%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 44.44%                                 |
| Total                  | 157 46.04%                               |
| 300003.05-CP 300003.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 96 56.14%                                |
| Precinct VBM           | 344 44.73%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 11 52.38%                                |
| Total                  | 451 46.93%                               |
| 300004.05-CP 300004.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 67 47.86%                                |
| Precinct VBM           | 211 44.23%                               |
| MB Precinct            | 0 -                                      |

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|                        | NO                                       |
| Provisional            | 9 75.00%                                 |
| Total                  | 287 45.63%                               |
| 300005.05-CP 300005.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 63 47.37%                                |
| Precinct VBM           | 247 44.11%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 11 61.11%                                |
| Total                  | 321 45.15%                               |
| 300008.05-CP 300008.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 68 56.67%                                |
| Precinct VBM           | 298 46.35%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 60.00%                                 |
| Total                  | 372 48.12%                               |
| 300009.04-CP 300009.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 58 58.00%                                |
| Precinct VBM           | 162 50.15%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 50.00%                                 |
| Total                  | 225 51.96%                               |
| 300010.04-CP 300010.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 89 60.54%                                |
| Precinct VBM           | 275 48.93%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 80.00%                                 |
| Total                  | 372 51.74%                               |
| 300011.04-CP 300011.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 68 53.13%                                |
| Precinct VBM           | 311 51.75%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 10 62.50%                                |
| Total                  | 389 52.21%                               |
| 300013.05-CP 300013.05 |  |
| Precinct TSx           | 1 50.00%                                 |
| Precinct OS            | 59 52.21%                                |
| Precinct VBM           | 295 46.02%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 33.33%                                 |
| Total                  | 358 46.80%                               |
| 300015.04-CP 300015.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 7 77.78%                                 |
| Precinct VBM           | 22 51.16%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 2 50.00%                                 |
| Total                  | 31 55.36%                                |
| 300015.05-CP 300015.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 30 55.56%                                |
| Precinct VBM           | 102 46.79%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 100.00%                                |
| Total                  | 133 48.72%                               |
| 300017.05-CP 300017.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 55 57.89%                                |

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|                        | NO                                       |
| Precinct VBM           | 230 41.22%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 14.29%                                 |
| Total                  | 286 43.33%                               |
| 300018.04-CP 300018.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 77 59.69%                                |
| Precinct VBM           | 250 58.14%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 10 45.45%                                |
| Total                  | 337 58.00%                               |
| 300020.04-CP 300020.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 87 60.00%                                |
| Precinct VBM           | 260 49.62%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 40.00%                                 |
| Total                  | 351 51.69%                               |
| 300021.03-CP 300021.03 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 62 50.41%                                |
| Precinct VBM           | 237 45.66%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 70.00%                                 |
| Total                  | 306 46.93%                               |
| 300022.05-CP 300022.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 59 70.24%                                |
| Precinct VBM           | 148 48.84%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 40.00%                                 |
| Total                  | 211 53.15%                               |
| 300023.03-CP 300023.03 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 103 51.50%                               |
| Precinct VBM           | 378 48.52%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 10 52.63%                                |
| Total                  | 491 49.20%                               |
| 300023.05-CP 300023.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 11 28.95%                                |
| Precinct VBM           | 78 46.71%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 2 40.00%                                 |
| Total                  | 91 43.33%                                |
| 400001.14-CP 400001.14 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 77 54.61%                                |
| Precinct VBM           | 360 43.64%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 12 60.00%                                |
| Total                  | 449 45.54%                               |
| 400002.08-CP 400002.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 96 58.54%                                |
| Precinct VBM           | 295 48.44%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 58.33%                                 |
| Total                  | 398 50.70%                               |
| 400003.08-CP 400003.08 |  |

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|                        | Measure A - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 125 61.27%                               |
| Precinct VBM           | 300 45.87%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 14 60.87%                                |
| Total                  | 439 49.83%                               |
| 400004.14-CP 400004.14 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 79 50.00%                                |
| Precinct VBM           | 243 43.32%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 42.86%                                 |
| Total                  | 328 44.75%                               |
| 400005.08-CP 400005.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 41 62.12%                                |
| Precinct VBM           | 97 45.33%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 66.67%                                 |
| Total                  | 144 49.83%                               |
| 400006.08-CP 400006.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 93 48.44%                                |
| Precinct VBM           | 286 48.64%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 60.00%                                 |
| Total                  | 388 48.81%                               |
| 400007.08-CP 400007.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 116 56.59%                               |
| Precinct VBM           | 334 50.53%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 44.44%                                 |
| Total                  | 458 51.81%                               |
| 400010.08-CP 400010.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 130 61.61%                               |
| Precinct VBM           | 415 55.70%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 10 47.62%                                |
| Total                  | 555 56.81%                               |
| 400012.08-CP 400012.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 53 54.64%                                |
| Precinct VBM           | 182 50.70%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 56.25%                                 |
| Total                  | 244 51.69%                               |
| 400013.08-CP 400013.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 69 63.89%                                |
| Precinct VBM           | 120 46.33%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 33.33%                                 |
| Total                  | 190 51.35%                               |
| 400014.08-CP 400014.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 95 51.08%                                |
| Precinct VBM           | 282 45.34%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 17 54.84%                                |



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|                        | Measure A - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| Total                  | 394 46.96%                               |
| 400017.08-CP 400017.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 74 54.81%                                |
| Precinct VBM           | 322 49.69%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 12 52.17%                                |
| Total                  | 408 50.62%                               |
| 400018.08-CP 400018.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 35 50.00%                                |
| Precinct VBM           | 101 41.91%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 75.00%                                 |
| Total                  | 139 44.13%                               |
| 400020.08-CP 400020.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 57 58.76%                                |
| Precinct VBM           | 147 51.22%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 50.00%                                 |
| Total                  | 205 53.11%                               |
| 400021.07-CP 400021.07 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 59 52.68%                                |
| Precinct VBM           | 329 50.08%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 53.85%                                 |
| Total                  | 395 50.51%                               |
| 500001.10-CP 500001.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 76 46.91%                                |
| Precinct VBM           | 227 46.23%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 53.85%                                 |
| Total                  | 310 46.55%                               |
| 500002.10-CP 500002.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 55 52.88%                                |
| Precinct VBM           | 156 48.45%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 13 56.52%                                |
| Total                  | 224 49.89%                               |
| 500003.10-CP 500003.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 42 42.42%                                |
| Precinct VBM           | 116 44.44%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 100.00%                                |
| Total                  | 162 44.51%                               |
| 500004.10-CP 500004.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 56 52.83%                                |
| Precinct VBM           | 101 48.56%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 41.18%                                 |
| Total                  | 164 49.55%                               |
| 500005.10-CP 500005.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 42 49.41%                                |
| Precinct VBM           | 106 52.48%                               |

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|                        | Measure A - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 45.45%                                 |
| Total                  | 153 51.34%                               |
| 500006.10-CP 500006.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 67 47.86%                                |
| Precinct VBM           | 175 45.22%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 14 38.89%                                |
| Total                  | 256 45.47%                               |
| 500007.10-CP 500007.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 72 42.86%                                |
| Precinct VBM           | 242 43.92%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 50.00%                                 |
| Total                  | 321 43.79%                               |
| 500009.10-CP 500009.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 39 41.49%                                |
| Precinct VBM           | 100 41.67%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 39.13%                                 |
| Total                  | 148 41.46%                               |
| 500011.10-CP 500011.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 73 50.00%                                |
| Precinct VBM           | 135 43.83%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 13 43.33%                                |
| Total                  | 221 45.66%                               |
| 500012.10-CP 500012.10 |  |
| Precinct TSx           | 1 100.00%                                |
| Precinct OS            | 76 55.07%                                |
| Precinct VBM           | 243 47.65%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 30.77%                                 |
| Total                  | 328 48.59%                               |
| 500015.10-CP 500015.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 42 48.28%                                |
| Precinct VBM           | 128 39.02%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 33.33%                                 |
| Total                  | 174 40.75%                               |
| 500020.10-CP 500020.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 75 52.82%                                |
| Precinct VBM           | 280 48.44%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 30.77%                                 |
| Total                  | 363 48.66%                               |
| 500021.10-CP 500021.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 77 57.46%                                |
| Precinct VBM           | 288 51.34%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 13 59.09%                                |
| Total                  | 378 52.72%                               |
| 95U201.01-MB 95U201.01 |  |
| Precinct TSx           | 0 -                                      |

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|                        | Measure A - County<br>(majority to pass) |         |
|------------------------|--|---------|
|                        | NO                                       |         |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 0  | 0.00%   |
| Provisional            | 0  | -       |
| Total                  | 0  | 0.00%   |
| 95U201.02-MB 95U201.02 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 136                                      | 47.72%  |
| Provisional            | 0  | -       |
| Total                  | 136                                      | 47.72%  |
| 95U202.02-MB 95U202.02 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 260                                      | 53.17%  |
| Provisional            | 3  | 100.00% |
| Total                  | 263                                      | 53.46%  |
| 95U203.02-MB 95U203.02 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 0  | -       |
| Provisional            | 0  | -       |
| Total                  | 0  | -       |
| 95U204.01-MB 95U204.01 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 294                                      | 51.04%  |
| Provisional            | 0  | -       |
| Total                  | 294                                      | 51.04%  |
| 95U301.05-MB 95U301.05 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 73                                       | 55.30%  |
| Provisional            | 1  | 100.00% |
| Total                  | 74                                       | 55.64%  |
| 95U302.04-MB 95U302.04 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 0  | 0.00%   |
| Provisional            | 0  | -       |
| Total                  | 0  | 0.00%   |
| 95U302.05-MB 95U302.05 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 39                                       | 52.00%  |
| Provisional            | 0  | -       |
| Total                  | 39                                       | 52.00%  |
| 95U303.03-MB 95U303.03 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 42                                       | 56.76%  |
| Provisional            | 0  | -       |
| Total                  | 42                                       | 56.76%  |

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|                        | NO                                       |
| 95U303.05-MB 95U303.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 50 55.56%                                |
| Provisional            | 1 100.00%                                |
| Total                  | 51 56.04%                                |
| 95U304.05-MB 95U304.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 -                                      |
| Total                  | 0 -                                      |
| 95U305.05-MB 95U305.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 2 50.00%                                 |
| Provisional            | 0 -                                      |
| Total                  | 2 50.00%                                 |
| 95U401.08-MB 95U401.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 6 60.00%                                 |
| Provisional            | 0 -                                      |
| Total                  | 6 60.00%                                 |
| 95U402.07-MB 95U402.07 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 106 50.72%                               |
| Provisional            | 1 100.00%                                |
| Total                  | 107 50.95%                               |
| 95U402.08-MB 95U402.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 216 45.19%                               |
| Provisional            | 1 100.00%                                |
| Total                  | 217 45.30%                               |
| 95U403.08-MB 95U403.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 466 52.30%                               |
| Provisional            | 4 40.00%                                 |
| Total                  | 470 52.16%                               |
| 95U404.06-MB 95U404.06 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 -                                      |
| Total                  | 0 -                                      |
| 95U404.08-MB 95U404.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 0 -                                      |

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|-------------------------------|--|
|                               | NO                                       |
| Provisional                   | 0 -                                      |
| Total                         | 0 -                                      |
| 95U501.09-MB 95U501.09        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 0 0.00%                                  |
| Provisional                   | 0 -                                      |
| Total                         | 0 0.00%                                  |
| 95U501.10-MB 95U501.10        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 82 53.59%                                |
| Provisional                   | 2 100.00%                                |
| Total                         | 84 54.19%                                |
| 95U598.10-MB 95U598.10        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 78 43.58%                                |
| Provisional                   | 0 0.00%                                  |
| Total                         | 78 43.33%                                |
| 95U599.10-MB 95U599.10        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 14 48.28%                                |
| Provisional                   | 0 -                                      |
| Total                         | 14 48.28%                                |
| 96U101.11-MB 96U101.11        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 141 41.11%                               |
| Provisional                   | 4 100.00%                                |
| Total                         | 145 41.79%                               |
| 96U401.14-MB 96U401.14        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 143 50.71%                               |
| Provisional                   | 4 66.67%                                 |
| Total                         | 147 51.04%                               |
| Total                         |  |
| Precinct TSx                  | 3 42.86%                                 |
| Precinct OS                   | 5105 51.53%                              |
| Precinct VBM                  | 17405 45.44%                             |
| MB Precinct                   | 2148 49.85%                              |
| Provisional                   | 599 50.55%                               |
| Total                         | 25260 47.03%                             |
| STATE BOARD OF EQUALIZATION   |  |
| STATE BOARD OF EQUALIZATION 1 |  |
| Precinct TSx                  | 3 42.86%                                 |
| Precinct OS                   | 5105 51.53%                              |
| Precinct VBM                  | 17405 45.44%                             |
| MB Precinct                   | 2148 49.85%                              |
| Provisional                   | 599 50.55%                               |
| Total                         | 25260 47.03%                             |
| Total                         |  |

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|                                     | Mearure A - County<br>(majority to pass) |               |
|-------------------------------------|--|---------------|
|                                     | NO                                       |               |
| Precinct TSx                        | 3  | 42.86%        |
| Precinct OS                         | 5105                                     | 51.53%        |
| Precinct VBM                        | 17405                                    | 45.44%        |
| MB Precinct                         | 2148                                     | 49.85%        |
| Provisional                         | 599                                      | 50.55%        |
| <b>Total</b>                        | <b>25260</b>                             | <b>47.03%</b> |
| <b>UNITED STATES SENATE</b>         |  |               |
| <b>UNITED STATES SENATE</b>         |  |               |
| Precinct TSx                        | 3  | 42.86%        |
| Precinct OS                         | 5105                                     | 51.53%        |
| Precinct VBM                        | 17405                                    | 45.44%        |
| MB Precinct                         | 2148                                     | 49.85%        |
| Provisional                         | 599                                      | 50.55%        |
| <b>Total</b>                        | <b>25260</b>                             | <b>47.03%</b> |
| <b>Total</b>                        |  |               |
| Precinct TSx                        | 3  | 42.86%        |
| Precinct OS                         | 5105                                     | 51.53%        |
| Precinct VBM                        | 17405                                    | 45.44%        |
| MB Precinct                         | 2148                                     | 49.85%        |
| Provisional                         | 599                                      | 50.55%        |
| <b>Total</b>                        | <b>25260</b>                             | <b>47.03%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |  |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |  |               |
| Precinct TSx                        | 3  | 42.86%        |
| Precinct OS                         | 5105                                     | 51.53%        |
| Precinct VBM                        | 17405                                    | 45.44%        |
| MB Precinct                         | 2148                                     | 49.85%        |
| Provisional                         | 599                                      | 50.55%        |
| <b>Total</b>                        | <b>25260</b>                             | <b>47.03%</b> |
| <b>Total</b>                        |  |               |
| Precinct TSx                        | 3  | 42.86%        |
| Precinct OS                         | 5105                                     | 51.53%        |
| Precinct VBM                        | 17405                                    | 45.44%        |
| MB Precinct                         | 2148                                     | 49.85%        |
| Provisional                         | 599                                      | 50.55%        |
| <b>Total</b>                        | <b>25260</b>                             | <b>47.03%</b> |
| <b>STATE SENATE</b>                 |  |               |
| <b>1ST STATE SENATE DISTRICT</b>    |  |               |
| Precinct TSx                        | 3  | 42.86%        |
| Precinct OS                         | 5105                                     | 51.53%        |
| Precinct VBM                        | 17405                                    | 45.44%        |
| MB Precinct                         | 2148                                     | 49.85%        |
| Provisional                         | 599                                      | 50.55%        |
| <b>Total</b>                        | <b>25260</b>                             | <b>47.03%</b> |
| <b>Total</b>                        |  |               |
| Precinct TSx                        | 3  | 42.86%        |
| Precinct OS                         | 5105                                     | 51.53%        |
| Precinct VBM                        | 17405                                    | 45.44%        |
| MB Precinct                         | 2148                                     | 49.85%        |
| Provisional                         | 599                                      | 50.55%        |
| <b>Total</b>                        | <b>25260</b>                             | <b>47.03%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |  |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |  |               |
| Precinct TSx                        | 2  | 66.67%        |
| Precinct OS                         | 3384                                     | 53.58%        |
| Precinct VBM                        | 11343                                    | 47.93%        |
| MB Precinct                         | 1864                                     | 50.60%        |

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|                                   | NO                                       |         |
| Provisional                       | 396                                      | 51.90%  |
| Total                             | 16989                                    | 49.34%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |  |         |
| Precinct TSx                      | 1  | 25.00%  |
| Precinct OS                       | 1721                                     | 47.94%  |
| Precinct VBM                      | 6062                                     | 41.41%  |
| MB Precinct                       | 284                                      | 45.44%  |
| Provisional                       | 203                                      | 48.10%  |
| Total                             | 8271                                     | 42.90%  |
| <b>Total</b>                      |  |         |
| Precinct TSx                      | 3  | 42.86%  |
| Precinct OS                       | 5105                                     | 51.53%  |
| Precinct VBM                      | 17405                                    | 45.44%  |
| MB Precinct                       | 2148                                     | 49.85%  |
| Provisional                       | 599                                      | 50.55%  |
| Total                             | 25260                                    | 47.03%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |  |         |
| <b>1ST SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 0  | -       |
| Precinct OS                       | 964                                      | 46.59%  |
| Precinct VBM                      | 3498                                     | 39.66%  |
| MB Precinct                       | 141                                      | 41.11%  |
| Provisional                       | 103                                      | 46.40%  |
| Total                             | 4706                                     | 41.09%  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 1  | 25.00%  |
| Precinct OS                       | 1060                                     | 50.84%  |
| Precinct VBM                      | 3777                                     | 46.44%  |
| MB Precinct                       | 690                                      | 50.85%  |
| Provisional                       | 140                                      | 54.69%  |
| Total                             | 5668                                     | 47.89%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 1  | 50.00%  |
| Precinct OS                       | 1090                                     | 54.47%  |
| Precinct VBM                      | 4020                                     | 47.42%  |
| MB Precinct                       | 206                                      | 54.79%  |
| Provisional                       | 110                                      | 53.66%  |
| Total                             | 5427                                     | 49.06%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 0  | -       |
| Precinct OS                       | 1199                                     | 55.87%  |
| Precinct VBM                      | 3813                                     | 48.08%  |
| MB Precinct                       | 937                                      | 50.11%  |
| Provisional                       | 132                                      | 54.55%  |
| Total                             | 6081                                     | 49.89%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 1  | 100.00% |
| Precinct OS                       | 792                                      | 49.35%  |
| Precinct VBM                      | 2297                                     | 46.43%  |
| MB Precinct                       | 174                                      | 47.93%  |
| Provisional                       | 114                                      | 43.85%  |
| Total                             | 3378                                     | 47.07%  |
| <b>Total</b>                      |  |         |
| Precinct TSx                      | 3  | 42.86%  |
| Precinct OS                       | 5105                                     | 51.53%  |
| Precinct VBM                      | 17405                                    | 45.44%  |
| MB Precinct                       | 2148                                     | 49.85%  |
| Provisional                       | 599                                      | 50.55%  |
| Total                             | 25260                                    | 47.03%  |

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|                                 | Measure A - County<br>(majority to pass) |
|---------------------------------|--|
|                                 | NO                                       |
| <b>CITIES</b>                   |  |
| <b>CITY OF PLACERVILLE</b>      |  |
| Precinct TSx                    | 0 -                                      |
| Precinct OS                     | 257 49.81%                               |
| Precinct VBM                    | 974 44.97%                               |
| MB Precinct                     | 0 -                                      |
| Provisional                     | 35 58.33%                                |
| Total                           | 1266 46.17%                              |
| <b>CITY OF SOUTH LAKE TAHOE</b> |  |
| Precinct TSx                    | 0 -                                      |
| Precinct OS                     | 404 47.92%                               |
| Precinct VBM                    | 1015 45.76%                              |
| MB Precinct                     | 0 -                                      |
| Provisional                     | 68 44.16%                                |
| Total                           | 1487 46.25%                              |
| <b>UNINCORPORATED AREA</b>      |  |
| Precinct TSx                    | 3 42.86%                                 |
| Precinct OS                     | 4444 51.99%                              |
| Precinct VBM                    | 15416 45.44%                             |
| MB Precinct                     | 2148 49.85%                              |
| Provisional                     | 496 51.08%                               |
| Total                           | 22507 47.13%                             |
| <b>Total</b>                    |  |
| Precinct TSx                    | 3 42.86%                                 |
| Precinct OS                     | 5105 51.53%                              |
| Precinct VBM                    | 17405 45.44%                             |
| MB Precinct                     | 2148 49.85%                              |
| Provisional                     | 599 50.55%                               |
| Total                           | 25260 47.03%                             |



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|                        | Measure E - County (majority to pass) |          |               |             |     |        |
|------------------------|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Jurisdiction Wide      |                                       |          |               |             |     |        |
| 100001.11-CP 100001.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1615                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1615                                  | 1        | 190           | 180         | 105 | 58.33% |
| Precinct VBM           | 1615                                  | 1        | 781           | 742         | 434 | 58.49% |
| MB Precinct            | 1615                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1615                                  | 1        | 17            | 15          | 9   | 60.00% |
| Total                  | 1615                                  | 5        | 988           | 937         | 548 | 58.48% |
| 100002.11-CP 100002.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1000                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1000                                  | 1        | 89            | 86          | 55  | 63.95% |
| Precinct VBM           | 1000                                  | 1        | 476           | 453         | 237 | 52.32% |
| MB Precinct            | 1000                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1000                                  | 1        | 20            | 19          | 16  | 84.21% |
| Total                  | 1000                                  | 5        | 585           | 558         | 308 | 55.20% |
| 100003.11-CP 100003.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1269                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1269                                  | 1        | 123           | 116         | 62  | 53.45% |
| Precinct VBM           | 1269                                  | 1        | 773           | 733         | 436 | 59.48% |
| MB Precinct            | 1269                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1269                                  | 1        | 24            | 24          | 20  | 83.33% |
| Total                  | 1269                                  | 5        | 920           | 873         | 518 | 59.34% |
| 100004.11-CP 100004.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1633                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1633                                  | 1        | 254           | 235         | 143 | 60.85% |
| Precinct VBM           | 1633                                  | 1        | 738           | 705         | 486 | 68.94% |
| MB Precinct            | 1633                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1633                                  | 1        | 4             | 3           | 2   | 66.67% |
| Total                  | 1633                                  | 5        | 996           | 943         | 631 | 66.91% |
| 100005.11-CP 100005.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1586                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1586                                  | 1        | 146           | 139         | 78  | 56.12% |
| Precinct VBM           | 1586                                  | 1        | 895           | 852         | 423 | 49.65% |
| MB Precinct            | 1586                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1586                                  | 1        | 23            | 23          | 17  | 73.91% |
| Total                  | 1586                                  | 5        | 1064          | 1014        | 518 | 51.08% |
| 100006.11-CP 100006.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1928                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1928                                  | 1        | 252           | 235         | 145 | 61.70% |
| Precinct VBM           | 1928                                  | 1        | 944           | 890         | 608 | 68.31% |
| MB Precinct            | 1928                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1928                                  | 1        | 31            | 27          | 17  | 62.96% |
| Total                  | 1928                                  | 5        | 1227          | 1152        | 770 | 66.84% |
| 100008.11-CP 100008.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1671                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1671                                  | 1        | 197           | 182         | 121 | 66.48% |
| Precinct VBM           | 1671                                  | 1        | 740           | 702         | 454 | 64.67% |
| MB Precinct            | 1671                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1671                                  | 1        | 17            | 13          | 10  | 76.92% |
| Total                  | 1671                                  | 5        | 954           | 897         | 585 | 65.22% |
| 100009.11-CP 100009.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1838                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1838                                  | 1        | 169           | 156         | 97  | 62.18% |
| Precinct VBM           | 1838                                  | 1        | 751           | 693         | 392 | 56.57% |
| MB Precinct            | 1838                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1838                                  | 1        | 26            | 19          | 13  | 68.42% |
| Total                  | 1838                                  | 5        | 946           | 868         | 502 | 57.83% |
| 100012.11-CP 100012.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 2108                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 2108                                  | 1        | 255           | 246         | 149 | 60.57% |
| Precinct VBM           | 2108                                  | 1        | 968           | 908         | 559 | 61.56% |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| MB Precinct            | 2108                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 2108                                  | 1        | 29            | 27          | 16  | 59.26% |
| Total                  | 2108                                  | 5        | 1252          | 1181        | 724 | 61.30% |
| 100013.11-CP 100013.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 2087                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 2087                                  | 1        | 250           | 233         | 137 | 58.80% |
| Precinct VBM           | 2087                                  | 1        | 908           | 865         | 528 | 61.04% |
| MB Precinct            | 2087                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 2087                                  | 1        | 28            | 26          | 19  | 73.08% |
| Total                  | 2087                                  | 5        | 1186          | 1124        | 684 | 60.85% |
| 100014.11-CP 100014.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1659                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1659                                  | 1        | 211           | 200         | 125 | 62.50% |
| Precinct VBM           | 1659                                  | 1        | 706           | 682         | 416 | 61.00% |
| MB Precinct            | 1659                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1659                                  | 1        | 30            | 27          | 17  | 62.96% |
| Total                  | 1659                                  | 5        | 947           | 909         | 558 | 61.39% |
| 100022.11-CP 100022.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1432                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1432                                  | 1        | 177           | 167         | 114 | 68.26% |
| Precinct VBM           | 1432                                  | 1        | 678           | 641         | 393 | 61.31% |
| MB Precinct            | 1432                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1432                                  | 1        | 13            | 10          | 9   | 90.00% |
| Total                  | 1432                                  | 5        | 868           | 818         | 516 | 63.08% |
| 100023.11-CP 100023.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 2135                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 2135                                  | 1        | 217           | 202         | 115 | 56.93% |
| Precinct VBM           | 2135                                  | 1        | 1053          | 1005        | 571 | 56.82% |
| MB Precinct            | 2135                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 2135                                  | 1        | 33            | 28          | 21  | 75.00% |
| Total                  | 2135                                  | 5        | 1303          | 1235        | 707 | 57.25% |
| 200003.12-CP 200003.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1362                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1362                                  | 1        | 166           | 150         | 85  | 56.67% |
| Precinct VBM           | 1362                                  | 1        | 558           | 517         | 260 | 50.29% |
| MB Precinct            | 1362                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1362                                  | 1        | 31            | 26          | 13  | 50.00% |
| Total                  | 1362                                  | 5        | 755           | 693         | 358 | 51.66% |
| 200004.12-CP 200004.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1507                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1507                                  | 1        | 182           | 174         | 93  | 53.45% |
| Precinct VBM           | 1507                                  | 1        | 662           | 627         | 332 | 52.95% |
| MB Precinct            | 1507                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1507                                  | 1        | 14            | 13          | 8   | 61.54% |
| Total                  | 1507                                  | 5        | 858           | 814         | 433 | 53.19% |
| 200005.02-CP 200005.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 867                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 867                                   | 1        | 94            | 90          | 35  | 38.89% |
| Precinct VBM           | 867                                   | 1        | 452           | 420         | 177 | 42.14% |
| MB Precinct            | 867                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 867                                   | 1        | 21            | 19          | 10  | 52.63% |
| Total                  | 867                                   | 5        | 567           | 529         | 222 | 41.97% |
| 200006.12-CP 200006.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1086                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1086                                  | 1        | 117           | 113         | 67  | 59.29% |
| Precinct VBM           | 1086                                  | 1        | 532           | 508         | 272 | 53.54% |
| MB Precinct            | 1086                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1086                                  | 1        | 29            | 28          | 17  | 60.71% |
| Total                  | 1086                                  | 5        | 678           | 649         | 356 | 54.85% |
| 200008.12-CP 200008.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 999                                   | 1        | 0             | 0           | 0   | -      |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |         |
| Precinct OS            | 999                                   | 1        | 155           | 143         | 90  | 62.94%  |
| Precinct VBM           | 999                                   | 1        | 379           | 350         | 181 | 51.71%  |
| MB Precinct            | 999                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 999                                   | 1        | 10            | 10          | 7   | 70.00%  |
| Total                  | 999                                   | 5        | 544           | 503         | 278 | 55.27%  |
| 200010.12-CP 200010.12 |                                       |          |               |             |     |         |
| Precinct TSx           | 1976                                  | 1        | 4             | 4           | 2   | 50.00%  |
| Precinct OS            | 1976                                  | 1        | 197           | 174         | 96  | 55.17%  |
| Precinct VBM           | 1976                                  | 1        | 859           | 796         | 444 | 55.78%  |
| MB Precinct            | 1976                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1976                                  | 1        | 34            | 27          | 17  | 62.96%  |
| Total                  | 1976                                  | 5        | 1094          | 1001        | 559 | 55.84%  |
| 200012.12-CP 200012.12 |                                       |          |               |             |     |         |
| Precinct TSx           | 1064                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1064                                  | 1        | 145           | 139         | 81  | 58.27%  |
| Precinct VBM           | 1064                                  | 1        | 464           | 437         | 255 | 58.35%  |
| MB Precinct            | 1064                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1064                                  | 1        | 14            | 12          | 6   | 50.00%  |
| Total                  | 1064                                  | 5        | 623           | 588         | 342 | 58.16%  |
| 200013.12-CP 200013.12 |                                       |          |               |             |     |         |
| Precinct TSx           | 1574                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1574                                  | 1        | 211           | 199         | 104 | 52.26%  |
| Precinct VBM           | 1574                                  | 1        | 701           | 667         | 334 | 50.07%  |
| MB Precinct            | 1574                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1574                                  | 1        | 23            | 22          | 16  | 72.73%  |
| Total                  | 1574                                  | 5        | 935           | 888         | 454 | 51.13%  |
| 200014.12-CP 200014.12 |                                       |          |               |             |     |         |
| Precinct TSx           | 1438                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1438                                  | 1        | 201           | 191         | 123 | 64.40%  |
| Precinct VBM           | 1438                                  | 1        | 613           | 566         | 326 | 57.60%  |
| MB Precinct            | 1438                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1438                                  | 1        | 30            | 28          | 17  | 60.71%  |
| Total                  | 1438                                  | 5        | 844           | 785         | 466 | 59.36%  |
| 200015.02-CP 200015.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 1832                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1832                                  | 1        | 218           | 206         | 100 | 48.54%  |
| Precinct VBM           | 1832                                  | 1        | 886           | 841         | 394 | 46.85%  |
| MB Precinct            | 1832                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1832                                  | 1        | 22            | 22          | 14  | 63.64%  |
| Total                  | 1832                                  | 5        | 1126          | 1069        | 508 | 47.52%  |
| 200016.12-CP 200016.12 |                                       |          |               |             |     |         |
| Precinct TSx           | 301                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 301                                   | 1        | 26            | 24          | 21  | 87.50%  |
| Precinct VBM           | 301                                   | 1        | 139           | 136         | 98  | 72.06%  |
| MB Precinct            | 301                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 301                                   | 1        | 6             | 5           | 5   | 100.00% |
| Total                  | 301                                   | 5        | 171           | 165         | 124 | 75.15%  |
| 200016.13-CP 200016.13 |                                       |          |               |             |     |         |
| Precinct TSx           | 565                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 565                                   | 1        | 80            | 74          | 55  | 74.32%  |
| Precinct VBM           | 565                                   | 1        | 338           | 331         | 242 | 73.11%  |
| MB Precinct            | 565                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 565                                   | 1        | 6             | 4           | 4   | 100.00% |
| Total                  | 565                                   | 5        | 424           | 409         | 301 | 73.59%  |
| 200017.02-CP 200017.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 1286                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1286                                  | 1        | 146           | 136         | 76  | 55.88%  |
| Precinct VBM           | 1286                                  | 1        | 594           | 551         | 249 | 45.19%  |
| MB Precinct            | 1286                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1286                                  | 1        | 12            | 11          | 8   | 72.73%  |
| Total                  | 1286                                  | 5        | 752           | 698         | 333 | 47.71%  |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| 200018.02-CP 200018.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1454                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1454                                  | 1        | 169           | 160         | 71  | 44.38% |
| Precinct VBM           | 1454                                  | 1        | 704           | 661         | 302 | 45.69% |
| MB Precinct            | 1454                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1454                                  | 1        | 13            | 11          | 5   | 45.45% |
| Total                  | 1454                                  | 5        | 886           | 832         | 378 | 45.43% |
| 200020.02-CP 200020.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1213                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1213                                  | 1        | 139           | 133         | 94  | 70.68% |
| Precinct VBM           | 1213                                  | 1        | 526           | 516         | 364 | 70.54% |
| MB Precinct            | 1213                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1213                                  | 1        | 19            | 16          | 9   | 56.25% |
| Total                  | 1213                                  | 5        | 684           | 665         | 467 | 70.23% |
| 200021.01-CP 200021.01 |                                       |          |               |             |     |        |
| Precinct TSx           | 436                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 436                                   | 1        | 38            | 38          | 26  | 68.42% |
| Precinct VBM           | 436                                   | 1        | 206           | 199         | 119 | 59.80% |
| MB Precinct            | 436                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 436                                   | 1        | 7             | 6           | 2   | 33.33% |
| Total                  | 436                                   | 5        | 251           | 243         | 147 | 60.49% |
| 200021.02-CP 200021.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1296                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1296                                  | 1        | 170           | 167         | 124 | 74.25% |
| Precinct VBM           | 1296                                  | 1        | 752           | 727         | 543 | 74.69% |
| MB Precinct            | 1296                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1296                                  | 1        | 20            | 17          | 14  | 82.35% |
| Total                  | 1296                                  | 5        | 942           | 911         | 681 | 74.75% |
| 200024.02-CP 200024.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 555                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 555                                   | 1        | 55            | 54          | 21  | 38.89% |
| Precinct VBM           | 555                                   | 1        | 282           | 265         | 90  | 33.96% |
| MB Precinct            | 555                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 555                                   | 1        | 9             | 8           | 4   | 50.00% |
| Total                  | 555                                   | 5        | 346           | 327         | 115 | 35.17% |
| 300001.04-CP 300001.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 259                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 259                                   | 1        | 12            | 11          | 3   | 27.27% |
| Precinct VBM           | 259                                   | 1        | 100           | 90          | 41  | 45.56% |
| MB Precinct            | 259                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 259                                   | 1        | 0             | 0           | 0   | -      |
| Total                  | 259                                   | 5        | 112           | 101         | 44  | 43.56% |
| 300001.05-CP 300001.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 731                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 731                                   | 1        | 76            | 67          | 30  | 44.78% |
| Precinct VBM           | 731                                   | 1        | 330           | 303         | 120 | 39.60% |
| MB Precinct            | 731                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 731                                   | 1        | 10            | 10          | 8   | 80.00% |
| Total                  | 731                                   | 5        | 416           | 380         | 158 | 41.58% |
| 300003.05-CP 300003.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1932                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1932                                  | 1        | 205           | 194         | 92  | 47.42% |
| Precinct VBM           | 1932                                  | 1        | 924           | 866         | 399 | 46.07% |
| MB Precinct            | 1932                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1932                                  | 1        | 27            | 22          | 13  | 59.09% |
| Total                  | 1932                                  | 5        | 1156          | 1082        | 504 | 46.58% |
| 300004.05-CP 300004.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1273                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1273                                  | 1        | 175           | 163         | 71  | 43.56% |
| Precinct VBM           | 1273                                  | 1        | 576           | 530         | 240 | 45.28% |
| MB Precinct            | 1273                                  | 1        | 0             | 0           | 0   | -      |

**COUNTY OF EL DORADO**  
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|------------------------|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Provisional            | 1273                                  | 1        | 17            | 15          | 10  | 66.67% |
| Total                  | 1273                                  | 5        | 768           | 708         | 321 | 45.34% |
| 300005.05-CP 300005.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1374                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1374                                  | 1        | 163           | 149         | 76  | 51.01% |
| Precinct VBM           | 1374                                  | 1        | 660           | 618         | 272 | 44.01% |
| MB Precinct            | 1374                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1374                                  | 1        | 26            | 21          | 11  | 52.38% |
| Total                  | 1374                                  | 5        | 849           | 788         | 359 | 45.56% |
| 300008.05-CP 300008.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1366                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1366                                  | 1        | 139           | 137         | 62  | 45.26% |
| Precinct VBM           | 1366                                  | 1        | 740           | 711         | 292 | 41.07% |
| MB Precinct            | 1366                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1366                                  | 1        | 14            | 13          | 8   | 61.54% |
| Total                  | 1366                                  | 5        | 893           | 861         | 362 | 42.04% |
| 300009.04-CP 300009.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 873                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 873                                   | 1        | 121           | 116         | 65  | 56.03% |
| Precinct VBM           | 873                                   | 1        | 395           | 377         | 216 | 57.29% |
| MB Precinct            | 873                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 873                                   | 1        | 13            | 12          | 7   | 58.33% |
| Total                  | 873                                   | 5        | 529           | 505         | 288 | 57.03% |
| 300010.04-CP 300010.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 1340                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1340                                  | 1        | 171           | 166         | 73  | 43.98% |
| Precinct VBM           | 1340                                  | 1        | 656           | 611         | 276 | 45.17% |
| MB Precinct            | 1340                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1340                                  | 1        | 11            | 10          | 7   | 70.00% |
| Total                  | 1340                                  | 5        | 838           | 787         | 356 | 45.24% |
| 300011.04-CP 300011.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 1491                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1491                                  | 1        | 142           | 139         | 71  | 51.08% |
| Precinct VBM           | 1491                                  | 1        | 711           | 672         | 387 | 57.59% |
| MB Precinct            | 1491                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1491                                  | 1        | 22            | 18          | 14  | 77.78% |
| Total                  | 1491                                  | 5        | 875           | 829         | 472 | 56.94% |
| 300013.05-CP 300013.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1490                                  | 1        | 3             | 3           | 2   | 66.67% |
| Precinct OS            | 1490                                  | 1        | 128           | 122         | 61  | 50.00% |
| Precinct VBM           | 1490                                  | 1        | 721           | 698         | 341 | 48.85% |
| MB Precinct            | 1490                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1490                                  | 1        | 15            | 15          | 9   | 60.00% |
| Total                  | 1490                                  | 5        | 867           | 838         | 413 | 49.28% |
| 300015.04-CP 300015.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 119                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 119                                   | 1        | 10            | 10          | 4   | 40.00% |
| Precinct VBM           | 119                                   | 1        | 63            | 54          | 33  | 61.11% |
| MB Precinct            | 119                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 119                                   | 1        | 4             | 3           | 2   | 66.67% |
| Total                  | 119                                   | 5        | 77            | 67          | 39  | 58.21% |
| 300015.05-CP 300015.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 503                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 503                                   | 1        | 66            | 63          | 23  | 36.51% |
| Precinct VBM           | 503                                   | 1        | 266           | 251         | 112 | 44.62% |
| MB Precinct            | 503                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 503                                   | 1        | 2             | 2           | 1   | 50.00% |
| Total                  | 503                                   | 5        | 334           | 316         | 136 | 43.04% |
| 300017.05-CP 300017.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1240                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1240                                  | 1        | 117           | 107         | 49  | 45.79% |

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|---------------------------------------|-------------|----------|---------------|-------------|-----|--------|--|
|                                       | Reg. Voters | Vote For | Times Counted | Total Votes | YES |        |  |
| Precinct VBM                          | 1240        | 1        | 655           | 626         | 282 | 45.05% |  |
| MB Precinct                           | 1240        | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 1240        | 1        | 9             | 8           | 2   | 25.00% |  |
| Total                                 | 1240        | 5        | 781           | 741         | 333 | 44.94% |  |
| 300018.04-CP 300018.04                |             |          |               |             |     |        |  |
| Precinct TSx                          | 1306        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 1306        | 1        | 148           | 138         | 68  | 49.28% |  |
| Precinct VBM                          | 1306        | 1        | 505           | 484         | 237 | 48.97% |  |
| MB Precinct                           | 1306        | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 1306        | 1        | 31            | 27          | 12  | 44.44% |  |
| Total                                 | 1306        | 5        | 684           | 649         | 317 | 48.84% |  |
| 300020.04-CP 300020.04                |             |          |               |             |     |        |  |
| Precinct TSx                          | 1401        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 1401        | 1        | 168           | 163         | 86  | 52.76% |  |
| Precinct VBM                          | 1401        | 1        | 632           | 592         | 287 | 48.48% |  |
| MB Precinct                           | 1401        | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 1401        | 1        | 14            | 12          | 7   | 58.33% |  |
| Total                                 | 1401        | 5        | 814           | 767         | 380 | 49.54% |  |
| 300021.03-CP 300021.03                |             |          |               |             |     |        |  |
| Precinct TSx                          | 1234        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 1234        | 1        | 153           | 137         | 58  | 42.34% |  |
| Precinct VBM                          | 1234        | 1        | 614           | 581         | 244 | 42.00% |  |
| MB Precinct                           | 1234        | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 1234        | 1        | 13            | 13          | 8   | 61.54% |  |
| Total                                 | 1234        | 5        | 780           | 731         | 310 | 42.41% |  |
| 300022.05-CP 300022.05                |             |          |               |             |     |        |  |
| Precinct TSx                          | 872         | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 872         | 1        | 101           | 95          | 46  | 48.42% |  |
| Precinct VBM                          | 872         | 1        | 356           | 329         | 160 | 48.63% |  |
| MB Precinct                           | 872         | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 872         | 1        | 12            | 11          | 7   | 63.64% |  |
| Total                                 | 872         | 5        | 469           | 435         | 213 | 48.97% |  |
| 300023.03-CP 300023.03                |             |          |               |             |     |        |  |
| Precinct TSx                          | 1819        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 1819        | 1        | 228           | 222         | 101 | 45.50% |  |
| Precinct VBM                          | 1819        | 1        | 924           | 882         | 340 | 38.55% |  |
| MB Precinct                           | 1819        | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 1819        | 1        | 26            | 24          | 9   | 37.50% |  |
| Total                                 | 1819        | 5        | 1178          | 1128        | 450 | 39.89% |  |
| 300023.05-CP 300023.05                |             |          |               |             |     |        |  |
| Precinct TSx                          | 386         | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 386         | 1        | 45            | 42          | 20  | 47.62% |  |
| Precinct VBM                          | 386         | 1        | 193           | 188         | 67  | 35.64% |  |
| MB Precinct                           | 386         | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 386         | 1        | 8             | 7           | 4   | 57.14% |  |
| Total                                 | 386         | 5        | 246           | 237         | 91  | 38.40% |  |
| 400001.14-CP 400001.14                |             |          |               |             |     |        |  |
| Precinct TSx                          | 1957        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 1957        | 1        | 178           | 162         | 88  | 54.32% |  |
| Precinct VBM                          | 1957        | 1        | 992           | 929         | 491 | 52.85% |  |
| MB Precinct                           | 1957        | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 1957        | 1        | 23            | 22          | 12  | 54.55% |  |
| Total                                 | 1957        | 5        | 1193          | 1113        | 591 | 53.10% |  |
| 400002.08-CP 400002.08                |             |          |               |             |     |        |  |
| Precinct TSx                          | 1508        | 1        | 0             | 0           | 0   | -      |  |
| Precinct OS                           | 1508        | 1        | 200           | 188         | 103 | 54.79% |  |
| Precinct VBM                          | 1508        | 1        | 730           | 694         | 399 | 57.49% |  |
| MB Precinct                           | 1508        | 1        | 0             | 0           | 0   | -      |  |
| Provisional                           | 1508        | 1        | 15            | 13          | 8   | 61.54% |  |
| Total                                 | 1508        | 5        | 945           | 895         | 510 | 56.98% |  |
| 400003.08-CP 400003.08                |             |          |               |             |     |        |  |

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|------------------------|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Precinct TSx           | 1800                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1800                                  | 1        | 245           | 227         | 79  | 34.80% |
| Precinct VBM           | 1800                                  | 1        | 773           | 706         | 317 | 44.90% |
| MB Precinct            | 1800                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1800                                  | 1        | 35            | 31          | 13  | 41.94% |
| Total                  | 1800                                  | 5        | 1053          | 964         | 409 | 42.43% |
| 400004.14-CP 400004.14 |                                       |          |               |             |     |        |
| Precinct TSx           | 1536                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1536                                  | 1        | 194           | 175         | 94  | 53.71% |
| Precinct VBM           | 1536                                  | 1        | 662           | 627         | 364 | 58.05% |
| MB Precinct            | 1536                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1536                                  | 1        | 19            | 18          | 14  | 77.78% |
| Total                  | 1536                                  | 5        | 875           | 820         | 472 | 57.56% |
| 400005.08-CP 400005.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 655                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 655                                   | 1        | 79            | 71          | 28  | 39.44% |
| Precinct VBM           | 655                                   | 1        | 262           | 239         | 108 | 45.19% |
| MB Precinct            | 655                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 655                                   | 1        | 11            | 10          | 4   | 40.00% |
| Total                  | 655                                   | 5        | 352           | 320         | 140 | 43.75% |
| 400006.08-CP 400006.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1537                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1537                                  | 1        | 228           | 204         | 65  | 31.86% |
| Precinct VBM           | 1537                                  | 1        | 722           | 652         | 246 | 37.73% |
| MB Precinct            | 1537                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1537                                  | 1        | 19            | 16          | 6   | 37.50% |
| Total                  | 1537                                  | 5        | 969           | 872         | 317 | 36.35% |
| 400007.08-CP 400007.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1766                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1766                                  | 1        | 228           | 218         | 98  | 44.95% |
| Precinct VBM           | 1766                                  | 1        | 765           | 725         | 276 | 38.07% |
| MB Precinct            | 1766                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1766                                  | 1        | 22            | 20          | 9   | 45.00% |
| Total                  | 1766                                  | 5        | 1015          | 963         | 383 | 39.77% |
| 400010.08-CP 400010.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1875                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1875                                  | 1        | 244           | 232         | 98  | 42.24% |
| Precinct VBM           | 1875                                  | 1        | 869           | 813         | 394 | 48.46% |
| MB Precinct            | 1875                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1875                                  | 1        | 31            | 27          | 18  | 66.67% |
| Total                  | 1875                                  | 5        | 1144          | 1072        | 510 | 47.57% |
| 400012.08-CP 400012.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 877                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 877                                   | 1        | 113           | 111         | 63  | 56.76% |
| Precinct VBM           | 877                                   | 1        | 434           | 413         | 183 | 44.31% |
| MB Precinct            | 877                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 877                                   | 1        | 22            | 21          | 6   | 28.57% |
| Total                  | 877                                   | 5        | 569           | 545         | 252 | 46.24% |
| 400013.08-CP 400013.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 758                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 758                                   | 1        | 127           | 118         | 46  | 38.98% |
| Precinct VBM           | 758                                   | 1        | 315           | 299         | 129 | 43.14% |
| MB Precinct            | 758                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 758                                   | 1        | 4             | 4           | 2   | 50.00% |
| Total                  | 758                                   | 5        | 446           | 421         | 177 | 42.04% |
| 400014.08-CP 400014.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1526                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1526                                  | 1        | 230           | 225         | 119 | 52.89% |
| Precinct VBM           | 1526                                  | 1        | 736           | 707         | 385 | 54.46% |
| MB Precinct            | 1526                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1526                                  | 1        | 36            | 36          | 22  | 61.11% |

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|------------------------|---------------------------------------|----------|---------------|-------------|-----|---------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |         |
| Total                  | 1526                                  | 5        | 1002          | 968         | 526 | 54.34%  |
| 400017.08-CP 400017.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 1495                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1495                                  | 1        | 175           | 168         | 124 | 73.81%  |
| Precinct VBM           | 1495                                  | 1        | 752           | 731         | 485 | 66.35%  |
| MB Precinct            | 1495                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1495                                  | 1        | 28            | 26          | 21  | 80.77%  |
| Total                  | 1495                                  | 5        | 955           | 925         | 630 | 68.11%  |
| 400018.08-CP 400018.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 597                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 597                                   | 1        | 86            | 80          | 53  | 66.25%  |
| Precinct VBM           | 597                                   | 1        | 276           | 265         | 143 | 53.96%  |
| MB Precinct            | 597                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 597                                   | 1        | 7             | 6           | 3   | 50.00%  |
| Total                  | 597                                   | 5        | 369           | 351         | 199 | 56.70%  |
| 400020.08-CP 400020.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 724                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 724                                   | 1        | 114           | 109         | 50  | 45.87%  |
| Precinct VBM           | 724                                   | 1        | 343           | 322         | 152 | 47.20%  |
| MB Precinct            | 724                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 724                                   | 1        | 2             | 2           | 2   | 100.00% |
| Total                  | 724                                   | 5        | 459           | 433         | 204 | 47.11%  |
| 400021.07-CP 400021.07 |                                       |          |               |             |     |         |
| Precinct TSx           | 1332                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1332                                  | 1        | 127           | 120         | 57  | 47.50%  |
| Precinct VBM           | 1332                                  | 1        | 767           | 736         | 343 | 46.60%  |
| MB Precinct            | 1332                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1332                                  | 1        | 18            | 14          | 5   | 35.71%  |
| Total                  | 1332                                  | 5        | 912           | 870         | 405 | 46.55%  |
| 500001.10-CP 500001.10 |                                       |          |               |             |     |         |
| Precinct TSx           | 1717                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1717                                  | 1        | 210           | 174         | 81  | 46.55%  |
| Precinct VBM           | 1717                                  | 1        | 617           | 531         | 248 | 46.70%  |
| MB Precinct            | 1717                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1717                                  | 1        | 19            | 15          | 8   | 53.33%  |
| Total                  | 1717                                  | 5        | 846           | 720         | 337 | 46.81%  |
| 500002.10-CP 500002.10 |                                       |          |               |             |     |         |
| Precinct TSx           | 1381                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1381                                  | 1        | 137           | 109         | 44  | 40.37%  |
| Precinct VBM           | 1381                                  | 1        | 412           | 350         | 142 | 40.57%  |
| MB Precinct            | 1381                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1381                                  | 1        | 31            | 26          | 18  | 69.23%  |
| Total                  | 1381                                  | 5        | 580           | 485         | 204 | 42.06%  |
| 500003.10-CP 500003.10 |                                       |          |               |             |     |         |
| Precinct TSx           | 1065                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1065                                  | 1        | 131           | 108         | 56  | 51.85%  |
| Precinct VBM           | 1065                                  | 1        | 346           | 301         | 132 | 43.85%  |
| MB Precinct            | 1065                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1065                                  | 1        | 11            | 5           | 3   | 60.00%  |
| Total                  | 1065                                  | 5        | 488           | 414         | 191 | 46.14%  |
| 500004.10-CP 500004.10 |                                       |          |               |             |     |         |
| Precinct TSx           | 981                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 981                                   | 1        | 122           | 109         | 53  | 48.62%  |
| Precinct VBM           | 981                                   | 1        | 264           | 222         | 87  | 39.19%  |
| MB Precinct            | 981                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 981                                   | 1        | 21            | 18          | 11  | 61.11%  |
| Total                  | 981                                   | 5        | 407           | 349         | 151 | 43.27%  |
| 500005.10-CP 500005.10 |                                       |          |               |             |     |         |
| Precinct TSx           | 946                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 946                                   | 1        | 107           | 87          | 48  | 55.17%  |
| Precinct VBM           | 946                                   | 1        | 250           | 214         | 94  | 43.93%  |



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|------------------------|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| MB Precinct            | 946                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 946                                   | 1        | 16            | 12          | 6   | 50.00% |
| Total                  | 946                                   | 5        | 373           | 313         | 148 | 47.28% |
| 500006.10-CP 500006.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1661                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1661                                  | 1        | 190           | 154         | 70  | 45.45% |
| Precinct VBM           | 1661                                  | 1        | 498           | 425         | 191 | 44.94% |
| MB Precinct            | 1661                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1661                                  | 1        | 58            | 38          | 24  | 63.16% |
| Total                  | 1661                                  | 5        | 746           | 617         | 285 | 46.19% |
| 500007.10-CP 500007.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1788                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1788                                  | 1        | 208           | 177         | 65  | 36.72% |
| Precinct VBM           | 1788                                  | 1        | 688           | 602         | 216 | 35.88% |
| MB Precinct            | 1788                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1788                                  | 1        | 20            | 15          | 8   | 53.33% |
| Total                  | 1788                                  | 5        | 916           | 794         | 289 | 36.40% |
| 500009.10-CP 500009.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1073                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1073                                  | 1        | 121           | 99          | 53  | 53.54% |
| Precinct VBM           | 1073                                  | 1        | 304           | 253         | 122 | 48.22% |
| MB Precinct            | 1073                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1073                                  | 1        | 38            | 25          | 17  | 68.00% |
| Total                  | 1073                                  | 5        | 463           | 377         | 192 | 50.93% |
| 500011.10-CP 500011.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1635                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1635                                  | 1        | 178           | 152         | 77  | 50.66% |
| Precinct VBM           | 1635                                  | 1        | 380           | 314         | 171 | 54.46% |
| MB Precinct            | 1635                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1635                                  | 1        | 40            | 30          | 15  | 50.00% |
| Total                  | 1635                                  | 5        | 598           | 496         | 263 | 53.02% |
| 500012.10-CP 500012.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1574                                  | 1        | 1             | 1           | 0   | 0.00%  |
| Precinct OS            | 1574                                  | 1        | 195           | 168         | 89  | 52.98% |
| Precinct VBM           | 1574                                  | 1        | 601           | 537         | 243 | 45.25% |
| MB Precinct            | 1574                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1574                                  | 1        | 37            | 27          | 15  | 55.56% |
| Total                  | 1574                                  | 5        | 834           | 733         | 347 | 47.34% |
| 500015.10-CP 500015.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 978                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 978                                   | 1        | 105           | 97          | 31  | 31.96% |
| Precinct VBM           | 978                                   | 1        | 407           | 362         | 145 | 40.06% |
| MB Precinct            | 978                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 978                                   | 1        | 13            | 13          | 4   | 30.77% |
| Total                  | 978                                   | 5        | 525           | 472         | 180 | 38.14% |
| 500020.10-CP 500020.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1530                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1530                                  | 1        | 161           | 153         | 83  | 54.25% |
| Precinct VBM           | 1530                                  | 1        | 659           | 633         | 257 | 40.60% |
| MB Precinct            | 1530                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1530                                  | 1        | 27            | 27          | 13  | 48.15% |
| Total                  | 1530                                  | 5        | 847           | 813         | 353 | 43.42% |
| 500021.10-CP 500021.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1404                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1404                                  | 1        | 169           | 157         | 66  | 42.04% |
| Precinct VBM           | 1404                                  | 1        | 632           | 589         | 231 | 39.22% |
| MB Precinct            | 1404                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1404                                  | 1        | 28            | 25          | 14  | 56.00% |
| Total                  | 1404                                  | 5        | 829           | 771         | 311 | 40.34% |
| 95U201.01-MB 95U201.01 |                                       |          |               |             |     |        |
| Precinct TSx           | 8                                     | 1        | 0             | 0           | 0   | -      |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |         |
| Precinct OS            | 8                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 8                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 8                                     | 1        | 7             | 7           | 7   | 100.00% |
| Provisional            | 8                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 8                                     | 5        | 7             | 7           | 7   | 100.00% |
| 95U201.02-MB 95U201.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 541                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 541                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 541                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 541                                   | 1        | 358           | 343         | 261 | 76.09%  |
| Provisional            | 541                                   | 1        | 1             | 1           | 0   | 0.00%   |
| Total                  | 541                                   | 5        | 359           | 344         | 261 | 75.87%  |
| 95U202.02-MB 95U202.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 859                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 859                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 859                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 859                                   | 1        | 574           | 532         | 255 | 47.93%  |
| Provisional            | 859                                   | 1        | 4             | 4           | 3   | 75.00%  |
| Total                  | 859                                   | 5        | 578           | 536         | 258 | 48.13%  |
| 95U203.02-MB 95U203.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                                     | 5        | 0             | 0           | 0   | -       |
| 95U204.01-MB 95U204.01 |                                       |          |               |             |     |         |
| Precinct TSx           | 895                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 895                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 895                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 895                                   | 1        | 651           | 620         | 391 | 63.06%  |
| Provisional            | 895                                   | 1        | 2             | 2           | 2   | 100.00% |
| Total                  | 895                                   | 5        | 653           | 622         | 393 | 63.18%  |
| 95U301.05-MB 95U301.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 244                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 244                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 244                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 244                                   | 1        | 151           | 138         | 53  | 38.41%  |
| Provisional            | 244                                   | 1        | 2             | 2           | 1   | 50.00%  |
| Total                  | 244                                   | 5        | 153           | 140         | 54  | 38.57%  |
| 95U302.04-MB 95U302.04 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 1             | 1           | 1   | 100.00% |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                                     | 5        | 1             | 1           | 1   | 100.00% |
| 95U302.05-MB 95U302.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 128                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 128                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 128                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 128                                   | 1        | 86            | 83          | 38  | 45.78%  |
| Provisional            | 128                                   | 1        | 0             | 0           | 0   | -       |
| Total                  | 128                                   | 5        | 86            | 83          | 38  | 45.78%  |
| 95U303.03-MB 95U303.03 |                                       |          |               |             |     |         |
| Precinct TSx           | 144                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 144                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 144                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 144                                   | 1        | 84            | 79          | 35  | 44.30%  |
| Provisional            | 144                                   | 1        | 0             | 0           | 0   | -       |
| Total                  | 144                                   | 5        | 84            | 79          | 35  | 44.30%  |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |         |
| 95U303.05-MB 95U303.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 159                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 159                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 159                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 159                                   | 1        | 111           | 100         | 42  | 42.00%  |
| Provisional            | 159                                   | 1        | 1             | 1           | 0   | 0.00%   |
| Total                  | 159                                   | 5        | 112           | 101         | 42  | 41.58%  |
| 95U304.05-MB 95U304.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                                     | 5        | 0             | 0           | 0   | -       |
| 95U305.05-MB 95U305.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 7                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 7                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 7                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 7                                     | 1        | 7             | 7           | 3   | 42.86%  |
| Provisional            | 7                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 7                                     | 5        | 7             | 7           | 3   | 42.86%  |
| 95U401.08-MB 95U401.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 39                                    | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 39                                    | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 39                                    | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 39                                    | 1        | 16            | 13          | 9   | 69.23%  |
| Provisional            | 39                                    | 1        | 0             | 0           | 0   | -       |
| Total                  | 39                                    | 5        | 16            | 13          | 9   | 69.23%  |
| 95U402.07-MB 95U402.07 |                                       |          |               |             |     |         |
| Precinct TSx           | 413                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 413                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 413                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 413                                   | 1        | 256           | 249         | 151 | 60.64%  |
| Provisional            | 413                                   | 1        | 1             | 1           | 0   | 0.00%   |
| Total                  | 413                                   | 5        | 257           | 250         | 151 | 60.40%  |
| 95U402.08-MB 95U402.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 873                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 873                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 873                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 873                                   | 1        | 566           | 544         | 351 | 64.52%  |
| Provisional            | 873                                   | 1        | 3             | 3           | 3   | 100.00% |
| Total                  | 873                                   | 5        | 569           | 547         | 354 | 64.72%  |
| 95U403.08-MB 95U403.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 1591                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1591                                  | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 1591                                  | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 1591                                  | 1        | 1042          | 1001        | 499 | 49.85%  |
| Provisional            | 1591                                  | 1        | 13            | 11          | 5   | 45.45%  |
| Total                  | 1591                                  | 5        | 1055          | 1012        | 504 | 49.80%  |
| 95U404.06-MB 95U404.06 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                                     | 5        | 0             | 0           | 0   | -       |
| 95U404.08-MB 95U404.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -       |

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|-------------------------------|---------------------------------------|----------|---------------|-------------|-------|---------|
|                               | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES   |         |
| Provisional                   | 0                                     | 1        | 0             | 0           | 0     | -       |
| Total                         | 0                                     | 5        | 0             | 0           | 0     | -       |
| 95U501.09-MB 95U501.09        |                                       |          |               |             |       |         |
| Precinct TSx                  | 0                                     | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 0                                     | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 0                                     | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 0                                     | 1        | 2             | 2           | 2     | 100.00% |
| Provisional                   | 0                                     | 1        | 0             | 0           | 0     | -       |
| Total                         | 0                                     | 5        | 2             | 2           | 2     | 100.00% |
| 95U501.10-MB 95U501.10        |                                       |          |               |             |       |         |
| Precinct TSx                  | 286                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 286                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 286                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 286                                   | 1        | 172           | 161         | 71    | 44.10%  |
| Provisional                   | 286                                   | 1        | 2             | 2           | 0     | 0.00%   |
| Total                         | 286                                   | 5        | 174           | 163         | 71    | 43.56%  |
| 95U598.10-MB 95U598.10        |                                       |          |               |             |       |         |
| Precinct TSx                  | 568                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 568                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 568                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 568                                   | 1        | 239           | 181         | 104   | 57.46%  |
| Provisional                   | 568                                   | 1        | 3             | 1           | 0     | 0.00%   |
| Total                         | 568                                   | 5        | 242           | 182         | 104   | 57.14%  |
| 95U599.10-MB 95U599.10        |                                       |          |               |             |       |         |
| Precinct TSx                  | 75                                    | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 75                                    | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 75                                    | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 75                                    | 1        | 38            | 31          | 11    | 35.48%  |
| Provisional                   | 75                                    | 1        | 0             | 0           | 0     | -       |
| Total                         | 75                                    | 5        | 38            | 31          | 11    | 35.48%  |
| 96U101.11-MB 96U101.11        |                                       |          |               |             |       |         |
| Precinct TSx                  | 626                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 626                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 626                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 626                                   | 1        | 423           | 407         | 287   | 70.52%  |
| Provisional                   | 626                                   | 1        | 6             | 6           | 5     | 83.33%  |
| Total                         | 626                                   | 5        | 429           | 413         | 292   | 70.70%  |
| 96U401.14-MB 96U401.14        |                                       |          |               |             |       |         |
| Precinct TSx                  | 566                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 566                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 566                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 566                                   | 1        | 359           | 336         | 201   | 59.82%  |
| Provisional                   | 566                                   | 1        | 6             | 6           | 2     | 33.33%  |
| Total                         | 566                                   | 5        | 365           | 342         | 203   | 59.36%  |
| Total                         |                                       |          |               |             |       |         |
| Precinct TSx                  | 109479                                | 102      | 8             | 8           | 4     | 50.00%  |
| Precinct OS                   | 109479                                | 102      | 12009         | 11135       | 5848  | 52.52%  |
| Precinct VBM                  | 109479                                | 102      | 45535         | 42640       | 21959 | 51.50%  |
| MB Precinct                   | 109479                                | 102      | 5143          | 4835        | 2772  | 57.33%  |
| Provisional                   | 109479                                | 102      | 1584          | 1371        | 823   | 60.03%  |
| Total                         | 109479                                | 510      | 64279         | 59989       | 31406 | 52.35%  |
| STATE BOARD OF EQUALIZATION   |                                       |          |               |             |       |         |
| STATE BOARD OF EQUALIZATION 1 |                                       |          |               |             |       |         |
| Precinct TSx                  | 109479                                | 102      | 8             | 8           | 4     | 50.00%  |
| Precinct OS                   | 109479                                | 102      | 12009         | 11135       | 5848  | 52.52%  |
| Precinct VBM                  | 109479                                | 102      | 45535         | 42640       | 21959 | 51.50%  |
| MB Precinct                   | 109479                                | 102      | 5143          | 4835        | 2772  | 57.33%  |
| Provisional                   | 109479                                | 102      | 1584          | 1371        | 823   | 60.03%  |
| Total                         | 109479                                | 510      | 64279         | 59989       | 31406 | 52.35%  |
| Total                         |                                       |          |               |             |       |         |

**COUNTY OF EL DORADO**  
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| Measure E - County (majority to pass) |               |            |               |              |              |               |
|---------------------------------------|---------------|------------|---------------|--------------|--------------|---------------|
|                                       | Reg. Voters   | Vote For   | Times Counted | Total Votes  | YES          |               |
| Precinct TSx                          | 109479        | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                           | 109479        | 102        | 12009         | 11135        | 5848         | 52.52%        |
| Precinct VBM                          | 109479        | 102        | 45535         | 42640        | 21959        | 51.50%        |
| MB Precinct                           | 109479        | 102        | 5143          | 4835         | 2772         | 57.33%        |
| Provisional                           | 109479        | 102        | 1584          | 1371         | 823          | 60.03%        |
| <b>Total</b>                          | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>59989</b> | <b>31406</b> | <b>52.35%</b> |
| <b>UNITED STATES SENATE</b>           |               |            |               |              |              |               |
| <b>UNITED STATES SENATE</b>           |               |            |               |              |              |               |
| Precinct TSx                          | 109479        | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                           | 109479        | 102        | 12009         | 11135        | 5848         | 52.52%        |
| Precinct VBM                          | 109479        | 102        | 45535         | 42640        | 21959        | 51.50%        |
| MB Precinct                           | 109479        | 102        | 5143          | 4835         | 2772         | 57.33%        |
| Provisional                           | 109479        | 102        | 1584          | 1371         | 823          | 60.03%        |
| <b>Total</b>                          | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>59989</b> | <b>31406</b> | <b>52.35%</b> |
| <b>Total</b>                          |               |            |               |              |              |               |
| Precinct TSx                          | 109479        | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                           | 109479        | 102        | 12009         | 11135        | 5848         | 52.52%        |
| Precinct VBM                          | 109479        | 102        | 45535         | 42640        | 21959        | 51.50%        |
| MB Precinct                           | 109479        | 102        | 5143          | 4835         | 2772         | 57.33%        |
| Provisional                           | 109479        | 102        | 1584          | 1371         | 823          | 60.03%        |
| <b>Total</b>                          | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>59989</b> | <b>31406</b> | <b>52.35%</b> |
| <b>Total</b>                          |               |            |               |              |              |               |
| <b>UNITED STATES REPRESENTATIVE</b>   |               |            |               |              |              |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>     |               |            |               |              |              |               |
| Precinct TSx                          | 109479        | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                           | 109479        | 102        | 12009         | 11135        | 5848         | 52.52%        |
| Precinct VBM                          | 109479        | 102        | 45535         | 42640        | 21959        | 51.50%        |
| MB Precinct                           | 109479        | 102        | 5143          | 4835         | 2772         | 57.33%        |
| Provisional                           | 109479        | 102        | 1584          | 1371         | 823          | 60.03%        |
| <b>Total</b>                          | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>59989</b> | <b>31406</b> | <b>52.35%</b> |
| <b>Total</b>                          |               |            |               |              |              |               |
| Precinct TSx                          | 109479        | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                           | 109479        | 102        | 12009         | 11135        | 5848         | 52.52%        |
| Precinct VBM                          | 109479        | 102        | 45535         | 42640        | 21959        | 51.50%        |
| MB Precinct                           | 109479        | 102        | 5143          | 4835         | 2772         | 57.33%        |
| Provisional                           | 109479        | 102        | 1584          | 1371         | 823          | 60.03%        |
| <b>Total</b>                          | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>59989</b> | <b>31406</b> | <b>52.35%</b> |
| <b>Total</b>                          |               |            |               |              |              |               |
| <b>STATE SENATE</b>                   |               |            |               |              |              |               |
| <b>1ST STATE SENATE DISTRICT</b>      |               |            |               |              |              |               |
| Precinct TSx                          | 109479        | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                           | 109479        | 102        | 12009         | 11135        | 5848         | 52.52%        |
| Precinct VBM                          | 109479        | 102        | 45535         | 42640        | 21959        | 51.50%        |
| MB Precinct                           | 109479        | 102        | 5143          | 4835         | 2772         | 57.33%        |
| Provisional                           | 109479        | 102        | 1584          | 1371         | 823          | 60.03%        |
| <b>Total</b>                          | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>59989</b> | <b>31406</b> | <b>52.35%</b> |
| <b>Total</b>                          |               |            |               |              |              |               |
| Precinct TSx                          | 109479        | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                           | 109479        | 102        | 12009         | 11135        | 5848         | 52.52%        |
| Precinct VBM                          | 109479        | 102        | 45535         | 42640        | 21959        | 51.50%        |
| MB Precinct                           | 109479        | 102        | 5143          | 4835         | 2772         | 57.33%        |
| Provisional                           | 109479        | 102        | 1584          | 1371         | 823          | 60.03%        |
| <b>Total</b>                          | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>59989</b> | <b>31406</b> | <b>52.35%</b> |
| <b>Total</b>                          |               |            |               |              |              |               |
| <b>STATE ASSEMBLY DISTRICTS</b>       |               |            |               |              |              |               |
| <b>5TH STATE ASSEMBLY DIST</b>        |               |            |               |              |              |               |
| Precinct TSx                          | 70961         | 75         | 4             | 4            | 2            | 50.00%        |
| Precinct OS                           | 70961         | 75         | 7627          | 7040         | 3405         | 48.37%        |
| Precinct VBM                          | 70961         | 75         | 28225         | 26278        | 12423        | 47.28%        |
| MB Precinct                           | 70961         | 75         | 4361          | 4092         | 2284         | 55.82%        |

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|---------------------------------------|-------------|----------|---------------|-------------|-------|--------|
|                                       | Reg. Voters | Vote For | Times Counted | Total Votes | YES   |        |
| Provisional                           | 70961       | 75       | 1038          | 883         | 494   | 55.95% |
| Total                                 | 70961       | 375      | 41255         | 38297       | 18608 | 48.59% |
| <b>6TH STATE ASSEMBLY DIST</b>        |             |          |               |             |       |        |
| Precinct TSx                          | 38518       | 27       | 4             | 4           | 2     | 50.00% |
| Precinct OS                           | 38518       | 27       | 4382          | 4095        | 2443  | 59.66% |
| Precinct VBM                          | 38518       | 27       | 17310         | 16362       | 9536  | 58.28% |
| MB Precinct                           | 38518       | 27       | 782           | 743         | 488   | 65.68% |
| Provisional                           | 38518       | 27       | 546           | 488         | 329   | 67.42% |
| Total                                 | 38518       | 135      | 23024         | 21692       | 12798 | 59.00% |
| <b>Total</b>                          |             |          |               |             |       |        |
| Precinct TSx                          | 109479      | 102      | 8             | 8           | 4     | 50.00% |
| Precinct OS                           | 109479      | 102      | 12009         | 11135       | 5848  | 52.52% |
| Precinct VBM                          | 109479      | 102      | 45535         | 42640       | 21959 | 51.50% |
| MB Precinct                           | 109479      | 102      | 5143          | 4835        | 2772  | 57.33% |
| Provisional                           | 109479      | 102      | 1584          | 1371        | 823   | 60.03% |
| Total                                 | 109479      | 510      | 64279         | 59989       | 31406 | 52.35% |
| <b>SUPERVISORIAL DISTRICTS</b>        |             |          |               |             |       |        |
| <b>1ST SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 22587       | 14       | 0             | 0           | 0     | -      |
| Precinct OS                           | 22587       | 14       | 2530          | 2377        | 1446  | 60.83% |
| Precinct VBM                          | 22587       | 14       | 10411         | 9871        | 5937  | 60.15% |
| MB Precinct                           | 22587       | 14       | 423           | 407         | 287   | 70.52% |
| Provisional                           | 22587       | 14       | 301           | 267         | 191   | 71.54% |
| Total                                 | 22587       | 70       | 13665         | 12922       | 7861  | 60.83% |
| <b>2ND SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 23114       | 23       | 4             | 4           | 2     | 50.00% |
| Precinct OS                           | 23114       | 23       | 2509          | 2365        | 1362  | 57.59% |
| Precinct VBM                          | 23114       | 23       | 9647          | 9115        | 4982  | 54.66% |
| MB Precinct                           | 23114       | 23       | 1590          | 1502        | 914   | 60.85% |
| Provisional                           | 23114       | 23       | 327           | 292         | 181   | 61.99% |
| Total                                 | 23114       | 115      | 14077         | 13278       | 7441  | 56.04% |
| <b>3RD SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 21691       | 26       | 3             | 3           | 2     | 66.67% |
| Precinct OS                           | 21691       | 26       | 2368          | 2241        | 1059  | 47.26% |
| Precinct VBM                          | 21691       | 26       | 10021         | 9463        | 4346  | 45.93% |
| MB Precinct                           | 21691       | 26       | 440           | 408         | 172   | 42.16% |
| Provisional                           | 21691       | 26       | 277           | 246         | 140   | 56.91% |
| Total                                 | 21691       | 130      | 13109         | 12361       | 5719  | 46.27% |
| <b>4TH SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 23425       | 22       | 0             | 0           | 0     | -      |
| Precinct OS                           | 23425       | 22       | 2568          | 2408        | 1165  | 48.38% |
| Precinct VBM                          | 23425       | 22       | 9398          | 8858        | 4415  | 49.84% |
| MB Precinct                           | 23425       | 22       | 2239          | 2143        | 1211  | 56.51% |
| Provisional                           | 23425       | 22       | 315           | 287         | 155   | 54.01% |
| Total                                 | 23425       | 110      | 14520         | 13696       | 6946  | 50.72% |
| <b>5TH SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 18662       | 17       | 1             | 1           | 0     | 0.00%  |
| Precinct OS                           | 18662       | 17       | 2034          | 1744        | 816   | 46.79% |
| Precinct VBM                          | 18662       | 17       | 6058          | 5333        | 2279  | 42.73% |
| MB Precinct                           | 18662       | 17       | 451           | 375         | 188   | 50.13% |
| Provisional                           | 18662       | 17       | 364           | 279         | 156   | 55.91% |
| Total                                 | 18662       | 85       | 8908          | 7732        | 3439  | 44.48% |
| <b>Total</b>                          |             |          |               |             |       |        |
| Precinct TSx                          | 109479      | 102      | 8             | 8           | 4     | 50.00% |
| Precinct OS                           | 109479      | 102      | 12009         | 11135       | 5848  | 52.52% |
| Precinct VBM                          | 109479      | 102      | 45535         | 42640       | 21959 | 51.50% |
| MB Precinct                           | 109479      | 102      | 5143          | 4835        | 2772  | 57.33% |
| Provisional                           | 109479      | 102      | 1584          | 1371        | 823   | 60.03% |
| Total                                 | 109479      | 510      | 64279         | 59989       | 31406 | 52.35% |

**COUNTY OF EL DORADO**  
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|---------------------------------|---------------------------------------|----------|---------------|-------------|-------|--------|
|                                 | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES   |        |
| <b>CITIES</b>                   |                                       |          |               |             |       |        |
| <b>CITY OF PLACERVILLE</b>      |                                       |          |               |             |       |        |
| Precinct TSx                    | 5569                                  | 5        | 0             | 0           | 0     | -      |
| Precinct OS                     | 5569                                  | 5        | 631           | 584         | 272   | 46.58% |
| Precinct VBM                    | 5569                                  | 5        | 2590          | 2407        | 1072  | 44.54% |
| MB Precinct                     | 5569                                  | 5        | 0             | 0           | 0     | -      |
| Provisional                     | 5569                                  | 5        | 80            | 68          | 42    | 61.76% |
| Total                           | 5569                                  | 25       | 3301          | 3059        | 1386  | 45.31% |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                                       |          |               |             |       |        |
| Precinct TSx                    | 9465                                  | 7        | 0             | 0           | 0     | -      |
| Precinct OS                     | 9465                                  | 7        | 1063          | 887         | 410   | 46.22% |
| Precinct VBM                    | 9465                                  | 7        | 2796          | 2380        | 1023  | 42.98% |
| MB Precinct                     | 9465                                  | 7        | 0             | 0           | 0     | -      |
| Provisional                     | 9465                                  | 7        | 224           | 164         | 99    | 60.37% |
| Total                           | 9465                                  | 35       | 4083          | 3431        | 1532  | 44.65% |
| <b>UNINCORPORATED AREA</b>      |                                       |          |               |             |       |        |
| Precinct TSx                    | 94445                                 | 90       | 8             | 8           | 4     | 50.00% |
| Precinct OS                     | 94445                                 | 90       | 10315         | 9664        | 5166  | 53.46% |
| Precinct VBM                    | 94445                                 | 90       | 40149         | 37853       | 19864 | 52.48% |
| MB Precinct                     | 94445                                 | 90       | 5143          | 4835        | 2772  | 57.33% |
| Provisional                     | 94445                                 | 90       | 1280          | 1139        | 682   | 59.88% |
| Total                           | 94445                                 | 450      | 56895         | 53499       | 28488 | 53.25% |
| <b>Total</b>                    |                                       |          |               |             |       |        |
| Precinct TSx                    | 109479                                | 102      | 8             | 8           | 4     | 50.00% |
| Precinct OS                     | 109479                                | 102      | 12009         | 11135       | 5848  | 52.52% |
| Precinct VBM                    | 109479                                | 102      | 45535         | 42640       | 21959 | 51.50% |
| MB Precinct                     | 109479                                | 102      | 5143          | 4835        | 2772  | 57.33% |
| Provisional                     | 109479                                | 102      | 1584          | 1371        | 823   | 60.03% |
| Total                           | 109479                                | 510      | 64279         | 59989       | 31406 | 52.35% |

**COUNTY OF EL DORADO**  
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|--------------------------|--|
|                          | NO                                       |
| <b>Jurisdiction Wide</b> |  |
| 100001.11-CP 100001.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 75 41.67%                                |
| Precinct VBM             | 308 41.51%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 6 40.00%                                 |
| Total                    | 389 41.52%                               |
| 100002.11-CP 100002.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 31 36.05%                                |
| Precinct VBM             | 216 47.68%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 3 15.79%                                 |
| Total                    | 250 44.80%                               |
| 100003.11-CP 100003.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 54 46.55%                                |
| Precinct VBM             | 297 40.52%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 4 16.67%                                 |
| Total                    | 355 40.66%                               |
| 100004.11-CP 100004.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 92 39.15%                                |
| Precinct VBM             | 219 31.06%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 1 33.33%                                 |
| Total                    | 312 33.09%                               |
| 100005.11-CP 100005.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 61 43.88%                                |
| Precinct VBM             | 429 50.35%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 6 26.09%                                 |
| Total                    | 496 48.92%                               |
| 100006.11-CP 100006.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 90 38.30%                                |
| Precinct VBM             | 282 31.69%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 10 37.04%                                |
| Total                    | 382 33.16%                               |
| 100008.11-CP 100008.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 61 33.52%                                |
| Precinct VBM             | 248 35.33%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 3 23.08%                                 |
| Total                    | 312 34.78%                               |
| 100009.11-CP 100009.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 59 37.82%                                |
| Precinct VBM             | 301 43.43%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 6 31.58%                                 |
| Total                    | 366 42.17%                               |
| 100012.11-CP 100012.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 97 39.43%                                |
| Precinct VBM             | 349 38.44%                               |



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|                        | Measure E - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| MB Precinct            | 0 -                                      |
| Provisional            | 11 40.74%                                |
| Total                  | 457 38.70%                               |
| 100013.11-CP 100013.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 96 41.20%                                |
| Precinct VBM           | 337 38.96%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 26.92%                                 |
| Total                  | 440 39.15%                               |
| 100014.11-CP 100014.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 75 37.50%                                |
| Precinct VBM           | 266 39.00%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 10 37.04%                                |
| Total                  | 351 38.61%                               |
| 100022.11-CP 100022.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 53 31.74%                                |
| Precinct VBM           | 248 38.69%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 10.00%                                 |
| Total                  | 302 36.92%                               |
| 100023.11-CP 100023.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 87 43.07%                                |
| Precinct VBM           | 434 43.18%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 25.00%                                 |
| Total                  | 528 42.75%                               |
| 200003.12-CP 200003.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 65 43.33%                                |
| Precinct VBM           | 257 49.71%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 13 50.00%                                |
| Total                  | 335 48.34%                               |
| 200004.12-CP 200004.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 81 46.55%                                |
| Precinct VBM           | 295 47.05%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 38.46%                                 |
| Total                  | 381 46.81%                               |
| 200005.02-CP 200005.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 55 61.11%                                |
| Precinct VBM           | 243 57.86%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 47.37%                                 |
| Total                  | 307 58.03%                               |
| 200006.12-CP 200006.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 46 40.71%                                |
| Precinct VBM           | 236 46.46%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 11 39.29%                                |
| Total                  | 293 45.15%                               |
| 200008.12-CP 200008.12 |  |
| Precinct TSx           | 0 -                                      |

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|------------------------|--|
|                        | NO                                       |
| Precinct OS            | 53 37.06%                                |
| Precinct VBM           | 169 48.29%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 30.00%                                 |
| Total                  | 225 44.73%                               |
| 200010.12-CP 200010.12 |  |
| Precinct TSx           | 2 50.00%                                 |
| Precinct OS            | 78 44.83%                                |
| Precinct VBM           | 352 44.22%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 10 37.04%                                |
| Total                  | 442 44.16%                               |
| 200012.12-CP 200012.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 58 41.73%                                |
| Precinct VBM           | 182 41.65%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 50.00%                                 |
| Total                  | 246 41.84%                               |
| 200013.12-CP 200013.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 95 47.74%                                |
| Precinct VBM           | 333 49.93%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 27.27%                                 |
| Total                  | 434 48.87%                               |
| 200014.12-CP 200014.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 68 35.60%                                |
| Precinct VBM           | 240 42.40%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 11 39.29%                                |
| Total                  | 319 40.64%                               |
| 200015.02-CP 200015.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 106 51.46%                               |
| Precinct VBM           | 447 53.15%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 36.36%                                 |
| Total                  | 561 52.48%                               |
| 200016.12-CP 200016.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 3 12.50%                                 |
| Precinct VBM           | 38 27.94%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 0.00%                                  |
| Total                  | 41 24.85%                                |
| 200016.13-CP 200016.13 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 19 25.68%                                |
| Precinct VBM           | 89 26.89%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 0.00%                                  |
| Total                  | 108 26.41%                               |
| 200017.02-CP 200017.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 60 44.12%                                |
| Precinct VBM           | 302 54.81%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 27.27%                                 |
| Total                  | 365 52.29%                               |

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|                        | Measure E - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| 200018.02-CP 200018.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 89 55.63%                                |
| Precinct VBM           | 359 54.31%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 54.55%                                 |
| Total                  | 454 54.57%                               |
| 200020.02-CP 200020.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 39 29.32%                                |
| Precinct VBM           | 152 29.46%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 43.75%                                 |
| Total                  | 198 29.77%                               |
| 200021.01-CP 200021.01 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 12 31.58%                                |
| Precinct VBM           | 80 40.20%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 66.67%                                 |
| Total                  | 96 39.51%                                |
| 200021.02-CP 200021.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 43 25.75%                                |
| Precinct VBM           | 184 25.31%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 17.65%                                 |
| Total                  | 230 25.25%                               |
| 200024.02-CP 200024.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 33 61.11%                                |
| Precinct VBM           | 175 66.04%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 50.00%                                 |
| Total                  | 212 64.83%                               |
| 300001.04-CP 300001.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 8 72.73%                                 |
| Precinct VBM           | 49 54.44%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 -                                      |
| Total                  | 57 56.44%                                |
| 300001.05-CP 300001.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 37 55.22%                                |
| Precinct VBM           | 183 60.40%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 2 20.00%                                 |
| Total                  | 222 58.42%                               |
| 300003.05-CP 300003.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 102 52.58%                               |
| Precinct VBM           | 467 53.93%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 40.91%                                 |
| Total                  | 578 53.42%                               |
| 300004.05-CP 300004.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 92 56.44%                                |
| Precinct VBM           | 290 54.72%                               |
| MB Precinct            | 0 -                                      |

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|                        | Measure E - County<br>(majority to pass) |        |
|------------------------|--|--------|
|                        | NO                                       |        |
| Provisional            | 5  | 33.33% |
| Total                  | 387                                      | 54.66% |
| 300005.05-CP 300005.05 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 73                                       | 48.99% |
| Precinct VBM           | 346                                      | 55.99% |
| MB Precinct            | 0  | -      |
| Provisional            | 10                                       | 47.62% |
| Total                  | 429                                      | 54.44% |
| 300008.05-CP 300008.05 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 75                                       | 54.74% |
| Precinct VBM           | 419                                      | 58.93% |
| MB Precinct            | 0  | -      |
| Provisional            | 5  | 38.46% |
| Total                  | 499                                      | 57.96% |
| 300009.04-CP 300009.04 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 51                                       | 43.97% |
| Precinct VBM           | 161                                      | 42.71% |
| MB Precinct            | 0  | -      |
| Provisional            | 5  | 41.67% |
| Total                  | 217                                      | 42.97% |
| 300010.04-CP 300010.04 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 93                                       | 56.02% |
| Precinct VBM           | 335                                      | 54.83% |
| MB Precinct            | 0  | -      |
| Provisional            | 3  | 30.00% |
| Total                  | 431                                      | 54.76% |
| 300011.04-CP 300011.04 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 68                                       | 48.92% |
| Precinct VBM           | 285                                      | 42.41% |
| MB Precinct            | 0  | -      |
| Provisional            | 4  | 22.22% |
| Total                  | 357                                      | 43.06% |
| 300013.05-CP 300013.05 |  |        |
| Precinct TSx           | 1  | 33.33% |
| Precinct OS            | 61                                       | 50.00% |
| Precinct VBM           | 357                                      | 51.15% |
| MB Precinct            | 0  | -      |
| Provisional            | 6  | 40.00% |
| Total                  | 425                                      | 50.72% |
| 300015.04-CP 300015.04 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 6  | 60.00% |
| Precinct VBM           | 21                                       | 38.89% |
| MB Precinct            | 0  | -      |
| Provisional            | 1  | 33.33% |
| Total                  | 28                                       | 41.79% |
| 300015.05-CP 300015.05 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 40                                       | 63.49% |
| Precinct VBM           | 139                                      | 55.38% |
| MB Precinct            | 0  | -      |
| Provisional            | 1  | 50.00% |
| Total                  | 180                                      | 56.96% |
| 300017.05-CP 300017.05 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 58                                       | 54.21% |

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|                        | Measure E - County<br>(majority to pass) |        |
|------------------------|--|--------|
|                        | NO                                       |        |
| Precinct VBM           | 344                                      | 54.95% |
| MB Precinct            | 0  | -      |
| Provisional            | 6  | 75.00% |
| Total                  | 408                                      | 55.06% |
| 300018.04-CP 300018.04 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 70                                       | 50.72% |
| Precinct VBM           | 247                                      | 51.03% |
| MB Precinct            | 0  | -      |
| Provisional            | 15                                       | 55.56% |
| Total                  | 332                                      | 51.16% |
| 300020.04-CP 300020.04 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 77                                       | 47.24% |
| Precinct VBM           | 305                                      | 51.52% |
| MB Precinct            | 0  | -      |
| Provisional            | 5  | 41.67% |
| Total                  | 387                                      | 50.46% |
| 300021.03-CP 300021.03 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 79                                       | 57.66% |
| Precinct VBM           | 337                                      | 58.00% |
| MB Precinct            | 0  | -      |
| Provisional            | 5  | 38.46% |
| Total                  | 421                                      | 57.59% |
| 300022.05-CP 300022.05 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 49                                       | 51.58% |
| Precinct VBM           | 169                                      | 51.37% |
| MB Precinct            | 0  | -      |
| Provisional            | 4  | 36.36% |
| Total                  | 222                                      | 51.03% |
| 300023.03-CP 300023.03 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 121                                      | 54.50% |
| Precinct VBM           | 542                                      | 61.45% |
| MB Precinct            | 0  | -      |
| Provisional            | 15                                       | 62.50% |
| Total                  | 678                                      | 60.11% |
| 300023.05-CP 300023.05 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 22                                       | 52.38% |
| Precinct VBM           | 121                                      | 64.36% |
| MB Precinct            | 0  | -      |
| Provisional            | 3  | 42.86% |
| Total                  | 146                                      | 61.60% |
| 400001.14-CP 400001.14 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 74                                       | 45.68% |
| Precinct VBM           | 438                                      | 47.15% |
| MB Precinct            | 0  | -      |
| Provisional            | 10                                       | 45.45% |
| Total                  | 522                                      | 46.90% |
| 400002.08-CP 400002.08 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 85                                       | 45.21% |
| Precinct VBM           | 295                                      | 42.51% |
| MB Precinct            | 0  | -      |
| Provisional            | 5  | 38.46% |
| Total                  | 385                                      | 43.02% |
| 400003.08-CP 400003.08 |  |        |

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|                        | Measure E - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 148 65.20%                               |
| Precinct VBM           | 389 55.10%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 18 58.06%                                |
| Total                  | 555 57.57%                               |
| 400004.14-CP 400004.14 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 81 46.29%                                |
| Precinct VBM           | 263 41.95%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 22.22%                                 |
| Total                  | 348 42.44%                               |
| 400005.08-CP 400005.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 43 60.56%                                |
| Precinct VBM           | 131 54.81%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 60.00%                                 |
| Total                  | 180 56.25%                               |
| 400006.08-CP 400006.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 139 68.14%                               |
| Precinct VBM           | 406 62.27%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 10 62.50%                                |
| Total                  | 555 63.65%                               |
| 400007.08-CP 400007.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 120 55.05%                               |
| Precinct VBM           | 449 61.93%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 11 55.00%                                |
| Total                  | 580 60.23%                               |
| 400010.08-CP 400010.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 134 57.76%                               |
| Precinct VBM           | 419 51.54%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 33.33%                                 |
| Total                  | 562 52.43%                               |
| 400012.08-CP 400012.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 48 43.24%                                |
| Precinct VBM           | 230 55.69%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 15 71.43%                                |
| Total                  | 293 53.76%                               |
| 400013.08-CP 400013.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 72 61.02%                                |
| Precinct VBM           | 170 56.86%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 2 50.00%                                 |
| Total                  | 244 57.96%                               |
| 400014.08-CP 400014.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 106 47.11%                               |
| Precinct VBM           | 322 45.54%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 14 38.89%                                |

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|                        | Measure E - County<br>(majority to pass) |        |
|------------------------|--|--------|
|                        | NO                                       |        |
| Total                  | 442                                      | 45.66% |
| 400017.08-CP 400017.08 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 44                                       | 26.19% |
| Precinct VBM           | 246                                      | 33.65% |
| MB Precinct            | 0  | -      |
| Provisional            | 5  | 19.23% |
| Total                  | 295                                      | 31.89% |
| 400018.08-CP 400018.08 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 27                                       | 33.75% |
| Precinct VBM           | 122                                      | 46.04% |
| MB Precinct            | 0  | -      |
| Provisional            | 3  | 50.00% |
| Total                  | 152                                      | 43.30% |
| 400020.08-CP 400020.08 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 59                                       | 54.13% |
| Precinct VBM           | 170                                      | 52.80% |
| MB Precinct            | 0  | -      |
| Provisional            | 0  | 0.00%  |
| Total                  | 229                                      | 52.89% |
| 400021.07-CP 400021.07 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 63                                       | 52.50% |
| Precinct VBM           | 393                                      | 53.40% |
| MB Precinct            | 0  | -      |
| Provisional            | 9  | 64.29% |
| Total                  | 465                                      | 53.45% |
| 500001.10-CP 500001.10 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 93                                       | 53.45% |
| Precinct VBM           | 283                                      | 53.30% |
| MB Precinct            | 0  | -      |
| Provisional            | 7  | 46.67% |
| Total                  | 383                                      | 53.19% |
| 500002.10-CP 500002.10 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 65                                       | 59.63% |
| Precinct VBM           | 208                                      | 59.43% |
| MB Precinct            | 0  | -      |
| Provisional            | 8  | 30.77% |
| Total                  | 281                                      | 57.94% |
| 500003.10-CP 500003.10 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 52                                       | 48.15% |
| Precinct VBM           | 169                                      | 56.15% |
| MB Precinct            | 0  | -      |
| Provisional            | 2  | 40.00% |
| Total                  | 223                                      | 53.86% |
| 500004.10-CP 500004.10 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 56                                       | 51.38% |
| Precinct VBM           | 135                                      | 60.81% |
| MB Precinct            | 0  | -      |
| Provisional            | 7  | 38.89% |
| Total                  | 198                                      | 56.73% |
| 500005.10-CP 500005.10 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 39                                       | 44.83% |
| Precinct VBM           | 120                                      | 56.07% |

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|                        | Measure E - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 50.00%                                 |
| Total                  | 165 52.72%                               |
| 500006.10-CP 500006.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 84 54.55%                                |
| Precinct VBM           | 234 55.06%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 14 36.84%                                |
| Total                  | 332 53.81%                               |
| 500007.10-CP 500007.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 112 63.28%                               |
| Precinct VBM           | 386 64.12%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 46.67%                                 |
| Total                  | 505 63.60%                               |
| 500009.10-CP 500009.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 46 46.46%                                |
| Precinct VBM           | 131 51.78%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 32.00%                                 |
| Total                  | 185 49.07%                               |
| 500011.10-CP 500011.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 75 49.34%                                |
| Precinct VBM           | 143 45.54%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 15 50.00%                                |
| Total                  | 233 46.98%                               |
| 500012.10-CP 500012.10 |  |
| Precinct TSx           | 1 100.00%                                |
| Precinct OS            | 79 47.02%                                |
| Precinct VBM           | 294 54.75%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 12 44.44%                                |
| Total                  | 386 52.66%                               |
| 500015.10-CP 500015.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 66 68.04%                                |
| Precinct VBM           | 217 59.94%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 69.23%                                 |
| Total                  | 292 61.86%                               |
| 500020.10-CP 500020.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 70 45.75%                                |
| Precinct VBM           | 376 59.40%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 14 51.85%                                |
| Total                  | 460 56.58%                               |
| 500021.10-CP 500021.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 91 57.96%                                |
| Precinct VBM           | 358 60.78%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 11 44.00%                                |
| Total                  | 460 59.66%                               |
| 95U201.01-MB 95U201.01 |  |
| Precinct TSx           | 0 -                                      |



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|                        | NO                                       |         |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 0  | 0.00%   |
| Provisional            | 0  | -       |
| Total                  | 0  | 0.00%   |
| 95U201.02-MB 95U201.02 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 82                                       | 23.91%  |
| Provisional            | 1  | 100.00% |
| Total                  | 83                                       | 24.13%  |
| 95U202.02-MB 95U202.02 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 277                                      | 52.07%  |
| Provisional            | 1  | 25.00%  |
| Total                  | 278                                      | 51.87%  |
| 95U203.02-MB 95U203.02 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 0  | -       |
| Provisional            | 0  | -       |
| Total                  | 0  | -       |
| 95U204.01-MB 95U204.01 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 229                                      | 36.94%  |
| Provisional            | 0  | 0.00%   |
| Total                  | 229                                      | 36.82%  |
| 95U301.05-MB 95U301.05 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 85                                       | 61.59%  |
| Provisional            | 1  | 50.00%  |
| Total                  | 86                                       | 61.43%  |
| 95U302.04-MB 95U302.04 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 0  | 0.00%   |
| Provisional            | 0  | -       |
| Total                  | 0  | 0.00%   |
| 95U302.05-MB 95U302.05 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 45                                       | 54.22%  |
| Provisional            | 0  | -       |
| Total                  | 45                                       | 54.22%  |
| 95U303.03-MB 95U303.03 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 44                                       | 55.70%  |
| Provisional            | 0  | -       |
| Total                  | 44                                       | 55.70%  |

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|                        | Measure E - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| 95U303.05-MB 95U303.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 58 58.00%                                |
| Provisional            | 1 100.00%                                |
| Total                  | 59 58.42%                                |
| 95U304.05-MB 95U304.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 -                                      |
| Total                  | 0 -                                      |
| 95U305.05-MB 95U305.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 4 57.14%                                 |
| Provisional            | 0 -                                      |
| Total                  | 4 57.14%                                 |
| 95U401.08-MB 95U401.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 4 30.77%                                 |
| Provisional            | 0 -                                      |
| Total                  | 4 30.77%                                 |
| 95U402.07-MB 95U402.07 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 98 39.36%                                |
| Provisional            | 1 100.00%                                |
| Total                  | 99 39.60%                                |
| 95U402.08-MB 95U402.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 193 35.48%                               |
| Provisional            | 0 0.00%                                  |
| Total                  | 193 35.28%                               |
| 95U403.08-MB 95U403.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 502 50.15%                               |
| Provisional            | 6 54.55%                                 |
| Total                  | 508 50.20%                               |
| 95U404.06-MB 95U404.06 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 -                                      |
| Total                  | 0 -                                      |
| 95U404.08-MB 95U404.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 0 -                                      |

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|                               | Measure E - County<br>(majority to pass) |
|-------------------------------|--|
|                               | NO                                       |
| Provisional                   | 0 -                                      |
| Total                         | 0 -                                      |
| 95U501.09-MB 95U501.09        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 0 0.00%                                  |
| Provisional                   | 0 -                                      |
| Total                         | 0 0.00%                                  |
| 95U501.10-MB 95U501.10        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 90 55.90%                                |
| Provisional                   | 2 100.00%                                |
| Total                         | 92 56.44%                                |
| 95U598.10-MB 95U598.10        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 77 42.54%                                |
| Provisional                   | 1 100.00%                                |
| Total                         | 78 42.86%                                |
| 95U599.10-MB 95U599.10        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 20 64.52%                                |
| Provisional                   | 0 -                                      |
| Total                         | 20 64.52%                                |
| 96U101.11-MB 96U101.11        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 120 29.48%                               |
| Provisional                   | 1 16.67%                                 |
| Total                         | 121 29.30%                               |
| 96U401.14-MB 96U401.14        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 135 40.18%                               |
| Provisional                   | 4 66.67%                                 |
| Total                         | 139 40.64%                               |
| Total                         |  |
| Precinct TSx                  | 4 50.00%                                 |
| Precinct OS                   | 5287 47.48%                              |
| Precinct VBM                  | 20681 48.50%                             |
| MB Precinct                   | 2063 42.67%                              |
| Provisional                   | 548 39.97%                               |
| Total                         | 28583 47.65%                             |
| STATE BOARD OF EQUALIZATION   |  |
| STATE BOARD OF EQUALIZATION 1 |  |
| Precinct TSx                  | 4 50.00%                                 |
| Precinct OS                   | 5287 47.48%                              |
| Precinct VBM                  | 20681 48.50%                             |
| MB Precinct                   | 2063 42.67%                              |
| Provisional                   | 548 39.97%                               |
| Total                         | 28583 47.65%                             |
| Total                         |  |

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|-------------------------------------|--|---------------|
|                                     | NO                                       |               |
| Precinct TSx                        | 4  | 50.00%        |
| Precinct OS                         | 5287                                     | 47.48%        |
| Precinct VBM                        | 20681                                    | 48.50%        |
| MB Precinct                         | 2063                                     | 42.67%        |
| Provisional                         | 548                                      | 39.97%        |
| <b>Total</b>                        | <b>28583</b>                             | <b>47.65%</b> |
| <b>UNITED STATES SENATE</b>         |  |               |
| <b>UNITED STATES SENATE</b>         |  |               |
| Precinct TSx                        | 4  | 50.00%        |
| Precinct OS                         | 5287                                     | 47.48%        |
| Precinct VBM                        | 20681                                    | 48.50%        |
| MB Precinct                         | 2063                                     | 42.67%        |
| Provisional                         | 548                                      | 39.97%        |
| <b>Total</b>                        | <b>28583</b>                             | <b>47.65%</b> |
| <b>Total</b>                        |  |               |
| Precinct TSx                        | 4  | 50.00%        |
| Precinct OS                         | 5287                                     | 47.48%        |
| Precinct VBM                        | 20681                                    | 48.50%        |
| MB Precinct                         | 2063                                     | 42.67%        |
| Provisional                         | 548                                      | 39.97%        |
| <b>Total</b>                        | <b>28583</b>                             | <b>47.65%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |  |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |  |               |
| Precinct TSx                        | 4  | 50.00%        |
| Precinct OS                         | 5287                                     | 47.48%        |
| Precinct VBM                        | 20681                                    | 48.50%        |
| MB Precinct                         | 2063                                     | 42.67%        |
| Provisional                         | 548                                      | 39.97%        |
| <b>Total</b>                        | <b>28583</b>                             | <b>47.65%</b> |
| <b>Total</b>                        |  |               |
| Precinct TSx                        | 4  | 50.00%        |
| Precinct OS                         | 5287                                     | 47.48%        |
| Precinct VBM                        | 20681                                    | 48.50%        |
| MB Precinct                         | 2063                                     | 42.67%        |
| Provisional                         | 548                                      | 39.97%        |
| <b>Total</b>                        | <b>28583</b>                             | <b>47.65%</b> |
| <b>STATE SENATE</b>                 |  |               |
| <b>1ST STATE SENATE DISTRICT</b>    |  |               |
| Precinct TSx                        | 4  | 50.00%        |
| Precinct OS                         | 5287                                     | 47.48%        |
| Precinct VBM                        | 20681                                    | 48.50%        |
| MB Precinct                         | 2063                                     | 42.67%        |
| Provisional                         | 548                                      | 39.97%        |
| <b>Total</b>                        | <b>28583</b>                             | <b>47.65%</b> |
| <b>Total</b>                        |  |               |
| Precinct TSx                        | 4  | 50.00%        |
| Precinct OS                         | 5287                                     | 47.48%        |
| Precinct VBM                        | 20681                                    | 48.50%        |
| MB Precinct                         | 2063                                     | 42.67%        |
| Provisional                         | 548                                      | 39.97%        |
| <b>Total</b>                        | <b>28583</b>                             | <b>47.65%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |  |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |  |               |
| Precinct TSx                        | 2  | 50.00%        |
| Precinct OS                         | 3635                                     | 51.63%        |
| Precinct VBM                        | 13855                                    | 52.72%        |
| MB Precinct                         | 1808                                     | 44.18%        |

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|                                   | Measure E - County<br>(majority to pass) |         |
|-----------------------------------|--|---------|
|                                   | NO                                       |         |
| Provisional                       | 389                                      | 44.05%  |
| Total                             | 19689                                    | 51.41%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |  |         |
| Precinct TSx                      | 2  | 50.00%  |
| Precinct OS                       | 1652                                     | 40.34%  |
| Precinct VBM                      | 6826                                     | 41.72%  |
| MB Precinct                       | 255                                      | 34.32%  |
| Provisional                       | 159                                      | 32.58%  |
| Total                             | 8894                                     | 41.00%  |
| <b>Total</b>                      |  |         |
| Precinct TSx                      | 4  | 50.00%  |
| Precinct OS                       | 5287                                     | 47.48%  |
| Precinct VBM                      | 20681                                    | 48.50%  |
| MB Precinct                       | 2063                                     | 42.67%  |
| Provisional                       | 548                                      | 39.97%  |
| Total                             | 28583                                    | 47.65%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |  |         |
| <b>1ST SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 0  | -       |
| Precinct OS                       | 931                                      | 39.17%  |
| Precinct VBM                      | 3934                                     | 39.85%  |
| MB Precinct                       | 120                                      | 29.48%  |
| Provisional                       | 76                                       | 28.46%  |
| Total                             | 5061                                     | 39.17%  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 2  | 50.00%  |
| Precinct OS                       | 1003                                     | 42.41%  |
| Precinct VBM                      | 4133                                     | 45.34%  |
| MB Precinct                       | 588                                      | 39.15%  |
| Provisional                       | 111                                      | 38.01%  |
| Total                             | 5837                                     | 43.96%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 1  | 33.33%  |
| Precinct OS                       | 1182                                     | 52.74%  |
| Precinct VBM                      | 5117                                     | 54.07%  |
| MB Precinct                       | 236                                      | 57.84%  |
| Provisional                       | 106                                      | 43.09%  |
| Total                             | 6642                                     | 53.73%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 0  | -       |
| Precinct OS                       | 1243                                     | 51.62%  |
| Precinct VBM                      | 4443                                     | 50.16%  |
| MB Precinct                       | 932                                      | 43.49%  |
| Provisional                       | 132                                      | 45.99%  |
| Total                             | 6750                                     | 49.28%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 1  | 100.00% |
| Precinct OS                       | 928                                      | 53.21%  |
| Precinct VBM                      | 3054                                     | 57.27%  |
| MB Precinct                       | 187                                      | 49.87%  |
| Provisional                       | 123                                      | 44.09%  |
| Total                             | 4293                                     | 55.52%  |
| <b>Total</b>                      |  |         |
| Precinct TSx                      | 4  | 50.00%  |
| Precinct OS                       | 5287                                     | 47.48%  |
| Precinct VBM                      | 20681                                    | 48.50%  |
| MB Precinct                       | 2063                                     | 42.67%  |
| Provisional                       | 548                                      | 39.97%  |
| Total                             | 28583                                    | 47.65%  |

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|                                 | Measure E - County<br>(majority to pass) |
|---------------------------------|--|
|                                 | NO                                       |
| <b>CITIES</b>                   |  |
| <b>CITY OF PLACERVILLE</b>      |  |
| Precinct TSx                    | 0 -                                      |
| Precinct OS                     | 312 53.42%                               |
| Precinct VBM                    | 1335 55.46%                              |
| MB Precinct                     | 0 -                                      |
| Provisional                     | 26 38.24%                                |
| Total                           | 1673 54.69%                              |
| <b>CITY OF SOUTH LAKE TAHOE</b> |  |
| Precinct TSx                    | 0 -                                      |
| Precinct OS                     | 477 53.78%                               |
| Precinct VBM                    | 1357 57.02%                              |
| MB Precinct                     | 0 -                                      |
| Provisional                     | 65 39.63%                                |
| Total                           | 1899 55.35%                              |
| <b>UNINCORPORATED AREA</b>      |  |
| Precinct TSx                    | 4 50.00%                                 |
| Precinct OS                     | 4498 46.54%                              |
| Precinct VBM                    | 17989 47.52%                             |
| MB Precinct                     | 2063 42.67%                              |
| Provisional                     | 457 40.12%                               |
| Total                           | 25011 46.75%                             |
| <b>Total</b>                    |  |
| Precinct TSx                    | 4 50.00%                                 |
| Precinct OS                     | 5287 47.48%                              |
| Precinct VBM                    | 20681 48.50%                             |
| MB Precinct                     | 2063 42.67%                              |
| Provisional                     | 548 39.97%                               |
| Total                           | 28583 47.65%                             |

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|                        | Measure G - County (majority to pass) |          |               |             |     |        |
|------------------------|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Jurisdiction Wide      |                                       |          |               |             |     |        |
| 100001.11-CP 100001.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1615                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1615                                  | 1        | 190           | 180         | 101 | 56.11% |
| Precinct VBM           | 1615                                  | 1        | 781           | 742         | 413 | 55.66% |
| MB Precinct            | 1615                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1615                                  | 1        | 17            | 14          | 8   | 57.14% |
| Total                  | 1615                                  | 5        | 988           | 936         | 522 | 55.77% |
| 100002.11-CP 100002.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1000                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1000                                  | 1        | 89            | 84          | 49  | 58.33% |
| Precinct VBM           | 1000                                  | 1        | 476           | 450         | 215 | 47.78% |
| MB Precinct            | 1000                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1000                                  | 1        | 20            | 18          | 14  | 77.78% |
| Total                  | 1000                                  | 5        | 585           | 552         | 278 | 50.36% |
| 100003.11-CP 100003.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1269                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1269                                  | 1        | 123           | 113         | 55  | 48.67% |
| Precinct VBM           | 1269                                  | 1        | 773           | 725         | 407 | 56.14% |
| MB Precinct            | 1269                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1269                                  | 1        | 24            | 21          | 17  | 80.95% |
| Total                  | 1269                                  | 5        | 920           | 859         | 479 | 55.76% |
| 100004.11-CP 100004.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1633                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1633                                  | 1        | 254           | 229         | 134 | 58.52% |
| Precinct VBM           | 1633                                  | 1        | 738           | 695         | 459 | 66.04% |
| MB Precinct            | 1633                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1633                                  | 1        | 4             | 3           | 2   | 66.67% |
| Total                  | 1633                                  | 5        | 996           | 927         | 595 | 64.19% |
| 100005.11-CP 100005.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1586                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1586                                  | 1        | 146           | 139         | 76  | 54.68% |
| Precinct VBM           | 1586                                  | 1        | 895           | 844         | 407 | 48.22% |
| MB Precinct            | 1586                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1586                                  | 1        | 23            | 22          | 12  | 54.55% |
| Total                  | 1586                                  | 5        | 1064          | 1005        | 495 | 49.25% |
| 100006.11-CP 100006.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1928                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1928                                  | 1        | 252           | 234         | 141 | 60.26% |
| Precinct VBM           | 1928                                  | 1        | 944           | 884         | 596 | 67.42% |
| MB Precinct            | 1928                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1928                                  | 1        | 31            | 27          | 16  | 59.26% |
| Total                  | 1928                                  | 5        | 1227          | 1145        | 753 | 65.76% |
| 100008.11-CP 100008.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1671                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1671                                  | 1        | 197           | 184         | 119 | 64.67% |
| Precinct VBM           | 1671                                  | 1        | 740           | 700         | 433 | 61.86% |
| MB Precinct            | 1671                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1671                                  | 1        | 17            | 13          | 11  | 84.62% |
| Total                  | 1671                                  | 5        | 954           | 897         | 563 | 62.76% |
| 100009.11-CP 100009.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1838                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1838                                  | 1        | 169           | 156         | 97  | 62.18% |
| Precinct VBM           | 1838                                  | 1        | 751           | 692         | 364 | 52.60% |
| MB Precinct            | 1838                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1838                                  | 1        | 26            | 16          | 10  | 62.50% |
| Total                  | 1838                                  | 5        | 946           | 864         | 471 | 54.51% |
| 100012.11-CP 100012.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 2108                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 2108                                  | 1        | 255           | 242         | 144 | 59.50% |
| Precinct VBM           | 2108                                  | 1        | 968           | 905         | 539 | 59.56% |

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|                        |  | Measure G - County (majority to pass) |          |               |             |     |        |
|------------------------|--|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        |  | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| MB Precinct            |  | 2108                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            |  | 2108                                  | 1        | 29            | 25          | 13  | 52.00% |
| Total                  |  | 2108                                  | 5        | 1252          | 1172        | 696 | 59.39% |
| 100013.11-CP 100013.11 |  |                                       |          |               |             |     |        |
| Precinct TSx           |  | 2087                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            |  | 2087                                  | 1        | 250           | 236         | 122 | 51.69% |
| Precinct VBM           |  | 2087                                  | 1        | 908           | 864         | 497 | 57.52% |
| MB Precinct            |  | 2087                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            |  | 2087                                  | 1        | 28            | 26          | 16  | 61.54% |
| Total                  |  | 2087                                  | 5        | 1186          | 1126        | 635 | 56.39% |
| 100014.11-CP 100014.11 |  |                                       |          |               |             |     |        |
| Precinct TSx           |  | 1659                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            |  | 1659                                  | 1        | 211           | 199         | 117 | 58.79% |
| Precinct VBM           |  | 1659                                  | 1        | 706           | 677         | 379 | 55.98% |
| MB Precinct            |  | 1659                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            |  | 1659                                  | 1        | 30            | 27          | 16  | 59.26% |
| Total                  |  | 1659                                  | 5        | 947           | 903         | 512 | 56.70% |
| 100022.11-CP 100022.11 |  |                                       |          |               |             |     |        |
| Precinct TSx           |  | 1432                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            |  | 1432                                  | 1        | 177           | 164         | 111 | 67.68% |
| Precinct VBM           |  | 1432                                  | 1        | 678           | 638         | 370 | 57.99% |
| MB Precinct            |  | 1432                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            |  | 1432                                  | 1        | 13            | 10          | 9   | 90.00% |
| Total                  |  | 1432                                  | 5        | 868           | 812         | 490 | 60.34% |
| 100023.11-CP 100023.11 |  |                                       |          |               |             |     |        |
| Precinct TSx           |  | 2135                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            |  | 2135                                  | 1        | 217           | 203         | 118 | 58.13% |
| Precinct VBM           |  | 2135                                  | 1        | 1053          | 1008        | 539 | 53.47% |
| MB Precinct            |  | 2135                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            |  | 2135                                  | 1        | 33            | 28          | 18  | 64.29% |
| Total                  |  | 2135                                  | 5        | 1303          | 1239        | 675 | 54.48% |
| 200003.12-CP 200003.12 |  |                                       |          |               |             |     |        |
| Precinct TSx           |  | 1362                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            |  | 1362                                  | 1        | 166           | 150         | 85  | 56.67% |
| Precinct VBM           |  | 1362                                  | 1        | 558           | 514         | 231 | 44.94% |
| MB Precinct            |  | 1362                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            |  | 1362                                  | 1        | 31            | 27          | 13  | 48.15% |
| Total                  |  | 1362                                  | 5        | 755           | 691         | 329 | 47.61% |
| 200004.12-CP 200004.12 |  |                                       |          |               |             |     |        |
| Precinct TSx           |  | 1507                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            |  | 1507                                  | 1        | 182           | 170         | 90  | 52.94% |
| Precinct VBM           |  | 1507                                  | 1        | 662           | 619         | 320 | 51.70% |
| MB Precinct            |  | 1507                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            |  | 1507                                  | 1        | 14            | 13          | 6   | 46.15% |
| Total                  |  | 1507                                  | 5        | 858           | 802         | 416 | 51.87% |
| 200005.02-CP 200005.02 |  |                                       |          |               |             |     |        |
| Precinct TSx           |  | 867                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            |  | 867                                   | 1        | 94            | 90          | 29  | 32.22% |
| Precinct VBM           |  | 867                                   | 1        | 452           | 423         | 163 | 38.53% |
| MB Precinct            |  | 867                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            |  | 867                                   | 1        | 21            | 19          | 10  | 52.63% |
| Total                  |  | 867                                   | 5        | 567           | 532         | 202 | 37.97% |
| 200006.12-CP 200006.12 |  |                                       |          |               |             |     |        |
| Precinct TSx           |  | 1086                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            |  | 1086                                  | 1        | 117           | 113         | 63  | 55.75% |
| Precinct VBM           |  | 1086                                  | 1        | 532           | 509         | 251 | 49.31% |
| MB Precinct            |  | 1086                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            |  | 1086                                  | 1        | 29            | 28          | 14  | 50.00% |
| Total                  |  | 1086                                  | 5        | 678           | 650         | 328 | 50.46% |
| 200008.12-CP 200008.12 |  |                                       |          |               |             |     |        |
| Precinct TSx           |  | 999                                   | 1        | 0             | 0           | 0   | -      |



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|------------------------|---------------------------------------|----------|---------------|-------------|-----|---------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |         |
| Precinct OS            | 999                                   | 1        | 155           | 144         | 82  | 56.94%  |
| Precinct VBM           | 999                                   | 1        | 379           | 349         | 178 | 51.00%  |
| MB Precinct            | 999                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 999                                   | 1        | 10            | 10          | 9   | 90.00%  |
| Total                  | 999                                   | 5        | 544           | 503         | 269 | 53.48%  |
| 200010.12-CP 200010.12 |                                       |          |               |             |     |         |
| Precinct TSx           | 1976                                  | 1        | 4             | 4           | 2   | 50.00%  |
| Precinct OS            | 1976                                  | 1        | 197           | 173         | 90  | 52.02%  |
| Precinct VBM           | 1976                                  | 1        | 859           | 792         | 405 | 51.14%  |
| MB Precinct            | 1976                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1976                                  | 1        | 34            | 27          | 14  | 51.85%  |
| Total                  | 1976                                  | 5        | 1094          | 996         | 511 | 51.31%  |
| 200012.12-CP 200012.12 |                                       |          |               |             |     |         |
| Precinct TSx           | 1064                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1064                                  | 1        | 145           | 139         | 78  | 56.12%  |
| Precinct VBM           | 1064                                  | 1        | 464           | 437         | 250 | 57.21%  |
| MB Precinct            | 1064                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1064                                  | 1        | 14            | 12          | 4   | 33.33%  |
| Total                  | 1064                                  | 5        | 623           | 588         | 332 | 56.46%  |
| 200013.12-CP 200013.12 |                                       |          |               |             |     |         |
| Precinct TSx           | 1574                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1574                                  | 1        | 211           | 198         | 101 | 51.01%  |
| Precinct VBM           | 1574                                  | 1        | 701           | 665         | 311 | 46.77%  |
| MB Precinct            | 1574                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1574                                  | 1        | 23            | 21          | 15  | 71.43%  |
| Total                  | 1574                                  | 5        | 935           | 884         | 427 | 48.30%  |
| 200014.12-CP 200014.12 |                                       |          |               |             |     |         |
| Precinct TSx           | 1438                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1438                                  | 1        | 201           | 192         | 119 | 61.98%  |
| Precinct VBM           | 1438                                  | 1        | 613           | 566         | 318 | 56.18%  |
| MB Precinct            | 1438                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1438                                  | 1        | 30            | 26          | 14  | 53.85%  |
| Total                  | 1438                                  | 5        | 844           | 784         | 451 | 57.53%  |
| 200015.02-CP 200015.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 1832                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1832                                  | 1        | 218           | 205         | 94  | 45.85%  |
| Precinct VBM           | 1832                                  | 1        | 886           | 840         | 373 | 44.40%  |
| MB Precinct            | 1832                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1832                                  | 1        | 22            | 22          | 14  | 63.64%  |
| Total                  | 1832                                  | 5        | 1126          | 1067        | 481 | 45.08%  |
| 200016.12-CP 200016.12 |                                       |          |               |             |     |         |
| Precinct TSx           | 301                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 301                                   | 1        | 26            | 24          | 16  | 66.67%  |
| Precinct VBM           | 301                                   | 1        | 139           | 133         | 90  | 67.67%  |
| MB Precinct            | 301                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 301                                   | 1        | 6             | 5           | 4   | 80.00%  |
| Total                  | 301                                   | 5        | 171           | 162         | 110 | 67.90%  |
| 200016.13-CP 200016.13 |                                       |          |               |             |     |         |
| Precinct TSx           | 565                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 565                                   | 1        | 80            | 75          | 52  | 69.33%  |
| Precinct VBM           | 565                                   | 1        | 338           | 329         | 236 | 71.73%  |
| MB Precinct            | 565                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 565                                   | 1        | 6             | 5           | 5   | 100.00% |
| Total                  | 565                                   | 5        | 424           | 409         | 293 | 71.64%  |
| 200017.02-CP 200017.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 1286                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1286                                  | 1        | 146           | 136         | 70  | 51.47%  |
| Precinct VBM           | 1286                                  | 1        | 594           | 548         | 239 | 43.61%  |
| MB Precinct            | 1286                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1286                                  | 1        | 12            | 11          | 8   | 72.73%  |
| Total                  | 1286                                  | 5        | 752           | 695         | 317 | 45.61%  |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| 200018.02-CP 200018.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1454                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1454                                  | 1        | 169           | 159         | 66  | 41.51% |
| Precinct VBM           | 1454                                  | 1        | 704           | 659         | 280 | 42.49% |
| MB Precinct            | 1454                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1454                                  | 1        | 13            | 11          | 6   | 54.55% |
| Total                  | 1454                                  | 5        | 886           | 829         | 352 | 42.46% |
| 200020.02-CP 200020.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1213                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1213                                  | 1        | 139           | 132         | 83  | 62.88% |
| Precinct VBM           | 1213                                  | 1        | 526           | 512         | 357 | 69.73% |
| MB Precinct            | 1213                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1213                                  | 1        | 19            | 17          | 9   | 52.94% |
| Total                  | 1213                                  | 5        | 684           | 661         | 449 | 67.93% |
| 200021.01-CP 200021.01 |                                       |          |               |             |     |        |
| Precinct TSx           | 436                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 436                                   | 1        | 38            | 37          | 25  | 67.57% |
| Precinct VBM           | 436                                   | 1        | 206           | 198         | 111 | 56.06% |
| MB Precinct            | 436                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 436                                   | 1        | 7             | 5           | 1   | 20.00% |
| Total                  | 436                                   | 5        | 251           | 240         | 137 | 57.08% |
| 200021.02-CP 200021.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1296                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1296                                  | 1        | 170           | 168         | 121 | 72.02% |
| Precinct VBM           | 1296                                  | 1        | 752           | 725         | 523 | 72.14% |
| MB Precinct            | 1296                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1296                                  | 1        | 20            | 16          | 14  | 87.50% |
| Total                  | 1296                                  | 5        | 942           | 909         | 658 | 72.39% |
| 200024.02-CP 200024.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 555                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 555                                   | 1        | 55            | 54          | 20  | 37.04% |
| Precinct VBM           | 555                                   | 1        | 282           | 269         | 86  | 31.97% |
| MB Precinct            | 555                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 555                                   | 1        | 9             | 8           | 2   | 25.00% |
| Total                  | 555                                   | 5        | 346           | 331         | 108 | 32.63% |
| 300001.04-CP 300001.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 259                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 259                                   | 1        | 12            | 10          | 0   | 0.00%  |
| Precinct VBM           | 259                                   | 1        | 100           | 91          | 34  | 37.36% |
| MB Precinct            | 259                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 259                                   | 1        | 0             | 0           | 0   | -      |
| Total                  | 259                                   | 5        | 112           | 101         | 34  | 33.66% |
| 300001.05-CP 300001.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 731                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 731                                   | 1        | 76            | 68          | 30  | 44.12% |
| Precinct VBM           | 731                                   | 1        | 330           | 303         | 117 | 38.61% |
| MB Precinct            | 731                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 731                                   | 1        | 10            | 10          | 6   | 60.00% |
| Total                  | 731                                   | 5        | 416           | 381         | 153 | 40.16% |
| 300003.05-CP 300003.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1932                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1932                                  | 1        | 205           | 194         | 90  | 46.39% |
| Precinct VBM           | 1932                                  | 1        | 924           | 861         | 372 | 43.21% |
| MB Precinct            | 1932                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1932                                  | 1        | 27            | 22          | 12  | 54.55% |
| Total                  | 1932                                  | 5        | 1156          | 1077        | 474 | 44.01% |
| 300004.05-CP 300004.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1273                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1273                                  | 1        | 175           | 166         | 67  | 40.36% |
| Precinct VBM           | 1273                                  | 1        | 576           | 521         | 210 | 40.31% |
| MB Precinct            | 1273                                  | 1        | 0             | 0           | 0   | -      |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |         |
| Provisional            | 1273                                  | 1        | 17            | 15          | 10  | 66.67%  |
| Total                  | 1273                                  | 5        | 768           | 702         | 287 | 40.88%  |
| 300005.05-CP 300005.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 1374                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1374                                  | 1        | 163           | 150         | 75  | 50.00%  |
| Precinct VBM           | 1374                                  | 1        | 660           | 612         | 248 | 40.52%  |
| MB Precinct            | 1374                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1374                                  | 1        | 26            | 21          | 10  | 47.62%  |
| Total                  | 1374                                  | 5        | 849           | 783         | 333 | 42.53%  |
| 300008.05-CP 300008.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 1366                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1366                                  | 1        | 139           | 136         | 60  | 44.12%  |
| Precinct VBM           | 1366                                  | 1        | 740           | 708         | 270 | 38.14%  |
| MB Precinct            | 1366                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1366                                  | 1        | 14            | 13          | 9   | 69.23%  |
| Total                  | 1366                                  | 5        | 893           | 857         | 339 | 39.56%  |
| 300009.04-CP 300009.04 |                                       |          |               |             |     |         |
| Precinct TSx           | 873                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 873                                   | 1        | 121           | 116         | 60  | 51.72%  |
| Precinct VBM           | 873                                   | 1        | 395           | 380         | 222 | 58.42%  |
| MB Precinct            | 873                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 873                                   | 1        | 13            | 12          | 7   | 58.33%  |
| Total                  | 873                                   | 5        | 529           | 508         | 289 | 56.89%  |
| 300010.04-CP 300010.04 |                                       |          |               |             |     |         |
| Precinct TSx           | 1340                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1340                                  | 1        | 171           | 168         | 70  | 41.67%  |
| Precinct VBM           | 1340                                  | 1        | 656           | 612         | 266 | 43.46%  |
| MB Precinct            | 1340                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1340                                  | 1        | 11            | 10          | 4   | 40.00%  |
| Total                  | 1340                                  | 5        | 838           | 790         | 340 | 43.04%  |
| 300011.04-CP 300011.04 |                                       |          |               |             |     |         |
| Precinct TSx           | 1491                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1491                                  | 1        | 142           | 139         | 70  | 50.36%  |
| Precinct VBM           | 1491                                  | 1        | 711           | 677         | 381 | 56.28%  |
| MB Precinct            | 1491                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1491                                  | 1        | 22            | 18          | 14  | 77.78%  |
| Total                  | 1491                                  | 5        | 875           | 834         | 465 | 55.76%  |
| 300013.05-CP 300013.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 1490                                  | 1        | 3             | 3           | 2   | 66.67%  |
| Precinct OS            | 1490                                  | 1        | 128           | 122         | 61  | 50.00%  |
| Precinct VBM           | 1490                                  | 1        | 721           | 696         | 329 | 47.27%  |
| MB Precinct            | 1490                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1490                                  | 1        | 15            | 15          | 9   | 60.00%  |
| Total                  | 1490                                  | 5        | 867           | 836         | 401 | 47.97%  |
| 300015.04-CP 300015.04 |                                       |          |               |             |     |         |
| Precinct TSx           | 119                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 119                                   | 1        | 10            | 10          | 6   | 60.00%  |
| Precinct VBM           | 119                                   | 1        | 63            | 55          | 30  | 54.55%  |
| MB Precinct            | 119                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 119                                   | 1        | 4             | 3           | 3   | 100.00% |
| Total                  | 119                                   | 5        | 77            | 68          | 39  | 57.35%  |
| 300015.05-CP 300015.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 503                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 503                                   | 1        | 66            | 63          | 24  | 38.10%  |
| Precinct VBM           | 503                                   | 1        | 266           | 248         | 102 | 41.13%  |
| MB Precinct            | 503                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 503                                   | 1        | 2             | 2           | 1   | 50.00%  |
| Total                  | 503                                   | 5        | 334           | 313         | 127 | 40.58%  |
| 300017.05-CP 300017.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 1240                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1240                                  | 1        | 117           | 108         | 46  | 42.59%  |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Precinct VBM           | 1240                                  | 1        | 655           | 625         | 252 | 40.32% |
| MB Precinct            | 1240                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1240                                  | 1        | 9             | 8           | 2   | 25.00% |
| Total                  | 1240                                  | 5        | 781           | 741         | 300 | 40.49% |
| 300018.04-CP 300018.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 1306                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1306                                  | 1        | 148           | 135         | 62  | 45.93% |
| Precinct VBM           | 1306                                  | 1        | 505           | 480         | 227 | 47.29% |
| MB Precinct            | 1306                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1306                                  | 1        | 31            | 27          | 10  | 37.04% |
| Total                  | 1306                                  | 5        | 684           | 642         | 299 | 46.57% |
| 300020.04-CP 300020.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 1401                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1401                                  | 1        | 168           | 164         | 80  | 48.78% |
| Precinct VBM           | 1401                                  | 1        | 632           | 591         | 269 | 45.52% |
| MB Precinct            | 1401                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1401                                  | 1        | 14            | 12          | 8   | 66.67% |
| Total                  | 1401                                  | 5        | 814           | 767         | 357 | 46.54% |
| 300021.03-CP 300021.03 |                                       |          |               |             |     |        |
| Precinct TSx           | 1234                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1234                                  | 1        | 153           | 139         | 55  | 39.57% |
| Precinct VBM           | 1234                                  | 1        | 614           | 580         | 230 | 39.66% |
| MB Precinct            | 1234                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1234                                  | 1        | 13            | 13          | 8   | 61.54% |
| Total                  | 1234                                  | 5        | 780           | 732         | 293 | 40.03% |
| 300022.05-CP 300022.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 872                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 872                                   | 1        | 101           | 95          | 47  | 49.47% |
| Precinct VBM           | 872                                   | 1        | 356           | 328         | 145 | 44.21% |
| MB Precinct            | 872                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 872                                   | 1        | 12            | 11          | 7   | 63.64% |
| Total                  | 872                                   | 5        | 469           | 434         | 199 | 45.85% |
| 300023.03-CP 300023.03 |                                       |          |               |             |     |        |
| Precinct TSx           | 1819                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1819                                  | 1        | 228           | 221         | 97  | 43.89% |
| Precinct VBM           | 1819                                  | 1        | 924           | 877         | 314 | 35.80% |
| MB Precinct            | 1819                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1819                                  | 1        | 26            | 24          | 9   | 37.50% |
| Total                  | 1819                                  | 5        | 1178          | 1122        | 420 | 37.43% |
| 300023.05-CP 300023.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 386                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 386                                   | 1        | 45            | 40          | 14  | 35.00% |
| Precinct VBM           | 386                                   | 1        | 193           | 187         | 62  | 33.16% |
| MB Precinct            | 386                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 386                                   | 1        | 8             | 7           | 4   | 57.14% |
| Total                  | 386                                   | 5        | 246           | 234         | 80  | 34.19% |
| 400001.14-CP 400001.14 |                                       |          |               |             |     |        |
| Precinct TSx           | 1957                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1957                                  | 1        | 178           | 161         | 83  | 51.55% |
| Precinct VBM           | 1957                                  | 1        | 992           | 928         | 450 | 48.49% |
| MB Precinct            | 1957                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1957                                  | 1        | 23            | 22          | 11  | 50.00% |
| Total                  | 1957                                  | 5        | 1193          | 1111        | 544 | 48.96% |
| 400002.08-CP 400002.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1508                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1508                                  | 1        | 200           | 186         | 98  | 52.69% |
| Precinct VBM           | 1508                                  | 1        | 730           | 690         | 386 | 55.94% |
| MB Precinct            | 1508                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1508                                  | 1        | 15            | 13          | 8   | 61.54% |
| Total                  | 1508                                  | 5        | 945           | 889         | 492 | 55.34% |
| 400003.08-CP 400003.08 |                                       |          |               |             |     |        |

**COUNTY OF EL DORADO**  
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|------------------------|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Precinct TSx           | 1800                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1800                                  | 1        | 245           | 227         | 76  | 33.48% |
| Precinct VBM           | 1800                                  | 1        | 773           | 709         | 278 | 39.21% |
| MB Precinct            | 1800                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1800                                  | 1        | 35            | 30          | 11  | 36.67% |
| Total                  | 1800                                  | 5        | 1053          | 966         | 365 | 37.78% |
| 400004.14-CP 400004.14 |                                       |          |               |             |     |        |
| Precinct TSx           | 1536                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1536                                  | 1        | 194           | 175         | 86  | 49.14% |
| Precinct VBM           | 1536                                  | 1        | 662           | 624         | 329 | 52.72% |
| MB Precinct            | 1536                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1536                                  | 1        | 19            | 18          | 14  | 77.78% |
| Total                  | 1536                                  | 5        | 875           | 817         | 429 | 52.51% |
| 400005.08-CP 400005.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 655                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 655                                   | 1        | 79            | 70          | 20  | 28.57% |
| Precinct VBM           | 655                                   | 1        | 262           | 238         | 87  | 36.55% |
| MB Precinct            | 655                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 655                                   | 1        | 11            | 11          | 4   | 36.36% |
| Total                  | 655                                   | 5        | 352           | 319         | 111 | 34.80% |
| 400006.08-CP 400006.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1537                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1537                                  | 1        | 228           | 206         | 65  | 31.55% |
| Precinct VBM           | 1537                                  | 1        | 722           | 652         | 228 | 34.97% |
| MB Precinct            | 1537                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1537                                  | 1        | 19            | 16          | 4   | 25.00% |
| Total                  | 1537                                  | 5        | 969           | 874         | 297 | 33.98% |
| 400007.08-CP 400007.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1766                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1766                                  | 1        | 228           | 216         | 92  | 42.59% |
| Precinct VBM           | 1766                                  | 1        | 765           | 720         | 262 | 36.39% |
| MB Precinct            | 1766                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1766                                  | 1        | 22            | 20          | 8   | 40.00% |
| Total                  | 1766                                  | 5        | 1015          | 956         | 362 | 37.87% |
| 400010.08-CP 400010.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1875                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1875                                  | 1        | 244           | 233         | 94  | 40.34% |
| Precinct VBM           | 1875                                  | 1        | 869           | 809         | 362 | 44.75% |
| MB Precinct            | 1875                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1875                                  | 1        | 31            | 27          | 19  | 70.37% |
| Total                  | 1875                                  | 5        | 1144          | 1069        | 475 | 44.43% |
| 400012.08-CP 400012.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 877                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 877                                   | 1        | 113           | 110         | 64  | 58.18% |
| Precinct VBM           | 877                                   | 1        | 434           | 413         | 165 | 39.95% |
| MB Precinct            | 877                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 877                                   | 1        | 22            | 21          | 6   | 28.57% |
| Total                  | 877                                   | 5        | 569           | 544         | 235 | 43.20% |
| 400013.08-CP 400013.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 758                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 758                                   | 1        | 127           | 116         | 40  | 34.48% |
| Precinct VBM           | 758                                   | 1        | 315           | 303         | 123 | 40.59% |
| MB Precinct            | 758                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 758                                   | 1        | 4             | 4           | 3   | 75.00% |
| Total                  | 758                                   | 5        | 446           | 423         | 166 | 39.24% |
| 400014.08-CP 400014.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1526                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1526                                  | 1        | 230           | 221         | 114 | 51.58% |
| Precinct VBM           | 1526                                  | 1        | 736           | 703         | 357 | 50.78% |
| MB Precinct            | 1526                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1526                                  | 1        | 36            | 36          | 21  | 58.33% |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Total                  | 1526                                  | 5        | 1002          | 960         | 492 | 51.25% |
| 400017.08-CP 400017.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1495                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1495                                  | 1        | 175           | 170         | 121 | 71.18% |
| Precinct VBM           | 1495                                  | 1        | 752           | 725         | 467 | 64.41% |
| MB Precinct            | 1495                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1495                                  | 1        | 28            | 25          | 17  | 68.00% |
| Total                  | 1495                                  | 5        | 955           | 920         | 605 | 65.76% |
| 400018.08-CP 400018.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 597                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 597                                   | 1        | 86            | 80          | 48  | 60.00% |
| Precinct VBM           | 597                                   | 1        | 276           | 266         | 143 | 53.76% |
| MB Precinct            | 597                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 597                                   | 1        | 7             | 6           | 3   | 50.00% |
| Total                  | 597                                   | 5        | 369           | 352         | 194 | 55.11% |
| 400020.08-CP 400020.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 724                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 724                                   | 1        | 114           | 109         | 51  | 46.79% |
| Precinct VBM           | 724                                   | 1        | 343           | 318         | 138 | 43.40% |
| MB Precinct            | 724                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 724                                   | 1        | 2             | 2           | 0   | 0.00%  |
| Total                  | 724                                   | 5        | 459           | 429         | 189 | 44.06% |
| 400021.07-CP 400021.07 |                                       |          |               |             |     |        |
| Precinct TSx           | 1332                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1332                                  | 1        | 127           | 121         | 54  | 44.63% |
| Precinct VBM           | 1332                                  | 1        | 767           | 733         | 330 | 45.02% |
| MB Precinct            | 1332                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1332                                  | 1        | 18            | 15          | 6   | 40.00% |
| Total                  | 1332                                  | 5        | 912           | 869         | 390 | 44.88% |
| 500001.10-CP 500001.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1717                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1717                                  | 1        | 210           | 171         | 74  | 43.27% |
| Precinct VBM           | 1717                                  | 1        | 617           | 526         | 209 | 39.73% |
| MB Precinct            | 1717                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1717                                  | 1        | 19            | 16          | 10  | 62.50% |
| Total                  | 1717                                  | 5        | 846           | 713         | 293 | 41.09% |
| 500002.10-CP 500002.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1381                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1381                                  | 1        | 137           | 109         | 41  | 37.61% |
| Precinct VBM           | 1381                                  | 1        | 412           | 356         | 148 | 41.57% |
| MB Precinct            | 1381                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1381                                  | 1        | 31            | 25          | 13  | 52.00% |
| Total                  | 1381                                  | 5        | 580           | 490         | 202 | 41.22% |
| 500003.10-CP 500003.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1065                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1065                                  | 1        | 131           | 106         | 49  | 46.23% |
| Precinct VBM           | 1065                                  | 1        | 346           | 290         | 122 | 42.07% |
| MB Precinct            | 1065                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1065                                  | 1        | 11            | 4           | 3   | 75.00% |
| Total                  | 1065                                  | 5        | 488           | 400         | 174 | 43.50% |
| 500004.10-CP 500004.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 981                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 981                                   | 1        | 122           | 109         | 49  | 44.95% |
| Precinct VBM           | 981                                   | 1        | 264           | 222         | 76  | 34.23% |
| MB Precinct            | 981                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 981                                   | 1        | 21            | 17          | 10  | 58.82% |
| Total                  | 981                                   | 5        | 407           | 348         | 135 | 38.79% |
| 500005.10-CP 500005.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 946                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 946                                   | 1        | 107           | 87          | 44  | 50.57% |
| Precinct VBM           | 946                                   | 1        | 250           | 214         | 84  | 39.25% |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| MB Precinct            | 946                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 946                                   | 1        | 16            | 12          | 5   | 41.67% |
| Total                  | 946                                   | 5        | 373           | 313         | 133 | 42.49% |
| 500006.10-CP 500006.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1661                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1661                                  | 1        | 190           | 146         | 57  | 39.04% |
| Precinct VBM           | 1661                                  | 1        | 498           | 417         | 174 | 41.73% |
| MB Precinct            | 1661                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1661                                  | 1        | 58            | 39          | 20  | 51.28% |
| Total                  | 1661                                  | 5        | 746           | 602         | 251 | 41.69% |
| 500007.10-CP 500007.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1788                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1788                                  | 1        | 208           | 178         | 70  | 39.33% |
| Precinct VBM           | 1788                                  | 1        | 688           | 594         | 194 | 32.66% |
| MB Precinct            | 1788                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1788                                  | 1        | 20            | 14          | 5   | 35.71% |
| Total                  | 1788                                  | 5        | 916           | 786         | 269 | 34.22% |
| 500009.10-CP 500009.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1073                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1073                                  | 1        | 121           | 103         | 41  | 39.81% |
| Precinct VBM           | 1073                                  | 1        | 304           | 245         | 111 | 45.31% |
| MB Precinct            | 1073                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1073                                  | 1        | 38            | 25          | 13  | 52.00% |
| Total                  | 1073                                  | 5        | 463           | 373         | 165 | 44.24% |
| 500011.10-CP 500011.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1635                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1635                                  | 1        | 178           | 157         | 70  | 44.59% |
| Precinct VBM           | 1635                                  | 1        | 380           | 310         | 136 | 43.87% |
| MB Precinct            | 1635                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1635                                  | 1        | 40            | 30          | 17  | 56.67% |
| Total                  | 1635                                  | 5        | 598           | 497         | 223 | 44.87% |
| 500012.10-CP 500012.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1574                                  | 1        | 1             | 1           | 0   | 0.00%  |
| Precinct OS            | 1574                                  | 1        | 195           | 163         | 73  | 44.79% |
| Precinct VBM           | 1574                                  | 1        | 601           | 535         | 235 | 43.93% |
| MB Precinct            | 1574                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1574                                  | 1        | 37            | 27          | 13  | 48.15% |
| Total                  | 1574                                  | 5        | 834           | 726         | 321 | 44.21% |
| 500015.10-CP 500015.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 978                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 978                                   | 1        | 105           | 94          | 35  | 37.23% |
| Precinct VBM           | 978                                   | 1        | 407           | 357         | 140 | 39.22% |
| MB Precinct            | 978                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 978                                   | 1        | 13            | 12          | 6   | 50.00% |
| Total                  | 978                                   | 5        | 525           | 463         | 181 | 39.09% |
| 500020.10-CP 500020.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1530                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1530                                  | 1        | 161           | 152         | 80  | 52.63% |
| Precinct VBM           | 1530                                  | 1        | 659           | 629         | 224 | 35.61% |
| MB Precinct            | 1530                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1530                                  | 1        | 27            | 27          | 14  | 51.85% |
| Total                  | 1530                                  | 5        | 847           | 808         | 318 | 39.36% |
| 500021.10-CP 500021.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1404                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1404                                  | 1        | 169           | 153         | 64  | 41.83% |
| Precinct VBM           | 1404                                  | 1        | 632           | 588         | 207 | 35.20% |
| MB Precinct            | 1404                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1404                                  | 1        | 28            | 26          | 13  | 50.00% |
| Total                  | 1404                                  | 5        | 829           | 767         | 284 | 37.03% |
| 95U201.01-MB 95U201.01 |                                       |          |               |             |     |        |
| Precinct TSx           | 8                                     | 1        | 0             | 0           | 0   | -      |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |         |
| Precinct OS            | 8                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 8                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 8                                     | 1        | 7             | 7           | 7   | 100.00% |
| Provisional            | 8                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 8                                     | 5        | 7             | 7           | 7   | 100.00% |
| 95U201.02-MB 95U201.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 541                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 541                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 541                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 541                                   | 1        | 358           | 342         | 250 | 73.10%  |
| Provisional            | 541                                   | 1        | 1             | 1           | 0   | 0.00%   |
| Total                  | 541                                   | 5        | 359           | 343         | 250 | 72.89%  |
| 95U202.02-MB 95U202.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 859                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 859                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 859                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 859                                   | 1        | 574           | 535         | 242 | 45.23%  |
| Provisional            | 859                                   | 1        | 4             | 4           | 2   | 50.00%  |
| Total                  | 859                                   | 5        | 578           | 539         | 244 | 45.27%  |
| 95U203.02-MB 95U203.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                                     | 5        | 0             | 0           | 0   | -       |
| 95U204.01-MB 95U204.01 |                                       |          |               |             |     |         |
| Precinct TSx           | 895                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 895                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 895                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 895                                   | 1        | 651           | 621         | 372 | 59.90%  |
| Provisional            | 895                                   | 1        | 2             | 1           | 1   | 100.00% |
| Total                  | 895                                   | 5        | 653           | 622         | 373 | 59.97%  |
| 95U301.05-MB 95U301.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 244                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 244                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 244                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 244                                   | 1        | 151           | 138         | 52  | 37.68%  |
| Provisional            | 244                                   | 1        | 2             | 2           | 0   | 0.00%   |
| Total                  | 244                                   | 5        | 153           | 140         | 52  | 37.14%  |
| 95U302.04-MB 95U302.04 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 1             | 1           | 1   | 100.00% |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                                     | 5        | 1             | 1           | 1   | 100.00% |
| 95U302.05-MB 95U302.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 128                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 128                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 128                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 128                                   | 1        | 86            | 83          | 33  | 39.76%  |
| Provisional            | 128                                   | 1        | 0             | 0           | 0   | -       |
| Total                  | 128                                   | 5        | 86            | 83          | 33  | 39.76%  |
| 95U303.03-MB 95U303.03 |                                       |          |               |             |     |         |
| Precinct TSx           | 144                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 144                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 144                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 144                                   | 1        | 84            | 81          | 34  | 41.98%  |
| Provisional            | 144                                   | 1        | 0             | 0           | 0   | -       |
| Total                  | 144                                   | 5        | 84            | 81          | 34  | 41.98%  |



**COUNTY OF EL DORADO**  
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|                        | Measure G - County (majority to pass) |          |               |             |     |         |
|------------------------|---------------------------------------|----------|---------------|-------------|-----|---------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |         |
| 95U303.05-MB 95U303.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 159                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 159                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 159                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 159                                   | 1        | 111           | 97          | 38  | 39.18%  |
| Provisional            | 159                                   | 1        | 1             | 1           | 0   | 0.00%   |
| Total                  | 159                                   | 5        | 112           | 98          | 38  | 38.78%  |
| 95U304.05-MB 95U304.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                                     | 5        | 0             | 0           | 0   | -       |
| 95U305.05-MB 95U305.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 7                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 7                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 7                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 7                                     | 1        | 7             | 7           | 4   | 57.14%  |
| Provisional            | 7                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 7                                     | 5        | 7             | 7           | 4   | 57.14%  |
| 95U401.08-MB 95U401.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 39                                    | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 39                                    | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 39                                    | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 39                                    | 1        | 16            | 13          | 9   | 69.23%  |
| Provisional            | 39                                    | 1        | 0             | 0           | 0   | -       |
| Total                  | 39                                    | 5        | 16            | 13          | 9   | 69.23%  |
| 95U402.07-MB 95U402.07 |                                       |          |               |             |     |         |
| Precinct TSx           | 413                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 413                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 413                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 413                                   | 1        | 256           | 248         | 140 | 56.45%  |
| Provisional            | 413                                   | 1        | 1             | 1           | 0   | 0.00%   |
| Total                  | 413                                   | 5        | 257           | 249         | 140 | 56.22%  |
| 95U402.08-MB 95U402.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 873                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 873                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 873                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 873                                   | 1        | 566           | 543         | 332 | 61.14%  |
| Provisional            | 873                                   | 1        | 3             | 3           | 3   | 100.00% |
| Total                  | 873                                   | 5        | 569           | 546         | 335 | 61.36%  |
| 95U403.08-MB 95U403.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 1591                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1591                                  | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 1591                                  | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 1591                                  | 1        | 1042          | 997         | 481 | 48.24%  |
| Provisional            | 1591                                  | 1        | 13            | 11          | 5   | 45.45%  |
| Total                  | 1591                                  | 5        | 1055          | 1008        | 486 | 48.21%  |
| 95U404.06-MB 95U404.06 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                                     | 5        | 0             | 0           | 0   | -       |
| 95U404.08-MB 95U404.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -       |

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|                               | Measure G - County (majority to pass) |          |               |             |       |         |
|-------------------------------|---------------------------------------|----------|---------------|-------------|-------|---------|
|                               | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES   |         |
| Provisional                   | 0                                     | 1        | 0             | 0           | 0     | -       |
| Total                         | 0                                     | 5        | 0             | 0           | 0     | -       |
| 95U501.09-MB 95U501.09        |                                       |          |               |             |       |         |
| Precinct TSx                  | 0                                     | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 0                                     | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 0                                     | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 0                                     | 1        | 2             | 2           | 2     | 100.00% |
| Provisional                   | 0                                     | 1        | 0             | 0           | 0     | -       |
| Total                         | 0                                     | 5        | 2             | 2           | 2     | 100.00% |
| 95U501.10-MB 95U501.10        |                                       |          |               |             |       |         |
| Precinct TSx                  | 286                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 286                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 286                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 286                                   | 1        | 172           | 157         | 63    | 40.13%  |
| Provisional                   | 286                                   | 1        | 2             | 2           | 0     | 0.00%   |
| Total                         | 286                                   | 5        | 174           | 159         | 63    | 39.62%  |
| 95U598.10-MB 95U598.10        |                                       |          |               |             |       |         |
| Precinct TSx                  | 568                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 568                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 568                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 568                                   | 1        | 239           | 176         | 79    | 44.89%  |
| Provisional                   | 568                                   | 1        | 3             | 1           | 0     | 0.00%   |
| Total                         | 568                                   | 5        | 242           | 177         | 79    | 44.63%  |
| 95U599.10-MB 95U599.10        |                                       |          |               |             |       |         |
| Precinct TSx                  | 75                                    | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 75                                    | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 75                                    | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 75                                    | 1        | 38            | 33          | 10    | 30.30%  |
| Provisional                   | 75                                    | 1        | 0             | 0           | 0     | -       |
| Total                         | 75                                    | 5        | 38            | 33          | 10    | 30.30%  |
| 96U101.11-MB 96U101.11        |                                       |          |               |             |       |         |
| Precinct TSx                  | 626                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 626                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 626                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 626                                   | 1        | 423           | 402         | 275   | 68.41%  |
| Provisional                   | 626                                   | 1        | 6             | 6           | 4     | 66.67%  |
| Total                         | 626                                   | 5        | 429           | 408         | 279   | 68.38%  |
| 96U401.14-MB 96U401.14        |                                       |          |               |             |       |         |
| Precinct TSx                  | 566                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 566                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 566                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 566                                   | 1        | 359           | 333         | 190   | 57.06%  |
| Provisional                   | 566                                   | 1        | 6             | 6           | 2     | 33.33%  |
| Total                         | 566                                   | 5        | 365           | 339         | 192   | 56.64%  |
| Total                         |                                       |          |               |             |       |         |
| Precinct TSx                  | 109479                                | 102      | 8             | 8           | 4     | 50.00%  |
| Precinct OS                   | 109479                                | 102      | 12009         | 11095       | 5535  | 49.89%  |
| Precinct VBM                  | 109479                                | 102      | 45535         | 42457       | 20585 | 48.48%  |
| MB Precinct                   | 109479                                | 102      | 5143          | 4816        | 2614  | 54.28%  |
| Provisional                   | 109479                                | 102      | 1584          | 1355        | 751   | 55.42%  |
| Total                         | 109479                                | 510      | 64279         | 59731       | 29489 | 49.37%  |
| STATE BOARD OF EQUALIZATION   |                                       |          |               |             |       |         |
| STATE BOARD OF EQUALIZATION 1 |                                       |          |               |             |       |         |
| Precinct TSx                  | 109479                                | 102      | 8             | 8           | 4     | 50.00%  |
| Precinct OS                   | 109479                                | 102      | 12009         | 11095       | 5535  | 49.89%  |
| Precinct VBM                  | 109479                                | 102      | 45535         | 42457       | 20585 | 48.48%  |
| MB Precinct                   | 109479                                | 102      | 5143          | 4816        | 2614  | 54.28%  |
| Provisional                   | 109479                                | 102      | 1584          | 1355        | 751   | 55.42%  |
| Total                         | 109479                                | 510      | 64279         | 59731       | 29489 | 49.37%  |
| Total                         |                                       |          |               |             |       |         |

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| Measure G - County (majority to pass) |               |            |               |              |              |               |
|---------------------------------------|---------------|------------|---------------|--------------|--------------|---------------|
|                                       | Reg. Voters   | Vote For   | Times Counted | Total Votes  | YES          |               |
| Precinct TSx                          | 109479        | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                           | 109479        | 102        | 12009         | 11095        | 5535         | 49.89%        |
| Precinct VBM                          | 109479        | 102        | 45535         | 42457        | 20585        | 48.48%        |
| MB Precinct                           | 109479        | 102        | 5143          | 4816         | 2614         | 54.28%        |
| Provisional                           | 109479        | 102        | 1584          | 1355         | 751          | 55.42%        |
| <b>Total</b>                          | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>59731</b> | <b>29489</b> | <b>49.37%</b> |
| <b>UNITED STATES SENATE</b>           |               |            |               |              |              |               |
| <b>UNITED STATES SENATE</b>           |               |            |               |              |              |               |
| Precinct TSx                          | 109479        | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                           | 109479        | 102        | 12009         | 11095        | 5535         | 49.89%        |
| Precinct VBM                          | 109479        | 102        | 45535         | 42457        | 20585        | 48.48%        |
| MB Precinct                           | 109479        | 102        | 5143          | 4816         | 2614         | 54.28%        |
| Provisional                           | 109479        | 102        | 1584          | 1355         | 751          | 55.42%        |
| <b>Total</b>                          | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>59731</b> | <b>29489</b> | <b>49.37%</b> |
| <b>Total</b>                          |               |            |               |              |              |               |
| Precinct TSx                          | 109479        | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                           | 109479        | 102        | 12009         | 11095        | 5535         | 49.89%        |
| Precinct VBM                          | 109479        | 102        | 45535         | 42457        | 20585        | 48.48%        |
| MB Precinct                           | 109479        | 102        | 5143          | 4816         | 2614         | 54.28%        |
| Provisional                           | 109479        | 102        | 1584          | 1355         | 751          | 55.42%        |
| <b>Total</b>                          | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>59731</b> | <b>29489</b> | <b>49.37%</b> |
| <b>Total</b>                          |               |            |               |              |              |               |
| <b>UNITED STATES REPRESENTATIVE</b>   |               |            |               |              |              |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>     |               |            |               |              |              |               |
| Precinct TSx                          | 109479        | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                           | 109479        | 102        | 12009         | 11095        | 5535         | 49.89%        |
| Precinct VBM                          | 109479        | 102        | 45535         | 42457        | 20585        | 48.48%        |
| MB Precinct                           | 109479        | 102        | 5143          | 4816         | 2614         | 54.28%        |
| Provisional                           | 109479        | 102        | 1584          | 1355         | 751          | 55.42%        |
| <b>Total</b>                          | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>59731</b> | <b>29489</b> | <b>49.37%</b> |
| <b>Total</b>                          |               |            |               |              |              |               |
| Precinct TSx                          | 109479        | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                           | 109479        | 102        | 12009         | 11095        | 5535         | 49.89%        |
| Precinct VBM                          | 109479        | 102        | 45535         | 42457        | 20585        | 48.48%        |
| MB Precinct                           | 109479        | 102        | 5143          | 4816         | 2614         | 54.28%        |
| Provisional                           | 109479        | 102        | 1584          | 1355         | 751          | 55.42%        |
| <b>Total</b>                          | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>59731</b> | <b>29489</b> | <b>49.37%</b> |
| <b>Total</b>                          |               |            |               |              |              |               |
| <b>STATE SENATE</b>                   |               |            |               |              |              |               |
| <b>1ST STATE SENATE DISTRICT</b>      |               |            |               |              |              |               |
| Precinct TSx                          | 109479        | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                           | 109479        | 102        | 12009         | 11095        | 5535         | 49.89%        |
| Precinct VBM                          | 109479        | 102        | 45535         | 42457        | 20585        | 48.48%        |
| MB Precinct                           | 109479        | 102        | 5143          | 4816         | 2614         | 54.28%        |
| Provisional                           | 109479        | 102        | 1584          | 1355         | 751          | 55.42%        |
| <b>Total</b>                          | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>59731</b> | <b>29489</b> | <b>49.37%</b> |
| <b>Total</b>                          |               |            |               |              |              |               |
| Precinct TSx                          | 109479        | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                           | 109479        | 102        | 12009         | 11095        | 5535         | 49.89%        |
| Precinct VBM                          | 109479        | 102        | 45535         | 42457        | 20585        | 48.48%        |
| MB Precinct                           | 109479        | 102        | 5143          | 4816         | 2614         | 54.28%        |
| Provisional                           | 109479        | 102        | 1584          | 1355         | 751          | 55.42%        |
| <b>Total</b>                          | <b>109479</b> | <b>510</b> | <b>64279</b>  | <b>59731</b> | <b>29489</b> | <b>49.37%</b> |
| <b>Total</b>                          |               |            |               |              |              |               |
| <b>STATE ASSEMBLY DISTRICTS</b>       |               |            |               |              |              |               |
| <b>5TH STATE ASSEMBLY DIST</b>        |               |            |               |              |              |               |
| Precinct TSx                          | 70961         | 75         | 4             | 4            | 2            | 50.00%        |
| Precinct OS                           | 70961         | 75         | 7627          | 7018         | 3206         | 45.68%        |
| Precinct VBM                          | 70961         | 75         | 28225         | 26168        | 11598        | 44.32%        |
| MB Precinct                           | 70961         | 75         | 4361          | 4081         | 2149         | 52.66%        |

**COUNTY OF EL DORADO**  
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| Measure G - County (majority to pass) |             |          |               |             |       |        |
|---------------------------------------|-------------|----------|---------------|-------------|-------|--------|
|                                       | Reg. Voters | Vote For | Times Counted | Total Votes | YES   |        |
| Provisional                           | 70961       | 75       | 1038          | 879         | 460   | 52.33% |
| Total                                 | 70961       | 375      | 41255         | 38150       | 17415 | 45.65% |
| <b>6TH STATE ASSEMBLY DIST</b>        |             |          |               |             |       |        |
| Precinct TSx                          | 38518       | 27       | 4             | 4           | 2     | 50.00% |
| Precinct OS                           | 38518       | 27       | 4382          | 4077        | 2329  | 57.13% |
| Precinct VBM                          | 38518       | 27       | 17310         | 16289       | 8987  | 55.17% |
| MB Precinct                           | 38518       | 27       | 782           | 735         | 465   | 63.27% |
| Provisional                           | 38518       | 27       | 546           | 476         | 291   | 61.13% |
| Total                                 | 38518       | 135      | 23024         | 21581       | 12074 | 55.95% |
| <b>Total</b>                          |             |          |               |             |       |        |
| Precinct TSx                          | 109479      | 102      | 8             | 8           | 4     | 50.00% |
| Precinct OS                           | 109479      | 102      | 12009         | 11095       | 5535  | 49.89% |
| Precinct VBM                          | 109479      | 102      | 45535         | 42457       | 20585 | 48.48% |
| MB Precinct                           | 109479      | 102      | 5143          | 4816        | 2614  | 54.28% |
| Provisional                           | 109479      | 102      | 1584          | 1355        | 751   | 55.42% |
| Total                                 | 109479      | 510      | 64279         | 59731       | 29489 | 49.37% |
| <b>SUPERVISORIAL DISTRICTS</b>        |             |          |               |             |       |        |
| <b>1ST SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 22587       | 14       | 0             | 0           | 0     | -      |
| Precinct OS                           | 22587       | 14       | 2530          | 2363        | 1384  | 58.57% |
| Precinct VBM                          | 22587       | 14       | 10411         | 9824        | 5618  | 57.19% |
| MB Precinct                           | 22587       | 14       | 423           | 402         | 275   | 68.41% |
| Provisional                           | 22587       | 14       | 301           | 256         | 166   | 64.84% |
| Total                                 | 22587       | 70       | 13665         | 12845       | 7443  | 57.94% |
| <b>2ND SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 23114       | 23       | 4             | 4           | 2     | 50.00% |
| Precinct OS                           | 23114       | 23       | 2509          | 2359        | 1284  | 54.43% |
| Precinct VBM                          | 23114       | 23       | 9647          | 9087        | 4722  | 51.96% |
| MB Precinct                           | 23114       | 23       | 1590          | 1505        | 871   | 57.87% |
| Provisional                           | 23114       | 23       | 327           | 289         | 165   | 57.09% |
| Total                                 | 23114       | 115      | 14077         | 13244       | 7044  | 53.19% |
| <b>3RD SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 21691       | 26       | 3             | 3           | 2     | 66.67% |
| Precinct OS                           | 21691       | 26       | 2368          | 2244        | 1014  | 45.19% |
| Precinct VBM                          | 21691       | 26       | 10021         | 9432        | 4080  | 43.26% |
| MB Precinct                           | 21691       | 26       | 440           | 407         | 162   | 39.80% |
| Provisional                           | 21691       | 26       | 277           | 246         | 133   | 54.07% |
| Total                                 | 21691       | 130      | 13109         | 12332       | 5391  | 43.72% |
| <b>4TH SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 23425       | 22       | 0             | 0           | 0     | -      |
| Precinct OS                           | 23425       | 22       | 2568          | 2401        | 1106  | 46.06% |
| Precinct VBM                          | 23425       | 22       | 9398          | 8831        | 4105  | 46.48% |
| MB Precinct                           | 23425       | 22       | 2239          | 2134        | 1152  | 53.98% |
| Provisional                           | 23425       | 22       | 315           | 287         | 145   | 50.52% |
| Total                                 | 23425       | 110      | 14520         | 13653       | 6508  | 47.67% |
| <b>5TH SUPERVISORIAL DISTRICT</b>     |             |          |               |             |       |        |
| Precinct TSx                          | 18662       | 17       | 1             | 1           | 0     | 0.00%  |
| Precinct OS                           | 18662       | 17       | 2034          | 1728        | 747   | 43.23% |
| Precinct VBM                          | 18662       | 17       | 6058          | 5283        | 2060  | 38.99% |
| MB Precinct                           | 18662       | 17       | 451           | 368         | 154   | 41.85% |
| Provisional                           | 18662       | 17       | 364           | 277         | 142   | 51.26% |
| Total                                 | 18662       | 85       | 8908          | 7657        | 3103  | 40.53% |
| <b>Total</b>                          |             |          |               |             |       |        |
| Precinct TSx                          | 109479      | 102      | 8             | 8           | 4     | 50.00% |
| Precinct OS                           | 109479      | 102      | 12009         | 11095       | 5535  | 49.89% |
| Precinct VBM                          | 109479      | 102      | 45535         | 42457       | 20585 | 48.48% |
| MB Precinct                           | 109479      | 102      | 5143          | 4816        | 2614  | 54.28% |
| Provisional                           | 109479      | 102      | 1584          | 1355        | 751   | 55.42% |
| Total                                 | 109479      | 510      | 64279         | 59731       | 29489 | 49.37% |

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|                                 |  | Measure G - County (majority to pass) |            |               |              |              |               |
|---------------------------------|--|---------------------------------------|------------|---------------|--------------|--------------|---------------|
|                                 |  | Reg. Voters                           | Vote For   | Times Counted | Total Votes  | YES          |               |
| <b>CITIES</b>                   |  |                                       |            |               |              |              |               |
| <b>CITY OF PLACERVILLE</b>      |  |                                       |            |               |              |              |               |
| Precinct TSx                    |  | 5569                                  | 5          | 0             | 0            | 0            | -             |
| Precinct OS                     |  | 5569                                  | 5          | 631           | 588          | 262          | 44.56%        |
| Precinct VBM                    |  | 5569                                  | 5          | 2590          | 2388         | 981          | 41.08%        |
| MB Precinct                     |  | 5569                                  | 5          | 0             | 0            | 0            | -             |
| Provisional                     |  | 5569                                  | 5          | 80            | 68           | 38           | 55.88%        |
| <b>Total</b>                    |  | <b>5569</b>                           | <b>25</b>  | <b>3301</b>   | <b>3044</b>  | <b>1281</b>  | <b>42.08%</b> |
| <b>CITY OF SOUTH LAKE TAHOE</b> |  |                                       |            |               |              |              |               |
| Precinct TSx                    |  | 9465                                  | 7          | 0             | 0            | 0            | -             |
| Precinct OS                     |  | 9465                                  | 7          | 1063          | 889          | 372          | 41.84%        |
| Precinct VBM                    |  | 9465                                  | 7          | 2796          | 2358         | 923          | 39.14%        |
| MB Precinct                     |  | 9465                                  | 7          | 0             | 0            | 0            | -             |
| Provisional                     |  | 9465                                  | 7          | 224           | 162          | 83           | 51.23%        |
| <b>Total</b>                    |  | <b>9465</b>                           | <b>35</b>  | <b>4083</b>   | <b>3409</b>  | <b>1378</b>  | <b>40.42%</b> |
| <b>UNINCORPORATED AREA</b>      |  |                                       |            |               |              |              |               |
| Precinct TSx                    |  | 94445                                 | 90         | 8             | 8            | 4            | 50.00%        |
| Precinct OS                     |  | 94445                                 | 90         | 10315         | 9618         | 4901         | 50.96%        |
| Precinct VBM                    |  | 94445                                 | 90         | 40149         | 37711        | 18681        | 49.54%        |
| MB Precinct                     |  | 94445                                 | 90         | 5143          | 4816         | 2614         | 54.28%        |
| Provisional                     |  | 94445                                 | 90         | 1280          | 1125         | 630          | 56.00%        |
| <b>Total</b>                    |  | <b>94445</b>                          | <b>450</b> | <b>56895</b>  | <b>53278</b> | <b>26830</b> | <b>50.36%</b> |
| <b>Total</b>                    |  |                                       |            |               |              |              |               |
| Precinct TSx                    |  | 109479                                | 102        | 8             | 8            | 4            | 50.00%        |
| Precinct OS                     |  | 109479                                | 102        | 12009         | 11095        | 5535         | 49.89%        |
| Precinct VBM                    |  | 109479                                | 102        | 45535         | 42457        | 20585        | 48.48%        |
| MB Precinct                     |  | 109479                                | 102        | 5143          | 4816         | 2614         | 54.28%        |
| Provisional                     |  | 109479                                | 102        | 1584          | 1355         | 751          | 55.42%        |
| <b>Total</b>                    |  | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>59731</b> | <b>29489</b> | <b>49.37%</b> |

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|                          | NO                                       |
| <b>Jurisdiction Wide</b> |  |
| 100001.11-CP 100001.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 79 43.89%                                |
| Precinct VBM             | 329 44.34%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 6 42.86%                                 |
| Total                    | 414 44.23%                               |
| 100002.11-CP 100002.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 35 41.67%                                |
| Precinct VBM             | 235 52.22%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 4 22.22%                                 |
| Total                    | 274 49.64%                               |
| 100003.11-CP 100003.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 58 51.33%                                |
| Precinct VBM             | 318 43.86%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 4 19.05%                                 |
| Total                    | 380 44.24%                               |
| 100004.11-CP 100004.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 95 41.48%                                |
| Precinct VBM             | 236 33.96%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 1 33.33%                                 |
| Total                    | 332 35.81%                               |
| 100005.11-CP 100005.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 63 45.32%                                |
| Precinct VBM             | 437 51.78%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 10 45.45%                                |
| Total                    | 510 50.75%                               |
| 100006.11-CP 100006.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 93 39.74%                                |
| Precinct VBM             | 288 32.58%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 11 40.74%                                |
| Total                    | 392 34.24%                               |
| 100008.11-CP 100008.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 65 35.33%                                |
| Precinct VBM             | 267 38.14%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 2 15.38%                                 |
| Total                    | 334 37.24%                               |
| 100009.11-CP 100009.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 59 37.82%                                |
| Precinct VBM             | 328 47.40%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 6 37.50%                                 |
| Total                    | 393 45.49%                               |
| 100012.11-CP 100012.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 98 40.50%                                |
| Precinct VBM             | 366 40.44%                               |

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|------------------------|--|
|                        | NO                                       |
| MB Precinct            | 0 -                                      |
| Provisional            | 12 48.00%                                |
| Total                  | 476 40.61%                               |
| 100013.11-CP 100013.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 114 48.31%                               |
| Precinct VBM           | 367 42.48%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 10 38.46%                                |
| Total                  | 491 43.61%                               |
| 100014.11-CP 100014.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 82 41.21%                                |
| Precinct VBM           | 298 44.02%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 11 40.74%                                |
| Total                  | 391 43.30%                               |
| 100022.11-CP 100022.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 53 32.32%                                |
| Precinct VBM           | 268 42.01%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 10.00%                                 |
| Total                  | 322 39.66%                               |
| 100023.11-CP 100023.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 85 41.87%                                |
| Precinct VBM           | 469 46.53%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 10 35.71%                                |
| Total                  | 564 45.52%                               |
| 200003.12-CP 200003.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 65 43.33%                                |
| Precinct VBM           | 283 55.06%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 14 51.85%                                |
| Total                  | 362 52.39%                               |
| 200004.12-CP 200004.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 80 47.06%                                |
| Precinct VBM           | 299 48.30%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 53.85%                                 |
| Total                  | 386 48.13%                               |
| 200005.02-CP 200005.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 61 67.78%                                |
| Precinct VBM           | 260 61.47%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 47.37%                                 |
| Total                  | 330 62.03%                               |
| 200006.12-CP 200006.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 50 44.25%                                |
| Precinct VBM           | 258 50.69%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 14 50.00%                                |
| Total                  | 322 49.54%                               |
| 200008.12-CP 200008.12 |  |
| Precinct TSx           | 0 -                                      |

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|                        | Measure G - County<br>(majority to pass) |        |
|------------------------|--|--------|
|                        | NO                                       |        |
| Precinct OS            | 62                                       | 43.06% |
| Precinct VBM           | 171                                      | 49.00% |
| MB Precinct            | 0  | -      |
| Provisional            | 1  | 10.00% |
| Total                  | 234                                      | 46.52% |
| 200010.12-CP 200010.12 |  |        |
| Precinct TSx           | 2  | 50.00% |
| Precinct OS            | 83                                       | 47.98% |
| Precinct VBM           | 387                                      | 48.86% |
| MB Precinct            | 0  | -      |
| Provisional            | 13                                       | 48.15% |
| Total                  | 485                                      | 48.69% |
| 200012.12-CP 200012.12 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 61                                       | 43.88% |
| Precinct VBM           | 187                                      | 42.79% |
| MB Precinct            | 0  | -      |
| Provisional            | 8  | 66.67% |
| Total                  | 256                                      | 43.54% |
| 200013.12-CP 200013.12 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 97                                       | 48.99% |
| Precinct VBM           | 354                                      | 53.23% |
| MB Precinct            | 0  | -      |
| Provisional            | 6  | 28.57% |
| Total                  | 457                                      | 51.70% |
| 200014.12-CP 200014.12 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 73                                       | 38.02% |
| Precinct VBM           | 248                                      | 43.82% |
| MB Precinct            | 0  | -      |
| Provisional            | 12                                       | 46.15% |
| Total                  | 333                                      | 42.47% |
| 200015.02-CP 200015.02 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 111                                      | 54.15% |
| Precinct VBM           | 467                                      | 55.60% |
| MB Precinct            | 0  | -      |
| Provisional            | 8  | 36.36% |
| Total                  | 586                                      | 54.92% |
| 200016.12-CP 200016.12 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 8  | 33.33% |
| Precinct VBM           | 43                                       | 32.33% |
| MB Precinct            | 0  | -      |
| Provisional            | 1  | 20.00% |
| Total                  | 52                                       | 32.10% |
| 200016.13-CP 200016.13 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 23                                       | 30.67% |
| Precinct VBM           | 93                                       | 28.27% |
| MB Precinct            | 0  | -      |
| Provisional            | 0  | 0.00%  |
| Total                  | 116                                      | 28.36% |
| 200017.02-CP 200017.02 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 66                                       | 48.53% |
| Precinct VBM           | 309                                      | 56.39% |
| MB Precinct            | 0  | -      |
| Provisional            | 3  | 27.27% |
| Total                  | 378                                      | 54.39% |



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|------------------------|--|
|                        | NO                                       |
| 200018.02-CP 200018.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 93 58.49%                                |
| Precinct VBM           | 379 57.51%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 45.45%                                 |
| Total                  | 477 57.54%                               |
| 200020.02-CP 200020.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 49 37.12%                                |
| Precinct VBM           | 155 30.27%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 47.06%                                 |
| Total                  | 212 32.07%                               |
| 200021.01-CP 200021.01 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 12 32.43%                                |
| Precinct VBM           | 87 43.94%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 80.00%                                 |
| Total                  | 103 42.92%                               |
| 200021.02-CP 200021.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 47 27.98%                                |
| Precinct VBM           | 202 27.86%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 2 12.50%                                 |
| Total                  | 251 27.61%                               |
| 200024.02-CP 200024.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 34 62.96%                                |
| Precinct VBM           | 183 68.03%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 75.00%                                 |
| Total                  | 223 67.37%                               |
| 300001.04-CP 300001.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 10 100.00%                               |
| Precinct VBM           | 57 62.64%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 -                                      |
| Total                  | 67 66.34%                                |
| 300001.05-CP 300001.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 38 55.88%                                |
| Precinct VBM           | 186 61.39%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 40.00%                                 |
| Total                  | 228 59.84%                               |
| 300003.05-CP 300003.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 104 53.61%                               |
| Precinct VBM           | 489 56.79%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 10 45.45%                                |
| Total                  | 603 55.99%                               |
| 300004.05-CP 300004.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 99 59.64%                                |
| Precinct VBM           | 311 59.69%                               |
| MB Precinct            | 0 -                                      |

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|                        | NO                                       |
| Provisional            | 5 33.33%                                 |
| Total                  | 415 59.12%                               |
| 300005.05-CP 300005.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 75 50.00%                                |
| Precinct VBM           | 364 59.48%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 11 52.38%                                |
| Total                  | 450 57.47%                               |
| 300008.05-CP 300008.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 76 55.88%                                |
| Precinct VBM           | 438 61.86%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 30.77%                                 |
| Total                  | 518 60.44%                               |
| 300009.04-CP 300009.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 56 48.28%                                |
| Precinct VBM           | 158 41.58%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 41.67%                                 |
| Total                  | 219 43.11%                               |
| 300010.04-CP 300010.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 98 58.33%                                |
| Precinct VBM           | 346 56.54%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 60.00%                                 |
| Total                  | 450 56.96%                               |
| 300011.04-CP 300011.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 69 49.64%                                |
| Precinct VBM           | 296 43.72%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 22.22%                                 |
| Total                  | 369 44.24%                               |
| 300013.05-CP 300013.05 |  |
| Precinct TSx           | 1 33.33%                                 |
| Precinct OS            | 61 50.00%                                |
| Precinct VBM           | 367 52.73%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 40.00%                                 |
| Total                  | 435 52.03%                               |
| 300015.04-CP 300015.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 4 40.00%                                 |
| Precinct VBM           | 25 45.45%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 0.00%                                  |
| Total                  | 29 42.65%                                |
| 300015.05-CP 300015.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 39 61.90%                                |
| Precinct VBM           | 146 58.87%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 50.00%                                 |
| Total                  | 186 59.42%                               |
| 300017.05-CP 300017.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 62 57.41%                                |

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|                        | NO                                       |
| Precinct VBM           | 373 59.68%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 75.00%                                 |
| Total                  | 441 59.51%                               |
| 300018.04-CP 300018.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 73 54.07%                                |
| Precinct VBM           | 253 52.71%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 17 62.96%                                |
| Total                  | 343 53.43%                               |
| 300020.04-CP 300020.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 84 51.22%                                |
| Precinct VBM           | 322 54.48%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 33.33%                                 |
| Total                  | 410 53.46%                               |
| 300021.03-CP 300021.03 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 84 60.43%                                |
| Precinct VBM           | 350 60.34%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 38.46%                                 |
| Total                  | 439 59.97%                               |
| 300022.05-CP 300022.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 48 50.53%                                |
| Precinct VBM           | 183 55.79%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 36.36%                                 |
| Total                  | 235 54.15%                               |
| 300023.03-CP 300023.03 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 124 56.11%                               |
| Precinct VBM           | 563 64.20%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 15 62.50%                                |
| Total                  | 702 62.57%                               |
| 300023.05-CP 300023.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 26 65.00%                                |
| Precinct VBM           | 125 66.84%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 42.86%                                 |
| Total                  | 154 65.81%                               |
| 400001.14-CP 400001.14 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 78 48.45%                                |
| Precinct VBM           | 478 51.51%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 11 50.00%                                |
| Total                  | 567 51.04%                               |
| 400002.08-CP 400002.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 88 47.31%                                |
| Precinct VBM           | 304 44.06%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 38.46%                                 |
| Total                  | 397 44.66%                               |
| 400003.08-CP 400003.08 |  |

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|                        | Measure G - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 151 66.52%                               |
| Precinct VBM           | 431 60.79%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 19 63.33%                                |
| Total                  | 601 62.22%                               |
| 400004.14-CP 400004.14 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 89 50.86%                                |
| Precinct VBM           | 295 47.28%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 22.22%                                 |
| Total                  | 388 47.49%                               |
| 400005.08-CP 400005.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 50 71.43%                                |
| Precinct VBM           | 151 63.45%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 63.64%                                 |
| Total                  | 208 65.20%                               |
| 400006.08-CP 400006.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 141 68.45%                               |
| Precinct VBM           | 424 65.03%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 12 75.00%                                |
| Total                  | 577 66.02%                               |
| 400007.08-CP 400007.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 124 57.41%                               |
| Precinct VBM           | 458 63.61%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 12 60.00%                                |
| Total                  | 594 62.13%                               |
| 400010.08-CP 400010.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 139 59.66%                               |
| Precinct VBM           | 447 55.25%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 29.63%                                 |
| Total                  | 594 55.57%                               |
| 400012.08-CP 400012.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 46 41.82%                                |
| Precinct VBM           | 248 60.05%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 15 71.43%                                |
| Total                  | 309 56.80%                               |
| 400013.08-CP 400013.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 76 65.52%                                |
| Precinct VBM           | 180 59.41%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 25.00%                                 |
| Total                  | 257 60.76%                               |
| 400014.08-CP 400014.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 107 48.42%                               |
| Precinct VBM           | 346 49.22%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 15 41.67%                                |

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|                        | Measure G - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| Total                  | 468 48.75%                               |
| 400017.08-CP 400017.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 49 28.82%                                |
| Precinct VBM           | 258 35.59%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 32.00%                                 |
| Total                  | 315 34.24%                               |
| 400018.08-CP 400018.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 32 40.00%                                |
| Precinct VBM           | 123 46.24%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 50.00%                                 |
| Total                  | 158 44.89%                               |
| 400020.08-CP 400020.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 58 53.21%                                |
| Precinct VBM           | 180 56.60%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 2 100.00%                                |
| Total                  | 240 55.94%                               |
| 400021.07-CP 400021.07 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 67 55.37%                                |
| Precinct VBM           | 403 54.98%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 60.00%                                 |
| Total                  | 479 55.12%                               |
| 500001.10-CP 500001.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 97 56.73%                                |
| Precinct VBM           | 317 60.27%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 37.50%                                 |
| Total                  | 420 58.91%                               |
| 500002.10-CP 500002.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 68 62.39%                                |
| Precinct VBM           | 208 58.43%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 12 48.00%                                |
| Total                  | 288 58.78%                               |
| 500003.10-CP 500003.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 57 53.77%                                |
| Precinct VBM           | 168 57.93%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 25.00%                                 |
| Total                  | 226 56.50%                               |
| 500004.10-CP 500004.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 60 55.05%                                |
| Precinct VBM           | 146 65.77%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 41.18%                                 |
| Total                  | 213 61.21%                               |
| 500005.10-CP 500005.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 43 49.43%                                |
| Precinct VBM           | 130 60.75%                               |

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|                        | Measure G - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 58.33%                                 |
| Total                  | 180 57.51%                               |
| 500006.10-CP 500006.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 89 60.96%                                |
| Precinct VBM           | 243 58.27%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 19 48.72%                                |
| Total                  | 351 58.31%                               |
| 500007.10-CP 500007.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 108 60.67%                               |
| Precinct VBM           | 400 67.34%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 64.29%                                 |
| Total                  | 517 65.78%                               |
| 500009.10-CP 500009.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 62 60.19%                                |
| Precinct VBM           | 134 54.69%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 12 48.00%                                |
| Total                  | 208 55.76%                               |
| 500011.10-CP 500011.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 87 55.41%                                |
| Precinct VBM           | 174 56.13%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 13 43.33%                                |
| Total                  | 274 55.13%                               |
| 500012.10-CP 500012.10 |  |
| Precinct TSx           | 1 100.00%                                |
| Precinct OS            | 90 55.21%                                |
| Precinct VBM           | 300 56.07%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 14 51.85%                                |
| Total                  | 405 55.79%                               |
| 500015.10-CP 500015.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 59 62.77%                                |
| Precinct VBM           | 217 60.78%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 50.00%                                 |
| Total                  | 282 60.91%                               |
| 500020.10-CP 500020.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 72 47.37%                                |
| Precinct VBM           | 405 64.39%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 13 48.15%                                |
| Total                  | 490 60.64%                               |
| 500021.10-CP 500021.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 89 58.17%                                |
| Precinct VBM           | 381 64.80%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 13 50.00%                                |
| Total                  | 483 62.97%                               |
| 95U201.01-MB 95U201.01 |  |
| Precinct TSx           | 0 -                                      |

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|                        | Measure G - County<br>(majority to pass) |         |
|------------------------|--|---------|
|                        | NO                                       |         |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 0  | 0.00%   |
| Provisional            | 0  | -       |
| Total                  | 0  | 0.00%   |
| 95U201.02-MB 95U201.02 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 92                                       | 26.90%  |
| Provisional            | 1  | 100.00% |
| Total                  | 93                                       | 27.11%  |
| 95U202.02-MB 95U202.02 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 293                                      | 54.77%  |
| Provisional            | 2  | 50.00%  |
| Total                  | 295                                      | 54.73%  |
| 95U203.02-MB 95U203.02 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 0  | -       |
| Provisional            | 0  | -       |
| Total                  | 0  | -       |
| 95U204.01-MB 95U204.01 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 249                                      | 40.10%  |
| Provisional            | 0  | 0.00%   |
| Total                  | 249                                      | 40.03%  |
| 95U301.05-MB 95U301.05 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 86                                       | 62.32%  |
| Provisional            | 2  | 100.00% |
| Total                  | 88                                       | 62.86%  |
| 95U302.04-MB 95U302.04 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 0  | 0.00%   |
| Provisional            | 0  | -       |
| Total                  | 0  | 0.00%   |
| 95U302.05-MB 95U302.05 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 50                                       | 60.24%  |
| Provisional            | 0  | -       |
| Total                  | 50                                       | 60.24%  |
| 95U303.03-MB 95U303.03 |  |         |
| Precinct TSx           | 0  | -       |
| Precinct OS            | 0  | -       |
| Precinct VBM           | 0  | -       |
| MB Precinct            | 47                                       | 58.02%  |
| Provisional            | 0  | -       |
| Total                  | 47                                       | 58.02%  |

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|                        | NO                                       |
| 95U303.05-MB 95U303.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 59 60.82%                                |
| Provisional            | 1 100.00%                                |
| Total                  | 60 61.22%                                |
| 95U304.05-MB 95U304.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 -                                      |
| Total                  | 0 -                                      |
| 95U305.05-MB 95U305.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 3 42.86%                                 |
| Provisional            | 0 -                                      |
| Total                  | 3 42.86%                                 |
| 95U401.08-MB 95U401.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 4 30.77%                                 |
| Provisional            | 0 -                                      |
| Total                  | 4 30.77%                                 |
| 95U402.07-MB 95U402.07 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 108 43.55%                               |
| Provisional            | 1 100.00%                                |
| Total                  | 109 43.78%                               |
| 95U402.08-MB 95U402.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 211 38.86%                               |
| Provisional            | 0 0.00%                                  |
| Total                  | 211 38.64%                               |
| 95U403.08-MB 95U403.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 516 51.76%                               |
| Provisional            | 6 54.55%                                 |
| Total                  | 522 51.79%                               |
| 95U404.06-MB 95U404.06 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 -                                      |
| Total                  | 0 -                                      |
| 95U404.08-MB 95U404.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 0 -                                      |



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|                               | Measure G - County<br>(majority to pass) |
|-------------------------------|--|
|                               | NO                                       |
| Provisional                   | 0 -                                      |
| Total                         | 0 -                                      |
| 95U501.09-MB 95U501.09        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 0 0.00%                                  |
| Provisional                   | 0 -                                      |
| Total                         | 0 0.00%                                  |
| 95U501.10-MB 95U501.10        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 94 59.87%                                |
| Provisional                   | 2 100.00%                                |
| Total                         | 96 60.38%                                |
| 95U598.10-MB 95U598.10        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 97 55.11%                                |
| Provisional                   | 1 100.00%                                |
| Total                         | 98 55.37%                                |
| 95U599.10-MB 95U599.10        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 23 69.70%                                |
| Provisional                   | 0 -                                      |
| Total                         | 23 69.70%                                |
| 96U101.11-MB 96U101.11        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 127 31.59%                               |
| Provisional                   | 2 33.33%                                 |
| Total                         | 129 31.62%                               |
| 96U401.14-MB 96U401.14        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 143 42.94%                               |
| Provisional                   | 4 66.67%                                 |
| Total                         | 147 43.36%                               |
| Total                         |  |
| Precinct TSx                  | 4 50.00%                                 |
| Precinct OS                   | 5560 50.11%                              |
| Precinct VBM                  | 21872 51.52%                             |
| MB Precinct                   | 2202 45.72%                              |
| Provisional                   | 604 44.58%                               |
| Total                         | 30242 50.63%                             |
| STATE BOARD OF EQUALIZATION   |  |
| STATE BOARD OF EQUALIZATION 1 |  |
| Precinct TSx                  | 4 50.00%                                 |
| Precinct OS                   | 5560 50.11%                              |
| Precinct VBM                  | 21872 51.52%                             |
| MB Precinct                   | 2202 45.72%                              |
| Provisional                   | 604 44.58%                               |
| Total                         | 30242 50.63%                             |
| Total                         |  |

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|-------------------------------------|--|---------------|
|                                     | NO                                       |               |
| Precinct TSx                        | 4  | 50.00%        |
| Precinct OS                         | 5560                                     | 50.11%        |
| Precinct VBM                        | 21872                                    | 51.52%        |
| MB Precinct                         | 2202                                     | 45.72%        |
| Provisional                         | 604                                      | 44.58%        |
| <b>Total</b>                        | <b>30242</b>                             | <b>50.63%</b> |
| <b>UNITED STATES SENATE</b>         |  |               |
| <b>UNITED STATES SENATE</b>         |  |               |
| Precinct TSx                        | 4  | 50.00%        |
| Precinct OS                         | 5560                                     | 50.11%        |
| Precinct VBM                        | 21872                                    | 51.52%        |
| MB Precinct                         | 2202                                     | 45.72%        |
| Provisional                         | 604                                      | 44.58%        |
| <b>Total</b>                        | <b>30242</b>                             | <b>50.63%</b> |
| <b>Total</b>                        |  |               |
| Precinct TSx                        | 4  | 50.00%        |
| Precinct OS                         | 5560                                     | 50.11%        |
| Precinct VBM                        | 21872                                    | 51.52%        |
| MB Precinct                         | 2202                                     | 45.72%        |
| Provisional                         | 604                                      | 44.58%        |
| <b>Total</b>                        | <b>30242</b>                             | <b>50.63%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |  |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |  |               |
| Precinct TSx                        | 4  | 50.00%        |
| Precinct OS                         | 5560                                     | 50.11%        |
| Precinct VBM                        | 21872                                    | 51.52%        |
| MB Precinct                         | 2202                                     | 45.72%        |
| Provisional                         | 604                                      | 44.58%        |
| <b>Total</b>                        | <b>30242</b>                             | <b>50.63%</b> |
| <b>Total</b>                        |  |               |
| Precinct TSx                        | 4  | 50.00%        |
| Precinct OS                         | 5560                                     | 50.11%        |
| Precinct VBM                        | 21872                                    | 51.52%        |
| MB Precinct                         | 2202                                     | 45.72%        |
| Provisional                         | 604                                      | 44.58%        |
| <b>Total</b>                        | <b>30242</b>                             | <b>50.63%</b> |
| <b>STATE SENATE</b>                 |  |               |
| <b>1ST STATE SENATE DISTRICT</b>    |  |               |
| Precinct TSx                        | 4  | 50.00%        |
| Precinct OS                         | 5560                                     | 50.11%        |
| Precinct VBM                        | 21872                                    | 51.52%        |
| MB Precinct                         | 2202                                     | 45.72%        |
| Provisional                         | 604                                      | 44.58%        |
| <b>Total</b>                        | <b>30242</b>                             | <b>50.63%</b> |
| <b>Total</b>                        |  |               |
| Precinct TSx                        | 4  | 50.00%        |
| Precinct OS                         | 5560                                     | 50.11%        |
| Precinct VBM                        | 21872                                    | 51.52%        |
| MB Precinct                         | 2202                                     | 45.72%        |
| Provisional                         | 604                                      | 44.58%        |
| <b>Total</b>                        | <b>30242</b>                             | <b>50.63%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |  |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |  |               |
| Precinct TSx                        | 2  | 50.00%        |
| Precinct OS                         | 3812                                     | 54.32%        |
| Precinct VBM                        | 14570                                    | 55.68%        |
| MB Precinct                         | 1932                                     | 47.34%        |

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|                                   | NO                                       |         |
| Provisional                       | 419                                      | 47.67%  |
| Total                             | 20735                                    | 54.35%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |  |         |
| Precinct TSx                      | 2  | 50.00%  |
| Precinct OS                       | 1748                                     | 42.87%  |
| Precinct VBM                      | 7302                                     | 44.83%  |
| MB Precinct                       | 270                                      | 36.73%  |
| Provisional                       | 185                                      | 38.87%  |
| Total                             | 9507                                     | 44.05%  |
| Total                             |  |         |
| Precinct TSx                      | 4  | 50.00%  |
| Precinct OS                       | 5560                                     | 50.11%  |
| Precinct VBM                      | 21872                                    | 51.52%  |
| MB Precinct                       | 2202                                     | 45.72%  |
| Provisional                       | 604                                      | 44.58%  |
| Total                             | 30242                                    | 50.63%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |  |         |
| <b>1ST SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 0  | -       |
| Precinct OS                       | 979                                      | 41.43%  |
| Precinct VBM                      | 4206                                     | 42.81%  |
| MB Precinct                       | 127                                      | 31.59%  |
| Provisional                       | 90                                       | 35.16%  |
| Total                             | 5402                                     | 42.06%  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 2  | 50.00%  |
| Precinct OS                       | 1075                                     | 45.57%  |
| Precinct VBM                      | 4365                                     | 48.04%  |
| MB Precinct                       | 634                                      | 42.13%  |
| Provisional                       | 124                                      | 42.91%  |
| Total                             | 6200                                     | 46.81%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 1  | 33.33%  |
| Precinct OS                       | 1230                                     | 54.81%  |
| Precinct VBM                      | 5352                                     | 56.74%  |
| MB Precinct                       | 245                                      | 60.20%  |
| Provisional                       | 113                                      | 45.93%  |
| Total                             | 6941                                     | 56.28%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 0  | -       |
| Precinct OS                       | 1295                                     | 53.94%  |
| Precinct VBM                      | 4726                                     | 53.52%  |
| MB Precinct                       | 982                                      | 46.02%  |
| Provisional                       | 142                                      | 49.48%  |
| Total                             | 7145                                     | 52.33%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 1  | 100.00% |
| Precinct OS                       | 981                                      | 56.77%  |
| Precinct VBM                      | 3223                                     | 61.01%  |
| MB Precinct                       | 214                                      | 58.15%  |
| Provisional                       | 135                                      | 48.74%  |
| Total                             | 4554                                     | 59.47%  |
| Total                             |  |         |
| Precinct TSx                      | 4  | 50.00%  |
| Precinct OS                       | 5560                                     | 50.11%  |
| Precinct VBM                      | 21872                                    | 51.52%  |
| MB Precinct                       | 2202                                     | 45.72%  |
| Provisional                       | 604                                      | 44.58%  |
| Total                             | 30242                                    | 50.63%  |

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|                                 | Measure G - County<br>(majority to pass) |
|---------------------------------|--|
|                                 | NO                                       |
| <b>CITIES</b>                   |  |
| <b>CITY OF PLACERVILLE</b>      |  |
| Precinct TSx                    | 0 -                                      |
| Precinct OS                     | 326 55.44%                               |
| Precinct VBM                    | 1407 58.92%                              |
| MB Precinct                     | 0 -                                      |
| Provisional                     | 30 44.12%                                |
| Total                           | 1763 57.92%                              |
| <b>CITY OF SOUTH LAKE TAHOE</b> |  |
| Precinct TSx                    | 0 -                                      |
| Precinct OS                     | 517 58.16%                               |
| Precinct VBM                    | 1435 60.86%                              |
| MB Precinct                     | 0 -                                      |
| Provisional                     | 79 48.77%                                |
| Total                           | 2031 59.58%                              |
| <b>UNINCORPORATED AREA</b>      |  |
| Precinct TSx                    | 4 50.00%                                 |
| Precinct OS                     | 4717 49.04%                              |
| Precinct VBM                    | 19030 50.46%                             |
| MB Precinct                     | 2202 45.72%                              |
| Provisional                     | 495 44.00%                               |
| Total                           | 26448 49.64%                             |
| <b>Total</b>                    |  |
| Precinct TSx                    | 4 50.00%                                 |
| Precinct OS                     | 5560 50.11%                              |
| Precinct VBM                    | 21872 51.52%                             |
| MB Precinct                     | 2202 45.72%                              |
| Provisional                     | 604 44.58%                               |
| Total                           | 30242 50.63%                             |

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|                        | Measure J - County (majority to pass) |          |               |             |     |        |
|------------------------|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Jurisdiction Wide      |                                       |          |               |             |     |        |
| 100001.11-CP 100001.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1615                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1615                                  | 1        | 190           | 157         | 106 | 67.52% |
| Precinct VBM           | 1615                                  | 1        | 781           | 642         | 446 | 69.47% |
| MB Precinct            | 1615                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1615                                  | 1        | 17            | 12          | 10  | 83.33% |
| Total                  | 1615                                  | 5        | 988           | 811         | 562 | 69.30% |
| 100002.11-CP 100002.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1000                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1000                                  | 1        | 89            | 69          | 43  | 62.32% |
| Precinct VBM           | 1000                                  | 1        | 476           | 393         | 239 | 60.81% |
| MB Precinct            | 1000                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1000                                  | 1        | 20            | 17          | 7   | 41.18% |
| Total                  | 1000                                  | 5        | 585           | 479         | 289 | 60.33% |
| 100003.11-CP 100003.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1269                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1269                                  | 1        | 123           | 103         | 63  | 61.17% |
| Precinct VBM           | 1269                                  | 1        | 773           | 660         | 469 | 71.06% |
| MB Precinct            | 1269                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1269                                  | 1        | 24            | 19          | 13  | 68.42% |
| Total                  | 1269                                  | 5        | 920           | 782         | 545 | 69.69% |
| 100004.11-CP 100004.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1633                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1633                                  | 1        | 254           | 208         | 134 | 64.42% |
| Precinct VBM           | 1633                                  | 1        | 738           | 619         | 416 | 67.21% |
| MB Precinct            | 1633                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1633                                  | 1        | 4             | 2           | 1   | 50.00% |
| Total                  | 1633                                  | 5        | 996           | 829         | 551 | 66.47% |
| 100005.11-CP 100005.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1586                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1586                                  | 1        | 146           | 120         | 72  | 60.00% |
| Precinct VBM           | 1586                                  | 1        | 895           | 766         | 542 | 70.76% |
| MB Precinct            | 1586                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1586                                  | 1        | 23            | 19          | 8   | 42.11% |
| Total                  | 1586                                  | 5        | 1064          | 905         | 622 | 68.73% |
| 100006.11-CP 100006.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1928                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1928                                  | 1        | 252           | 195         | 122 | 62.56% |
| Precinct VBM           | 1928                                  | 1        | 944           | 759         | 505 | 66.53% |
| MB Precinct            | 1928                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1928                                  | 1        | 31            | 18          | 10  | 55.56% |
| Total                  | 1928                                  | 5        | 1227          | 972         | 637 | 65.53% |
| 100008.11-CP 100008.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1671                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1671                                  | 1        | 197           | 147         | 76  | 51.70% |
| Precinct VBM           | 1671                                  | 1        | 740           | 620         | 420 | 67.74% |
| MB Precinct            | 1671                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1671                                  | 1        | 17            | 9           | 8   | 88.89% |
| Total                  | 1671                                  | 5        | 954           | 776         | 504 | 64.95% |
| 100009.11-CP 100009.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1838                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1838                                  | 1        | 169           | 136         | 92  | 67.65% |
| Precinct VBM           | 1838                                  | 1        | 751           | 644         | 423 | 65.68% |
| MB Precinct            | 1838                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1838                                  | 1        | 26            | 15          | 9   | 60.00% |
| Total                  | 1838                                  | 5        | 946           | 795         | 524 | 65.91% |
| 100012.11-CP 100012.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 2108                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 2108                                  | 1        | 255           | 201         | 115 | 57.21% |
| Precinct VBM           | 2108                                  | 1        | 968           | 807         | 552 | 68.40% |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| MB Precinct            | 2108                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 2108                                  | 1        | 29            | 23          | 15  | 65.22% |
| Total                  | 2108                                  | 5        | 1252          | 1031        | 682 | 66.15% |
| 100013.11-CP 100013.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 2087                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 2087                                  | 1        | 250           | 203         | 127 | 62.56% |
| Precinct VBM           | 2087                                  | 1        | 908           | 761         | 520 | 68.33% |
| MB Precinct            | 2087                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 2087                                  | 1        | 28            | 22          | 14  | 63.64% |
| Total                  | 2087                                  | 5        | 1186          | 986         | 661 | 67.04% |
| 100014.11-CP 100014.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1659                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1659                                  | 1        | 211           | 157         | 86  | 54.78% |
| Precinct VBM           | 1659                                  | 1        | 706           | 585         | 394 | 67.35% |
| MB Precinct            | 1659                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1659                                  | 1        | 30            | 22          | 13  | 59.09% |
| Total                  | 1659                                  | 5        | 947           | 764         | 493 | 64.53% |
| 100022.11-CP 100022.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 1432                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1432                                  | 1        | 177           | 152         | 90  | 59.21% |
| Precinct VBM           | 1432                                  | 1        | 678           | 555         | 371 | 66.85% |
| MB Precinct            | 1432                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1432                                  | 1        | 13            | 8           | 5   | 62.50% |
| Total                  | 1432                                  | 5        | 868           | 715         | 466 | 65.17% |
| 100023.11-CP 100023.11 |                                       |          |               |             |     |        |
| Precinct TSx           | 2135                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 2135                                  | 1        | 217           | 170         | 91  | 53.53% |
| Precinct VBM           | 2135                                  | 1        | 1053          | 904         | 612 | 67.70% |
| MB Precinct            | 2135                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 2135                                  | 1        | 33            | 22          | 14  | 63.64% |
| Total                  | 2135                                  | 5        | 1303          | 1096        | 717 | 65.42% |
| 200003.12-CP 200003.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1362                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1362                                  | 1        | 166           | 137         | 86  | 62.77% |
| Precinct VBM           | 1362                                  | 1        | 558           | 465         | 315 | 67.74% |
| MB Precinct            | 1362                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1362                                  | 1        | 31            | 24          | 12  | 50.00% |
| Total                  | 1362                                  | 5        | 755           | 626         | 413 | 65.97% |
| 200004.12-CP 200004.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1507                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1507                                  | 1        | 182           | 153         | 86  | 56.21% |
| Precinct VBM           | 1507                                  | 1        | 662           | 567         | 383 | 67.55% |
| MB Precinct            | 1507                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1507                                  | 1        | 14            | 10          | 6   | 60.00% |
| Total                  | 1507                                  | 5        | 858           | 730         | 475 | 65.07% |
| 200005.02-CP 200005.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 867                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 867                                   | 1        | 94            | 84          | 44  | 52.38% |
| Precinct VBM           | 867                                   | 1        | 452           | 384         | 210 | 54.69% |
| MB Precinct            | 867                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 867                                   | 1        | 21            | 15          | 7   | 46.67% |
| Total                  | 867                                   | 5        | 567           | 483         | 261 | 54.04% |
| 200006.12-CP 200006.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1086                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1086                                  | 1        | 117           | 98          | 54  | 55.10% |
| Precinct VBM           | 1086                                  | 1        | 532           | 460         | 285 | 61.96% |
| MB Precinct            | 1086                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1086                                  | 1        | 29            | 25          | 18  | 72.00% |
| Total                  | 1086                                  | 5        | 678           | 583         | 357 | 61.23% |
| 200008.12-CP 200008.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 999                                   | 1        | 0             | 0           | 0   | -      |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Precinct OS            | 999                                   | 1        | 155           | 123         | 69  | 56.10% |
| Precinct VBM           | 999                                   | 1        | 379           | 318         | 211 | 66.35% |
| MB Precinct            | 999                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 999                                   | 1        | 10            | 8           | 6   | 75.00% |
| Total                  | 999                                   | 5        | 544           | 449         | 286 | 63.70% |
| 200010.12-CP 200010.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1976                                  | 1        | 4             | 4           | 3   | 75.00% |
| Precinct OS            | 1976                                  | 1        | 197           | 155         | 95  | 61.29% |
| Precinct VBM           | 1976                                  | 1        | 859           | 725         | 491 | 67.72% |
| MB Precinct            | 1976                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1976                                  | 1        | 34            | 24          | 16  | 66.67% |
| Total                  | 1976                                  | 5        | 1094          | 908         | 605 | 66.63% |
| 200012.12-CP 200012.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1064                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1064                                  | 1        | 145           | 114         | 66  | 57.89% |
| Precinct VBM           | 1064                                  | 1        | 464           | 388         | 266 | 68.56% |
| MB Precinct            | 1064                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1064                                  | 1        | 14            | 11          | 5   | 45.45% |
| Total                  | 1064                                  | 5        | 623           | 513         | 337 | 65.69% |
| 200013.12-CP 200013.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1574                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1574                                  | 1        | 211           | 179         | 114 | 63.69% |
| Precinct VBM           | 1574                                  | 1        | 701           | 598         | 421 | 70.40% |
| MB Precinct            | 1574                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1574                                  | 1        | 23            | 17          | 8   | 47.06% |
| Total                  | 1574                                  | 5        | 935           | 794         | 543 | 68.39% |
| 200014.12-CP 200014.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 1438                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1438                                  | 1        | 201           | 165         | 116 | 70.30% |
| Precinct VBM           | 1438                                  | 1        | 613           | 503         | 334 | 66.40% |
| MB Precinct            | 1438                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1438                                  | 1        | 30            | 23          | 15  | 65.22% |
| Total                  | 1438                                  | 5        | 844           | 691         | 465 | 67.29% |
| 200015.02-CP 200015.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1832                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1832                                  | 1        | 218           | 176         | 96  | 54.55% |
| Precinct VBM           | 1832                                  | 1        | 886           | 744         | 473 | 63.58% |
| MB Precinct            | 1832                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1832                                  | 1        | 22            | 19          | 11  | 57.89% |
| Total                  | 1832                                  | 5        | 1126          | 939         | 580 | 61.77% |
| 200016.12-CP 200016.12 |                                       |          |               |             |     |        |
| Precinct TSx           | 301                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 301                                   | 1        | 26            | 23          | 15  | 65.22% |
| Precinct VBM           | 301                                   | 1        | 139           | 116         | 78  | 67.24% |
| MB Precinct            | 301                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 301                                   | 1        | 6             | 3           | 2   | 66.67% |
| Total                  | 301                                   | 5        | 171           | 142         | 95  | 66.90% |
| 200016.13-CP 200016.13 |                                       |          |               |             |     |        |
| Precinct TSx           | 565                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 565                                   | 1        | 80            | 66          | 37  | 56.06% |
| Precinct VBM           | 565                                   | 1        | 338           | 273         | 169 | 61.90% |
| MB Precinct            | 565                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 565                                   | 1        | 6             | 5           | 4   | 80.00% |
| Total                  | 565                                   | 5        | 424           | 344         | 210 | 61.05% |
| 200017.02-CP 200017.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1286                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1286                                  | 1        | 146           | 119         | 69  | 57.98% |
| Precinct VBM           | 1286                                  | 1        | 594           | 491         | 319 | 64.97% |
| MB Precinct            | 1286                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1286                                  | 1        | 12            | 9           | 8   | 88.89% |
| Total                  | 1286                                  | 5        | 752           | 619         | 396 | 63.97% |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| 200018.02-CP 200018.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1454                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1454                                  | 1        | 169           | 136         | 81  | 59.56% |
| Precinct VBM           | 1454                                  | 1        | 704           | 592         | 339 | 57.26% |
| MB Precinct            | 1454                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1454                                  | 1        | 13            | 9           | 6   | 66.67% |
| Total                  | 1454                                  | 5        | 886           | 737         | 426 | 57.80% |
| 200020.02-CP 200020.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1213                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1213                                  | 1        | 139           | 112         | 70  | 62.50% |
| Precinct VBM           | 1213                                  | 1        | 526           | 446         | 275 | 61.66% |
| MB Precinct            | 1213                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1213                                  | 1        | 19            | 15          | 7   | 46.67% |
| Total                  | 1213                                  | 5        | 684           | 573         | 352 | 61.43% |
| 200021.01-CP 200021.01 |                                       |          |               |             |     |        |
| Precinct TSx           | 436                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 436                                   | 1        | 38            | 27          | 15  | 55.56% |
| Precinct VBM           | 436                                   | 1        | 206           | 178         | 97  | 54.49% |
| MB Precinct            | 436                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 436                                   | 1        | 7             | 5           | 1   | 20.00% |
| Total                  | 436                                   | 5        | 251           | 210         | 113 | 53.81% |
| 200021.02-CP 200021.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 1296                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1296                                  | 1        | 170           | 143         | 79  | 55.24% |
| Precinct VBM           | 1296                                  | 1        | 752           | 585         | 352 | 60.17% |
| MB Precinct            | 1296                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1296                                  | 1        | 20            | 14          | 6   | 42.86% |
| Total                  | 1296                                  | 5        | 942           | 742         | 437 | 58.89% |
| 200024.02-CP 200024.02 |                                       |          |               |             |     |        |
| Precinct TSx           | 555                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 555                                   | 1        | 55            | 50          | 28  | 56.00% |
| Precinct VBM           | 555                                   | 1        | 282           | 252         | 145 | 57.54% |
| MB Precinct            | 555                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 555                                   | 1        | 9             | 7           | 6   | 85.71% |
| Total                  | 555                                   | 5        | 346           | 309         | 179 | 57.93% |
| 300001.04-CP 300001.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 259                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 259                                   | 1        | 12            | 10          | 7   | 70.00% |
| Precinct VBM           | 259                                   | 1        | 100           | 88          | 59  | 67.05% |
| MB Precinct            | 259                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 259                                   | 1        | 0             | 0           | 0   | -      |
| Total                  | 259                                   | 5        | 112           | 98          | 66  | 67.35% |
| 300001.05-CP 300001.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 731                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 731                                   | 1        | 76            | 63          | 43  | 68.25% |
| Precinct VBM           | 731                                   | 1        | 330           | 267         | 179 | 67.04% |
| MB Precinct            | 731                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 731                                   | 1        | 10            | 9           | 6   | 66.67% |
| Total                  | 731                                   | 5        | 416           | 339         | 228 | 67.26% |
| 300003.05-CP 300003.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1932                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1932                                  | 1        | 205           | 166         | 102 | 61.45% |
| Precinct VBM           | 1932                                  | 1        | 924           | 760         | 506 | 66.58% |
| MB Precinct            | 1932                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1932                                  | 1        | 27            | 18          | 12  | 66.67% |
| Total                  | 1932                                  | 5        | 1156          | 944         | 620 | 65.68% |
| 300004.05-CP 300004.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1273                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1273                                  | 1        | 175           | 132         | 76  | 57.58% |
| Precinct VBM           | 1273                                  | 1        | 576           | 475         | 304 | 64.00% |
| MB Precinct            | 1273                                  | 1        | 0             | 0           | 0   | -      |



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|------------------------|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Provisional            | 1273                                  | 1        | 17            | 11          | 2   | 18.18% |
| Total                  | 1273                                  | 5        | 768           | 618         | 382 | 61.81% |
| 300005.05-CP 300005.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1374                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1374                                  | 1        | 163           | 129         | 83  | 64.34% |
| Precinct VBM           | 1374                                  | 1        | 660           | 562         | 403 | 71.71% |
| MB Precinct            | 1374                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1374                                  | 1        | 26            | 17          | 6   | 35.29% |
| Total                  | 1374                                  | 5        | 849           | 708         | 492 | 69.49% |
| 300008.05-CP 300008.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1366                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1366                                  | 1        | 139           | 121         | 68  | 56.20% |
| Precinct VBM           | 1366                                  | 1        | 740           | 627         | 413 | 65.87% |
| MB Precinct            | 1366                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1366                                  | 1        | 14            | 10          | 4   | 40.00% |
| Total                  | 1366                                  | 5        | 893           | 758         | 485 | 63.98% |
| 300009.04-CP 300009.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 873                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 873                                   | 1        | 121           | 102         | 55  | 53.92% |
| Precinct VBM           | 873                                   | 1        | 395           | 327         | 197 | 60.24% |
| MB Precinct            | 873                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 873                                   | 1        | 13            | 10          | 6   | 60.00% |
| Total                  | 873                                   | 5        | 529           | 439         | 258 | 58.77% |
| 300010.04-CP 300010.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 1340                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1340                                  | 1        | 171           | 151         | 70  | 46.36% |
| Precinct VBM           | 1340                                  | 1        | 656           | 563         | 362 | 64.30% |
| MB Precinct            | 1340                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1340                                  | 1        | 11            | 9           | 6   | 66.67% |
| Total                  | 1340                                  | 5        | 838           | 723         | 438 | 60.58% |
| 300011.04-CP 300011.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 1491                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1491                                  | 1        | 142           | 126         | 78  | 61.90% |
| Precinct VBM           | 1491                                  | 1        | 711           | 602         | 367 | 60.96% |
| MB Precinct            | 1491                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1491                                  | 1        | 22            | 15          | 11  | 73.33% |
| Total                  | 1491                                  | 5        | 875           | 743         | 456 | 61.37% |
| 300013.05-CP 300013.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1490                                  | 1        | 3             | 2           | 1   | 50.00% |
| Precinct OS            | 1490                                  | 1        | 128           | 111         | 57  | 51.35% |
| Precinct VBM           | 1490                                  | 1        | 721           | 630         | 421 | 66.83% |
| MB Precinct            | 1490                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1490                                  | 1        | 15            | 9           | 4   | 44.44% |
| Total                  | 1490                                  | 5        | 867           | 752         | 483 | 64.23% |
| 300015.04-CP 300015.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 119                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 119                                   | 1        | 10            | 10          | 5   | 50.00% |
| Precinct VBM           | 119                                   | 1        | 63            | 48          | 34  | 70.83% |
| MB Precinct            | 119                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 119                                   | 1        | 4             | 3           | 2   | 66.67% |
| Total                  | 119                                   | 5        | 77            | 61          | 41  | 67.21% |
| 300015.05-CP 300015.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 503                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 503                                   | 1        | 66            | 52          | 32  | 61.54% |
| Precinct VBM           | 503                                   | 1        | 266           | 223         | 149 | 66.82% |
| MB Precinct            | 503                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 503                                   | 1        | 2             | 1           | 0   | 0.00%  |
| Total                  | 503                                   | 5        | 334           | 276         | 181 | 65.58% |
| 300017.05-CP 300017.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 1240                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1240                                  | 1        | 117           | 96          | 55  | 57.29% |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Precinct VBM           | 1240                                  | 1        | 655           | 554         | 369 | 66.61% |
| MB Precinct            | 1240                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1240                                  | 1        | 9             | 7           | 2   | 28.57% |
| Total                  | 1240                                  | 5        | 781           | 657         | 426 | 64.84% |
| 300018.04-CP 300018.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 1306                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1306                                  | 1        | 148           | 122         | 69  | 56.56% |
| Precinct VBM           | 1306                                  | 1        | 505           | 424         | 252 | 59.43% |
| MB Precinct            | 1306                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1306                                  | 1        | 31            | 21          | 15  | 71.43% |
| Total                  | 1306                                  | 5        | 684           | 567         | 336 | 59.26% |
| 300020.04-CP 300020.04 |                                       |          |               |             |     |        |
| Precinct TSx           | 1401                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1401                                  | 1        | 168           | 149         | 83  | 55.70% |
| Precinct VBM           | 1401                                  | 1        | 632           | 515         | 326 | 63.30% |
| MB Precinct            | 1401                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1401                                  | 1        | 14            | 10          | 5   | 50.00% |
| Total                  | 1401                                  | 5        | 814           | 674         | 414 | 61.42% |
| 300021.03-CP 300021.03 |                                       |          |               |             |     |        |
| Precinct TSx           | 1234                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1234                                  | 1        | 153           | 120         | 70  | 58.33% |
| Precinct VBM           | 1234                                  | 1        | 614           | 522         | 341 | 65.33% |
| MB Precinct            | 1234                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1234                                  | 1        | 13            | 11          | 8   | 72.73% |
| Total                  | 1234                                  | 5        | 780           | 653         | 419 | 64.17% |
| 300022.05-CP 300022.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 872                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 872                                   | 1        | 101           | 83          | 43  | 51.81% |
| Precinct VBM           | 872                                   | 1        | 356           | 295         | 197 | 66.78% |
| MB Precinct            | 872                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 872                                   | 1        | 12            | 10          | 5   | 50.00% |
| Total                  | 872                                   | 5        | 469           | 388         | 245 | 63.14% |
| 300023.03-CP 300023.03 |                                       |          |               |             |     |        |
| Precinct TSx           | 1819                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1819                                  | 1        | 228           | 196         | 123 | 62.76% |
| Precinct VBM           | 1819                                  | 1        | 924           | 768         | 505 | 65.76% |
| MB Precinct            | 1819                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1819                                  | 1        | 26            | 19          | 12  | 63.16% |
| Total                  | 1819                                  | 5        | 1178          | 983         | 640 | 65.11% |
| 300023.05-CP 300023.05 |                                       |          |               |             |     |        |
| Precinct TSx           | 386                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 386                                   | 1        | 45            | 36          | 22  | 61.11% |
| Precinct VBM           | 386                                   | 1        | 193           | 170         | 113 | 66.47% |
| MB Precinct            | 386                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 386                                   | 1        | 8             | 4           | 3   | 75.00% |
| Total                  | 386                                   | 5        | 246           | 210         | 138 | 65.71% |
| 400001.14-CP 400001.14 |                                       |          |               |             |     |        |
| Precinct TSx           | 1957                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1957                                  | 1        | 178           | 138         | 78  | 56.52% |
| Precinct VBM           | 1957                                  | 1        | 992           | 820         | 534 | 65.12% |
| MB Precinct            | 1957                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1957                                  | 1        | 23            | 20          | 14  | 70.00% |
| Total                  | 1957                                  | 5        | 1193          | 978         | 626 | 64.01% |
| 400002.08-CP 400002.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1508                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1508                                  | 1        | 200           | 167         | 96  | 57.49% |
| Precinct VBM           | 1508                                  | 1        | 730           | 604         | 379 | 62.75% |
| MB Precinct            | 1508                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1508                                  | 1        | 15            | 13          | 8   | 61.54% |
| Total                  | 1508                                  | 5        | 945           | 784         | 483 | 61.61% |
| 400003.08-CP 400003.08 |                                       |          |               |             |     |        |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| Precinct TSx           | 1800                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1800                                  | 1        | 245           | 205         | 101 | 49.27% |
| Precinct VBM           | 1800                                  | 1        | 773           | 650         | 393 | 60.46% |
| MB Precinct            | 1800                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1800                                  | 1        | 35            | 24          | 10  | 41.67% |
| Total                  | 1800                                  | 5        | 1053          | 879         | 504 | 57.34% |
| 400004.14-CP 400004.14 |                                       |          |               |             |     |        |
| Precinct TSx           | 1536                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1536                                  | 1        | 194           | 152         | 79  | 51.97% |
| Precinct VBM           | 1536                                  | 1        | 662           | 574         | 348 | 60.63% |
| MB Precinct            | 1536                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1536                                  | 1        | 19            | 14          | 9   | 64.29% |
| Total                  | 1536                                  | 5        | 875           | 740         | 436 | 58.92% |
| 400005.08-CP 400005.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 655                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 655                                   | 1        | 79            | 62          | 35  | 56.45% |
| Precinct VBM           | 655                                   | 1        | 262           | 211         | 141 | 66.82% |
| MB Precinct            | 655                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 655                                   | 1        | 11            | 9           | 2   | 22.22% |
| Total                  | 655                                   | 5        | 352           | 282         | 178 | 63.12% |
| 400006.08-CP 400006.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1537                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1537                                  | 1        | 228           | 194         | 123 | 63.40% |
| Precinct VBM           | 1537                                  | 1        | 722           | 591         | 397 | 67.17% |
| MB Precinct            | 1537                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1537                                  | 1        | 19            | 14          | 7   | 50.00% |
| Total                  | 1537                                  | 5        | 969           | 799         | 527 | 65.96% |
| 400007.08-CP 400007.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1766                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1766                                  | 1        | 228           | 204         | 127 | 62.25% |
| Precinct VBM           | 1766                                  | 1        | 765           | 658         | 390 | 59.27% |
| MB Precinct            | 1766                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1766                                  | 1        | 22            | 18          | 10  | 55.56% |
| Total                  | 1766                                  | 5        | 1015          | 880         | 527 | 59.89% |
| 400010.08-CP 400010.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1875                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1875                                  | 1        | 244           | 212         | 106 | 50.00% |
| Precinct VBM           | 1875                                  | 1        | 869           | 733         | 426 | 58.12% |
| MB Precinct            | 1875                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1875                                  | 1        | 31            | 21          | 11  | 52.38% |
| Total                  | 1875                                  | 5        | 1144          | 966         | 543 | 56.21% |
| 400012.08-CP 400012.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 877                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 877                                   | 1        | 113           | 96          | 59  | 61.46% |
| Precinct VBM           | 877                                   | 1        | 434           | 359         | 212 | 59.05% |
| MB Precinct            | 877                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 877                                   | 1        | 22            | 14          | 8   | 57.14% |
| Total                  | 877                                   | 5        | 569           | 469         | 279 | 59.49% |
| 400013.08-CP 400013.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 758                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 758                                   | 1        | 127           | 103         | 53  | 51.46% |
| Precinct VBM           | 758                                   | 1        | 315           | 266         | 175 | 65.79% |
| MB Precinct            | 758                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 758                                   | 1        | 4             | 4           | 3   | 75.00% |
| Total                  | 758                                   | 5        | 446           | 373         | 231 | 61.93% |
| 400014.08-CP 400014.08 |                                       |          |               |             |     |        |
| Precinct TSx           | 1526                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1526                                  | 1        | 230           | 184         | 106 | 57.61% |
| Precinct VBM           | 1526                                  | 1        | 736           | 626         | 409 | 65.34% |
| MB Precinct            | 1526                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1526                                  | 1        | 36            | 30          | 14  | 46.67% |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |         |
| Total                  | 1526                                  | 5        | 1002          | 840         | 529 | 62.98%  |
| 400017.08-CP 400017.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 1495                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1495                                  | 1        | 175           | 142         | 76  | 53.52%  |
| Precinct VBM           | 1495                                  | 1        | 752           | 639         | 353 | 55.24%  |
| MB Precinct            | 1495                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1495                                  | 1        | 28            | 23          | 9   | 39.13%  |
| Total                  | 1495                                  | 5        | 955           | 804         | 438 | 54.48%  |
| 400018.08-CP 400018.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 597                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 597                                   | 1        | 86            | 61          | 42  | 68.85%  |
| Precinct VBM           | 597                                   | 1        | 276           | 238         | 133 | 55.88%  |
| MB Precinct            | 597                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 597                                   | 1        | 7             | 4           | 2   | 50.00%  |
| Total                  | 597                                   | 5        | 369           | 303         | 177 | 58.42%  |
| 400020.08-CP 400020.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 724                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 724                                   | 1        | 114           | 100         | 64  | 64.00%  |
| Precinct VBM           | 724                                   | 1        | 343           | 290         | 171 | 58.97%  |
| MB Precinct            | 724                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 724                                   | 1        | 2             | 2           | 2   | 100.00% |
| Total                  | 724                                   | 5        | 459           | 392         | 237 | 60.46%  |
| 400021.07-CP 400021.07 |                                       |          |               |             |     |         |
| Precinct TSx           | 1332                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1332                                  | 1        | 127           | 113         | 61  | 53.98%  |
| Precinct VBM           | 1332                                  | 1        | 767           | 642         | 400 | 62.31%  |
| MB Precinct            | 1332                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1332                                  | 1        | 18            | 11          | 6   | 54.55%  |
| Total                  | 1332                                  | 5        | 912           | 766         | 467 | 60.97%  |
| 500001.10-CP 500001.10 |                                       |          |               |             |     |         |
| Precinct TSx           | 1717                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1717                                  | 1        | 210           | 158         | 107 | 67.72%  |
| Precinct VBM           | 1717                                  | 1        | 617           | 489         | 336 | 68.71%  |
| MB Precinct            | 1717                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1717                                  | 1        | 19            | 11          | 9   | 81.82%  |
| Total                  | 1717                                  | 5        | 846           | 658         | 452 | 68.69%  |
| 500002.10-CP 500002.10 |                                       |          |               |             |     |         |
| Precinct TSx           | 1381                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1381                                  | 1        | 137           | 101         | 65  | 64.36%  |
| Precinct VBM           | 1381                                  | 1        | 412           | 330         | 214 | 64.85%  |
| MB Precinct            | 1381                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1381                                  | 1        | 31            | 23          | 10  | 43.48%  |
| Total                  | 1381                                  | 5        | 580           | 454         | 289 | 63.66%  |
| 500003.10-CP 500003.10 |                                       |          |               |             |     |         |
| Precinct TSx           | 1065                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1065                                  | 1        | 131           | 102         | 66  | 64.71%  |
| Precinct VBM           | 1065                                  | 1        | 346           | 264         | 174 | 65.91%  |
| MB Precinct            | 1065                                  | 1        | 0             | 0           | 0   | -       |
| Provisional            | 1065                                  | 1        | 11            | 4           | 1   | 25.00%  |
| Total                  | 1065                                  | 5        | 488           | 370         | 241 | 65.14%  |
| 500004.10-CP 500004.10 |                                       |          |               |             |     |         |
| Precinct TSx           | 981                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 981                                   | 1        | 122           | 105         | 54  | 51.43%  |
| Precinct VBM           | 981                                   | 1        | 264           | 208         | 136 | 65.38%  |
| MB Precinct            | 981                                   | 1        | 0             | 0           | 0   | -       |
| Provisional            | 981                                   | 1        | 21            | 17          | 12  | 70.59%  |
| Total                  | 981                                   | 5        | 407           | 330         | 202 | 61.21%  |
| 500005.10-CP 500005.10 |                                       |          |               |             |     |         |
| Precinct TSx           | 946                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 946                                   | 1        | 107           | 85          | 54  | 63.53%  |
| Precinct VBM           | 946                                   | 1        | 250           | 202         | 134 | 66.34%  |

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|------------------------|---------------------------------------|----------|---------------|-------------|-----|--------|
|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |        |
| MB Precinct            | 946                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 946                                   | 1        | 16            | 10          | 7   | 70.00% |
| Total                  | 946                                   | 5        | 373           | 297         | 195 | 65.66% |
| 500006.10-CP 500006.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1661                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1661                                  | 1        | 190           | 138         | 81  | 58.70% |
| Precinct VBM           | 1661                                  | 1        | 498           | 382         | 246 | 64.40% |
| MB Precinct            | 1661                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1661                                  | 1        | 58            | 29          | 20  | 68.97% |
| Total                  | 1661                                  | 5        | 746           | 549         | 347 | 63.21% |
| 500007.10-CP 500007.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1788                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1788                                  | 1        | 208           | 168         | 110 | 65.48% |
| Precinct VBM           | 1788                                  | 1        | 688           | 543         | 374 | 68.88% |
| MB Precinct            | 1788                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1788                                  | 1        | 20            | 12          | 6   | 50.00% |
| Total                  | 1788                                  | 5        | 916           | 723         | 490 | 67.77% |
| 500009.10-CP 500009.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1073                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1073                                  | 1        | 121           | 95          | 57  | 60.00% |
| Precinct VBM           | 1073                                  | 1        | 304           | 232         | 156 | 67.24% |
| MB Precinct            | 1073                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1073                                  | 1        | 38            | 24          | 10  | 41.67% |
| Total                  | 1073                                  | 5        | 463           | 351         | 223 | 63.53% |
| 500011.10-CP 500011.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1635                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1635                                  | 1        | 178           | 142         | 79  | 55.63% |
| Precinct VBM           | 1635                                  | 1        | 380           | 292         | 192 | 65.75% |
| MB Precinct            | 1635                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1635                                  | 1        | 40            | 28          | 17  | 60.71% |
| Total                  | 1635                                  | 5        | 598           | 462         | 288 | 62.34% |
| 500012.10-CP 500012.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1574                                  | 1        | 1             | 1           | 0   | 0.00%  |
| Precinct OS            | 1574                                  | 1        | 195           | 128         | 75  | 58.59% |
| Precinct VBM           | 1574                                  | 1        | 601           | 500         | 304 | 60.80% |
| MB Precinct            | 1574                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1574                                  | 1        | 37            | 22          | 14  | 63.64% |
| Total                  | 1574                                  | 5        | 834           | 651         | 393 | 60.37% |
| 500015.10-CP 500015.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 978                                   | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 978                                   | 1        | 105           | 85          | 50  | 58.82% |
| Precinct VBM           | 978                                   | 1        | 407           | 332         | 227 | 68.37% |
| MB Precinct            | 978                                   | 1        | 0             | 0           | 0   | -      |
| Provisional            | 978                                   | 1        | 13            | 11          | 7   | 63.64% |
| Total                  | 978                                   | 5        | 525           | 428         | 284 | 66.36% |
| 500020.10-CP 500020.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1530                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1530                                  | 1        | 161           | 135         | 79  | 58.52% |
| Precinct VBM           | 1530                                  | 1        | 659           | 578         | 347 | 60.03% |
| MB Precinct            | 1530                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1530                                  | 1        | 27            | 25          | 17  | 68.00% |
| Total                  | 1530                                  | 5        | 847           | 738         | 443 | 60.03% |
| 500021.10-CP 500021.10 |                                       |          |               |             |     |        |
| Precinct TSx           | 1404                                  | 1        | 0             | 0           | 0   | -      |
| Precinct OS            | 1404                                  | 1        | 169           | 130         | 77  | 59.23% |
| Precinct VBM           | 1404                                  | 1        | 632           | 557         | 351 | 63.02% |
| MB Precinct            | 1404                                  | 1        | 0             | 0           | 0   | -      |
| Provisional            | 1404                                  | 1        | 28            | 21          | 15  | 71.43% |
| Total                  | 1404                                  | 5        | 829           | 708         | 443 | 62.57% |
| 95U201.01-MB 95U201.01 |                                       |          |               |             |     |        |
| Precinct TSx           | 8                                     | 1        | 0             | 0           | 0   | -      |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |         |
| Precinct OS            | 8                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 8                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 8                                     | 1        | 7             | 7           | 7   | 100.00% |
| Provisional            | 8                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 8                                     | 5        | 7             | 7           | 7   | 100.00% |
| 95U201.02-MB 95U201.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 541                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 541                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 541                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 541                                   | 1        | 358           | 290         | 176 | 60.69%  |
| Provisional            | 541                                   | 1        | 1             | 0           | 0   | -       |
| Total                  | 541                                   | 5        | 359           | 290         | 176 | 60.69%  |
| 95U202.02-MB 95U202.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 859                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 859                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 859                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 859                                   | 1        | 574           | 481         | 308 | 64.03%  |
| Provisional            | 859                                   | 1        | 4             | 3           | 1   | 33.33%  |
| Total                  | 859                                   | 5        | 578           | 484         | 309 | 63.84%  |
| 95U203.02-MB 95U203.02 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                                     | 5        | 0             | 0           | 0   | -       |
| 95U204.01-MB 95U204.01 |                                       |          |               |             |     |         |
| Precinct TSx           | 895                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 895                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 895                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 895                                   | 1        | 651           | 574         | 342 | 59.58%  |
| Provisional            | 895                                   | 1        | 2             | 0           | 0   | -       |
| Total                  | 895                                   | 5        | 653           | 574         | 342 | 59.58%  |
| 95U301.05-MB 95U301.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 244                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 244                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 244                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 244                                   | 1        | 151           | 131         | 77  | 58.78%  |
| Provisional            | 244                                   | 1        | 2             | 1           | 1   | 100.00% |
| Total                  | 244                                   | 5        | 153           | 132         | 78  | 59.09%  |
| 95U302.04-MB 95U302.04 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 1             | 0           | 0   | -       |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                                     | 5        | 1             | 0           | 0   | -       |
| 95U302.05-MB 95U302.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 128                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 128                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 128                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 128                                   | 1        | 86            | 77          | 52  | 67.53%  |
| Provisional            | 128                                   | 1        | 0             | 0           | 0   | -       |
| Total                  | 128                                   | 5        | 86            | 77          | 52  | 67.53%  |
| 95U303.03-MB 95U303.03 |                                       |          |               |             |     |         |
| Precinct TSx           | 144                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 144                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 144                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 144                                   | 1        | 84            | 75          | 40  | 53.33%  |
| Provisional            | 144                                   | 1        | 0             | 0           | 0   | -       |
| Total                  | 144                                   | 5        | 84            | 75          | 40  | 53.33%  |

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|                        | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES |         |
| 95U303.05-MB 95U303.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 159                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 159                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 159                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 159                                   | 1        | 111           | 88          | 48  | 54.55%  |
| Provisional            | 159                                   | 1        | 1             | 1           | 0   | 0.00%   |
| Total                  | 159                                   | 5        | 112           | 89          | 48  | 53.93%  |
| 95U304.05-MB 95U304.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                                     | 5        | 0             | 0           | 0   | -       |
| 95U305.05-MB 95U305.05 |                                       |          |               |             |     |         |
| Precinct TSx           | 7                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 7                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 7                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 7                                     | 1        | 7             | 6           | 4   | 66.67%  |
| Provisional            | 7                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 7                                     | 5        | 7             | 6           | 4   | 66.67%  |
| 95U401.08-MB 95U401.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 39                                    | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 39                                    | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 39                                    | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 39                                    | 1        | 16            | 11          | 8   | 72.73%  |
| Provisional            | 39                                    | 1        | 0             | 0           | 0   | -       |
| Total                  | 39                                    | 5        | 16            | 11          | 8   | 72.73%  |
| 95U402.07-MB 95U402.07 |                                       |          |               |             |     |         |
| Precinct TSx           | 413                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 413                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 413                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 413                                   | 1        | 256           | 205         | 134 | 65.37%  |
| Provisional            | 413                                   | 1        | 1             | 1           | 1   | 100.00% |
| Total                  | 413                                   | 5        | 257           | 206         | 135 | 65.53%  |
| 95U402.08-MB 95U402.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 873                                   | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 873                                   | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 873                                   | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 873                                   | 1        | 566           | 481         | 299 | 62.16%  |
| Provisional            | 873                                   | 1        | 3             | 2           | 1   | 50.00%  |
| Total                  | 873                                   | 5        | 569           | 483         | 300 | 62.11%  |
| 95U403.08-MB 95U403.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 1591                                  | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 1591                                  | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 1591                                  | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 1591                                  | 1        | 1042          | 876         | 534 | 60.96%  |
| Provisional            | 1591                                  | 1        | 13            | 10          | 6   | 60.00%  |
| Total                  | 1591                                  | 5        | 1055          | 886         | 540 | 60.95%  |
| 95U404.06-MB 95U404.06 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Provisional            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Total                  | 0                                     | 5        | 0             | 0           | 0   | -       |
| 95U404.08-MB 95U404.08 |                                       |          |               |             |     |         |
| Precinct TSx           | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct OS            | 0                                     | 1        | 0             | 0           | 0   | -       |
| Precinct VBM           | 0                                     | 1        | 0             | 0           | 0   | -       |
| MB Precinct            | 0                                     | 1        | 0             | 0           | 0   | -       |

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|-------------------------------|---------------------------------------|----------|---------------|-------------|-------|---------|
|                               | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES   |         |
| Provisional                   | 0                                     | 1        | 0             | 0           | 0     | -       |
| Total                         | 0                                     | 5        | 0             | 0           | 0     | -       |
| 95U501.09-MB 95U501.09        |                                       |          |               |             |       |         |
| Precinct TSx                  | 0                                     | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 0                                     | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 0                                     | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 0                                     | 1        | 2             | 2           | 2     | 100.00% |
| Provisional                   | 0                                     | 1        | 0             | 0           | 0     | -       |
| Total                         | 0                                     | 5        | 2             | 2           | 2     | 100.00% |
| 95U501.10-MB 95U501.10        |                                       |          |               |             |       |         |
| Precinct TSx                  | 286                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 286                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 286                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 286                                   | 1        | 172           | 149         | 102   | 68.46%  |
| Provisional                   | 286                                   | 1        | 2             | 2           | 1     | 50.00%  |
| Total                         | 286                                   | 5        | 174           | 151         | 103   | 68.21%  |
| 95U598.10-MB 95U598.10        |                                       |          |               |             |       |         |
| Precinct TSx                  | 568                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 568                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 568                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 568                                   | 1        | 239           | 171         | 123   | 71.93%  |
| Provisional                   | 568                                   | 1        | 3             | 1           | 0     | 0.00%   |
| Total                         | 568                                   | 5        | 242           | 172         | 123   | 71.51%  |
| 95U599.10-MB 95U599.10        |                                       |          |               |             |       |         |
| Precinct TSx                  | 75                                    | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 75                                    | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 75                                    | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 75                                    | 1        | 38            | 28          | 23    | 82.14%  |
| Provisional                   | 75                                    | 1        | 0             | 0           | 0     | -       |
| Total                         | 75                                    | 5        | 38            | 28          | 23    | 82.14%  |
| 96U101.11-MB 96U101.11        |                                       |          |               |             |       |         |
| Precinct TSx                  | 626                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 626                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 626                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 626                                   | 1        | 423           | 337         | 219   | 64.99%  |
| Provisional                   | 626                                   | 1        | 6             | 5           | 1     | 20.00%  |
| Total                         | 626                                   | 5        | 429           | 342         | 220   | 64.33%  |
| 96U401.14-MB 96U401.14        |                                       |          |               |             |       |         |
| Precinct TSx                  | 566                                   | 1        | 0             | 0           | 0     | -       |
| Precinct OS                   | 566                                   | 1        | 0             | 0           | 0     | -       |
| Precinct VBM                  | 566                                   | 1        | 0             | 0           | 0     | -       |
| MB Precinct                   | 566                                   | 1        | 359           | 275         | 147   | 53.45%  |
| Provisional                   | 566                                   | 1        | 6             | 6           | 5     | 83.33%  |
| Total                         | 566                                   | 5        | 365           | 281         | 152   | 54.09%  |
| Total                         |                                       |          |               |             |       |         |
| Precinct TSx                  | 109479                                | 102      | 8             | 7           | 4     | 57.14%  |
| Precinct OS                   | 109479                                | 102      | 12009         | 9758        | 5738  | 58.80%  |
| Precinct VBM                  | 109479                                | 102      | 45535         | 38030       | 24621 | 64.74%  |
| MB Precinct                   | 109479                                | 102      | 5143          | 4264        | 2645  | 62.03%  |
| Provisional                   | 109479                                | 102      | 1584          | 1135        | 657   | 57.89%  |
| Total                         | 109479                                | 510      | 64279         | 53194       | 33665 | 63.29%  |
| STATE BOARD OF EQUALIZATION   |                                       |          |               |             |       |         |
| STATE BOARD OF EQUALIZATION 1 |                                       |          |               |             |       |         |
| Precinct TSx                  | 109479                                | 102      | 8             | 7           | 4     | 57.14%  |
| Precinct OS                   | 109479                                | 102      | 12009         | 9758        | 5738  | 58.80%  |
| Precinct VBM                  | 109479                                | 102      | 45535         | 38030       | 24621 | 64.74%  |
| MB Precinct                   | 109479                                | 102      | 5143          | 4264        | 2645  | 62.03%  |
| Provisional                   | 109479                                | 102      | 1584          | 1135        | 657   | 57.89%  |
| Total                         | 109479                                | 510      | 64279         | 53194       | 33665 | 63.29%  |
| Total                         |                                       |          |               |             |       |         |



**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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|                                     | Measure J - County (majority to pass) |            |               |              |              |               |
|-------------------------------------|---------------------------------------|------------|---------------|--------------|--------------|---------------|
|                                     | Reg. Voters                           | Vote For   | Times Counted | Total Votes  | YES          |               |
| Precinct TSx                        | 109479                                | 102        | 8             | 7            | 4            | 57.14%        |
| Precinct OS                         | 109479                                | 102        | 12009         | 9758         | 5738         | 58.80%        |
| Precinct VBM                        | 109479                                | 102        | 45535         | 38030        | 24621        | 64.74%        |
| MB Precinct                         | 109479                                | 102        | 5143          | 4264         | 2645         | 62.03%        |
| Provisional                         | 109479                                | 102        | 1584          | 1135         | 657          | 57.89%        |
| <b>Total</b>                        | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53194</b> | <b>33665</b> | <b>63.29%</b> |
| <b>UNITED STATES SENATE</b>         |                                       |            |               |              |              |               |
| <b>UNITED STATES SENATE</b>         |                                       |            |               |              |              |               |
| Precinct TSx                        | 109479                                | 102        | 8             | 7            | 4            | 57.14%        |
| Precinct OS                         | 109479                                | 102        | 12009         | 9758         | 5738         | 58.80%        |
| Precinct VBM                        | 109479                                | 102        | 45535         | 38030        | 24621        | 64.74%        |
| MB Precinct                         | 109479                                | 102        | 5143          | 4264         | 2645         | 62.03%        |
| Provisional                         | 109479                                | 102        | 1584          | 1135         | 657          | 57.89%        |
| <b>Total</b>                        | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53194</b> | <b>33665</b> | <b>63.29%</b> |
| <b>Total</b>                        |                                       |            |               |              |              |               |
| Precinct TSx                        | 109479                                | 102        | 8             | 7            | 4            | 57.14%        |
| Precinct OS                         | 109479                                | 102        | 12009         | 9758         | 5738         | 58.80%        |
| Precinct VBM                        | 109479                                | 102        | 45535         | 38030        | 24621        | 64.74%        |
| MB Precinct                         | 109479                                | 102        | 5143          | 4264         | 2645         | 62.03%        |
| Provisional                         | 109479                                | 102        | 1584          | 1135         | 657          | 57.89%        |
| <b>Total</b>                        | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53194</b> | <b>33665</b> | <b>63.29%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |                                       |            |               |              |              |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |                                       |            |               |              |              |               |
| Precinct TSx                        | 109479                                | 102        | 8             | 7            | 4            | 57.14%        |
| Precinct OS                         | 109479                                | 102        | 12009         | 9758         | 5738         | 58.80%        |
| Precinct VBM                        | 109479                                | 102        | 45535         | 38030        | 24621        | 64.74%        |
| MB Precinct                         | 109479                                | 102        | 5143          | 4264         | 2645         | 62.03%        |
| Provisional                         | 109479                                | 102        | 1584          | 1135         | 657          | 57.89%        |
| <b>Total</b>                        | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53194</b> | <b>33665</b> | <b>63.29%</b> |
| <b>Total</b>                        |                                       |            |               |              |              |               |
| Precinct TSx                        | 109479                                | 102        | 8             | 7            | 4            | 57.14%        |
| Precinct OS                         | 109479                                | 102        | 12009         | 9758         | 5738         | 58.80%        |
| Precinct VBM                        | 109479                                | 102        | 45535         | 38030        | 24621        | 64.74%        |
| MB Precinct                         | 109479                                | 102        | 5143          | 4264         | 2645         | 62.03%        |
| Provisional                         | 109479                                | 102        | 1584          | 1135         | 657          | 57.89%        |
| <b>Total</b>                        | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53194</b> | <b>33665</b> | <b>63.29%</b> |
| <b>STATE SENATE</b>                 |                                       |            |               |              |              |               |
| <b>1ST STATE SENATE DISTRICT</b>    |                                       |            |               |              |              |               |
| Precinct TSx                        | 109479                                | 102        | 8             | 7            | 4            | 57.14%        |
| Precinct OS                         | 109479                                | 102        | 12009         | 9758         | 5738         | 58.80%        |
| Precinct VBM                        | 109479                                | 102        | 45535         | 38030        | 24621        | 64.74%        |
| MB Precinct                         | 109479                                | 102        | 5143          | 4264         | 2645         | 62.03%        |
| Provisional                         | 109479                                | 102        | 1584          | 1135         | 657          | 57.89%        |
| <b>Total</b>                        | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53194</b> | <b>33665</b> | <b>63.29%</b> |
| <b>Total</b>                        |                                       |            |               |              |              |               |
| Precinct TSx                        | 109479                                | 102        | 8             | 7            | 4            | 57.14%        |
| Precinct OS                         | 109479                                | 102        | 12009         | 9758         | 5738         | 58.80%        |
| Precinct VBM                        | 109479                                | 102        | 45535         | 38030        | 24621        | 64.74%        |
| MB Precinct                         | 109479                                | 102        | 5143          | 4264         | 2645         | 62.03%        |
| Provisional                         | 109479                                | 102        | 1584          | 1135         | 657          | 57.89%        |
| <b>Total</b>                        | <b>109479</b>                         | <b>510</b> | <b>64279</b>  | <b>53194</b> | <b>33665</b> | <b>63.29%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |                                       |            |               |              |              |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |                                       |            |               |              |              |               |
| Precinct TSx                        | 70961                                 | 75         | 4             | 3            | 1            | 33.33%        |
| Precinct OS                         | 70961                                 | 75         | 7627          | 6237         | 3626         | 58.14%        |
| Precinct VBM                        | 70961                                 | 75         | 28225         | 23508        | 14877        | 63.28%        |
| MB Precinct                         | 70961                                 | 75         | 4361          | 3652         | 2279         | 62.40%        |

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|                                   |  | Measure J - County (majority to pass) |          |               |             |       |        |  |
|-----------------------------------|--|---------------------------------------|----------|---------------|-------------|-------|--------|--|
|                                   |  | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES   |        |  |
| Provisional                       |  | 70961                                 | 75       | 1038          | 732         | 409   | 55.87% |  |
| Total                             |  | 70961                                 | 375      | 41255         | 34132       | 21192 | 62.09% |  |
| <b>6TH STATE ASSEMBLY DIST</b>    |  |                                       |          |               |             |       |        |  |
| Precinct TSx                      |  | 38518                                 | 27       | 4             | 4           | 3     | 75.00% |  |
| Precinct OS                       |  | 38518                                 | 27       | 4382          | 3521        | 2112  | 59.98% |  |
| Precinct VBM                      |  | 38518                                 | 27       | 17310         | 14522       | 9744  | 67.10% |  |
| MB Precinct                       |  | 38518                                 | 27       | 782           | 612         | 366   | 59.80% |  |
| Provisional                       |  | 38518                                 | 27       | 546           | 403         | 248   | 61.54% |  |
| Total                             |  | 38518                                 | 135      | 23024         | 19062       | 12473 | 65.43% |  |
| <b>Total</b>                      |  |                                       |          |               |             |       |        |  |
| Precinct TSx                      |  | 109479                                | 102      | 8             | 7           | 4     | 57.14% |  |
| Precinct OS                       |  | 109479                                | 102      | 12009         | 9758        | 5738  | 58.80% |  |
| Precinct VBM                      |  | 109479                                | 102      | 45535         | 38030       | 24621 | 64.74% |  |
| MB Precinct                       |  | 109479                                | 102      | 5143          | 4264        | 2645  | 62.03% |  |
| Provisional                       |  | 109479                                | 102      | 1584          | 1135        | 657   | 57.89% |  |
| Total                             |  | 109479                                | 510      | 64279         | 53194       | 33665 | 63.29% |  |
| <b>SUPERVISORIAL DISTRICTS</b>    |  |                                       |          |               |             |       |        |  |
| <b>1ST SUPERVISORIAL DISTRICT</b> |  |                                       |          |               |             |       |        |  |
| Precinct TSx                      |  | 22587                                 | 14       | 0             | 0           | 0     | -      |  |
| Precinct OS                       |  | 22587                                 | 14       | 2530          | 2018        | 1217  | 60.31% |  |
| Precinct VBM                      |  | 22587                                 | 14       | 10411         | 8715        | 5909  | 67.80% |  |
| MB Precinct                       |  | 22587                                 | 14       | 423           | 337         | 219   | 64.99% |  |
| Provisional                       |  | 22587                                 | 14       | 301           | 213         | 128   | 60.09% |  |
| Total                             |  | 22587                                 | 70       | 13665         | 11283       | 7473  | 66.23% |  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |  |                                       |          |               |             |       |        |  |
| Precinct TSx                      |  | 23114                                 | 23       | 4             | 4           | 3     | 75.00% |  |
| Precinct OS                       |  | 23114                                 | 23       | 2509          | 2060        | 1220  | 59.22% |  |
| Precinct VBM                      |  | 23114                                 | 23       | 9647          | 8085        | 5163  | 63.86% |  |
| MB Precinct                       |  | 23114                                 | 23       | 1590          | 1352        | 833   | 61.61% |  |
| Provisional                       |  | 23114                                 | 23       | 327           | 246         | 145   | 58.94% |  |
| Total                             |  | 23114                                 | 115      | 14077         | 11747       | 7364  | 62.69% |  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |  |                                       |          |               |             |       |        |  |
| Precinct TSx                      |  | 21691                                 | 26       | 3             | 2           | 1     | 50.00% |  |
| Precinct OS                       |  | 21691                                 | 26       | 2368          | 1975        | 1141  | 57.77% |  |
| Precinct VBM                      |  | 21691                                 | 26       | 10021         | 8420        | 5497  | 65.29% |  |
| MB Precinct                       |  | 21691                                 | 26       | 440           | 377         | 221   | 58.62% |  |
| Provisional                       |  | 21691                                 | 26       | 277           | 196         | 110   | 56.12% |  |
| Total                             |  | 21691                                 | 130      | 13109         | 10970       | 6970  | 63.54% |  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |  |                                       |          |               |             |       |        |  |
| Precinct TSx                      |  | 23425                                 | 22       | 0             | 0           | 0     | -      |  |
| Precinct OS                       |  | 23425                                 | 22       | 2568          | 2133        | 1206  | 56.54% |  |
| Precinct VBM                      |  | 23425                                 | 22       | 9398          | 7901        | 4861  | 61.52% |  |
| MB Precinct                       |  | 23425                                 | 22       | 2239          | 1848        | 1122  | 60.71% |  |
| Provisional                       |  | 23425                                 | 22       | 315           | 240         | 128   | 53.33% |  |
| Total                             |  | 23425                                 | 110      | 14520         | 12122       | 7317  | 60.36% |  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |  |                                       |          |               |             |       |        |  |
| Precinct TSx                      |  | 18662                                 | 17       | 1             | 1           | 0     | 0.00%  |  |
| Precinct OS                       |  | 18662                                 | 17       | 2034          | 1572        | 954   | 60.69% |  |
| Precinct VBM                      |  | 18662                                 | 17       | 6058          | 4909        | 3191  | 65.00% |  |
| MB Precinct                       |  | 18662                                 | 17       | 451           | 350         | 250   | 71.43% |  |
| Provisional                       |  | 18662                                 | 17       | 364           | 240         | 146   | 60.83% |  |
| Total                             |  | 18662                                 | 85       | 8908          | 7072        | 4541  | 64.21% |  |
| <b>Total</b>                      |  |                                       |          |               |             |       |        |  |
| Precinct TSx                      |  | 109479                                | 102      | 8             | 7           | 4     | 57.14% |  |
| Precinct OS                       |  | 109479                                | 102      | 12009         | 9758        | 5738  | 58.80% |  |
| Precinct VBM                      |  | 109479                                | 102      | 45535         | 38030       | 24621 | 64.74% |  |
| MB Precinct                       |  | 109479                                | 102      | 5143          | 4264        | 2645  | 62.03% |  |
| Provisional                       |  | 109479                                | 102      | 1584          | 1135        | 657   | 57.89% |  |
| Total                             |  | 109479                                | 510      | 64279         | 53194       | 33665 | 63.29% |  |

**COUNTY OF EL DORADO**  
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|---------------------------------|---------------------------------------|----------|---------------|-------------|-------|--------|
|                                 | Reg. Voters                           | Vote For | Times Counted | Total Votes | YES   |        |
| <b>CITIES</b>                   |                                       |          |               |             |       |        |
| <b>CITY OF PLACERVILLE</b>      |                                       |          |               |             |       |        |
| Precinct TSx                    | 5569                                  | 5        | 0             | 0           | 0     | -      |
| Precinct OS                     | 5569                                  | 5        | 631           | 500         | 311   | 62.20% |
| Precinct VBM                    | 5569                                  | 5        | 2590          | 2152        | 1451  | 67.43% |
| MB Precinct                     | 5569                                  | 5        | 0             | 0           | 0     | -      |
| Provisional                     | 5569                                  | 5        | 80            | 55          | 26    | 47.27% |
| Total                           | 5569                                  | 25       | 3301          | 2707        | 1788  | 66.05% |
| <b>CITY OF SOUTH LAKE TAHOE</b> |                                       |          |               |             |       |        |
| Precinct TSx                    | 9465                                  | 7        | 0             | 0           | 0     | -      |
| Precinct OS                     | 9465                                  | 7        | 1063          | 834         | 500   | 59.95% |
| Precinct VBM                    | 9465                                  | 7        | 2796          | 2189        | 1452  | 66.33% |
| MB Precinct                     | 9465                                  | 7        | 0             | 0           | 0     | -      |
| Provisional                     | 9465                                  | 7        | 224           | 143         | 82    | 57.34% |
| Total                           | 9465                                  | 35       | 4083          | 3166        | 2034  | 64.25% |
| <b>UNINCORPORATED AREA</b>      |                                       |          |               |             |       |        |
| Precinct TSx                    | 94445                                 | 90       | 8             | 7           | 4     | 57.14% |
| Precinct OS                     | 94445                                 | 90       | 10315         | 8424        | 4927  | 58.49% |
| Precinct VBM                    | 94445                                 | 90       | 40149         | 33689       | 21718 | 64.47% |
| MB Precinct                     | 94445                                 | 90       | 5143          | 4264        | 2645  | 62.03% |
| Provisional                     | 94445                                 | 90       | 1280          | 937         | 549   | 58.59% |
| Total                           | 94445                                 | 450      | 56895         | 47321       | 29843 | 63.07% |
| <b>Total</b>                    |                                       |          |               |             |       |        |
| Precinct TSx                    | 109479                                | 102      | 8             | 7           | 4     | 57.14% |
| Precinct OS                     | 109479                                | 102      | 12009         | 9758        | 5738  | 58.80% |
| Precinct VBM                    | 109479                                | 102      | 45535         | 38030       | 24621 | 64.74% |
| MB Precinct                     | 109479                                | 102      | 5143          | 4264        | 2645  | 62.03% |
| Provisional                     | 109479                                | 102      | 1584          | 1135        | 657   | 57.89% |
| Total                           | 109479                                | 510      | 64279         | 53194       | 33665 | 63.29% |

**COUNTY OF EL DORADO**  
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|--------------------------|--|
|                          | NO                                       |
| <b>Jurisdiction Wide</b> |  |
| 100001.11-CP 100001.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 51 32.48%                                |
| Precinct VBM             | 196 30.53%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 2 16.67%                                 |
| Total                    | 249 30.70%                               |
| 100002.11-CP 100002.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 26 37.68%                                |
| Precinct VBM             | 154 39.19%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 10 58.82%                                |
| Total                    | 190 39.67%                               |
| 100003.11-CP 100003.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 40 38.83%                                |
| Precinct VBM             | 191 28.94%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 6 31.58%                                 |
| Total                    | 237 30.31%                               |
| 100004.11-CP 100004.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 74 35.58%                                |
| Precinct VBM             | 203 32.79%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 1 50.00%                                 |
| Total                    | 278 33.53%                               |
| 100005.11-CP 100005.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 48 40.00%                                |
| Precinct VBM             | 224 29.24%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 11 57.89%                                |
| Total                    | 283 31.27%                               |
| 100006.11-CP 100006.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 73 37.44%                                |
| Precinct VBM             | 254 33.47%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 8 44.44%                                 |
| Total                    | 335 34.47%                               |
| 100008.11-CP 100008.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 71 48.30%                                |
| Precinct VBM             | 200 32.26%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 1 11.11%                                 |
| Total                    | 272 35.05%                               |
| 100009.11-CP 100009.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 44 32.35%                                |
| Precinct VBM             | 221 34.32%                               |
| MB Precinct              | 0 -                                      |
| Provisional              | 6 40.00%                                 |
| Total                    | 271 34.09%                               |
| 100012.11-CP 100012.11   |  |
| Precinct TSx             | 0 -                                      |
| Precinct OS              | 86 42.79%                                |
| Precinct VBM             | 255 31.60%                               |

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|                        | Measure J - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 34.78%                                 |
| Total                  | 349 33.85%                               |
| 100013.11-CP 100013.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 76 37.44%                                |
| Precinct VBM           | 241 31.67%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 36.36%                                 |
| Total                  | 325 32.96%                               |
| 100014.11-CP 100014.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 71 45.22%                                |
| Precinct VBM           | 191 32.65%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 40.91%                                 |
| Total                  | 271 35.47%                               |
| 100022.11-CP 100022.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 62 40.79%                                |
| Precinct VBM           | 184 33.15%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 37.50%                                 |
| Total                  | 249 34.83%                               |
| 100023.11-CP 100023.11 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 79 46.47%                                |
| Precinct VBM           | 292 32.30%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 36.36%                                 |
| Total                  | 379 34.58%                               |
| 200003.12-CP 200003.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 51 37.23%                                |
| Precinct VBM           | 150 32.26%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 12 50.00%                                |
| Total                  | 213 34.03%                               |
| 200004.12-CP 200004.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 67 43.79%                                |
| Precinct VBM           | 184 32.45%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 40.00%                                 |
| Total                  | 255 34.93%                               |
| 200005.02-CP 200005.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 40 47.62%                                |
| Precinct VBM           | 174 45.31%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 53.33%                                 |
| Total                  | 222 45.96%                               |
| 200006.12-CP 200006.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 44 44.90%                                |
| Precinct VBM           | 175 38.04%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 28.00%                                 |
| Total                  | 226 38.77%                               |
| 200008.12-CP 200008.12 |  |
| Precinct TSx           | 0 -                                      |

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|                        | Measure J - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| Precinct OS            | 54 43.90%                                |
| Precinct VBM           | 107 33.65%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 2 25.00%                                 |
| Total                  | 163 36.30%                               |
| 200010.12-CP 200010.12 |  |
| Precinct TSx           | 1 25.00%                                 |
| Precinct OS            | 60 38.71%                                |
| Precinct VBM           | 234 32.28%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 33.33%                                 |
| Total                  | 303 33.37%                               |
| 200012.12-CP 200012.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 48 42.11%                                |
| Precinct VBM           | 122 31.44%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 54.55%                                 |
| Total                  | 176 34.31%                               |
| 200013.12-CP 200013.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 65 36.31%                                |
| Precinct VBM           | 177 29.60%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 52.94%                                 |
| Total                  | 251 31.61%                               |
| 200014.12-CP 200014.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 49 29.70%                                |
| Precinct VBM           | 169 33.60%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 34.78%                                 |
| Total                  | 226 32.71%                               |
| 200015.02-CP 200015.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 80 45.45%                                |
| Precinct VBM           | 271 36.42%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 42.11%                                 |
| Total                  | 359 38.23%                               |
| 200016.12-CP 200016.12 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 8 34.78%                                 |
| Precinct VBM           | 38 32.76%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 33.33%                                 |
| Total                  | 47 33.10%                                |
| 200016.13-CP 200016.13 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 29 43.94%                                |
| Precinct VBM           | 104 38.10%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 20.00%                                 |
| Total                  | 134 38.95%                               |
| 200017.02-CP 200017.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 50 42.02%                                |
| Precinct VBM           | 172 35.03%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 11.11%                                 |
| Total                  | 223 36.03%                               |

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|------------------------|--|
|                        | NO                                       |
| 200018.02-CP 200018.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 55 40.44%                                |
| Precinct VBM           | 253 42.74%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 33.33%                                 |
| Total                  | 311 42.20%                               |
| 200020.02-CP 200020.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 42 37.50%                                |
| Precinct VBM           | 171 38.34%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 53.33%                                 |
| Total                  | 221 38.57%                               |
| 200021.01-CP 200021.01 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 12 44.44%                                |
| Precinct VBM           | 81 45.51%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 80.00%                                 |
| Total                  | 97 46.19%                                |
| 200021.02-CP 200021.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 64 44.76%                                |
| Precinct VBM           | 233 39.83%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 57.14%                                 |
| Total                  | 305 41.11%                               |
| 200024.02-CP 200024.02 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 22 44.00%                                |
| Precinct VBM           | 107 42.46%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 14.29%                                 |
| Total                  | 130 42.07%                               |
| 300001.04-CP 300001.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 3 30.00%                                 |
| Precinct VBM           | 29 32.95%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 -                                      |
| Total                  | 32 32.65%                                |
| 300001.05-CP 300001.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 20 31.75%                                |
| Precinct VBM           | 88 32.96%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 33.33%                                 |
| Total                  | 111 32.74%                               |
| 300003.05-CP 300003.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 64 38.55%                                |
| Precinct VBM           | 254 33.42%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 33.33%                                 |
| Total                  | 324 34.32%                               |
| 300004.05-CP 300004.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 56 42.42%                                |
| Precinct VBM           | 171 36.00%                               |
| MB Precinct            | 0 -                                      |

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|                        | Measure J - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| Provisional            | 9 81.82%                                 |
| Total                  | 236 38.19%                               |
| 300005.05-CP 300005.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 46 35.66%                                |
| Precinct VBM           | 159 28.29%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 11 64.71%                                |
| Total                  | 216 30.51%                               |
| 300008.05-CP 300008.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 53 43.80%                                |
| Precinct VBM           | 214 34.13%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 60.00%                                 |
| Total                  | 273 36.02%                               |
| 300009.04-CP 300009.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 47 46.08%                                |
| Precinct VBM           | 130 39.76%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 40.00%                                 |
| Total                  | 181 41.23%                               |
| 300010.04-CP 300010.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 81 53.64%                                |
| Precinct VBM           | 201 35.70%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 33.33%                                 |
| Total                  | 285 39.42%                               |
| 300011.04-CP 300011.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 48 38.10%                                |
| Precinct VBM           | 235 39.04%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 26.67%                                 |
| Total                  | 287 38.63%                               |
| 300013.05-CP 300013.05 |  |
| Precinct TSx           | 1 50.00%                                 |
| Precinct OS            | 54 48.65%                                |
| Precinct VBM           | 209 33.17%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 55.56%                                 |
| Total                  | 269 35.77%                               |
| 300015.04-CP 300015.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 5 50.00%                                 |
| Precinct VBM           | 14 29.17%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 33.33%                                 |
| Total                  | 20 32.79%                                |
| 300015.05-CP 300015.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 20 38.46%                                |
| Precinct VBM           | 74 33.18%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 100.00%                                |
| Total                  | 95 34.42%                                |
| 300017.05-CP 300017.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 41 42.71%                                |



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|                        | NO                                       |
| Precinct VBM           | 185 33.39%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 71.43%                                 |
| Total                  | 231 35.16%                               |
| 300018.04-CP 300018.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 53 43.44%                                |
| Precinct VBM           | 172 40.57%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 28.57%                                 |
| Total                  | 231 40.74%                               |
| 300020.04-CP 300020.04 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 66 44.30%                                |
| Precinct VBM           | 189 36.70%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 50.00%                                 |
| Total                  | 260 38.58%                               |
| 300021.03-CP 300021.03 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 50 41.67%                                |
| Precinct VBM           | 181 34.67%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 27.27%                                 |
| Total                  | 234 35.83%                               |
| 300022.05-CP 300022.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 40 48.19%                                |
| Precinct VBM           | 98 33.22%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 50.00%                                 |
| Total                  | 143 36.86%                               |
| 300023.03-CP 300023.03 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 73 37.24%                                |
| Precinct VBM           | 263 34.24%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 36.84%                                 |
| Total                  | 343 34.89%                               |
| 300023.05-CP 300023.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 14 38.89%                                |
| Precinct VBM           | 57 33.53%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 25.00%                                 |
| Total                  | 72 34.29%                                |
| 400001.14-CP 400001.14 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 60 43.48%                                |
| Precinct VBM           | 286 34.88%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 30.00%                                 |
| Total                  | 352 35.99%                               |
| 400002.08-CP 400002.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 71 42.51%                                |
| Precinct VBM           | 225 37.25%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 38.46%                                 |
| Total                  | 301 38.39%                               |
| 400003.08-CP 400003.08 |  |

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|                        | NO                                       |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 104 50.73%                               |
| Precinct VBM           | 257 39.54%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 14 58.33%                                |
| Total                  | 375 42.66%                               |
| 400004.14-CP 400004.14 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 73 48.03%                                |
| Precinct VBM           | 226 39.37%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 35.71%                                 |
| Total                  | 304 41.08%                               |
| 400005.08-CP 400005.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 27 43.55%                                |
| Precinct VBM           | 70 33.18%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 77.78%                                 |
| Total                  | 104 36.88%                               |
| 400006.08-CP 400006.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 71 36.60%                                |
| Precinct VBM           | 194 32.83%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 7 50.00%                                 |
| Total                  | 272 34.04%                               |
| 400007.08-CP 400007.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 77 37.75%                                |
| Precinct VBM           | 268 40.73%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 44.44%                                 |
| Total                  | 353 40.11%                               |
| 400010.08-CP 400010.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 106 50.00%                               |
| Precinct VBM           | 307 41.88%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 10 47.62%                                |
| Total                  | 423 43.79%                               |
| 400012.08-CP 400012.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 37 38.54%                                |
| Precinct VBM           | 147 40.95%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 42.86%                                 |
| Total                  | 190 40.51%                               |
| 400013.08-CP 400013.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 50 48.54%                                |
| Precinct VBM           | 91 34.21%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 1 25.00%                                 |
| Total                  | 142 38.07%                               |
| 400014.08-CP 400014.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 78 42.39%                                |
| Precinct VBM           | 217 34.66%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 16 53.33%                                |

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|                        | NO                                       |
| Total                  | 311 37.02%                               |
| 400017.08-CP 400017.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 66 46.48%                                |
| Precinct VBM           | 286 44.76%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 14 60.87%                                |
| Total                  | 366 45.52%                               |
| 400018.08-CP 400018.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 19 31.15%                                |
| Precinct VBM           | 105 44.12%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 2 50.00%                                 |
| Total                  | 126 41.58%                               |
| 400020.08-CP 400020.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 36 36.00%                                |
| Precinct VBM           | 119 41.03%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 0.00%                                  |
| Total                  | 155 39.54%                               |
| 400021.07-CP 400021.07 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 52 46.02%                                |
| Precinct VBM           | 242 37.69%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 45.45%                                 |
| Total                  | 299 39.03%                               |
| 500001.10-CP 500001.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 51 32.28%                                |
| Precinct VBM           | 153 31.29%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 2 18.18%                                 |
| Total                  | 206 31.31%                               |
| 500002.10-CP 500002.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 36 35.64%                                |
| Precinct VBM           | 116 35.15%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 13 56.52%                                |
| Total                  | 165 36.34%                               |
| 500003.10-CP 500003.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 36 35.29%                                |
| Precinct VBM           | 90 34.09%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 75.00%                                 |
| Total                  | 129 34.86%                               |
| 500004.10-CP 500004.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 51 48.57%                                |
| Precinct VBM           | 72 34.62%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 5 29.41%                                 |
| Total                  | 128 38.79%                               |
| 500005.10-CP 500005.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 31 36.47%                                |
| Precinct VBM           | 68 33.66%                                |

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|                        | NO                                       |
| MB Precinct            | 0 -                                      |
| Provisional            | 3 30.00%                                 |
| Total                  | 102 34.34%                               |
| 500006.10-CP 500006.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 57 41.30%                                |
| Precinct VBM           | 136 35.60%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 9 31.03%                                 |
| Total                  | 202 36.79%                               |
| 500007.10-CP 500007.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 58 34.52%                                |
| Precinct VBM           | 169 31.12%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 50.00%                                 |
| Total                  | 233 32.23%                               |
| 500009.10-CP 500009.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 38 40.00%                                |
| Precinct VBM           | 76 32.76%                                |
| MB Precinct            | 0 -                                      |
| Provisional            | 14 58.33%                                |
| Total                  | 128 36.47%                               |
| 500011.10-CP 500011.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 63 44.37%                                |
| Precinct VBM           | 100 34.25%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 11 39.29%                                |
| Total                  | 174 37.66%                               |
| 500012.10-CP 500012.10 |  |
| Precinct TSx           | 1 100.00%                                |
| Precinct OS            | 53 41.41%                                |
| Precinct VBM           | 196 39.20%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 36.36%                                 |
| Total                  | 258 39.63%                               |
| 500015.10-CP 500015.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 35 41.18%                                |
| Precinct VBM           | 105 31.63%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 4 36.36%                                 |
| Total                  | 144 33.64%                               |
| 500020.10-CP 500020.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 56 41.48%                                |
| Precinct VBM           | 231 39.97%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 8 32.00%                                 |
| Total                  | 295 39.97%                               |
| 500021.10-CP 500021.10 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 53 40.77%                                |
| Precinct VBM           | 206 36.98%                               |
| MB Precinct            | 0 -                                      |
| Provisional            | 6 28.57%                                 |
| Total                  | 265 37.43%                               |
| 95U201.01-MB 95U201.01 |  |
| Precinct TSx           | 0 -                                      |

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|------------------------|--|--------|
|                        | NO                                       |        |
| Precinct OS            | 0  | -      |
| Precinct VBM           | 0  | -      |
| MB Precinct            | 0  | 0.00%  |
| Provisional            | 0  | -      |
| Total                  | 0  | 0.00%  |
| 95U201.02-MB 95U201.02 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 0  | -      |
| Precinct VBM           | 0  | -      |
| MB Precinct            | 114                                      | 39.31% |
| Provisional            | 0  | -      |
| Total                  | 114                                      | 39.31% |
| 95U202.02-MB 95U202.02 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 0  | -      |
| Precinct VBM           | 0  | -      |
| MB Precinct            | 173                                      | 35.97% |
| Provisional            | 2  | 66.67% |
| Total                  | 175                                      | 36.16% |
| 95U203.02-MB 95U203.02 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 0  | -      |
| Precinct VBM           | 0  | -      |
| MB Precinct            | 0  | -      |
| Provisional            | 0  | -      |
| Total                  | 0  | -      |
| 95U204.01-MB 95U204.01 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 0  | -      |
| Precinct VBM           | 0  | -      |
| MB Precinct            | 232                                      | 40.42% |
| Provisional            | 0  | -      |
| Total                  | 232                                      | 40.42% |
| 95U301.05-MB 95U301.05 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 0  | -      |
| Precinct VBM           | 0  | -      |
| MB Precinct            | 54                                       | 41.22% |
| Provisional            | 0  | 0.00%  |
| Total                  | 54                                       | 40.91% |
| 95U302.04-MB 95U302.04 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 0  | -      |
| Precinct VBM           | 0  | -      |
| MB Precinct            | 0  | -      |
| Provisional            | 0  | -      |
| Total                  | 0  | -      |
| 95U302.05-MB 95U302.05 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 0  | -      |
| Precinct VBM           | 0  | -      |
| MB Precinct            | 25                                       | 32.47% |
| Provisional            | 0  | -      |
| Total                  | 25                                       | 32.47% |
| 95U303.03-MB 95U303.03 |  |        |
| Precinct TSx           | 0  | -      |
| Precinct OS            | 0  | -      |
| Precinct VBM           | 0  | -      |
| MB Precinct            | 35                                       | 46.67% |
| Provisional            | 0  | -      |
| Total                  | 35                                       | 46.67% |

**COUNTY OF EL DORADO**  
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|                        | Measure J - County<br>(majority to pass) |
|------------------------|--|
|                        | NO                                       |
| 95U303.05-MB 95U303.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 40 45.45%                                |
| Provisional            | 1 100.00%                                |
| Total                  | 41 46.07%                                |
| 95U304.05-MB 95U304.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 -                                      |
| Total                  | 0 -                                      |
| 95U305.05-MB 95U305.05 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 2 33.33%                                 |
| Provisional            | 0 -                                      |
| Total                  | 2 33.33%                                 |
| 95U401.08-MB 95U401.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 3 27.27%                                 |
| Provisional            | 0 -                                      |
| Total                  | 3 27.27%                                 |
| 95U402.07-MB 95U402.07 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 71 34.63%                                |
| Provisional            | 0 0.00%                                  |
| Total                  | 71 34.47%                                |
| 95U402.08-MB 95U402.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 182 37.84%                               |
| Provisional            | 1 50.00%                                 |
| Total                  | 183 37.89%                               |
| 95U403.08-MB 95U403.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 342 39.04%                               |
| Provisional            | 4 40.00%                                 |
| Total                  | 346 39.05%                               |
| 95U404.06-MB 95U404.06 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 0 -                                      |
| Provisional            | 0 -                                      |
| Total                  | 0 -                                      |
| 95U404.08-MB 95U404.08 |  |
| Precinct TSx           | 0 -                                      |
| Precinct OS            | 0 -                                      |
| Precinct VBM           | 0 -                                      |
| MB Precinct            | 0 -                                      |

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|                               | Measure J - County<br>(majority to pass) |
|-------------------------------|--|
|                               | NO                                       |
| Provisional                   | 0 -                                      |
| Total                         | 0 -                                      |
| 95U501.09-MB 95U501.09        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 0 0.00%                                  |
| Provisional                   | 0 -                                      |
| Total                         | 0 0.00%                                  |
| 95U501.10-MB 95U501.10        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 47 31.54%                                |
| Provisional                   | 1 50.00%                                 |
| Total                         | 48 31.79%                                |
| 95U598.10-MB 95U598.10        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 48 28.07%                                |
| Provisional                   | 1 100.00%                                |
| Total                         | 49 28.49%                                |
| 95U599.10-MB 95U599.10        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 5 17.86%                                 |
| Provisional                   | 0 -                                      |
| Total                         | 5 17.86%                                 |
| 96U101.11-MB 96U101.11        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 118 35.01%                               |
| Provisional                   | 4 80.00%                                 |
| Total                         | 122 35.67%                               |
| 96U401.14-MB 96U401.14        |  |
| Precinct TSx                  | 0 -                                      |
| Precinct OS                   | 0 -                                      |
| Precinct VBM                  | 0 -                                      |
| MB Precinct                   | 128 46.55%                               |
| Provisional                   | 1 16.67%                                 |
| Total                         | 129 45.91%                               |
| Total                         |  |
| Precinct TSx                  | 3 42.86%                                 |
| Precinct OS                   | 4020 41.20%                              |
| Precinct VBM                  | 13409 35.26%                             |
| MB Precinct                   | 1619 37.97%                              |
| Provisional                   | 478 42.11%                               |
| Total                         | 19529 36.71%                             |
| STATE BOARD OF EQUALIZATION   |  |
| STATE BOARD OF EQUALIZATION 1 |  |
| Precinct TSx                  | 3 42.86%                                 |
| Precinct OS                   | 4020 41.20%                              |
| Precinct VBM                  | 13409 35.26%                             |
| MB Precinct                   | 1619 37.97%                              |
| Provisional                   | 478 42.11%                               |
| Total                         | 19529 36.71%                             |
| Total                         |  |

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|                                     | Measure J - County<br>(majority to pass) |               |
|-------------------------------------|--|---------------|
|                                     | NO                                       |               |
| Precinct TSx                        | 3  | 42.86%        |
| Precinct OS                         | 4020                                     | 41.20%        |
| Precinct VBM                        | 13409                                    | 35.26%        |
| MB Precinct                         | 1619                                     | 37.97%        |
| Provisional                         | 478                                      | 42.11%        |
| <b>Total</b>                        | <b>19529</b>                             | <b>36.71%</b> |
| <b>UNITED STATES SENATE</b>         |  |               |
| <b>UNITED STATES SENATE</b>         |  |               |
| Precinct TSx                        | 3  | 42.86%        |
| Precinct OS                         | 4020                                     | 41.20%        |
| Precinct VBM                        | 13409                                    | 35.26%        |
| MB Precinct                         | 1619                                     | 37.97%        |
| Provisional                         | 478                                      | 42.11%        |
| <b>Total</b>                        | <b>19529</b>                             | <b>36.71%</b> |
| <b>Total</b>                        |  |               |
| Precinct TSx                        | 3  | 42.86%        |
| Precinct OS                         | 4020                                     | 41.20%        |
| Precinct VBM                        | 13409                                    | 35.26%        |
| MB Precinct                         | 1619                                     | 37.97%        |
| Provisional                         | 478                                      | 42.11%        |
| <b>Total</b>                        | <b>19529</b>                             | <b>36.71%</b> |
| <b>UNITED STATES REPRESENTATIVE</b> |  |               |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |  |               |
| Precinct TSx                        | 3  | 42.86%        |
| Precinct OS                         | 4020                                     | 41.20%        |
| Precinct VBM                        | 13409                                    | 35.26%        |
| MB Precinct                         | 1619                                     | 37.97%        |
| Provisional                         | 478                                      | 42.11%        |
| <b>Total</b>                        | <b>19529</b>                             | <b>36.71%</b> |
| <b>Total</b>                        |  |               |
| Precinct TSx                        | 3  | 42.86%        |
| Precinct OS                         | 4020                                     | 41.20%        |
| Precinct VBM                        | 13409                                    | 35.26%        |
| MB Precinct                         | 1619                                     | 37.97%        |
| Provisional                         | 478                                      | 42.11%        |
| <b>Total</b>                        | <b>19529</b>                             | <b>36.71%</b> |
| <b>STATE SENATE</b>                 |  |               |
| <b>1ST STATE SENATE DISTRICT</b>    |  |               |
| Precinct TSx                        | 3  | 42.86%        |
| Precinct OS                         | 4020                                     | 41.20%        |
| Precinct VBM                        | 13409                                    | 35.26%        |
| MB Precinct                         | 1619                                     | 37.97%        |
| Provisional                         | 478                                      | 42.11%        |
| <b>Total</b>                        | <b>19529</b>                             | <b>36.71%</b> |
| <b>Total</b>                        |  |               |
| Precinct TSx                        | 3  | 42.86%        |
| Precinct OS                         | 4020                                     | 41.20%        |
| Precinct VBM                        | 13409                                    | 35.26%        |
| MB Precinct                         | 1619                                     | 37.97%        |
| Provisional                         | 478                                      | 42.11%        |
| <b>Total</b>                        | <b>19529</b>                             | <b>36.71%</b> |
| <b>STATE ASSEMBLY DISTRICTS</b>     |  |               |
| <b>5TH STATE ASSEMBLY DIST</b>      |  |               |
| Precinct TSx                        | 2  | 66.67%        |
| Precinct OS                         | 2611                                     | 41.86%        |
| Precinct VBM                        | 8631                                     | 36.72%        |
| MB Precinct                         | 1373                                     | 37.60%        |



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|                                   | Measure J - County<br>(majority to pass) |         |
|-----------------------------------|--|---------|
|                                   | NO                                       |         |
| Provisional                       | 323                                      | 44.13%  |
| Total                             | 12940                                    | 37.91%  |
| <b>6TH STATE ASSEMBLY DIST</b>    |  |         |
| Precinct TSx                      | 1  | 25.00%  |
| Precinct OS                       | 1409                                     | 40.02%  |
| Precinct VBM                      | 4778                                     | 32.90%  |
| MB Precinct                       | 246                                      | 40.20%  |
| Provisional                       | 155                                      | 38.46%  |
| Total                             | 6589                                     | 34.57%  |
| <b>Total</b>                      |  |         |
| Precinct TSx                      | 3  | 42.86%  |
| Precinct OS                       | 4020                                     | 41.20%  |
| Precinct VBM                      | 13409                                    | 35.26%  |
| MB Precinct                       | 1619                                     | 37.97%  |
| Provisional                       | 478                                      | 42.11%  |
| Total                             | 19529                                    | 36.71%  |
| <b>SUPERVISORIAL DISTRICTS</b>    |  |         |
| <b>1ST SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 0  | -       |
| Precinct OS                       | 801                                      | 39.69%  |
| Precinct VBM                      | 2806                                     | 32.20%  |
| MB Precinct                       | 118                                      | 35.01%  |
| Provisional                       | 85                                       | 39.91%  |
| Total                             | 3810                                     | 33.77%  |
| <b>2ND SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 1  | 25.00%  |
| Precinct OS                       | 840                                      | 40.78%  |
| Precinct VBM                      | 2922                                     | 36.14%  |
| MB Precinct                       | 519                                      | 38.39%  |
| Provisional                       | 101                                      | 41.06%  |
| Total                             | 4383                                     | 37.31%  |
| <b>3RD SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 1  | 50.00%  |
| Precinct OS                       | 834                                      | 42.23%  |
| Precinct VBM                      | 2923                                     | 34.71%  |
| MB Precinct                       | 156                                      | 41.38%  |
| Provisional                       | 86                                       | 43.88%  |
| Total                             | 4000                                     | 36.46%  |
| <b>4TH SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 0  | -       |
| Precinct OS                       | 927                                      | 43.46%  |
| Precinct VBM                      | 3040                                     | 38.48%  |
| MB Precinct                       | 726                                      | 39.29%  |
| Provisional                       | 112                                      | 46.67%  |
| Total                             | 4805                                     | 39.64%  |
| <b>5TH SUPERVISORIAL DISTRICT</b> |  |         |
| Precinct TSx                      | 1  | 100.00% |
| Precinct OS                       | 618                                      | 39.31%  |
| Precinct VBM                      | 1718                                     | 35.00%  |
| MB Precinct                       | 100                                      | 28.57%  |
| Provisional                       | 94                                       | 39.17%  |
| Total                             | 2531                                     | 35.79%  |
| <b>Total</b>                      |  |         |
| Precinct TSx                      | 3  | 42.86%  |
| Precinct OS                       | 4020                                     | 41.20%  |
| Precinct VBM                      | 13409                                    | 35.26%  |
| MB Precinct                       | 1619                                     | 37.97%  |
| Provisional                       | 478                                      | 42.11%  |
| Total                             | 19529                                    | 36.71%  |

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|                                 | Measure J - County<br>(majority to pass) |        |
|---------------------------------|--|--------|
|                                 | NO                                       |        |
| <b>CITIES</b>                   |  |        |
| <b>CITY OF PLACERVILLE</b>      |  |        |
| Precinct TSx                    | 0  | -      |
| Precinct OS                     | 189                                      | 37.80% |
| Precinct VBM                    | 701                                      | 32.57% |
| MB Precinct                     | 0  | -      |
| Provisional                     | 29                                       | 52.73% |
| Total                           | 919                                      | 33.95% |
| <b>CITY OF SOUTH LAKE TAHOE</b> |  |        |
| Precinct TSx                    | 0  | -      |
| Precinct OS                     | 334                                      | 40.05% |
| Precinct VBM                    | 737                                      | 33.67% |
| MB Precinct                     | 0  | -      |
| Provisional                     | 61                                       | 42.66% |
| Total                           | 1132                                     | 35.75% |
| <b>UNINCORPORATED AREA</b>      |  |        |
| Precinct TSx                    | 3  | 42.86% |
| Precinct OS                     | 3497                                     | 41.51% |
| Precinct VBM                    | 11971                                    | 35.53% |
| MB Precinct                     | 1619                                     | 37.97% |
| Provisional                     | 388                                      | 41.41% |
| Total                           | 17478                                    | 36.93% |
| <b>Total</b>                    |  |        |
| Precinct TSx                    | 3  | 42.86% |
| Precinct OS                     | 4020                                     | 41.20% |
| Precinct VBM                    | 13409                                    | 35.26% |
| MB Precinct                     | 1619                                     | 37.97% |
| Provisional                     | 478                                      | 42.11% |
| <b>Total</b>                    | 19529                                    | 36.71% |



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|                        | Measure B - Cameron Estates CSD (advisory only) |          |                  |             |     |    |
|------------------------|---|----------|------------------|-------------|-----|----|
|                        | Reg. Voters                                     | Vote For | Times<br>Counted | Total Votes | YES | NO |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 100013.11-CP 100013.11 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 100014.11-CP 100014.11 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 100022.11-CP 100022.11 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 100023.11-CP 100023.11 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 200003.12-CP 200003.12 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 200004.12-CP 200004.12 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 200005.02-CP 200005.02 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 200006.12-CP 200006.12 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 200008.12-CP 200008.12 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |







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| Measure B - Cameron Estates CSD (advisory only) |             |          |                  |             |     |    |
|---|-------------|----------|------------------|-------------|-----|----|
|   | Reg. Voters | Vote For | Times<br>Counted | Total Votes | YES | NO |
| Precinct VBM                                    | -           | -        | -                | -           | -   | -  |
| MB Precinct                                     | -           | -        | -                | -           | -   | -  |
| Provisional                                     | -           | -        | -                | -           | -   | -  |
| Total   | -           | -        | -                | -           | -   | -  |
| 300018.04-CP 300018.04                          |             |          |                  |             |     |    |
| Precinct TSx                                    | -           | -        | -                | -           | -   | -  |
| Precinct OS                                     | -           | -        | -                | -           | -   | -  |
| Precinct VBM                                    | -           | -        | -                | -           | -   | -  |
| MB Precinct                                     | -           | -        | -                | -           | -   | -  |
| Provisional                                     | -           | -        | -                | -           | -   | -  |
| Total   | -           | -        | -                | -           | -   | -  |
| 300020.04-CP 300020.04                          |             |          |                  |             |     |    |
| Precinct TSx                                    | -           | -        | -                | -           | -   | -  |
| Precinct OS                                     | -           | -        | -                | -           | -   | -  |
| Precinct VBM                                    | -           | -        | -                | -           | -   | -  |
| MB Precinct                                     | -           | -        | -                | -           | -   | -  |
| Provisional                                     | -           | -        | -                | -           | -   | -  |
| Total   | -           | -        | -                | -           | -   | -  |
| 300021.03-CP 300021.03                          |             |          |                  |             |     |    |
| Precinct TSx                                    | -           | -        | -                | -           | -   | -  |
| Precinct OS                                     | -           | -        | -                | -           | -   | -  |
| Precinct VBM                                    | -           | -        | -                | -           | -   | -  |
| MB Precinct                                     | -           | -        | -                | -           | -   | -  |
| Provisional                                     | -           | -        | -                | -           | -   | -  |
| Total   | -           | -        | -                | -           | -   | -  |
| 300022.05-CP 300022.05                          |             |          |                  |             |     |    |
| Precinct TSx                                    | -           | -        | -                | -           | -   | -  |
| Precinct OS                                     | -           | -        | -                | -           | -   | -  |
| Precinct VBM                                    | -           | -        | -                | -           | -   | -  |
| MB Precinct                                     | -           | -        | -                | -           | -   | -  |
| Provisional                                     | -           | -        | -                | -           | -   | -  |
| Total   | -           | -        | -                | -           | -   | -  |
| 300023.03-CP 300023.03                          |             |          |                  |             |     |    |
| Precinct TSx                                    | -           | -        | -                | -           | -   | -  |
| Precinct OS                                     | -           | -        | -                | -           | -   | -  |
| Precinct VBM                                    | -           | -        | -                | -           | -   | -  |
| MB Precinct                                     | -           | -        | -                | -           | -   | -  |
| Provisional                                     | -           | -        | -                | -           | -   | -  |
| Total   | -           | -        | -                | -           | -   | -  |
| 300023.05-CP 300023.05                          |             |          |                  |             |     |    |
| Precinct TSx                                    | -           | -        | -                | -           | -   | -  |
| Precinct OS                                     | -           | -        | -                | -           | -   | -  |
| Precinct VBM                                    | -           | -        | -                | -           | -   | -  |
| MB Precinct                                     | -           | -        | -                | -           | -   | -  |
| Provisional                                     | -           | -        | -                | -           | -   | -  |
| Total   | -           | -        | -                | -           | -   | -  |
| 400001.14-CP 400001.14                          |             |          |                  |             |     |    |
| Precinct TSx                                    | -           | -        | -                | -           | -   | -  |
| Precinct OS                                     | -           | -        | -                | -           | -   | -  |
| Precinct VBM                                    | -           | -        | -                | -           | -   | -  |
| MB Precinct                                     | -           | -        | -                | -           | -   | -  |
| Provisional                                     | -           | -        | -                | -           | -   | -  |
| Total   | -           | -        | -                | -           | -   | -  |
| 400002.08-CP 400002.08                          |             |          |                  |             |     |    |
| Precinct TSx                                    | -           | -        | -                | -           | -   | -  |
| Precinct OS                                     | -           | -        | -                | -           | -   | -  |
| Precinct VBM                                    | -           | -        | -                | -           | -   | -  |
| MB Precinct                                     | -           | -        | -                | -           | -   | -  |
| Provisional                                     | -           | -        | -                | -           | -   | -  |
| Total   | -           | -        | -                | -           | -   | -  |
| 400003.08-CP 400003.08                          |             |          |                  |             |     |    |



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|                        | Measure B - Cameron Estates CSD (advisory only) |          |                  |             |     |    |
|------------------------|---|----------|------------------|-------------|-----|----|
|                        | Reg. Voters                                     | Vote For | Times<br>Counted | Total Votes | YES | NO |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 400004.14-CP 400004.14 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 400005.08-CP 400005.08 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 400006.08-CP 400006.08 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 400007.08-CP 400007.08 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 400010.08-CP 400010.08 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 400012.08-CP 400012.08 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 400013.08-CP 400013.08 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 400014.08-CP 400014.08 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |



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|------------------------|---|----------|------------------|-------------|-----|----|
|                        | Reg. Voters                                     | Vote For | Times<br>Counted | Total Votes | YES | NO |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 500006.10-CP 500006.10 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 500007.10-CP 500007.10 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 500009.10-CP 500009.10 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 500011.10-CP 500011.10 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 500012.10-CP 500012.10 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 500015.10-CP 500015.10 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 500020.10-CP 500020.10 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 500021.10-CP 500021.10 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |
| Precinct OS            | -   | -        | -                | -           | -   | -  |
| Precinct VBM           | -   | -        | -                | -           | -   | -  |
| MB Precinct            | -   | -        | -                | -           | -   | -  |
| Provisional            | -   | -        | -                | -           | -   | -  |
| Total                  | -   | -        | -                | -           | -   | -  |
| 95U201.01-MB 95U201.01 | -   | -        | -                | -           | -   | -  |
| Precinct TSx           | -   | -        | -                | -           | -   | -  |



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|------------------------|---|----------|---------------|-------------|-----|----|
|                        | Reg. Voters                                     | Vote For | Times Counted | Total Votes | YES | NO |
| 95U303.05-MB 95U303.05 |   |          |               |             |     |    |
| Precinct TSx           | -   | -        | -             | -           | -   | -  |
| Precinct OS            | -   | -        | -             | -           | -   | -  |
| Precinct VBM           | -   | -        | -             | -           | -   | -  |
| MB Precinct            | -   | -        | -             | -           | -   | -  |
| Provisional            | -   | -        | -             | -           | -   | -  |
| Total                  | -   | -        | -             | -           | -   | -  |
| 95U304.05-MB 95U304.05 |   |          |               |             |     |    |
| Precinct TSx           | -   | -        | -             | -           | -   | -  |
| Precinct OS            | -   | -        | -             | -           | -   | -  |
| Precinct VBM           | -   | -        | -             | -           | -   | -  |
| MB Precinct            | -   | -        | -             | -           | -   | -  |
| Provisional            | -   | -        | -             | -           | -   | -  |
| Total                  | -   | -        | -             | -           | -   | -  |
| 95U305.05-MB 95U305.05 |   |          |               |             |     |    |
| Precinct TSx           | -   | -        | -             | -           | -   | -  |
| Precinct OS            | -   | -        | -             | -           | -   | -  |
| Precinct VBM           | -   | -        | -             | -           | -   | -  |
| MB Precinct            | -   | -        | -             | -           | -   | -  |
| Provisional            | -   | -        | -             | -           | -   | -  |
| Total                  | -   | -        | -             | -           | -   | -  |
| 95U401.08-MB 95U401.08 |   |          |               |             |     |    |
| Precinct TSx           | -   | -        | -             | -           | -   | -  |
| Precinct OS            | -   | -        | -             | -           | -   | -  |
| Precinct VBM           | -   | -        | -             | -           | -   | -  |
| MB Precinct            | -   | -        | -             | -           | -   | -  |
| Provisional            | -   | -        | -             | -           | -   | -  |
| Total                  | -   | -        | -             | -           | -   | -  |
| 95U402.07-MB 95U402.07 |   |          |               |             |     |    |
| Precinct TSx           | -   | -        | -             | -           | -   | -  |
| Precinct OS            | -   | -        | -             | -           | -   | -  |
| Precinct VBM           | -   | -        | -             | -           | -   | -  |
| MB Precinct            | -   | -        | -             | -           | -   | -  |
| Provisional            | -   | -        | -             | -           | -   | -  |
| Total                  | -   | -        | -             | -           | -   | -  |
| 95U402.08-MB 95U402.08 |   |          |               |             |     |    |
| Precinct TSx           | -   | -        | -             | -           | -   | -  |
| Precinct OS            | -   | -        | -             | -           | -   | -  |
| Precinct VBM           | -   | -        | -             | -           | -   | -  |
| MB Precinct            | -   | -        | -             | -           | -   | -  |
| Provisional            | -   | -        | -             | -           | -   | -  |
| Total                  | -   | -        | -             | -           | -   | -  |
| 95U403.08-MB 95U403.08 |   |          |               |             |     |    |
| Precinct TSx           | -   | -        | -             | -           | -   | -  |
| Precinct OS            | -   | -        | -             | -           | -   | -  |
| Precinct VBM           | -   | -        | -             | -           | -   | -  |
| MB Precinct            | -   | -        | -             | -           | -   | -  |
| Provisional            | -   | -        | -             | -           | -   | -  |
| Total                  | -   | -        | -             | -           | -   | -  |
| 95U404.06-MB 95U404.06 |   |          |               |             |     |    |
| Precinct TSx           | -   | -        | -             | -           | -   | -  |
| Precinct OS            | -   | -        | -             | -           | -   | -  |
| Precinct VBM           | -   | -        | -             | -           | -   | -  |
| MB Precinct            | -   | -        | -             | -           | -   | -  |
| Provisional            | -   | -        | -             | -           | -   | -  |
| Total                  | -   | -        | -             | -           | -   | -  |
| 95U404.08-MB 95U404.08 |   |          |               |             |     |    |
| Precinct TSx           | -   | -        | -             | -           | -   | -  |
| Precinct OS            | -   | -        | -             | -           | -   | -  |
| Precinct VBM           | -   | -        | -             | -           | -   | -  |
| MB Precinct            | -   | -        | -             | -           | -   | -  |

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|-------------------------------|--|---|----------|------------------|-------------|-----|-------|------------|
|                               |  | Reg. Voters                                     | Vote For | Times<br>Counted | Total Votes | YES | NO    |            |
| Provisional                   |  | -   | -        | -                | -           | -   | -     | -          |
| Total                         |  | -   | -        | -                | -           | -   | -     | -          |
| 95U501.09-MB 95U501.09        |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct TSx                  |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct OS                   |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct VBM                  |  | -   | -        | -                | -           | -   | -     | -          |
| MB Precinct                   |  | -   | -        | -                | -           | -   | -     | -          |
| Provisional                   |  | -   | -        | -                | -           | -   | -     | -          |
| Total                         |  | -   | -        | -                | -           | -   | -     | -          |
| 95U501.10-MB 95U501.10        |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct TSx                  |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct OS                   |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct VBM                  |  | -   | -        | -                | -           | -   | -     | -          |
| MB Precinct                   |  | -   | -        | -                | -           | -   | -     | -          |
| Provisional                   |  | -   | -        | -                | -           | -   | -     | -          |
| Total                         |  | -   | -        | -                | -           | -   | -     | -          |
| 95U598.10-MB 95U598.10        |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct TSx                  |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct OS                   |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct VBM                  |  | -   | -        | -                | -           | -   | -     | -          |
| MB Precinct                   |  | -   | -        | -                | -           | -   | -     | -          |
| Provisional                   |  | -   | -        | -                | -           | -   | -     | -          |
| Total                         |  | -   | -        | -                | -           | -   | -     | -          |
| 95U599.10-MB 95U599.10        |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct TSx                  |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct OS                   |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct VBM                  |  | -   | -        | -                | -           | -   | -     | -          |
| MB Precinct                   |  | -   | -        | -                | -           | -   | -     | -          |
| Provisional                   |  | -   | -        | -                | -           | -   | -     | -          |
| Total                         |  | -   | -        | -                | -           | -   | -     | -          |
| 96U101.11-MB 96U101.11        |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct TSx                  |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct OS                   |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct VBM                  |  | -   | -        | -                | -           | -   | -     | -          |
| MB Precinct                   |  | -   | -        | -                | -           | -   | -     | -          |
| Provisional                   |  | -   | -        | -                | -           | -   | -     | -          |
| Total                         |  | -   | -        | -                | -           | -   | -     | -          |
| 96U401.14-MB 96U401.14        |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct TSx                  |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct OS                   |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct VBM                  |  | -   | -        | -                | -           | -   | -     | -          |
| MB Precinct                   |  | -   | -        | -                | -           | -   | -     | -          |
| Provisional                   |  | -   | -        | -                | -           | -   | -     | -          |
| Total                         |  | -   | -        | -                | -           | -   | -     | -          |
| Total                         |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct TSx                  |  | 565   | 1        | 0                | 0           | 0   | -     | 0          |
| Precinct OS                   |  | 565   | 1        | 80               | 78          | 6   | 7.69% | 72 92.31%  |
| Precinct VBM                  |  | 565   | 1        | 338              | 336         | 27  | 8.04% | 309 91.96% |
| MB Precinct                   |  | 565   | 1        | 0                | 0           | 0   | -     | 0          |
| Provisional                   |  | 565   | 1        | 6                | 1           | 0   | 0.00% | 1 100.00%  |
| Total                         |  | 565   | 5        | 424              | 415         | 33  | 7.95% | 382 92.05% |
| STATE BOARD OF EQUALIZATION   |  | -   | -        | -                | -           | -   | -     | -          |
| STATE BOARD OF EQUALIZATION 1 |  | -   | -        | -                | -           | -   | -     | -          |
| Precinct TSx                  |  | 565   | 1        | 0                | 0           | 0   | -     | 0          |
| Precinct OS                   |  | 565   | 1        | 80               | 78          | 6   | 7.69% | 72 92.31%  |
| Precinct VBM                  |  | 565   | 1        | 338              | 336         | 27  | 8.04% | 309 91.96% |
| MB Precinct                   |  | 565   | 1        | 0                | 0           | 0   | -     | 0          |
| Provisional                   |  | 565   | 1        | 6                | 1           | 0   | 0.00% | 1 100.00%  |
| Total                         |  | 565   | 5        | 424              | 415         | 33  | 7.95% | 382 92.05% |
| Total                         |  | -   | -        | -                | -           | -   | -     | -          |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
**TUESDAY, JUNE 7, 2016**  
**OFFICIAL STATEMENT OF VOTES CAST**

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|                                     |  | Measure B - Cameron Estates CSD (advisory only) |          |               |             |           |              |            |               |  |
|-------------------------------------|--|---|----------|---------------|-------------|-----------|--------------|------------|---------------|--|
|                                     |  | Reg. Voters                                     | Vote For | Times Counted | Total Votes | YES       |              | NO         |               |  |
| Precinct TSx                        |  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |  |
| Precinct OS                         |  | 565   | 1        | 80            | 78          | 6         | 7.69%        | 72         | 92.31%        |  |
| Precinct VBM                        |  | 565   | 1        | 338           | 336         | 27        | 8.04%        | 309        | 91.96%        |  |
| MB Precinct                         |  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |  |
| Provisional                         |  | 565   | 1        | 6             | 1           | 0         | 0.00%        | 1          | 100.00%       |  |
| <b>Total</b>                        |  | <b>565</b>                                      | <b>5</b> | <b>424</b>    | <b>415</b>  | <b>33</b> | <b>7.95%</b> | <b>382</b> | <b>92.05%</b> |  |
| <b>UNITED STATES SENATE</b>         |  |   |          |               |             |           |              |            |               |  |
| <b>UNITED STATES SENATE</b>         |  |   |          |               |             |           |              |            |               |  |
| Precinct TSx                        |  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |  |
| Precinct OS                         |  | 565   | 1        | 80            | 78          | 6         | 7.69%        | 72         | 92.31%        |  |
| Precinct VBM                        |  | 565   | 1        | 338           | 336         | 27        | 8.04%        | 309        | 91.96%        |  |
| MB Precinct                         |  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |  |
| Provisional                         |  | 565   | 1        | 6             | 1           | 0         | 0.00%        | 1          | 100.00%       |  |
| <b>Total</b>                        |  | <b>565</b>                                      | <b>5</b> | <b>424</b>    | <b>415</b>  | <b>33</b> | <b>7.95%</b> | <b>382</b> | <b>92.05%</b> |  |
| <b>Total</b>                        |  |   |          |               |             |           |              |            |               |  |
| Precinct TSx                        |  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |  |
| Precinct OS                         |  | 565   | 1        | 80            | 78          | 6         | 7.69%        | 72         | 92.31%        |  |
| Precinct VBM                        |  | 565   | 1        | 338           | 336         | 27        | 8.04%        | 309        | 91.96%        |  |
| MB Precinct                         |  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |  |
| Provisional                         |  | 565   | 1        | 6             | 1           | 0         | 0.00%        | 1          | 100.00%       |  |
| <b>Total</b>                        |  | <b>565</b>                                      | <b>5</b> | <b>424</b>    | <b>415</b>  | <b>33</b> | <b>7.95%</b> | <b>382</b> | <b>92.05%</b> |  |
| <b>Total</b>                        |  |   |          |               |             |           |              |            |               |  |
| <b>UNITED STATES REPRESENTATIVE</b> |  |   |          |               |             |           |              |            |               |  |
| <b>4TH CONGRESSIONAL DISTRICT</b>   |  |   |          |               |             |           |              |            |               |  |
| Precinct TSx                        |  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |  |
| Precinct OS                         |  | 565   | 1        | 80            | 78          | 6         | 7.69%        | 72         | 92.31%        |  |
| Precinct VBM                        |  | 565   | 1        | 338           | 336         | 27        | 8.04%        | 309        | 91.96%        |  |
| MB Precinct                         |  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |  |
| Provisional                         |  | 565   | 1        | 6             | 1           | 0         | 0.00%        | 1          | 100.00%       |  |
| <b>Total</b>                        |  | <b>565</b>                                      | <b>5</b> | <b>424</b>    | <b>415</b>  | <b>33</b> | <b>7.95%</b> | <b>382</b> | <b>92.05%</b> |  |
| <b>Total</b>                        |  |   |          |               |             |           |              |            |               |  |
| Precinct TSx                        |  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |  |
| Precinct OS                         |  | 565   | 1        | 80            | 78          | 6         | 7.69%        | 72         | 92.31%        |  |
| Precinct VBM                        |  | 565   | 1        | 338           | 336         | 27        | 8.04%        | 309        | 91.96%        |  |
| MB Precinct                         |  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |  |
| Provisional                         |  | 565   | 1        | 6             | 1           | 0         | 0.00%        | 1          | 100.00%       |  |
| <b>Total</b>                        |  | <b>565</b>                                      | <b>5</b> | <b>424</b>    | <b>415</b>  | <b>33</b> | <b>7.95%</b> | <b>382</b> | <b>92.05%</b> |  |
| <b>Total</b>                        |  |   |          |               |             |           |              |            |               |  |
| <b>STATE SENATE</b>                 |  |   |          |               |             |           |              |            |               |  |
| <b>1ST STATE SENATE DISTRICT</b>    |  |   |          |               |             |           |              |            |               |  |
| Precinct TSx                        |  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |  |
| Precinct OS                         |  | 565   | 1        | 80            | 78          | 6         | 7.69%        | 72         | 92.31%        |  |
| Precinct VBM                        |  | 565   | 1        | 338           | 336         | 27        | 8.04%        | 309        | 91.96%        |  |
| MB Precinct                         |  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |  |
| Provisional                         |  | 565   | 1        | 6             | 1           | 0         | 0.00%        | 1          | 100.00%       |  |
| <b>Total</b>                        |  | <b>565</b>                                      | <b>5</b> | <b>424</b>    | <b>415</b>  | <b>33</b> | <b>7.95%</b> | <b>382</b> | <b>92.05%</b> |  |
| <b>Total</b>                        |  |   |          |               |             |           |              |            |               |  |
| Precinct TSx                        |  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |  |
| Precinct OS                         |  | 565   | 1        | 80            | 78          | 6         | 7.69%        | 72         | 92.31%        |  |
| Precinct VBM                        |  | 565   | 1        | 338           | 336         | 27        | 8.04%        | 309        | 91.96%        |  |
| MB Precinct                         |  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |  |
| Provisional                         |  | 565   | 1        | 6             | 1           | 0         | 0.00%        | 1          | 100.00%       |  |
| <b>Total</b>                        |  | <b>565</b>                                      | <b>5</b> | <b>424</b>    | <b>415</b>  | <b>33</b> | <b>7.95%</b> | <b>382</b> | <b>92.05%</b> |  |
| <b>Total</b>                        |  |   |          |               |             |           |              |            |               |  |
| <b>STATE ASSEMBLY DISTRICTS</b>     |  |   |          |               |             |           |              |            |               |  |
| <b>5TH STATE ASSEMBLY DIST</b>      |  |   |          |               |             |           |              |            |               |  |
| Precinct TSx                        |  | 0   | 0        | 0             | 0           | 0         | -            | 0          | -             |  |
| Precinct OS                         |  | 0   | 0        | 0             | 0           | 0         | -            | 0          | -             |  |
| Precinct VBM                        |  | 0   | 0        | 0             | 0           | 0         | -            | 0          | -             |  |
| MB Precinct                         |  | 0   | 0        | 0             | 0           | 0         | -            | 0          | -             |  |

**COUNTY OF EL DORADO**  
**CONSOLIDATED PRESIDENTIAL PRIMARY ELECTION**  
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| Measure B - Cameron Estates CSD (advisory only) |             |          |               |             |     |       |            |
|---|-------------|----------|---------------|-------------|-----|-------|------------|
|   | Reg. Voters | Vote For | Times Counted | Total Votes | YES |       | NO         |
| Provisional                                     | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Total   | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| <b>6TH STATE ASSEMBLY DIST</b>                  |             |          |               |             |     |       |            |
| Precinct TSx                                    | 565         | 1        | 0             | 0           | 0   | -     | 0          |
| Precinct OS                                     | 565         | 1        | 80            | 78          | 6   | 7.69% | 72 92.31%  |
| Precinct VBM                                    | 565         | 1        | 338           | 336         | 27  | 8.04% | 309 91.96% |
| MB Precinct                                     | 565         | 1        | 0             | 0           | 0   | -     | 0          |
| Provisional                                     | 565         | 1        | 6             | 1           | 0   | 0.00% | 1 100.00%  |
| Total   | 565         | 5        | 424           | 415         | 33  | 7.95% | 382 92.05% |
| <b>Total</b>                                    |             |          |               |             |     |       |            |
| Precinct TSx                                    | 565         | 1        | 0             | 0           | 0   | -     | 0          |
| Precinct OS                                     | 565         | 1        | 80            | 78          | 6   | 7.69% | 72 92.31%  |
| Precinct VBM                                    | 565         | 1        | 338           | 336         | 27  | 8.04% | 309 91.96% |
| MB Precinct                                     | 565         | 1        | 0             | 0           | 0   | -     | 0          |
| Provisional                                     | 565         | 1        | 6             | 1           | 0   | 0.00% | 1 100.00%  |
| Total   | 565         | 5        | 424           | 415         | 33  | 7.95% | 382 92.05% |
| <b>SUPERVISORIAL DISTRICTS</b>                  |             |          |               |             |     |       |            |
| <b>1ST SUPERVISORIAL DISTRICT</b>               |             |          |               |             |     |       |            |
| Precinct TSx                                    | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Precinct OS                                     | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Precinct VBM                                    | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| MB Precinct                                     | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Provisional                                     | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Total   | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| <b>2ND SUPERVISORIAL DISTRICT</b>               |             |          |               |             |     |       |            |
| Precinct TSx                                    | 565         | 1        | 0             | 0           | 0   | -     | 0          |
| Precinct OS                                     | 565         | 1        | 80            | 78          | 6   | 7.69% | 72 92.31%  |
| Precinct VBM                                    | 565         | 1        | 338           | 336         | 27  | 8.04% | 309 91.96% |
| MB Precinct                                     | 565         | 1        | 0             | 0           | 0   | -     | 0          |
| Provisional                                     | 565         | 1        | 6             | 1           | 0   | 0.00% | 1 100.00%  |
| Total   | 565         | 5        | 424           | 415         | 33  | 7.95% | 382 92.05% |
| <b>3RD SUPERVISORIAL DISTRICT</b>               |             |          |               |             |     |       |            |
| Precinct TSx                                    | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Precinct OS                                     | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Precinct VBM                                    | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| MB Precinct                                     | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Provisional                                     | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Total   | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| <b>4TH SUPERVISORIAL DISTRICT</b>               |             |          |               |             |     |       |            |
| Precinct TSx                                    | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Precinct OS                                     | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Precinct VBM                                    | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| MB Precinct                                     | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Provisional                                     | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Total   | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| <b>5TH SUPERVISORIAL DISTRICT</b>               |             |          |               |             |     |       |            |
| Precinct TSx                                    | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Precinct OS                                     | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Precinct VBM                                    | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| MB Precinct                                     | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Provisional                                     | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| Total   | 0           | 0        | 0             | 0           | 0   | -     | 0          |
| <b>Total</b>                                    |             |          |               |             |     |       |            |
| Precinct TSx                                    | 565         | 1        | 0             | 0           | 0   | -     | 0          |
| Precinct OS                                     | 565         | 1        | 80            | 78          | 6   | 7.69% | 72 92.31%  |
| Precinct VBM                                    | 565         | 1        | 338           | 336         | 27  | 8.04% | 309 91.96% |
| MB Precinct                                     | 565         | 1        | 0             | 0           | 0   | -     | 0          |
| Provisional                                     | 565         | 1        | 6             | 1           | 0   | 0.00% | 1 100.00%  |
| Total   | 565         | 5        | 424           | 415         | 33  | 7.95% | 382 92.05% |



**COUNTY OF EL DORADO**  
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|                                 |              | Measure B - Cameron Estates CSD (advisory only) |          |               |             |           |              |            |               |
|---------------------------------|--------------|---|----------|---------------|-------------|-----------|--------------|------------|---------------|
|                                 |              | Reg. Voters                                     | Vote For | Times Counted | Total Votes | YES       | NO           |            |               |
| <b>CITIES</b>                   |              |   |          |               |             |           |              |            |               |
| <b>CITY OF PLACERVILLE</b>      |              |   |          |               |             |           |              |            |               |
|                                 | Precinct TSx | 0   | 0        | 0             | 0           | 0         | -            | 0          | -             |
|                                 | Precinct OS  | 0   | 0        | 0             | 0           | 0         | -            | 0          | -             |
|                                 | Precinct VBM | 0   | 0        | 0             | 0           | 0         | -            | 0          | -             |
|                                 | MB Precinct  | 0   | 0        | 0             | 0           | 0         | -            | 0          | -             |
|                                 | Provisional  | 0   | 0        | 0             | 0           | 0         | -            | 0          | -             |
|                                 | <b>Total</b> | <b>0</b>  | <b>0</b> | <b>0</b>      | <b>0</b>    | <b>0</b>  | <b>-</b>     | <b>0</b>   | <b>-</b>      |
| <b>CITY OF SOUTH LAKE TAHOE</b> |              |   |          |               |             |           |              |            |               |
|                                 | Precinct TSx | 0   | 0        | 0             | 0           | 0         | -            | 0          | -             |
|                                 | Precinct OS  | 0   | 0        | 0             | 0           | 0         | -            | 0          | -             |
|                                 | Precinct VBM | 0   | 0        | 0             | 0           | 0         | -            | 0          | -             |
|                                 | MB Precinct  | 0   | 0        | 0             | 0           | 0         | -            | 0          | -             |
|                                 | Provisional  | 0   | 0        | 0             | 0           | 0         | -            | 0          | -             |
|                                 | <b>Total</b> | <b>0</b>  | <b>0</b> | <b>0</b>      | <b>0</b>    | <b>0</b>  | <b>-</b>     | <b>0</b>   | <b>-</b>      |
| <b>UNINCORPORATED AREA</b>      |              |   |          |               |             |           |              |            |               |
|                                 | Precinct TSx | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |
|                                 | Precinct OS  | 565   | 1        | 80            | 78          | 6         | 7.69%        | 72         | 92.31%        |
|                                 | Precinct VBM | 565   | 1        | 338           | 336         | 27        | 8.04%        | 309        | 91.96%        |
|                                 | MB Precinct  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |
|                                 | Provisional  | 565   | 1        | 6             | 1           | 0         | 0.00%        | 1          | 100.00%       |
|                                 | <b>Total</b> | <b>565</b>                                      | <b>5</b> | <b>424</b>    | <b>415</b>  | <b>33</b> | <b>7.95%</b> | <b>382</b> | <b>92.05%</b> |
| <b>Total</b>                    |              |   |          |               |             |           |              |            |               |
|                                 | Precinct TSx | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |
|                                 | Precinct OS  | 565   | 1        | 80            | 78          | 6         | 7.69%        | 72         | 92.31%        |
|                                 | Precinct VBM | 565   | 1        | 338           | 336         | 27        | 8.04%        | 309        | 91.96%        |
|                                 | MB Precinct  | 565   | 1        | 0             | 0           | 0         | -            | 0          | -             |
|                                 | Provisional  | 565   | 1        | 6             | 1           | 0         | 0.00%        | 1          | 100.00%       |
|                                 | <b>Total</b> | <b>565</b>                                      | <b>5</b> | <b>424</b>    | <b>415</b>  | <b>33</b> | <b>7.95%</b> | <b>382</b> | <b>92.05%</b> |